



FT-2000 OC-48 Lightwave System Releases Through 7.2

User/Service Manual — Volume I

365-575-102
Issue 8.0
July 2002

Copyright© 2002 Lucent Technologies
All Rights Reserved
Printed in U.S.A.

This material is protected by the copyright and trade secret laws of the United States and other countries. It may not be reproduced, distributed or altered in any fashion by any entity (either internal or external to Lucent Technologies), except in accordance with applicable agreements, contracts, or licensing, without the express written consent of the Customer Training and Information Products organization and the business management owner of the material.

For permission to reproduce or distribute this document please contact your Account Executive.

Notice

The information in this document is subject to change without notice. Although every effort has been made to make this document as accurate, complete, and clear as possible, Lucent Technologies Inc. and its predecessors assume no responsibility for any errors that may appear in this document.

Federal Communications Commission (FCC) Notification and Repair Information¹

This equipment is designed to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions manual, may cause interference to radio communications. Operation of this equipment in a residence is likely to cause harmful interference, in which case, the user will be required to correct the interference at his own expense.

Security

In rare instances, unauthorized individuals make connections to the telecommunications network. In such an event, applicable tariffs require that the customer pay all network charges for traffic. Lucent Technologies Inc. and its predecessors cannot be responsible for such charges and will not make any allowance or give any credit for charges that result from unauthorized access.

Trademarks

5ESS, DACScan, LGX, SLC, and ST and are registered trademarks of Lucent Technologies.

ANSI is a registered trademark of American National Standards Institute, Inc.

Common Language is a registered trademark and CLEI, CLLI, CLCI, and CLFI are trademarks of Bell Communications Research, Inc.

CSA is a registered trademark of Canadian Standards Association.

DANTEL is a registered trademark of DANTEL Incorporated.

ESS is a trademark of Lucent Technologies.

Gateway 2000 is a registered trademark of Gateway 2000, Inc.

IBM is a registered trademark of Microsoft Corporation.

MS-DOS is a registered trademark of Microsoft Corporation.

NCR is a trademark of NCR Corporation.

NEC is a registered of Nippon Denki Kabushiki Kaisha.

Netscape is a registered trademark of Netscape Communications Corporation.

PROCOMM PLUS is a registered trademark of Datastorm Technologies Inc.

SPARC is a registered trademark of SPARC International, Inc., licensed exclusively to Sun Microsystems Inc.

Sun is a registered trademark of SUN Microsystems Inc.

Titan is a registered trademark of Tellabs, Inc.

WaveStar is a registered trademark of Lucent Technologies.

Windows is registered trademark of Microsoft Corporation.

Warranty

The terms and conditions of sale will include a 1-year warranty on hardware and applicable software.

1. Language of statute.

Customer Assistance and Technical Support

The Lucent Technologies Regional Technical Assistance Center (RTAC) provides a technical assistance telephone number that is monitored 24 hours a day. For technical assistance, call 1-800-225-RTAC. You can also call this telephone number to provide comments on FT-2000 OC-48 Lightwave System or to suggest enhancements.

Documentation

This document was developed by the Lucent Technologies Optical Networking Group, Lucent Learning Services (formerly Customer Training and Information Products [CTIP]).

Lucent Technologies values your comments!

FT-2000 OC-48 Lightwave System Releases Through 7.2

365-575-102 Issue 8 Date: July 2002

Lucent Technologies welcomes your comments on this information product. Your opinion is of great value and helps us to improve.

1. Was the information product:

	Yes	No	Not applicable
In the language of your choice?	☐	☐	☐
In the desired media (paper, CD-ROM, etc.)?	☐	☐	☐
Available when you needed it?	☐	☐	☐

Please provide any additional comments:

2. Please rate the effectiveness of this information product:

	Excellent	More than satisfactory	Satisfactory	Less than satisfactory	Unsatisfactory	Not applicable
Ease of use	☐	☐	☐	☐	☐	☐
Level of detail	☐	☐	☐	☐	☐	☐
Readability and clarity	☐	☐	☐	☐	☐	☐
Organization	☐	☐	☐	☐	☐	☐
Completeness	☐	☐	☐	☐	☐	☐
Technical accuracy	☐	☐	☐	☐	☐	☐
Quality of translation	☐	☐	☐	☐	☐	☐
Appearance	☐	☐	☐	☐	☐	☐

If your response to any of the above questions is “*Less than satisfactory*” or “*Unsatisfactory*,” please explain your rating.

3. If you could change one thing about this information product, what would it be?

4. Please write any other comments about this information product:

Please complete the following if we may contact you for clarification or to address your concerns:

Name: _____ Date: _____

Company/organization: _____ Telephone number: _____

Address: _____

Email address: _____ Job function: _____

If you choose to complete this form online, go to <http://www.lucent-info.com/comments>

Otherwise fax to 407 767 2760 (U.S.) or +1 407 767 2760 (outside the U.S.) or email comments to ctiphotline@lucent.com



Contents

About This Document	xxxi
■ Purpose	xxxi
■ Intended Audiences	xxxi
■ Reason for Reissue	xxxii
■ How to Use This Document	xxxiii
■ Conventions Used	xxxiv
■ Safety Instructions	xxxiv
Safety Labels	xxxiv
Lightwave Safety Guidelines	xxxv
Electrostatic Discharge (ESD) Considerations	xxxix
■ Security	xli
IMPORTANT SAFETY INSTRUCTIONS	xli
IMPORTANT INSTALLATION SAFETY INSTRUCTIONS	xlii
■ Related Documentation/Training	xlili
Lucent Technologies	xlili
Lucent Technologies Drawings	xlvi
Lucent Technologies Practices for Related Equipment	l
Training	li
■ Technical Support	lii
Introduction	lii
Technical support groups	lii
Contacting NARTAC	lii
NARTAC services	lii
CTS services	liii
A technical support platform	liii
■ How to Order Documents	liv
■ How to Comment on the FT-2000 OC-48 Lightwave System	lv
■ Commenting on this Document	lv
Introduction	lv
■ Customer comment form	lv
Address for Comments	lv

Contents

■ Electronic Documentation	lvi
■ Product Change Notifications	lvi

1	System Introduction	1-1
■	Overview	1-1
■	Introduction to the Lucent Technologies 2000 Product Family	1-1
■	What Is the FT-2000 OC-48 Lightwave System?	1-2

2	Applications	2-1
■	Overview	2-1
■	Network Configurations	2-2
	Two-Fiber Bidirectional Line-Switched Rings	2-2
	Folded Rings (Point-to-Point)	2-6
	End-to-End Interface Mixing	2-8
	OC-48 Hubbing	2-10
	Dual Ring Interworking (DRI)	2-11
	DDM-2000 OC-3 Ring Transport	2-17
	DDM-2000 OC-12 Ring Transport	2-18
	Dual-Wire Center Architecture	2-18
	Broadband Service Transport —STS-3c/STS-12c	2-21
	STS-1/STS-3 Non-Preemptible Protection Access	2-24
	One-Way Video Distribution	2-26
	Path-In-Line Architecture	2-27
	Loop Feeder Network	2-30
	Extra Traffic for Restoration and Revenue Generation	2-32
	0% Add/Drop	2-34
	FT-Lightwave Booster Amplifier (FT-LBA) for Extended Distances	2-35
	Wavelength Division Multiplexing (WDM)	2-37

Contents

Dense Wavelength Division Multiplexing (EON Compatible)	2-41
Operations Interworking	2-42
Gateway Network Element (GNE)	2-46
Synchronization Messaging	2-48

3	Platform Descriptions	3-1
	■ Overview	3-1
	■ Introduction	3-1
	■ FT-2000 OC-48 Add/Drop-Rings Terminal (E-Bay)	3-2
	Equipment Packages	3-2
	Shelf Descriptions	3-4
	■ FT-2000 OC-48 Repeater (R-Bay)	3-32
	Equipment Packages	3-32
	Shelf Descriptions	3-35
	Upgrades	3-44
	■ FT-2000 OC-48 Dual ADR Terminal (D-Bay)	3-45
	Equipment Packages	3-45
	Shelf Descriptions	3-47

4	Power	4-1
	■ Overview	4-1
	■ General	4-1
	■ Power Distribution	4-2
	■ Power Dissipation	4-6
	■ LEDs	4-7

Contents

5	Control, Transmission, and Synchronization Interfaces	5-1
■	Overview	5-1
■	Control	5-1
■	Transmission	5-5
	FT-2000 OC-48 Add/Drop-Rings Terminal	5-5
	FT-2000 OC-48 Repeater Bay	5-9
■	Synchronization Modes	5-10
	Platform Synchronization	5-10
	Network Synchronization	5-19

6	Operations Interfaces	6-1
■	Overview	6-1
■	Craft Interface Terminal	6-1
	General	6-1
	PC with FT-2000 CIT-PC Software	6-2
	PC with CPro-2000 Software	6-5
	ASCII-Based Terminal	6-7
	Local Access Through the CIT (DCE) Port	6-8
	Dial-Up Access Using a Modem Through the CIT (DTE) Port	6-8
	Remote Access Using the OC-48 Data Communications Channel (DCC)	6-10
	Remote Access Using the OC-48 Section Orderwire	6-10
	Remote Access Using the OC-3/OC-12 Data Communications Channel (DCC)	6-12
■	User Panel	6-13
■	Filter Panel	6-17
■	Office Alarms Interface	6-19
■	Parallel Telemetry Interface	6-20
■	User-Settable Miscellaneous Discrete Interface	6-20
■	Serial Telemetry Interface	6-22

Contents

■ Message-Based Operations System Interface	6-23
■ Orderwire Interface	6-30

7	Circuit Pack Descriptions	7-1
■	Overview	7-1
■	General	7-1
■	Circuit Pack and Software Compatibility	7-4
■	Control	7-6
	SYSCTL (LAA23, LAA23B, and LAA23C) Circuit Pack Description	7-6
	SYSMEM (LAA25) Circuit Pack Description	7-11
	LNCTL (LAA28) Circuit Pack Description	7-17
	OHCTL (TERM) (LAA21 and LAA22) Circuit Pack Description	7-21
	TOHCTL (LAA26) Circuit Pack Description	7-26
	OW (LAA14) Circuit Pack Description	7-32
■	Transmission	7-35
	DS3 (LAA2/LAA2B) Circuit Pack Description	7-35
	STS1E (LAA4) Circuit Pack Description	7-44
	OC3 (LAA10/LAA10B) Circuit Pack Description	7-52
	IS3 (LAA5) Circuit Pack Description	7-62
	OC-12 (T939A) Circuit Pack Description	7-71
	LSSW (LAA12B) Circuit Pack Description	7-80
	OC48 RCVR A/D (839B2, 839B4B, 839B5, and 839E5) Circuit Pack Description	7-86
	OC48 TRMTR A/D (739B_, 739C_, 739E_, 739G_, 739H_, 739J_, 739P5, 739R_, 739S_) Circuit Pack Description	7-97
	OC48 REGENR (39B1, 39B2, 39C1, 39C2, 39J2, 39R2, and 39S2) Circuit Pack Description	7-108
■	Synchronization	7-116
	TG3 (DS1) (LAA18) Circuit Pack Description	7-116

Contents

8	Administration and Provisioning	8-1
■	Overview	8-1
■	Administration	8-1
	Memory Administration	8-1
	Version Recognition	8-4
	Security	8-4
	Software Upgrades	8-9
	Cross-Connections	8-11
	Access Identifiers	8-93
■	Provisioning	8-98
	Provisionable Parameters and Original Values	8-98
	Local Provisioning	8-109
	Pre-provisioning Circuit Packs	8-143
	Provisioning on Circuit Pack Replacement	8-143
	Circuit Pack Slot and Low-Speed Port Provisioning States	8-144

9	Maintenance Description	9-1
■	Overview	9-1
■	Remote Maintenance Philosophy	9-1
■	Maintenance Signals	9-2
■	Fault Detection, Isolation, and Reporting	9-8
■	Protection Switching	9-31
	High-Speed Protection	9-31
	Electrical Low Speed Protection	9-43
	Optical Low Speed Protection — (OC-3)	9-47
	Optical Low Speed Protection — (OC-12)	9-52
	Dual Ring Interworking Protection	9-57
	Synchronization Protection	9-65
■	Loopbacks	9-72
	Low Speed (DS3, EC-1, OC-3, and OC-12)	9-72

Contents

High-Speed (OC-48)	9-73
■ Tests	9-74
Auto Turnup	9-74
Operations Interface Tests	9-78
■ Circuit Provisioning Audits	9-79
■ Performance Monitoring	9-81
SONET Performance Parameters	9-81
OC-48 Optical Parameters	9-83
OC-3/OC-12 Optical Parameters	9-84
STS-48/STS-12/STS-3 Section Parameters	9-85
OC-48/OC-12/OC-3 Line Parameters	9-86
STS-1 Path Parameters	9-87
EC-1 Section/Line Parameters	9-88
DS3 Line/Path	9-89
Performance-Monitoring Data Storage and Reports	9-90
Performance Monitoring During Failed Conditions	9-90
Performance-Monitoring Parameter Thresholds	9-91
Threshold Crossing Alert Transmission to an OS	9-91
■ Reports	9-92
Active Alarms and Status	9-92
Provisioning Reports	9-93
Performance Monitoring	9-94
State	9-94
Version/Equipment List	9-95
Synchronization Report	9-95

10	Technical Specifications	10-1
■	Overview	10-1
■	Optical Line Interface	10-1
■	Optical Connector Interfaces	10-1
■	Transmission Medium	10-1
■	Lightguide Jumpers	10-2

Contents

■ Optical Source	10-2
■ Optical Detector	10-2
■ Optical Safety (CDRH Classification)	10-2
■ Operating Wavelength	10-3
■ Optical Dispersion	10-4
■ Optical Return Loss	10-4
■ Outside Plant Loss	10-5
■ Optical Line Loss Budgets	10-5
FT-2000 OC-48 Lightwave System	10-5
FT-2000 OC-48 Lightwave System and FT-Lightwave Booster Amplifier	10-11
FT-2000 OC-48 Lightwave System and Wavelength Division Multiplexing Systems	10-12
FT-2000 OC-48 Lightwave System with Wavelength Division Multiplexing and FT-LBA Systems	10-13
■ Transmission Distances	10-14
FT-2000 OC-48 Lightwave System without Optical Amplifiers	10-14
FT-2000 OC-48 Lightwave System and the FT-Lightwave Booster Amplifier	10-15
FT-2000 OC-48 Lightwave System with Wavelength Division Multiplexing and FT-LBA Systems	10-16
■ Capacity	10-16
■ Transmission Characteristics	10-17
■ DS3 Access	10-17
■ EC-1 Access	10-18
■ OC-3 Access	10-19
Optical Line Interface	10-19
Optical Connector Interfaces	10-19
Transmission Medium	10-19
Lightguide Jumpers	10-19
Optical Source	10-19
Optical Detector	10-20
Optical Safety (BRH Classification)	10-20
Operating Wavelength	10-20
Spectral Width	10-20
Optical Dispersion	10-20

Contents

OC-3 Line Loss Budgets	10-20
■ IS-3 Access	10-22
Optical Line Interface	10-22
Optical Connector Interfaces	10-22
Transmission Medium	10-22
Lightguide Jumpers	10-22
Optical Source	10-22
Optical Detector	10-22
Optical Safety (BRH Classification)	10-23
Operating Wavelength	10-23
Spectral Width	10-23
Optical Dispersion	10-23
IS-3 Line Loss Budgets	10-24
■ OC-12 Access	10-25
Optical Line Interface	10-25
Optical Connector Interfaces	10-25
Transmission Medium	10-25
Lightguide Jumpers	10-25
Optical Source	10-25
Optical Detector	10-25
Optical Safety (BRH Classification)	10-26
Operating Wavelength	10-26
Spectral Width	10-26
Optical Dispersion	10-26
OC-12 Line Loss Budgets	10-27
■ OC-3 to IS-3 Interface Interconnection	10-28
■ Transmission Delay	10-28
■ Protection Switching (OC-3, OC-12, OC-48 Lines)	10-29
■ Electrical Line Buildout Specifications	10-30
■ Cable Access	10-31
■ Power Specifications	10-32
■ Bay/Shelf Dimensions	10-33
■ Circuit Pack Dimensions	10-35
■ Floor Loading Specifications	10-38

Contents

■ Environmental Specifications	10-38
■ Craft Interface Terminal	10-39
Personal Computer Requirements	10-39
ASCII-Based Terminal Requirements	10-41
■ Modem Requirements	10-41
■ Operations Interfaces	10-42
■ TL1/X.25 Interface	10-43
General	10-43
TL1 Autonomous Messaging in Release 4.1 and Earlier	10-43
Provisioning TL1 Autonomous Messages for Release 5 and Later Releases	10-44
DTE Configuration	10-44
■ Circuit Pack FIT Rates	10-50

11	Craft Interface Terminal Usage	11-1
■	Overview	11-1
■	CPro-2000	11-1
■	FT-2000 CIT-PC Software Introduction	11-3
■	Inputs	11-3
■	Outputs	11-38
■	Examples of Reports [Generated by Various Inputs (Commands)]	11-39
	CIT Tutorial Using CIT-PC Software	11-152

GL	Glossary	GL-1

Figures

1 System Introduction

1-1	FT-2000 OC-48 Add/Drop-Rings Terminal for Two-Fiber Rings	1-3

2 Applications

2-1	Two-Fiber Bidirectional Ring Application	2-3
2-2	Folded Ring Application	2-6
2-3	Folded Rings Evolving to Full Ring Networks	2-7
2-4	End-to-End Interface Mixing Application	2-9
2-5	OC-48 Hubbing Application	2-10
2-6	Dual Ring Interworking Application	2-13
2-7	Dual Ring Interworking Application with the DDM-2000 OC-3 Multiplexer	2-15
2-8	DDM-2000 OC-3 Ring Transport Application	2-17
2-9	Dual-Wire Center Architecture Application	2-19
2-10	Broadband Service Transport Application	2-22
2-11	One-Way Video Distribution Application	2-26
2-12	Path-In-Line Architecture	2-28
2-13	Loop Feeder Network Application	2-30
2-14	Extra Traffic (Protection Line Access) for Restoration and Revenue Generation Application	2-32
2-15	0% Add/Drop Application	2-34
2-16	Operation with FT-Lightwave Booster Amplifier	2-35
2-17	Wavelength Division Multiplexing Application (1.3 mm/1.5 mm)	2-37
2-18	Section by Section Wavelength Division Multiplexing (1.3 mm/1.5 mm)	2-38
2-19	Wavelength Division Multiplexing Application (1.5 mm/1.5 mm)	2-39
2-20	Section by Section WDM (1.5 mm/1.5 mm)	2-40
2-21	Operations Interworking Application	2-42

Figures

2-22	Gateway Network Element Application (Releases 4.1 and 5)	2-46
2-23	Gateway Network Element Application (Release 6 and Later)	2-47

3

Platform Descriptions

3-1	FT-2000 OC-48 Add/Drop-Rings Terminal for Two-Fiber Rings (E-Bay)	3-3
3-2	Power Distribution and Fuse Panel	3-4
3-3	Low-Speed Shelf — System Controller (Without Circuit Pack Cover)— ED6G999-31, G5	3-5
3-4	Low-Speed Shelf — System Controller (Without Circuit Pack Cover) — ED6G999-32, G3	3-6
3-5	Interconnection Panel (Low-Speed Shelf — System Controller)	3-12
3-6	Interconnection Panel (Low-Speed Shelf — System Controller) —ED6G999-32, G3	3-13
3-7	User Panel (Enhanced) 6G999-31, G5	3-14
3-8	User Panel 6G999-32, G3	3-15
3-9	Low-Speed Shelf — System Controller (with Covers)	3-16
3-10	High-Speed Shelf (Without Circuit Pack Cover)	3-18
3-11	Interconnection Panel (High-Speed Shelf)	3-20
3-12	Filter Panel (High-Speed Shelf and Enhanced High-Speed Shelf)	3-22
3-13	High-Speed Shelf (with Covers)	3-23
3-14	Enhanced High-Speed Shelf (Without Circuit Pack Cover)	3-26
3-15	Interconnection Panel (Enhanced High-Speed Shelf)	3-28
3-16	Enhanced High-Speed Shelf (with Covers)	3-30
3-17	FT-2000 OC-48 Repeater Bay	3-33
3-18	FT-2000 OC-48 Repeater Shelf (Miscellaneous Mounted)	3-34

Figures

3-19	Power Distribution and Fuse Panel	3-35
3-20	Repeater Shelf — System Controller (Without Circuit Pack Cover)	3-36
3-21	Interconnection Panel (Repeater Shelf — System Controller)	3-39
3-22	User Panel (FT-2000 OC-48 Repeater Shelf) for Miscellaneous Mounting Only	3-41
3-23	Repeater Shelf — System Controller with Covers (FT-2000 OC-48 Repeater Bay)	3-42
3-24	Repeater Shelf — System Controller with Covers (FT-2000 OC-48 Repeater Shelf)	3-43
3-25	FT-2000 OC-48 Dual ADR Terminal for Two-Fiber Rings (D-Bay)	3-46
3-26	FT-2000 OC-48 D-Bay Power Distribution and Fuse Panel	3-47
3-27	Low-Speed Shelf — System Controller (Without Circuit Pack Cover) — ED6G999-31, G5	3-48
3-28	Low-Speed Shelf — System Controller (Without Circuit Pack Cover) — ED6G999-32, G3	3-49
3-29	Interconnection Panel (Low-Speed Shelf - System Controller)—ED6G999-31,G5	3-54
3-30	User Panel (Enhanced)	3-55
3-31	Low-Speed Shelf - System Controller (with Covers)	3-56
3-32	Condensed High-Speed Shelf (Without Circuit Pack Cover)	3-59
3-33	Interconnection and Filter Panel (Condensed High-Speed Shelf)	3-62

4 Power

4-1	Bay Power Distribution	4-2
4-2	Miscellaneous-Mounted FT-2000 OC-48 Repeater Shelf Power Distribution	4-3
4-3	Shelf Power Distribution	4-4
4-4	Circuit Pack Power Distribution	4-5

Figures

5	Control, Transmission, and Synchronization Interfaces	
5-1	System Control Architecture for the FT-2000 OC-48 Add/Drop-Rings Terminal	5-2
5-2	System Control Architecture for the FT-2000 OC-48 Repeater Bay	5-3
5-3	FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings Transmission Block Diagram	5-5
5-4	FT-2000 OC-48 Repeater Bay Transmission Block Diagram	5-9
5-5	Free Running Synchronization	5-11
5-6	Through Timed Synchronization (FT-2000 OC-48 Add/Drop-Rings Terminal)	5-13
5-7	Through Timed Synchronization (FT-2000 OC-48 Repeater Bay)	5-14
5-8	Loop Timed Synchronization	5-15
5-9	Externally Timed Synchronization	5-17
5-10	Free Running/Free Running Configuration	5-21
5-11	Free Running/Through Timed Configuration	5-22
5-12	Externally Timed/Through Timed Configuration	5-23
5-13	Phase Locked/Phase Locked Configuration	5-24
5-14	Phase Locked/Phase Locked Configuration [LAA18 TG3 (DS1) Circuit Pack]	5-25
5-15	Externally Timed/Externally Timed Configuration	5-26
5-16	Example of a Ring Application with One Node Provisioned Free Running and All Other Nodes Provisioned Through Timed (Without Timing Drops)	5-28
5-17	Example of a Ring Application with One Node Provisioned Free Running and All Other Nodes Provisioned Line Timed (Without Timing Drops)	5-29
5-18	Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Through Timed (Without Timing Drops)	5-31

Figures

5-19	Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Line Timed (Without Timing Drops)	5-32
5-20	Example of a Ring Application with Two Nodes Provisioned Externally Timed and All Other Nodes Provisioned Through Timed or Externally Timed (Without Timing Drops)	5-33
5-21	Example of a Ring Application with Three Nodes Provisioned Externally Timed and One Node Provisioned Line Timed (Without Timing Drops)	5-34
5-22	Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Line Timed (With Two Timing Drops)	5-35
5-23	Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Through Timed (with Timing Drops)	5-36
5-24	Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Through Timed or Externally Timed (with Timing Drops)	5-37
5-25	Example of a Ring Application with All Nodes Provisioned Externally Timed (with Timing Drops)	5-39
5-26	Example of a Timing Loop Resulting from Incorrect Provisioning	5-40
5-27	Example of a Timing Loop Resulting from Inconsistent DS1 Timing Interfaces	5-41
5-28	Example of a Ring Application with All Nodes Provisioned Externally Timed (with External Timing Emulation Cable)	5-42

6 Operations Interfaces

6-1	Remote Access Using the OC-48 Section Orderwire	6-11
6-2	User Panel (Low Speed Shelf - System Controller and Repeater Shelf - System Controller) — Cabled Version (Before 06/97)	6-13

Figures

6-3	User Panel (Low Speed Shelf - System Controller and Repeater Shelf - System Controller) — Plug-In-Type (After 06/97)	6-14
6-4	User Panel (FT-2000 OC-48 Repeater Shelf)	6-16
6-5	Filter Panel for the Enhanced High Speed Shelf	6-17
6-6	Power Indicating Panel for the Condensed High Speed Shelf	6-18

7 Circuit Pack Descriptions

7-1	SYSCTL (LAA23C) Circuit Pack	7-6
7-2	SYSCTL Circuit Pack Block Diagram	7-8
7-3	SYSMEM (LAA25) Circuit Pack	7-11
7-4	SYSMEM Circuit Pack Block Diagram	7-13
7-5	LNCTL (LAA28) Circuit Pack	7-17
7-6	LNCTL Circuit Pack Block Diagram	7-18
7-7	OHCTL (TERM) (LAA21 and LAA22) Circuit Pack	7-21
7-8	OHCTL (TERM) Circuit Pack Block Diagram	7-22
7-9	TOHCTL (LAA26) Circuit Pack	7-27
7-10	TOHCTL Circuit Pack Block Diagram	7-28
7-11	OW (LAA14) Circuit Pack	7-32
7-12	OW Circuit Pack Block Diagram	7-33
7-13	DS3 (LAA2/LAA2B) Circuit Pack	7-35
7-14	DS3 Circuit Pack Block Diagram	7-36
7-15	STS1E (LAA4) Circuit Pack	7-44
7-16	STS1E Circuit Pack Block Diagram	7-45
7-17	OC3 (1.3 STD) (LAA10/LAA10B) Circuit Pack	7-53
7-18	OC3 (1.3 STD) LAA10 Circuit Pack Block Diagram	7-54
7-19	OC3 (1.3 STD) LAA10B Circuit Pack Block Diagram	7-54
7-20	IS3 (1.3 STD) (LAA5) Circuit Pack	7-62
7-21	IS3 (1.3 STD) Circuit Pack Block Diagram	7-63
7-22	OC12 (1.3 STD) (T939A) Circuit Pack	7-71

Figures

7-23	OC12 (1.3 STD) Circuit Pack Block Diagram	7-73
7-24	LSSW (LAA12B) Circuit Pack	7-80
7-25	LSSW Circuit Pack Block Diagram	7-82
7-26	OC48 RCVR A/D (839B2, 839B4B, 839B5, and 839E5) Circuit Pack	7-87
7-27	OC48 RCVR A/D (839B2 and 839B4B) Circuit Pack Block Diagram	7-89
7-28	OC48 RCVR A/D (839B5 and 839E5) Circuit Pack Block Diagram	7-90
7-29	OC48 TRMTR A/D (739B2, 739B4, 739B5, 739C2, 739C5, 739E[1-16], 739G[1-8], 739H[1-16], 739J2, 739J3, 739J4, 739J5, 739P5, 739R2, 739R5, 739S2, and 739S5) Circuit Pack	7-98
7-30	OC48 TRMTR A/D (739B2, 739B4, 739C2, 739J2, 739J3, 739J4, 739R2, and 739S2) Circuit Pack Block Diagram	7-100
7-31	OC48 TRMTR A/D Circuit Pack (739B5, 739C5, 739E[1-16], 739G[1-8], 739H[1-16], 739J5, 739P5, 739R5, and 739S5) Block Diagram	7-101
7-32	OC48 REGENR (39B1, 39B2, 39C1, 39C2, 39J2, 39R2, and 39S2) Circuit Pack	7-108
7-33	OC48 REGENR Circuit Pack Block Diagram	7-110
7-34	TG3 (DS1) (LAA18) Circuit Pack	7-116
7-35	TG3 (DS1) (LAA18) Circuit Pack Block Diagram	7-119

8

Administration and Provisioning

8-1	STS-3 1-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-14
8-2	STS-3 1-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal (1+1 Protected Slots)	8-15
8-3	STS-3 Bridge Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-17
8-4	Multiple STS-3 Bridge Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-19

Figures

8-5	STS-3 1-Way-DRI Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-20
8-6	STS-3 2-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-22
8-7	Limited STS-1 Cross-Connections Across and STS-3 Tributary	8-25
8-8	STS-1 1-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-29
8-9	1-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal (1+1 Protected Slots)	8-30
8-10	STS-1 Bridge Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-32
8-11	Multiple STS-1 Bridge Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-33
8-12	STS-1 1-Way-DRI Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-34
8-13	STS-1 2-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal	8-36
8-14	Extra Traffic Capability	8-40
8-15	Example of 1-Way and 2-Way STS-1 Cross-Connections in a 2-Fiber Bidirectional Ring	8-51
8-16	STS-1 Cross-Connection Worksheet	8-55
8-17	Example of 1-Way and 2-Way Cross-Connections in a 2-Fiber Bidirectional Ring	8-56
8-18	STS-3 Cross-Connection Worksheet	8-60
8-19	Example of STS-1 1-Way-DRI Cross-Connections in a 2-Fiber Bidirectional Ring	8-61
8-20	Example of STS-3 1-Way-DRI Cross-Connections in a 2-Fiber Bidirectional Ring	8-64
8-21	Example of 1-Way and 1-Way-DRI Cross-Connections in Video Applications	8-67
8-22	Example of Rolling STS-1 Cross-Connections in a 2-Fiber Bidirectional Ring	8-72
8-23	Example of Rolling STS-3 Cross-Connections in a 2-Fiber Bidirectional Ring	8-74
8-24	Example of STS-1 Cross-Connections in a 2-Fiber Bidirectional Ring	8-75

Figures

8-25	Example of STS-3 Cross-Connections in a 2-Fiber Bidirectional Ring	8-78
8-26	Example of Limited STS-1 Cross-Connections in a 2-Fiber Bidirectional Ring	8-81
8-27	Example of OC-3 DCC Connectivity	8-86
8-28	Example of OC-12 DCC Connectivity	8-91

9 Maintenance Description

9-1	FT-2000 OC-48 Add/Drop-Rings Terminal Maintenance Signals (When Interfacing with DS3 Signals)	9-4
9-2	FT-2000 OC-48 Add/Drop-Rings Terminal Maintenance Signals (When Interfacing With EC-1, OC-3, IS-3 and OC-12 Signals)	9-5
9-3	FT-2000 OC-48 Add/Drop-Rings Terminal Maintenance Signals for Through Tributaries	9-6
9-4	FT-2000 OC-48 Repeater Bay Maintenance Signals	9-7
9-5	2-Fiber Ring Protection Switching Architecture (FT-2000 OC-48 Add/Drop-Rings Terminal)	9-33
9-6	Bidirectional Line-Switched 2-Fiber Ring (Normal Transmission)	9-35
9-7	Bidirectional Line-Switched 2-Fiber Ring (Single Failure)	9-36
9-8	Bidirectional Line-Switched 2-Fiber Ring (Multiple Failures)	9-39
9-9	Single and Double Failure States	9-41
9-10	Single and Double Failures	9-43
9-11	Electrical Low Speed Protection Switching	9-45
9-12	Optical Low Speed Protection Switching	9-49
9-13	1+1 OC-3 Line (Normal Transmission)	9-50
9-14	1+1 OC-3 Line (Fiber Cut)	9-51
9-15	Optical Low Speed Protection Switching	9-54
9-16	1+1 OC-12 Line (Normal Transmission)	9-55
9-17	1+1 OC-12 Line (Fiber Cut)	9-56
9-18	Dual Ring Interworking Circuit (Normal Transmission)	9-59
9-19	Dual Ring Interworking Circuit (Node Failure in Other Ring)	9-61

Figures

9-20	Dual Ring Interworking Circuit (Primary Node Failure)	9-63
9-21	Dual Ring Interworking Circuit (Secondary Node Failure)	9-64
9-22	TG3 (DS1) Circuit Pack Protection (FT-2000 OC-48 Add/Drop-Rings Terminal)	9-66
9-23	DS1 Reference Protection	9-69
9-24	OC-48 Line Reference Protection (FT-2000 OC-48 Add/Drop-Rings Terminal)	9-70
9-25	Local (Terminal Self-Test)	9-74
9-26	DSX (Low Speed Cabling Self-Test)	9-76
9-27	Regenerator Self-Test	9-77
9-28	SONET Section, Line, and Path	9-81

10 Technical Specifications

10-1	Optical System Interfaces	10-10
10-2	Common Coaxial Cable Interoffice Cross-Connect Options for DS-3 and STS-1E	10-31

11 Craft Interface Terminal Usage

Tables

1-1	Major Features of Product Releases 1 Through 7.2	1-6
2-2	Synchronization Status Messages	2-48
2-3	FT-2000 Ring Environments	2-49
3-1	Permitted Slot Locations for the Quad Circuit Pack	3-8
3-2	Low-Speed Shelf — System Controller Circuit Pack Slot/Circuit Pack Code/Software Compatibility	3-10
3-3	High-Speed Shelf Circuit Pack Slot/Circuit Pack Code/Software Compatibility	3-19
3-4	Enhanced High-Speed Shelf Circuit Pack Slot/Circuit Pack Code/Software Compatibility	3-27
3-5	Repeater Shelf — System Controller Circuit Pack Slot/Circuit Pack Code/Software Compatibility	3-38
3-6	Permitted Slot Locations for the Quad Circuit Pack	3-50
3-7	Low-Speed Shelf — System Controller Circuit Pack Slot/Circuit Pack Code/Software Compatibility	3-52
3-8	Enhanced High-Speed Shelf Circuit Pack Slot/Circuit Pack Code/Software Compatibility	3-60
4-1	Power Dissipation and Current Drains	4-6
5-1	Sync-Messages—for Line Timed Mode	5-12
6-1	FT-LBA Miscellaneous Discrete Mapping	6-21
7-1	Circuit Packs and Compatible Software	7-4
7-2	Overhead Bytes	7-24
7-3	Overhead Bytes	7-30
7-4	Section Overhead Bytes	7-34
8-3	FT-2000 OC-48 Repeater Bay Input Values	8-97
8-4	FT-2000 OC-48 Repeater Bay Output Values	8-97
8-6	Provisionable Threshold Crossing Alert (TCA) Parameters	8-106
8-7	Provisionable Parameters (FT-2000 OC-48 Repeater Bay)	8-107
9-1	Incoming Signals and Monitored Conditions	9-9
9-2	Condition Descriptions	9-14
9-3	2-Fiber Ring Protection Switching Priorities	9-31
9-4	2-Fiber Ring Tributary Mapping	9-32
9-5	Electrical low Speed Protection Switching Priorities	9-44
9-6	Optical Low Speed Protection Switching Priorities	9-47
9-7	Optical Low Speed Protection Switching Priorities	9-52
9-8	Dual Ring Interworking Protection Switching Priorities	9-57
9-9	Summary of DRI Feature Capabilities	9-58

Tables

9-10	Synchronization Hardware Protection Switching Priorities	9-65
9-11	Synchronization Reference Protection Switching Priorities	9-68
9-12	Synchronization Mode Protection Switching Priorities	9-71
9-13	Performance-Monitoring Parameters	9-82
10-1	OC-48 Loss Budget Specifications	10-5
10-2	OC-48 Loss Budget Specifications for 739E/G[1-8] OC48 TRMTR Circuit Packs	10-6
10-3	OC-48 Loss Budget Specifications for 739E/G[9-16] OC48 TRMTR Circuit Packs	10-7
10-4	OC-48 Loss Budget Specifications for 739H[1-8] OC48 TRMTR Circuit Packs	10-8
10-5	OC-48 Loss Budget Specifications for 739H[9-16] OC48 TRMTR Circuit Packs	10-9
10-6	OC-48 Loss Budget Specifications for 839B5 and 839E5 OC48 RCVR Circuit Packs	10-9
10-7	FT-2000 OC-48 Lightwave System and FT-Lightwave Booster Amplifier Loss Budgets	10-11
10-8	FT-2000 OC-48 Lightwave System and Wavelength Division Multiplexing Systems Loss Budgets	10-12
10-9	FT-2000 OC-48 Lightwave System with Wavelength Division Multiplexing Systems and FT-LBA Loss Budgets	10-13
10-10	Transmission Distances without Optical Amplifiers	10-14
10-11	Transmission Distances with the FT-Lightwave Booster Amplifier	10-15
10-12	Transmission Distances with Wavelength Division Multiplexing and the FT-LBA	10-16
10-13	OC-3 Line Loss Budget Specifications (Note)	10-21
10-14	IS-3 Line Loss Budget Specifications	10-24
10-15	OC-12 Line Loss Budget Specifications	10-27
10-16	Maximum Distances	10-28
10-17	Electrical Line Buildout Specifications	10-30
10-20	TL1/X.25 Interface — X.25 Packet Layer Parameters	10-45
10-21	TL1/X.25 Interface — LAPB Link Layer Parameters	10-45
10-22	TL1/X.25 Interface — EIA-232D Pin Connections	10-46
11-1	CPro-2000 Cross-Reference Procedures	11-2
11-2	Release 7 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal	11-8
11-3	Release 6 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal	11-12

Tables

11-4	Release 5 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal	11-16
11-5	Release 4 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal	11-20
11-6	Release 3 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal	11-23
11-7	Releases 2, 4, and 6 Inputs (Commands) Menu for FT-2000 Repeater Bay/Shelf	11-25
11-8	Input (Command) Definitions	11-26
11-9	List of Inputs (Commands) That Generate Reports	11-36



Tables

About This Document

Purpose

This user/service manual provides the following information about the FT-2000 OC-48 Lightwave System through Release 7.2:

- Detailed descriptive information to circuit pack level
- Craft interface terminal usage
- Operation and maintenance (O&M) task oriented practices (TOPs) supporting acceptance, circuit order, operation, and trouble clearing tasks.

The FT-2000 OC-48 Lightwave System has a phased product release plan. Document 365-575-102, Issue 8.0, dated July 2002, is the FT-2000 User/Service Manual for Releases 1 through 7.2.

Intended Audiences

This user/service manual is primarily for end users responsible for operating and maintaining the FT-2000 OC-48 Lightwave System. It may be used by anyone desiring specific operation and maintenance information.

Reason for Reissue

This document, Issue 8.0, replaces the *FT-2000 OC-48 Lightwave System, Releases 1 through 7.2.8, User/Service Manual, Addendum Issue 7.2C*.



NOTE:

This document contains change bars to identify where there has been new information added and where obsolete information has been deleted.

Record of Changes

Besides correcting inconsistencies and/or errors that occurred during the processing of previous issues, significant changes in this issue are:

- New procedure:
 - DLP-551 (“Power Down/Power Up an Optical Network Element”)
- Updated procedures:
 - DLP-550 (provides more details and illustrations)
 - TAP-113
- New circuit packs
 - LAA2B
 - LAA10B
 - LAA12B
 - LAA18
 - LAA23C
 - 39B2
 - 39C2
- DA'd circuit packs
 - LAA12
 - LAA17
 - 39B1
 - 39C1
- Added loss budget information to Chapter 10 with regard to the following transmitters and receivers:
 - 739E (9-16)
 - 739H (1-16)
 - 839B5
 - 839E5

How to Use This Document

The sections in this document are marked with white tabs and provide the following information:

- "About This Document" describes the purpose, intended audience, and organization of this document. This section also presents safety information, references other related documentation, and provides product support information. It also explains how to order and make comments or recommendations for changes to this document.
- Volume I, Section 1, "System Introduction," introduces the members of the Lucent Technologies 2000 Product Family and briefly describes the FT-2000 OC-48 Lightwave System.
- Volume I, Section 2, "Applications," describes how the FT-2000 OC-48 Lightwave System meets point-to-point, hubbing, bidirectional line-switched rings, dual ring interworking, dual-wire center architecture, broadband service transport, FT-Lightwave Booster Amplifier (FT-LBA) for extended distances, loop feeder network applications, path-in-line architecture, wavelength division multiplexing, operations interworking, and gateway network element operation.
- Volume I, Section 3, "Platform Descriptions," introduces the FT-2000 OC-48 Lightwave System platforms and provides a more detailed view of the FT-2000 OC-48 Lightwave System physical design.
- Volume I, Section 4, "Power," describes the power distribution and dissipation of the FT-2000 OC-48 Lightwave System.
- Volume I, Section 5, "Control, Transmission, and Synchronization Interfaces," describes the FT-2000 OC-48 Lightwave System interfaces and architecture. The system control, transmission, and synchronization interfaces and architecture are described down to the circuit pack level.
- Volume I, Section 6, "Operations Interfaces," describes the interfaces that allow access to the FT-2000 OC-48 Lightwave System and provides alarm and status information.
- Volume I, Section 7, "Circuit Pack Descriptions," provides a detailed circuit description of each FT-2000 OC-48 Lightwave System circuit pack.
- Volume I, Section 8, "Administration and Provisioning," describes administration and provisioning of the FT-2000 OC-48 Lightwave System.
- Volume I, Section 9, "Maintenance Description," defines the "maintenance philosophy" outlining the various features available to monitor and maintain the FT-2000 OC-48 Lightwave System.
- Volume I, Section 10, "Technical Specifications," lists the technical specifications for the FT-2000 OC-48 Lightwave System.

- Volume I, Section 11, "Craft Interface Terminal Usage," describes how the craft interface terminal is used and shows the FT-2000 OC-48 Lightwave System inputs (commands), output messages, and reports. It also includes a tutorial on the use of the craft interface terminal.
- The "Operation and Maintenance (TOP)" contains specific task-oriented procedures for the acceptance, circuit order, operation, and trouble clearing of the FT-2000 OC-48 Lightwave System.

Conventions Used

The **BOLD** font is used throughout this manual for emphasis. The descriptive sections in this manual use a **Courier Bold** font to identify menu input selections to the system. UPPERCASE letters identify letter designations on panels, shelves, and circuit packs.

The procedures in the "Operation and Maintenance (TOP)" section (Volume II) use a MONOSPACE font to identify text on a screen or a response displayed from the system. A **BOLD** font identifies letter designations on panels, shelves, and circuit packs.

Safety Instructions

Safety Labels

This manual may contain safety labels in the form of **DANGERS**, **WARNINGS**, and **CAUTIONS**. These safety labels have the following definitions:

- **DANGER** shows the presence of a hazard that *will* cause death or severe personal injury if the hazard is not avoided.
- **WARNING** shows the presence of a hazard that *can* cause death or severe personal injury if the hazard is not avoided.
- **CAUTION** shows the presence of a hazard that *will* or can cause minor personal injury or property damage if the hazard is not avoided. Caution is also used for property-damage-only accidents. This includes equipment damage, loss of software, or service interruption.

These admonishments are noted by the alert symbol  .

Lightwave Safety Guidelines

General Laser Information

The FT-2000 OC-48 Lightwave System and associated optical test sets use semiconductor laser transmitters that emit light at wavelengths between approximately 800 nanometers (nm) and 1600 nm. The emitted light is above the red end of the visible spectrum, which is normally not visible to the human eye. Although radiant energy at near-infrared wavelengths is officially designated invisible, some people can see the shorter wavelength energy even at power levels several orders of magnitude below any that have been shown to cause injury to the eye.

Conventional lasers can produce an intense beam of monochromatic light. Monochromatic light is a single wavelength output of pure color that may be visible or invisible to the eye. A conventional laser produces a small-size beam of light, and because the beam size is small the power density (also called irradiance) is very high. Consequently, lasers and laser products are subject to federal and applicable state regulations as well as international standards for their safe operation.

A conventional laser beam expands very little over distance or is said to be very well collimated. Thus, conventional laser irradiance remains relatively constant over distance. However, lasers used in lightwave systems have a large beam divergence, typically 10 to 20 degrees. Here, irradiance obeys the inverse square law (doubling the distance reduces the irradiance by a factor of 4) and rapidly decreases over distance.

Lasers and Eye Damage

Light energy emitted by laser and high-radiance light-emitting diodes (LEDs) in the 400- to 1400-nm range may cause eye damage if absorbed by the retina. When a beam of light enters the eye, the eye magnifies and focuses the energy, magnifying the irradiance. The irradiance of the energy that reaches the retina is approximately 10^5 or 100,000 times that at the cornea, and if sufficiently intense, may cause a retinal burn.

The damage mechanism at the wavelengths used in telecommunications is thermal in origin, for example, damage caused by heating. Therefore, a specific amount of energy is required for a definite time to heat an area of retinal tissue. Damage is not instantaneous. It occurs only when one looks at the light sufficiently long enough that the product of the retinal irradiance and the viewing time exceeds the damage threshold. Light energies above 1400 nm would cause surface and skin burns and do not affect the retina.

Classification of Lasers

Manufacturers of lasers and laser products in the United States are regulated by the Food and Drug Administration's Center for Devices and Radiological Health (FDA/CDRH) under 21 CFR 1040. These regulations require manufacturers to certify each laser or laser product as belonging to one of the major Classes I, II, IIIa, IIIb, or IV. Lasers are classified according to the accessible emission limits and their potential for causing injury. Lightwave systems are generally classified as Class I, because, under normal operating conditions, all energized laser transmitting circuit packs are terminated on optical fibers which enclose the laser energy with the fiber sheath forming a protective housing. Also, covers are in place over the circuit pack shelves.

Lightwave Safety Precautions

Under normal operating conditions, the FT-2000 OC-48 Lightwave System is totally enclosed and presents no risk of eye injury. It is a Class I system under the FDA/CDRH scheme.

The lightguide cables that interconnect various components of a lightwave system can disconnect or break and may expose people to lightwave emission. Also, certain measures and maintenance procedures may expose the technician to emission from the semiconductor laser during installation and servicing. Unlike more familiar laser devices, such as solid-state and gas lasers, the emission pattern of a semiconductor laser results in a highly divergent beam. In a divergent beam, the irradiance (power density) decreases rapidly with distance. The greater the distance, the less energy will enter the eye and the less potential risk for eye injury.

Inadvertently viewing an unterminated fiber or damaged fiber with the unaided eye at distances greater than 5 to 6 inches normally will not cause eye injury provided the power in the fiber is less than a few milliwatts at the shorter wavelengths and higher at the longer wavelengths. However, damage may occur if an optical instrument such as a microscope, magnifying glass, or eye loupe is used to stare at the energized fiber end.



WARNING:

Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous laser radiation exposure.

Safety Precautions for Enclosed Systems

Under normal operating conditions the FT-2000 OC-48 Lightwave System is completely enclosed; nonetheless, the following precautions should be observed:

- Because of the potential for eye damage, technicians should neither disconnect any lightwave cable nor splice and stare into the optical connectors terminating the cables.
- Under no circumstance should lightwave/lightguide operations be performed by a technician before satisfactorily completing an approved training course.
- Since viewing lightwave emission directly with an optical instrument, such as an eye loupe, greatly increases the risk of eye damage, an appropriate label must appear in plain view on the front of the main frame or lightguide termination/interconnection equipment. The label shall read as follows:

NOTICE: UNTERMINATED OPTICAL CONNECTORS MAY EMIT LASER RADIATION. AVOID DIRECT EXPOSURE TO THE BEAM. DO NOT VIEW THIS BEAM WITH OPTICAL INSTRUMENTS.

A warning label (Figure 1) is provided on the filter panel of the Enhanced High-Speed Shelf and on the right front of the High Speed Shelf and Repeater Shelf–System Controller. The word DANGER is in central office white lettering on a red background. The text of the warning label is in black lettering on a white background.

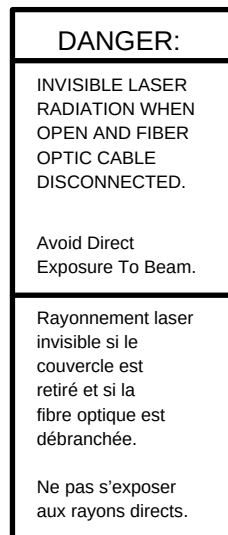


Figure 1. Warning Label

In addition, a compliance label stating that the system has been certified along with the manufacturer's name and place of manufacture is attached to each equipment bay. Figure 2 shows an example of a compliance label. The compliance label is located on the rear of the equipment bays (at eye level) and miscellaneous-mounted shelves.

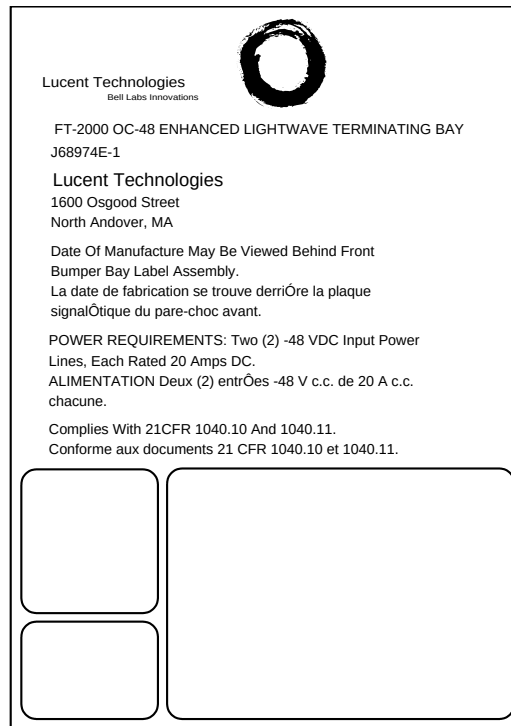


Figure 2. Compliance Label

Safety Precautions for Unenclosed Systems

During service, maintenance, or restoration, the FT-2000 OC-48 Lightwave System is considered unenclosed. During service, maintenance, or restoration, observe the following precautions:

- Only authorized, trained personnel should be permitted to do service, maintenance, and restoration. Avoid exposing the eye to emissions from unterminated, energized optical connectors at close distances. Connectors associated with lightwave regenerators are recessed, which limits the

exposure distance. However, technicians removing or replacing regenerators should not stare or look directly into the vacant regenerator slot with optical instruments or magnifying lenses. (Normal eyewear or indirect viewing instruments, such as Find-R-Scope are not considered magnifying lenses or optical instruments.)

- Only authorized, trained personnel should use the lightwave test equipment during installation or servicing, since this equipment contains semiconductor lasers. (Some examples of lightguide test equipment are optical time domain reflectometers (OTDRs), Hand-Held Loss Test Sets, and Feature Finders.)
- Under no circumstances should any personnel scan a fiber with an optical test set without verifying that all lightwave sources on the fiber are turned off.
- All unauthorized personnel should be excluded from the immediate area of lightwave transmission systems during installation and service.

Consult ANSI Z136.1, American National Standard for Safe Use of Lasers, for guidance on the safe use of lasers in the workplace.

Electrostatic Discharge (ESD) Considerations



CAUTION:

Industry experience has shown that all integrated circuit packs can be damaged by static electricity that builds up on work surfaces and personnel. The static charges are produced by various charging effects of movement and contact with other objects. Dry air allows greater static charges to accumulate. Higher potentials are measured in areas with low relative humidity, but potentials high enough to cause damage can occur anywhere.

Observe the following precautions when handling circuit packs to prevent damage by electrostatic discharge:

- Assume all circuit packs contain solid-state electronic components that can be damaged by ESD.
- When handling circuit packs (storing, installing, removing, etc.) or when working on the backplane, always wear a grounded wrist strap or wear a heel strap and stand on a grounded, static-dissipating floor mat.
- Handle all circuit packs by the faceplate or latch and by the top and bottom outermost edges. Never touch the components, conductors, or connector pins.
- Observe all warning labels on bags and cartons. Whenever possible, do not remove circuit packs from antistatic packaging until ready to insert them into slots.
- If possible, open all circuit packs at a static-safe work position, using properly grounded wrist straps and static-dissipating table mats.

- Always store and transport circuit packs in static-safe packaging. Shielding is not required unless specified.
- Keep all static-generating materials such as food wrappers, plastics, and *styrofoam** containers away from all circuit packs. When removing circuit packs from a bay, immediately place the circuit packs in static-safe packages.
- Whenever possible, maintain relative humidity above 20 percent.
- Always keep the electromagnetic interference (EMI)/ESD protective front covers on the shelves except during an upgrade or maintenance procedure. Once a circuit pack is replaced in the shelf, immediately close the front cover.

To reduce the possibility of ESD damage, shelves are equipped with grounding jacks to enable personnel to ground themselves using wrist straps (Figure 3), while handling circuit packs or working on a shelf. The jacks for connection of wrist straps are located at the lower right-hand corner of each user panel and filter panel and at the rear of the bay. These jacks are labeled. The wrist straps should be checked periodically with a wrist strap tester to ensure that they are working properly.

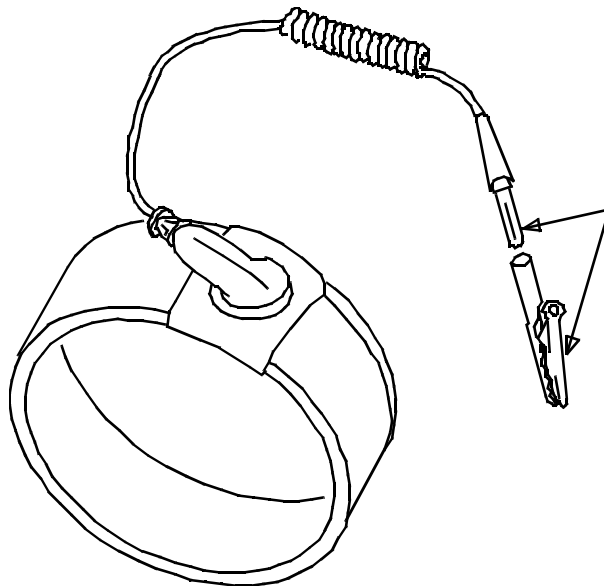


Figure 3. Static Control Wrist Strap

Security

Lucent Technologies has designed the craft interface terminal (CIT) so that, when properly administered, it will minimize the ability of unauthorized persons to gain access to the network. Each authorized user should be instructed about the proper use of the CIT.

IMPORTANT SAFETY INSTRUCTIONS

READ AND UNDERSTAND ALL INSTRUCTIONS.

When using this telecommunication equipment, basic safety precautions should always be followed to reduce the risk of fire, electric shock, and injury to persons, including the following:

1. Follow all warnings and instructions marked on the product.
2. Slots and openings in this product and the back or bottom are provided for ventilation. To protect it from overheating, these openings must not be blocked or covered.
3. Opening or removing rear covers or sheet-metal parts may present exposure to high current or electrical energy levels, or to other risks. Refer all servicing in those areas to qualified service personnel.
4. Never push objects of any kind into this product through slots, as they may touch dangerous voltage points or short out parts that could result in a risk of fire or electrical shock. Never spill liquid of any kind on the product.
5. Refer servicing to qualified service personnel.

SAVE THESE INSTRUCTIONS.

IMPORTANT INSTALLATION SAFETY INSTRUCTIONS

READ AND UNDERSTAND ALL INSTRUCTIONS.

1. Use caution when installing and modifying telecommunications lines.
2. Never install telecommunication wiring during a lightning storm.
3. Never install telecommunication jacks in wet locations unless the jack is specifically designed for wet locations.
4. Never touch uninsulated telecommunication wires or terminals unless the telecommunication line has been disconnected at the network interface.

5. Installation must include an independent frame ground conductor to building ground. Grounding/bonding circuit continuity is vital for safe operation of this equipment. Never operate with grounding/bonding conductor disconnected.
6. This product has two –48 V DC input power feeders. Disconnecting one power feeder will not de-energize the product. To reduce the risk of injury, disconnect both power supply cables when removing power from the system.
7. Metallic telecommunication interfaces should not leave the building premises unless connected to telecommunication devices providing primary and secondary protection, as applicable.
8. For continued protection against risk of fire, replace only with same type and rating of fuse.
9. Use only Lucent Technologies manufactured, recognized circuit packs. Refer to 365-575-104, *FT-2000 OC-48 Lightwave System, Releases 1 and 2, Installation Manual*, and 365-575-115, *FT-2000 OC-48 Lightwave System, Releases 4, 5, 6, and 7 Installation Manual*, for a list of recognized circuit packs.

SAVE THESE INSTRUCTIONS.

Related Documentation/Training

Lucent Technologies

The following Lucent Technologies practices provide information about the FT-2000 OC-48 Lightwave System:

- Number: 365-575-100
Title: *FT-2000 OC-48 Lightwave System, Applications, Planning, and Ordering Guide*
Audience: System planners and engineers
Content: Features, applications, general description, system planning/engineering, and ordering information
- Number: 365-575-102 (NON-TARP Releases 1 through 7.2) (Comcode: 108042458)
Title: *FT-2000 OC-48 Lightwave System, User/Service Manual*
Audience: End user maintenance personnel
Content: Features, feature applications, configuration descriptions, equipment descriptions, administration and provisioning information, as well as operation and maintenance procedures.

- Number: 365-575-103 (TARP Releases 8.1 through 9.1)
(Comcode 108140757)
Title: *FT-2000 OC-48 Lightwave System, User/Service Manual*
Audience: End user maintenance personnel
Content: Features, feature applications, configuration descriptions, equipment descriptions, administration and provisioning information, as well as operation and maintenance procedures.
- Number: 365-575-171
Title: *FT-2000 OC-48 Lightwave System, Quick Reference Guide*
Audience: End user maintenance personnel
Content: Abbreviated list of common report commands and a trouble clearing procedure that can be used to clear most trouble
- Number: 365-575-115
Title: *FT-2000 OC-48 Lightwave System, Releases 3 Through 8 Installation Manual*
Audience: Customers planning to install and turn up the equipment
Content: Customer installation instructions for the FT-2000 OC-48 Add/Drop-Rings Terminal and FT-2000 OC-48 Repeater Bay platforms
- Number: Comcode 109191668
Title: *FT-2000 OC-48 Lightwave System, Software Release Description, Release 7.2.10-ADR*
Audience: End user maintenance personnel
Content: Status of problems fixed and known problems for Release 7.2-ADR software
- Number: 365-575-121
Title: *FT-2000 OC-48 Repeater Bay/Shelf Provisioning Job Aid (Releases 2, 4, and 6)*
Audience: End user maintenance personnel
Content: List of provisionable parameters, original values, options, and the corresponding inputs for the FT-2000 OC-48 Repeater Bay/Shelf
- Number: 365-575-172
Title: *FT-2000 OC-48 Add/Drop-Rings Terminal Provisioning Job Aid (Release 8.1 and Later)*
Audience: End user maintenance personnel
Content: List of provisionable parameters, original values, options, and the corresponding inputs for the FT-2000 OC-48 Add/Drop-Rings Terminal
- Number: 365-575-123

Title: *FT-2000 Threshold Crossing Alert (TCA) Parameters Provisioning Job Aid (Releases 4, and Higher)*

Audience: End user maintenance personnel

Content: List of provisionable TCA parameters, original values, and the corresponding inputs for the FT-2000 OC-48 Add/Drop-Rings Terminal

- Number: 365-575-124

Title: *FT-2000 Cross-Connections Job Aid (Releases 8.1 and Later)*

Audience: End user maintenance personnel

Content: Considerations pertaining to Cross-Connect design and a blank "Work-Sheet for use when designing Cross-Connections"

■ Number: 365-575-500

Title: *Digital Transmission System FT Lightwave Booster Amplifier Description Engineering and Maintenance Manual (Issue 2)*

Audience: Customers planning to install and turn up the equipment.

■ Number: 824-102-144

Title: *Lucent Technologies 2000 Product Family Multi-Vendor Operations Interworking Guide*

Audience: System planners and engineers

Content: Operations Interworking information for Lucent's 2000 Product Family systems, including DDM-2000 OC-3 R13.0, DDM-2000 OC-12 R7.0, FiberReach R3.0, and FT-2000 OC-48 R8.1 and later, as well as Lucent's 3000 Product Family systems, including *WaveStar* BandWidth Manager R2.0, *WaveStar* 2.5G R3.0, and *WaveStar* 10G R1.0 in Multi-Vendor subnetworks.

■ Number: 824-102-147

Title: *Lucent Technologies 2000 Product Family Operations Interworking Guide (Comcode: 108261272)*

Audience: Engineers responsible for system planning, using, or maintaining networks containing a mixture of 2000 Product Family products

Content: Information on using operations interworking capabilities in the 2000 Product Family

- Number: 824-102-151
Title: *DDM-2000 Multiplexers Operations Systems Engineering Guide*
Audience: Engineers
Content: Operations systems engineering information for the DDM-2000 Multiplexers
- Number: 824-102-148
Title: *FT-2000 OC-48 Lightwave System Operations Systems Engineering Guide*
Audience: Engineers
Content: Operations systems engineering information for the FT-2000 OC-48 Lightwave System
- Number: MIP0030-MV
Title: *Modification Implementation Procedure 0030-MV, FT-2000 End Terminal to 2-Fiber Add/Drop-Rings Terminal Out-of-Service Upgrade*
Audience: RTAC, Installers, Engineers, Planners
Content: Procedures for upgrading out-of-service FT-2000 OC-48 1x1 End Terminals to FT-2000 OC-48 Add/Drop-Rings Terminals for 2-Fiber Rings
- Number: MIP0036-MV
Title: *Modification Implementation Procedure 0036-MV, FT-2000 End Terminal to 2-Fiber Add/Drop-Rings Terminal In-Service Upgrade*
Audience: RTAC, Installers, Engineers, Planners
Content: Procedures for transferring service off an in-service FT-2000 OC-48 1x1 End Terminal in preparation for the MIP0030-MV out-of-service upgrade to be performed
- Number: MIP0046-MV
Title: *Modification Implementation Procedure 0046-MV, FT-2000 Add/Drop Ring Terminal DCC Cable Upgrade.*
Audience: RTAC, Installers, Engineers, Planners
Content: Convert an in-service FT-2000 OC-48 ADR Terminal J68974, L3 or equivalent to an ADR Terminal with OC-3 Data Communications Channel (DCC) capability, J68974, L5 or equivalent

Lucent Technologies Drawings

The following Lucent Technologies drawings provide information about the FT-2000 OC-48 Lightwave System:

J68974AK-1	<i>FT-2000 OC-48 Lightwave Terminating Bay (Circuit Packs, Miscellaneous Hardware, and Spare Parts Shipped Separately from Equipment)</i>
J68974D-1	<i>FT-2000 OC-48 Dual Lightwave Terminating Bay(Equipment and Circuit Packs Ordered with Equipment)</i>
J68974E-1	<i>FT-2000 OC-48 Enhanced Lightwave Terminating Bay(Equipment and Circuit Packs Ordered with Equipment)</i>
J68974EK-1	<i>FT-2000 OC-48 Enhanced Lightwave Terminating Bay (Circuit Packs, Miscellaneous Hardware, and Spare Parts Shipped Separately from Equipment)</i>
J68974ES-1	<i>FT-2000 OC-48 Enhanced Lightwave Terminating Bay (Software and Documentation)</i>
J68974R-1	<i>FT-2000 OC-48 Lightwave Repeater Bay (Equipment and Circuit Packs Ordered with Equipment)</i>
J68974RK-1	<i>FT-2000 OC-48 Lightwave Repeater Bay (Circuit Packs, Miscellaneous Hardware, and Spare Parts Shipped Separately from Equipment)</i>
J68974RS-1	<i>FT-2000 OC-48 Lightwave Repeater Bay (Software and Documentation)</i>
SD-5G250-01	<i>FT-2000 OC-48 Lightwave Terminating Bay Application Schematic</i>
SD-5G250-02	<i>FT-2000 OC-48 Enhanced Lightwave Terminating Bay Application Schematic</i>
SD-5G250-03	<i>FT-2000 OC-48 Dual Lightwave Terminating Bay Application Schematic</i>
SD-5G252-01	<i>FT-2000 OC-48 Lightwave Repeater Bay Application Schematic</i>
T-5G250-31	<i>FT-2000 Lightwave Transmission System OC-48 Lightwave Terminating Bay Interconnect Circuit</i>
T-5G250-33	<i>FT-2000 Lightwave Transmission System OC-48 Enhanced Lightwave Terminating Bay Interconnect Circuit</i>

T-5G252-31	<i>FT-2000 Lightwave Transmission System OC-48 Lightwave Repeater Bay Interconnect Circuit</i>
ED-6G994-30	<i>User/Filter Panel</i>
ED-6G995-30	<i>High Speed Shelf Assembly</i>
ED-6G995-31	<i>Enhanced High Speed Shelf Assembly</i>
ED-6G999-30	<i>Low Speed Shelf Assembly</i>
ED-6G999-31	<i>Low Speed Shelf Assembly</i>
ED-7G000-30	<i>Repeater Shelf Assembly</i>
ED-7G001-10	<i>Method of Cabling for ED-7G001-30 OC-48 Lightwave Terminating Bay</i>
ED-7G001-11	<i>Method of Cabling for ED-7G001-31 OC-48 Enhanced Lightwave Terminating Bay</i>
ED-7G001-20	<i>Cable Assemblies for ED-7G001-30 OC-48 Lightwave Terminating Bay</i>
ED-7G001-24	<i>Cable Assemblies for ED-7G001-31 OC-48 Enhanced Lightwave Terminating Bay</i>
ED-7G001-21	<i>FT-2000 OC-48 Coaxial Cable Assemblies for All EC-1/ DS3 Applications (Intraoffice Transmission Cables)</i>
ED-7G001-22	<i>FT-2000 OC-48 Interbay Cable Assemblies (Intraoffice Non-Transmission Cables)</i>
ED-7G001-30	<i>FT-2000 Lightwave Transmission System OC-48 Lightwave Terminating Bay Framework</i>
ED-7G001-31	<i>FT-2000 Lightwave Transmission System OC-48 Enhanced Lightwave Terminating Bay Framework</i>
ED-7G002-10	<i>Method of Cabling for ED-7G002-30 OC-48 Lightwave Repeater Bay</i>
ED-7G002-30	<i>FT-2000 Lightwave Transmission System OC-48 Lightwave Repeater Bay Framework</i>
ED-8C500-50	<i>Network Bay Frame</i>
ED-8C800-50	<i>Seismic Network Bay Frame</i>
FPD 804-911-168-1	<i>FT-2000 Lightwave Transmission System OC-48 Lightwave Terminating Bay (with 2 Active Shelves)</i>
FPD 804-911-168-2	<i>FT-2000 Lightwave Transmission System OC-48 Lightwave Terminating Bay (with 3 Active Shelves)</i>

FPD 804-911-168-3	<i>FT-2000 Lightwave Transmission System OC-48 Enhanced Lightwave Terminating Bay with 2 Active Shelves on Nonseismic Framework</i>
FPD 804-911-168-4	<i>FT-2000 Lightwave Transmission System OC-48 Enhanced Lightwave Terminating Bay with 3 Active Shelves on Nonseismic Framework</i>
FPD 804-911-168-5	<i>FT-2000 Lightwave Transmission System OC-48 Enhanced Lightwave Terminating Bay with 2 Active Shelves on Seismic Framework</i>
FPD 804-911-168-6	<i>FT-2000 Lightwave Transmission System OC-48 Enhanced Lightwave Terminating Bay with 3 Active Shelves on Seismic Framework</i>
FPD 804-911-168-10	<i>FT-2000 Lightwave Transmission System OC-48 Repeater Bay with 1 Active Shelf on Nonseismic Framework</i>
FPD 804-911-168-11	<i>FT-2000 Lightwave Transmission System OC-48 Repeater Bay with 2 Active Shelves on Nonseismic Framework</i>
FPD 804-911-168-12	<i>FT-2000 Lightwave Transmission System OC-48 Repeater Bay OC-48 Repeater Bay with 3 Active Shelves on Nonseismic Framework</i>
FPD 804-911-168-13	<i>FT-2000 Lightwave Transmission System OC-48 Repeater Shelf Miscellaneously Mounted</i>
FPD 804-911-168-14	<i>FT-2000 Lightwave Transmission System OC-48 Repeater Bay with 1 Active Shelf on Seismic Framework</i>
FPD 804-911-168-15	<i>FT-2000 Lightwave Transmission System OC-48 Repeater Bay with 2 Active Shelves on Seismic Framework</i>
FPD 804-911-168-16	<i>FT-2000 Lightwave Transmission System OC-48 Repeater Bay with 3 Active Shelves on Seismic Framework.</i>

Lucent Technologies Practices for Related Equipment

The following Lucent Technologies documents provide information about related equipment:

065-215-200	<i>Seismic Network Bay Frame Application, Planning, and Ordering Guide</i>
065-215-250	<i>Seismic Network Bay Frame Installation Guide</i>
190-523-101	<i>Guide to Using CPro-2000</i>
365-331-000	<i>DACS III-2000 Release 2.0 Applications, Planning, and Ordering Guide</i>
365-340-004	<i>DACS IV-2000 Release 2.1 Reference Manual</i>
363-206-200	<i>DDM-2000 OC-3 and OC-12 Multiplexers, Applications, Planning, and Ordering Guide</i>
363-206-285	<i>DDM-2000 OC-3 Multiplexer, User/Service Manual, TARP Release 13.0 and later</i>
363-206-295	<i>DDM-2000 OC-12 Multiplexer, User/Service Manual, Release 7.0 TARP</i>
365-301-130	<i>System III DSX-3/4, Planning, Engineering, Installation, and Operation System Reference Guide</i>
365-303-102	<i>DSX-3 Cross-Connect Bay, Description, Operation, and Maintenance Manual</i>
636-299-120	<i>LGX[®] Distribution System, Planning, Engineering, Installation, and Operation System Reference Guide</i>
365-565-500	<i>Digital Transmission Systems, FT-Lightwave Booster Amplifier, Description, Engineering, Installation, and Maintenance Manual.</i>

Training

No product offering is complete without a formal training package. Lucent Learning provides management courses for system planning, engineering, and ordering, as well as training telecommunications technicians in installation, operations, and maintenance. Suitcasing of these courses is also available.

To enroll in a training class at one of the Lucent Technologies corporate training centers, please call **1-888-LUCENT8 (1-888-582-3688)**: **Prompt 2**. To arrange a suitcase session at your facility, call **1-800-432-6317**

The following courses are provided by Lucent Learning:

- Number: LW2248

Title: *FT-2000 OC-48 Lightwave System Applications and Architecture*

Audience: Fundamental planners, account executives, and private telecommunications network technical consultants

Content: Basic synchronous optical network (SONET) terms, applications (for example, point-to-point, add/drop, rings), architecture (that is, bays, shelves, circuit packs), and operation, administration, maintenance, and provisioning (OAM&P) features

- Number: LW2348

Title: *FT-2000 OC-48 Lightwave System Equipment Engineering and Planning*

Audience: Facility planners, outside plant engineers, central office equipment engineers, and private network design engineers

Content: Equipment and cabling specifications for engineering and ordering the FT-2000 OC-48 Lightwave System

- Number: LW2616

Title: *FT-2000 OC-48 Add/Drop-Rings Terminal Operations and Maintenance*

Audience: Technicians, installers, maintenance engineers, technical support personnel, product evaluators, and anyone desiring operations and maintenance information for the FT-2000 OC-48 Add/Drop-Rings Terminal

Content: Description of initial turnup and day-to-day operations and maintenance tasks. Emphasis on developing:

- Skills using the installation manual and the user/service manual
- Competence performing turnup and day-to-day provisioning and maintenance tasks

Technical Support

Introduction

This section describes the technical support available for FT-2000 OC-48 Lightwave System.

Technical support groups

Technical support is available through the following:

- North American Regional Technical Assistance Center (NARTAC)
- Customer Technical Support (CTS)

Lucent Technologies technical support groups are committed to providing customers with high-quality product support services. The NARTAC and CTS organizations regard customers as their highest priority and understand the obligation of customers to maintain quality services.

Contacting NARTAC

NARTAC is the first point of contact for technical support. NARTAC personnel troubleshoot field problems 24 hours a day over the phone and on site (if necessary).

For technical assistance, call **1-866-LUCENT8** (1-866-582-3688).

International customers, please contact your account executive for your local technical support number.

NARTAC services

NARTAC provides the following technical support services:

- Tracks and maintains visible ownership of all reported customer assistance requests, from inception through resolution
- Analyzes and diagnoses reported problems
- Communicates actions, plans, and problem status to the reporting customer
- Provides restoration and recovery services
- Provides preventative and/or circumvention measures
- Initiates action to establish modifications requests (MRs) for design issues

- Writes technical bulletins
- Provides technical assistance to installers

NARTAC personnel are supported by the Lucent Technologies Customer Technical Support (CTS) organization.

CTS services

The CTS organization provides quality product support services and regards the customer as its highest priority. The CTS organization maintains direct contact with development and manufacturing organizations of Lucent Technologies to assure prompt resolution of all customer assistance requests.

The CTS organization provides the following technical support services:

- Manages customer assistance requests
- Supports first service applications (FSAs), customer lab evaluations (CLEs), and initial customer applications (ICAs)
- Develops software release descriptions, installation manuals, engineering change procedures (ECPs), method of procedures (MOPs), modification implementation procedures (MIPs)
- Provides installation and NARTAC training

A technical support platform

The CTS organization provides an online system, the Customer Assistance Request Entry System (CARES), to track all customer assistance requests. The CTS CARES communicates details about assistance requests, product bulletins, and other critical information to customer sites. To access this site, customers will need a Customer Web Access login. To inquire about obtaining a CTS CARES Customer Web Access login, please visit us at [**http://www.lucent.com/support**](http://www.lucent.com/support).

How to Order Documents

To order additional copies of this document and/or to request placement on the standing order list, send or call in an order as follows:

Customer	Mail Order	Telephone Order (Monday through Friday)
Commercial Customers*	Lucent Technologies Customer Information Center Attention: Order Entry Section 2855 N. Franklin Road P.O. Box 19901 Indianapolis, IN 46219	Within USA: 1-888-LUCENT-8 <i>7:30 a.m to 6:30 p.m. EST</i> FAX: 1-317-322-6484 Europe, Middle East: and Africa Toll: 1-317-322-6416
		Caribbean, Latin America, and Canada Toll: 1-317-322-6646 All Other Countries: Toll: 1-317-322-6411 Worldwide: FAX: 1-317-322-6699
RBOC/BOC	Process through your Company Documentation Coordinator	
* For commercial customers, a check, money order, purchase order number, or charge card number is required with all orders. Make checks payable to Lucent Technologies.		
Lucent Technologies entities should use Form IND 1-80.80 FA, available through the Customer Information Center.		

One-time orders include a binder (if applicable) and the document contents for the current issue in effect at the time of order. Also, you may request placement on the standing order list for all later reissues of any document. The standing order list for each document provides automatic distribution for all reissues of the document. RBOC/BOC customers should process document orders or standing order requests through their Company Documentation Coordinator. If you do not have a Company Documentation Coordinator, use the commercial customer telephone number listed above.

How to Comment on the FT-2000 OC-48 Lightwave System

The Regional Technical Assistance Center (RTAC) technical assistance telephone number is available 24 hours a day for customers to provide feedback and enhancement suggestions for improving the FT-2000 OC-48 Lightwave System. The toll free number is **1-800-225-RTAC**.

Commenting on this Document

Introduction

Because customer satisfaction is extremely important to Lucent Technologies, every attempt is made to encourage feedback from customers about our information products.

Customer comment form

A customer comment form is located immediately after the title page of this document. Please fill out the form and fax it to the number provided on the form.

Address for Comments

If the customer comment form is missing, send or fax comments about this document to the following:

Lucent Technologies Inc.
Lucent Learning
Building 21, Room 3A-06
1600 Osgood Street
North Andover, MA 01845
Fax: 1-978-960-6835

Electronic Documentation

Documentation for the FT-2000 OC-48 Lightwave System is now available in electronic form, on compact disk, read-only memory (CD-ROM). CD-ROM has many advantages over traditional paper documentation, including cost savings, search and retrieve capability, and the assurance of the most current documentation.

CD-ROM is available by annual subscription (on standing order).

- To order, call your Technical Information Resource Manager, your Lucent Technologies Account Executive, or the Lucent Technologies Customer Information Center (1-888-LUCENT-8). The CD-ROM Product Line Order Number for the Lucent Technologies transmission product documentation is 300-100-010.
- For pricing information, contact your Lucent Technologies Account Executive or the Lucent Technologies Customer Information Center (1-888-LUCENT-8).
- For technical information, call Lucent Technologies Documentation Support (1-800-334-0404).

Product Change Notifications

During the life of a product, changes may be required in service to correct an existing or potential problem. Product changes are issued in the form of product change notices (PCNs). Customers are notified about PCNs through the Design Change Management System (DCMS). The DCMS is an on-line tool similar to GTSIP. For more information about DCMS, contact your local Account Executive.

Table of Contents

Overview	1-1
Introduction to the Lucent Technologies 2000 Product Family	1-1
What Is the FT-2000 OC-48 Lightwave System?	1-2



Table of Contents

System Introduction

1

Overview

This section introduces the Lucent Technologies 2000 Product Family and briefly describes the FT-2000 OC-48 Lightwave System.

Introduction to the Lucent Technologies 2000 Product Family

Lucent Technologies offers the industry's widest range of high-quality transmission systems and related services designed to provide total network solutions. The Lucent Technologies 2000 Product Family is designed to help telecommunications service providers enter a new century of advanced services and revenue-generating capabilities. The 2000 Product Family complies with the synchronous optical network (SONET) Standard and builds on items that customers require to build an efficient and successful network. These items include bidirectional line-switched ring features and in-service upgrade capabilities. Bidirectional line-switched ring features almost instantaneously restore a network if a fiber cut occurs. In-service upgrade capabilities allow a graceful evolution from today's asynchronous networks to the world-class intelligent networks of the future. The 2000 Product Family provides broadband services, a high degree of reliability, and full compatibility with industry standards.

The FT-2000 OC-48 Lightwave System is the high capacity optical carrier level 48 (OC-48) lightwave system in the 2000 Product Family. The 2000 Product Family also includes the DDM-2000 Multiplexers, the DACS III-2000 Cross-Connect System, the DACS IV-2000 Cross-Connect System, ITM XM, CPro-2000, ITM SNC, the *SLC*[®]-2000 Access System, and the BRT-2000 Access System.

The 2000 Product Family, along with the 5ESS®-2000 switch, provides the significant elements of the Lucent Technologies Service Net-2000 Architecture.

Service Net-2000 Architecture furnishes increased network capabilities with standard interfaces, increased bandwidth for end-customer services, faster provisioning, and more robust networks.

What Is the FT-2000 OC-48 Lightwave System?

The FT-2000 OC-48 Lightwave System is a flexible, high capacity lightwave system that transports digitally encoded information through single-mode optical fibers at the SONET OC-48 signal level of 2.5 Gb/s (up to 32,256 voice channels). The FT-2000 OC-48 Lightwave System consists of several platforms. A platform is a family of equipment and software configurations designed to support a particular set of applications. These platforms include the:

- FT-2000 OC-48 Dual Lightwave Terminating Bay
- FT-2000 OC-48 Add/Drop-Rings Terminal
- FT-2000 OC-48 Repeater Bay.



NOTE:

The FT-2000 Add/Drop-Rings Terminal has been rated discontinued availability (DA) as of December 31, 1999. Information regarding the ADR Terminal will remain in this user/service manual for those terminals still in use.

The different platforms enable the FT-2000 OC-48 Lightwave System to offer flexible features for different applications in high capacity loop, interoffice, outstate, and long-haul environments, and to provide different sets of features. The FT-2000 OC-48 Lightwave System software enables the platforms to be upgraded with new features when they become available. Figure 1-1 shows one equipment package of the FT-2000 OC-48 Add/Drop-Rings Terminal platform. For more information about the FT-2000 OC-48 Add/Drop-Rings Terminal and FT-2000 OC-48 Repeater Bay platforms, refer to Volume I, Section 3, "Platform Descriptions."

The FT-2000 OC-48 Lightwave System transports up to 48 DS3 equivalent signals over the OC-48 line. It supports a mix of DS3, electrical carrier level 1 (EC-1), OC-3, IS-3, and OC-12 low-speed interfaces. The OC-3, IS-3, and OC-12 low-speed interfaces support optical extensions. The electrical DS3 and EC-1 low-speed interfaces are 1xM (M≤16) protected. Both the OC-3 and IS-3 interfaces may be provisioned for either 0x1 protection or 1+1 protection. While, (with Release 7.1.0) the OC-12 interface may be provisioned for 0x1 protection with DCC, or for 1+1 protection without DCC.

NOTE:
The IS-3 interface is only used for intraoffice applications.

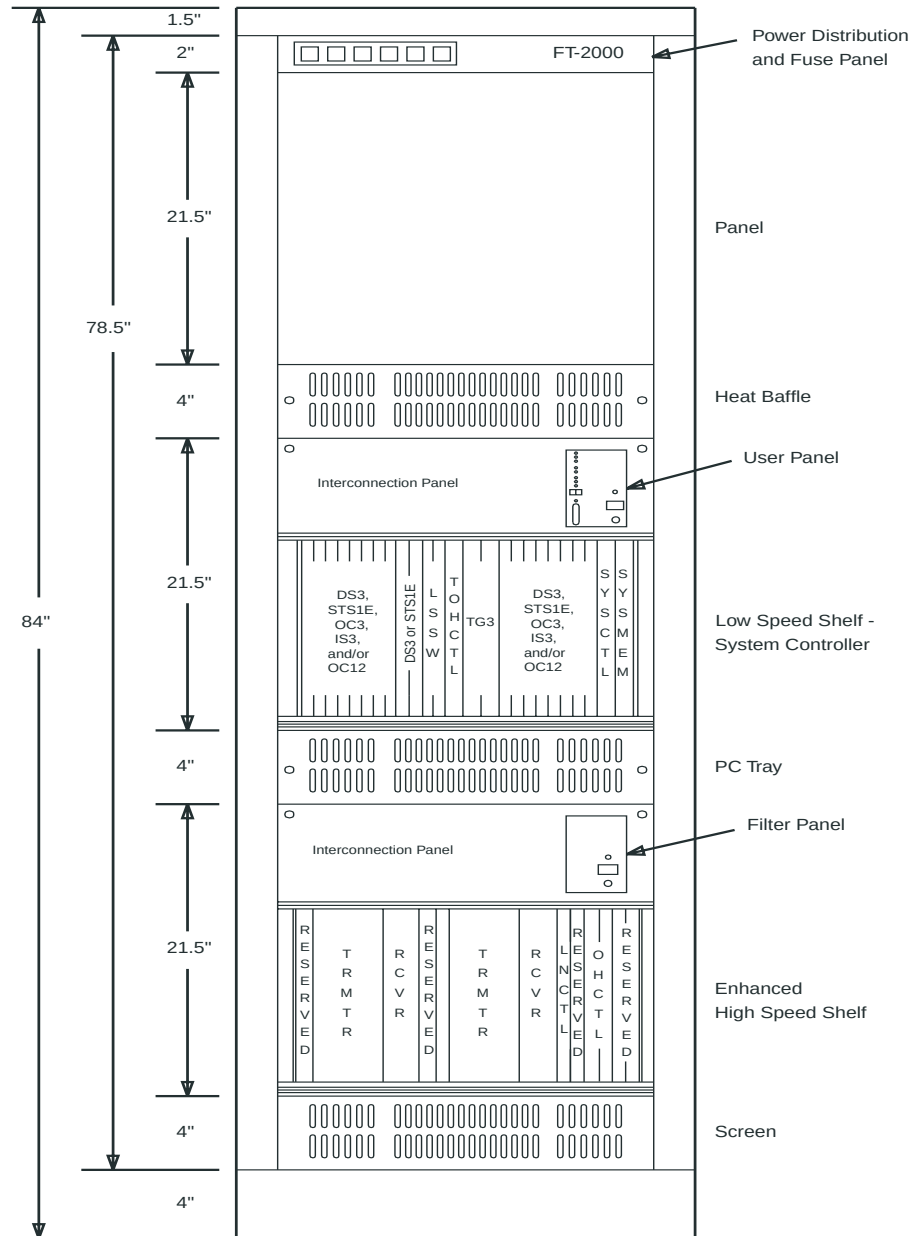


Figure 1-1. FT-2000 OC-48 Add/Drop-Rings Terminal for Two-Fiber Rings

The FT-2000 OC-48 Lightwave System also offers flexible OC-48 high-speed interfaces. Standard performance and high performance 1.3 μm OC-48 high-speed interfaces support spans of at least 53 and 60 kilometers, respectively. The 1.5 μm OC-48 high-speed interface* supports spans of at least 92 kilometers. The lightwave booster amplifier (LBA) standard performance 1.5 μm OC-48 high-speed interface* supports spans up to 140 km using standard fiber and spans up to 160 km using dispersion-shifted fiber. For information about OC-48 high-speed protection, refer to Volume I, Section 9, "Maintenance Description." *

The FT-2000 OC-48 Lightwave System is designed for easy installation and operation. Centralized operation is supported by a full set of remote maintenance and control features. Automatic turnup tests and original value provisioning simplify installation and eliminate the need for external test equipment. Most tasks can be performed using the user panel and circuit pack faceplate light-emitting diodes (LEDs), displays, and controls. The personal computer-based craft interface terminal gives access to sophisticated maintenance and reporting features.

The FT-2000 OC-48 Lightwave System will be made available in a series of phased product releases. These phased product releases will provide new sets of features. This user/service manual covers Releases 1 through 7.2 and will be updated to cover additional releases as they become available. As a review, Release 1 provides the FT-2000 OC-48 1x1 End Terminal platform and a basic OC-48 point-to-point system. Release 2 updates the FT-2000 OC-48 1x1 End Terminal platform and provides the FT-2000 OC-48 Repeater Bay platform. Release 3 provides the FT-2000 OC-48 Add/Drop-Rings Terminal platform and two-fiber bidirectional, line-switched rings. Release 4.0 updates the FT-2000 OC-48 Add/Drop-Rings Terminal and FT-2000 OC-48 Repeater Bay platforms. Release 4.1 updates the FT-2000 OC-48 Add/Drop-Rings Terminal platform with enhanced TL1 commands/reports, password aging, and one-way cross-connections. It also provides dual-ring interworking and gateway network element operation. Release 5 updates the FT-2000 OC-48 Add/Drop-Rings Terminal platform with OC-3 low-speed interfaces, F and M bit adjusted DS3 performance monitoring, more TL1 commands/reports, login aging, circuit provisioning audits, and multiple bridge cross-connections. Release 6 updates the FT-2000 OC-48 Add/Drop-Rings Terminal platform with new operations interworking capabilities available through the OC-3 data communications channel (DCC). Release 6 also updates the FT-2000 OC-48 Repeater Bay with access to the OC-48 section overhead bytes for orderwire.

Release 7 updates the FT-2000 OC-48 Add/Drop-Rings Terminal platform with STS-1 cross-connections across an OC-48 signal.

* The standard performance 1.5 μm OC-48 high-speed interfaces are only available with the FT-2000 OC-48 Add/Drop-Rings Terminal.

Release 7.1.0 updates the FT-2000 OC-48 Add/Drop-Rings Terminal platform with the following features:



NOTE:

Release 7.1.0 was rated discontinued availability (DA) in April 1998.

- OC-12 optical low-speed interface
- IS-3 optical low-speed interface for use in intraoffice networks
- OC-3/OC-12 port states
- Synchronization messaging
- Enhanced TL1 command:
- STS-3 Non-Preemptible Protection Access (NPPA) (B5 Optics Only)
- DRI Enhancements
 - Revertive DRI
 - Switching on AIS-P
- Expanded reporting for up to 32 Network/Subnetwork elements

Release 7.2 updates the FT-2000 OC-48 Add/Drop-Rings Terminal platform with the following new features:

- New Low Speed Shelf/User Panel/Interconnection Panel
- Dense Wavelength Division Multiplexing (DWDM)—OLS-compatible transmitters (739E1-739E16 and 739G1-G16) and receiver (839E5)
- OC12 1+1 transmission with DCC
- STS-1 Non-Preemptible Protection Access (NPPA) with B5 optics
- STS-3 Non-Preemptible Protection Access (NPPA) with all optics
- Operations Interworking (OI) via the DCC channel for OC-12 1+1 protected links
- Synchronization messaging: line timing and auto reconfiguration
- Deny enhancements for Delete command
- Switch on Line Signal Fail for DRI
- DRI switching on Signal Degrade



NOTE:

Release 7.2 is not OI compatible with Release 9.0 or greater.

Release 7.2.8 and Release 7.2.10 maintain/enhance existing features.

Table 1-1 also shows the major features of the FT-2000 OC-48 Lightwave System and the corresponding product releases.

Table 1-1. Major Features of Product Releases 1 Through 7.2

Major Features	Available in Product Release:									
	1	2	3	4.0	4.1	5	6	7	7.1	7.2
Applications										
- Point-to-Point	X	X								X
- Point-to-Point with Repeaters		X								X
- 2-Fiber Rings			X	X	X	X	X	X	X	X
- 2-Fiber Rings with Repeaters				X	X	X	X	X	X	X
- Dual Ring Interworking (DRI)					X	X	X	X	X	X
- TL1/X.25-Gateway Network Element (GNE)					X	X	X	X	X	X
- Lucent Directory Services (LDS)										X
- Operation with the FT-Lightwave Booster Amplifier						X	X	X	X	X
- Path-in-Line Architecture							X	X	X	X
- Lucent Operations Interworking							X	X	X	X
High-Speed Transmission Interfaces										
- 1.3 μ m Standard Performance	X	X	X	X	X	X	X	X	X	X
- 1.3 μ m High Performance	X	X	X	X	X	X	X	X	X	X
- 1.5 μ m Standard Performance				X	X	X	X	X	X	X
- Wavelength Division Multiplexing and Lightwave Booster Amplifier Compatible Transmitters/Regenerators							X	X		X
- Dense Wavelength Division Multiplexing (DWDM)—OLS-compatible (739E1-739E16)										X
- Dense Wavelength Division Multiplexing (DWDM)—OLS-compatible (739H1-739H16)										X
- Dense Wavelength Division Multiplexing (DWDM)—OLS-compatible (739G1-739G8)										X
- Dense Wavelength Division Multiplexing (DWDM)—non-OLS-compatible (739H1-739H16)										X
- Universal Optical Connectors (ST [®] , FC/PC, and SC Connectors)			X	X	X	X	X	X	X	X
- Transmitters and Receivers Compatible with STS-1 Cross-Connections								X	X	X
Low-Speed Transmission Interfaces										
- DS3	X	X	X	X	X	X	X	X	X	X
- EC-1/STS-1E				X	X	X	X	X	X	X
- OC-3						X	X	X	X	X
- IS-3									X	X
- OC-12									X	X
Administration and Provisioning										
- One-Way STS-3 Cross-Connections					X	X	X	X	X	X

Table 1-1. Major Features of Product Releases 1 Through 7.2 (Contd)

Major Features	Available in Product Release:									
	1	2	3	4.0	4.1	5	6	7	7.1	7.2
- Two-Way STS-3 Cross-Connections			X	X	X	X	X	X	X	X
- Limited STS-1 Cross-Connections						X	X	X	X	X
- Revertive DRI Protection Switching on STS (low speed) Links									X	X
- STS-1 Cross-Connections Across an OC-48 Signal								X	X	X
- STS-3 Non-Preemptible Protection Access (NPPA) (B5 optics)									X	X
- STS-1 Non-Preemptible Protection Access (NPPA)										X
- Security		X	X	X	X	X	X	X	X	X
- Remote Software Downloads						X	X	X	X	X
Subnetwork Size										
- 50 NEs (Single Level 1 area only)										X
Maintenance										
- Performance Monitoring for Incoming Low Speed Signals				X	X	X	X	X	X	X
- Expanded Performance Monitoring of up to 32 Network/Subnetwork Elements									X	X
- F and M Bit, Adjusted DS3 Performance Monitoring						X	X	X	X	X
- Threshold Crossing Alert Reporting				X	X	X	X	X	X	X
- Enhanced TL1 Commands and Reports					X	X	X	X	X	X
- ASCII-Based Terminal Interface					X	X	X	X	X	X
- Circuit Provisioning Audits					X	X	X	X	X	X
- TL1 Message Enhancements					X	X	X	X	X	
- OI via the DCC for OC-3 0x1 and 1+1 links						X	X	X	X	X
- OI via the DCC for OC-12 0x1 links									X	X
- OI via the DCC for OC-12 1+1 links										X
- Orderwire at Repeaters							X	X	X	X
- Larger TL1 Command/Message Set								X	X	X
- Remote Alarms (NE-to-NE Status) reporting										X
- Synchronization Messaging									X	X
- Line Timed Mode Synchronization With Auto Reconfiguration										X
- Switch on Line Signal Degrade for DRI										X

For more information about the FT-2000 OC-48 Lightwave System releases and their availability, refer to 365-575-100, *FT-2000 OC-48 Lightwave System, Applications, Planning, and Ordering Guide*.

Table of Contents

Overview	2-1
Network Configurations	2-2
■ Two-Fiber Bidirectional Line-Switched Rings	2-2
■ Folded Rings (Point-to-Point)	2-6
■ End-to-End Interface Mixing	2-8
■ OC-48 Hubbing	2-10
■ Dual Ring Interworking (DRI)	2-11
■ DDM-2000 OC-3 Ring Transport	2-17
■ DDM-2000 OC-12 Ring Transport	2-18
■ Dual-Wire Center Architecture	2-18
■ Broadband Service Transport —STS-3c/STS-12c	2-21
■ STS-1/STS-3 Non-Preemptible Protection Access	2-24
■ One-Way Video Distribution	2-26
■ Path-In-Line Architecture	2-27
■ Loop Feeder Network	2-30
■ Extra Traffic for Restoration and Revenue Generation	2-32
■ 0% Add/Drop	2-34
■ FT-Lightwave Booster Amplifier (FT-LBA) for Extended Distances	2-35
■ Wavelength Division Multiplexing (WDM)	2-37
■ Dense Wavelength Division Multiplexing (EON Compatible)	2-41
■ Operations Interworking	2-42
■ Gateway Network Element (GNE)	2-46

Table of Contents

■ Synchronization Messaging	2-48

Overview

The FT-2000 OC-48 Lightwave System satisfies a wide range of applications with maximum economy and efficiency. These applications include OC-48 point-to-point, two-fiber bidirectional line-switched rings, hubbing, DRI, dual-wire center architecture, broadband service transport, and loop feeder network applications. This section describes how the FT-2000 OC-48 Lightwave System meets these diverse needs with the following platforms:

- FT-2000 OC-48 Dual Lightwave Terminating Bay
- FT-2000 OC-48 Add/Drop-Rings Terminal
- FT-2000 OC-48 Repeater Bay.

**NOTE:**

The FT-2000 OC-48 Add/Drop-Rings Terminal has been rated discontinued availability as of December 31, 1999. Information regarding the ADR Terminal will remain in this user/service manual for those ADR Terminals still in use. The FT-2000 OC-48 Dual Lightwave Terminating Bay functions similarly to the ADR Terminal.

For more information about the FT-2000 OC-48 Lightwave System platforms, refer to Volume I, Section 3, "Platform Descriptions."

The FT-2000 OC-48 Lightwave System provides cost-effective DS3 transport in interoffice suburban and outstate trunk routes. Up to 48 DS3 equivalent signals (32,256 voice channels) may be transported over an OC-48 line at 2.5 Gb/s.

To accommodate loop and interoffice routes, the standard performance and high performance 1.3 μm OC-48 high-speed interfaces support spans of at least 53 and 60 kilometers, respectively. The standard performance 1.5 μm OC-48 high-speed interface supports spans of at least 92 kilometers. In Release 4 and later releases, the standard performance 1.5 μm OC-48 high-speed interface supports spans of at least 92 kilometers. In Release 5 and later releases, the lightwave booster amplifier (LBA) standard performance 1.5 μm OC-48 high-speed interface supports spans up to 140 kilometers (km) using standard fiber and spans up to 160 km using dispersion-shifted fiber. For more information about transmission distances, refer to Volume I, Section 10, "Technical Specifications."

The FT-2000 OC-48 Add/Drop-Rings Terminal typically interfaces to digital multiplexers, such as the DDM-2000 Multiplexers, to digital access and cross-connect systems such as DACS III-2000, DACS IV-2000, or Tellabs *TITAN* 5500/S, a digital switch, or other facilities.

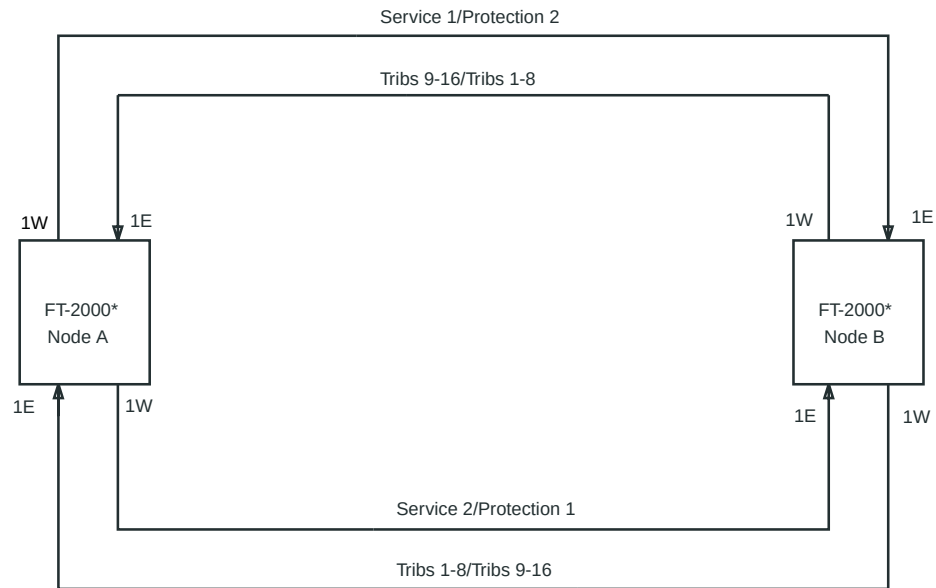
Network Configurations

This section describes many of the network configurations in which the FT-2000 OC-48 ADR Terminals can be used. In addition to the configurations specifically described, many other combinations of these network configurations can be used to meet specific application/customer needs.

Two-Fiber Bidirectional Line-Switched Rings

The FT-2000 OC-48 Add/Drop-Rings Terminal and FT-2000 OC-48 Dual Lightwave Terminating Bay may be configured to support two-fiber bidirectional line-switched rings. Bidirectional line-switched rings provide redundant bandwidth and/or equipment to ensure system integrity in the event of any transmission or timing failure, including a fiber cut or node failure.

Figure 2-1 shows an example of a two-fiber bidirectional line-switched ring with two nodes.



* FT-2000 OC-48 Add/Drop-Rings Terminal

Figure 2-1. Two-Fiber Bidirectional Ring Application

The FT-2000 OC-48 Add/Drop-Rings Terminals and FT-2000 OC-48 Dual Lightwave Terminating Bay may be equipped with Release 3.0.1-ADR or later software. In a two-fiber bidirectional ring, all FT-2000 OC-48 Add/Drop-Rings Terminals must be equipped with the same release of software.

The FT-2000 OC-48 Repeater Bay equipped with Release 4.1.0-RPTR or 6.0.0-RPTR software is also available to increase the distance between two FT-2000 OC-48 Add/Drop-Rings Terminals or FT-2000 Dual Lightwave Terminating Bays.

Each node in a two-fiber bidirectional ring interfaces with two OC-48 high-speed lines (1W and 1E). Each OC-48 high-speed line consists of sixteen 155 Mb/s (STS-3) tributaries. The STS-3 tributaries 1 through 8 are service tributaries and STS-3 tributaries 9 through 16 are protection tributaries. In Figure 2-1, the Eastbound service STS-3 tributaries are identified as *Service 1*, and the Westbound service tributaries are identified as *Service 2*. The Eastbound protection STS-3 tributaries are identified as *Protection 2*, and the Westbound protection STS-3 tributaries are identified as *Protection 1*.

Under normal conditions, traffic travels over the service STS-3 tributaries of the OC-48 high-speed lines in a ring. When a fiber cut occurs, traffic is switched from

the service STS-3 tributaries to the protection STS-3 tributaries using revertive line switches in ≤ 60 milliseconds. This includes detection and switching time. In Figure 2-1, the Eastbound service STS-3 tributaries (Service 1) are protected by the Westbound protection STS-3 tributaries (Protection 1). The Westbound service STS-3 tributaries (Service 2) are protected by the Eastbound protection STS-3 tributaries (Protection 2). For example, in Figure 2-1, traffic may enter Node A and be routed over the service STS-3 tributaries (*Service 1*) of OC-48 high-speed line 1E to Node B. If a fiber cut occurs, the traffic is switched to the protection STS-3 tributaries (*Protection 1*) of OC-48 high-speed line 1W to Node B. For more information about two-fiber ring protection switching, refer to Volume 1, Section 9, "Maintenance Description."

The FT-2000 OC-48 Add/Drop-Rings Terminal offers a time slot assignment (TSA) capability that allows any service STS-3 tributary of OC-48 high-speed line 1E or 1W to be cross-connected to any service low-speed interface slot (add/drop cross-connection) or to the same-numbered service STS-3 tributary of the other OC-48 high-speed line (through cross-connection).

In Release 7 and later, the FT-2000 OC-48 Add/Drop-Rings Terminal and FT-2000 OC-48 Dual Lightwave Terminating Bay also offer a TSA capability that allows any service STS-1 tributary of OC-48 high-speed line 1E or 1W to be cross-connected to any service low-speed interface slot (add/drop cross-connection) or to the same-numbered service STS-1 tributary of the other OC-48 high-speed line (through cross-connection).

The FT-2000 OC-48 Add/Drop-Rings Terminal can also provide *extra traffic*. Extra traffic allows access to the protection STS-3 tributaries for low-priority traffic or for restoration purposes. In Release 7 and later releases, extra traffic also allows access to the protection STS-1 tributaries for low-priority traffic or for restoration purposes. Extra traffic is preempted if a two-fiber ring protection switch occurs.

Non-Preemptible Protection Access (NPPA) is a type of extra traffic. It increases the "service traffic" capacity of a node at the expense of reducing bandwidth on the protection fiber. However, service on corresponding tributaries will be lost (for example, tributary 9 would be preserved and tributary 1 would be lost). It should be understood that this type of traffic is not impacted by a ring protection switch. STS-3 NPPA is available in Release 7.1 and later. STS-1 NPPA is available in Release 7.2 and later. Also, for each slot of STS-3 NPPA traffic (or STS-1 traffic) on the protection fiber, there will also be a corresponding "non-protected" slot on the service fiber.



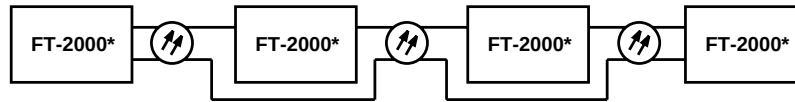
NOTE:

Cross-connections (to administer and provision extra traffic) can be made using the craft interface terminal (CIT) interface.

In Release 4.1 and later, cross-connections can also be made through the message-based operations system interface, using X.25 protocol and Transaction Language 1 (TL1) messages. For more information about cross-connections, refer to Volume I, Section 8, "Administration and Provisioning."

Folded Rings (Point-to-Point)

The FT-2000 OC-48 Add/Drop-Rings Terminal and FT-2000 Dual Lightwave Terminating Bay may be configured to support the folded rings architecture. Folded rings are rings without fiber diversity. This terminology is derived from the image of folding a ring into a linear segment. Figure 2-2 shows an example of a folded ring where adjacent ring nodes are connected and the end nodes are connected together across the whole network.



* FT-2000 OC-48 Add/Drop-Rings Terminal

Figure 2-2. Folded Ring Application

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 3.0.1 or later software or the FT-2000 OC-48 Dual Lightwave Terminating Bay may be used in folded rings. In a folded ring application, *all* FT-2000 OC-48 Add/Drop-Rings Terminals must be equipped with the same release of software.

The FT-2000 OC-48 Repeater Bay equipped with Release 4.1.0-RPTR or 6.0.0-RPTR software is also available to increase the distance between two FT-2000 OC-48 Add/Drop-Rings Terminals equipped with Release 4.1.0-ADR or later software or two FT-2000 OC-48 Dual Lightwave Terminating Bay equipped with Release 4.1.0-ADR or later software.

The FT-2000 OC-48 Add/Drop-Rings Terminal, FT-2000 OC-48 Dual Lightwave Terminating Bay, and FT-2000 OC-48 Repeater Bay allow folded ring applications to be used in place of non-ring applications, for example, linear add/drop.

Folded rings also provide flexibility and can help evolve networks into full ring applications. Figure 2-3 shows an example of a folded ring that evolves into a full ring application. In many cases, a network starts out as a folded ring because of short-term service needs between some of the nodes. Then the folded ring evolves into a full ring later when service and fiber facilities to other nodes in the network are needed. It is easier to evolve a folded ring into a full-ring application if a folded ring is used in the nodes that have this short-term service need. Folded rings have upgrade, operational, and self-healing advantages over other applications for this type of evolution.

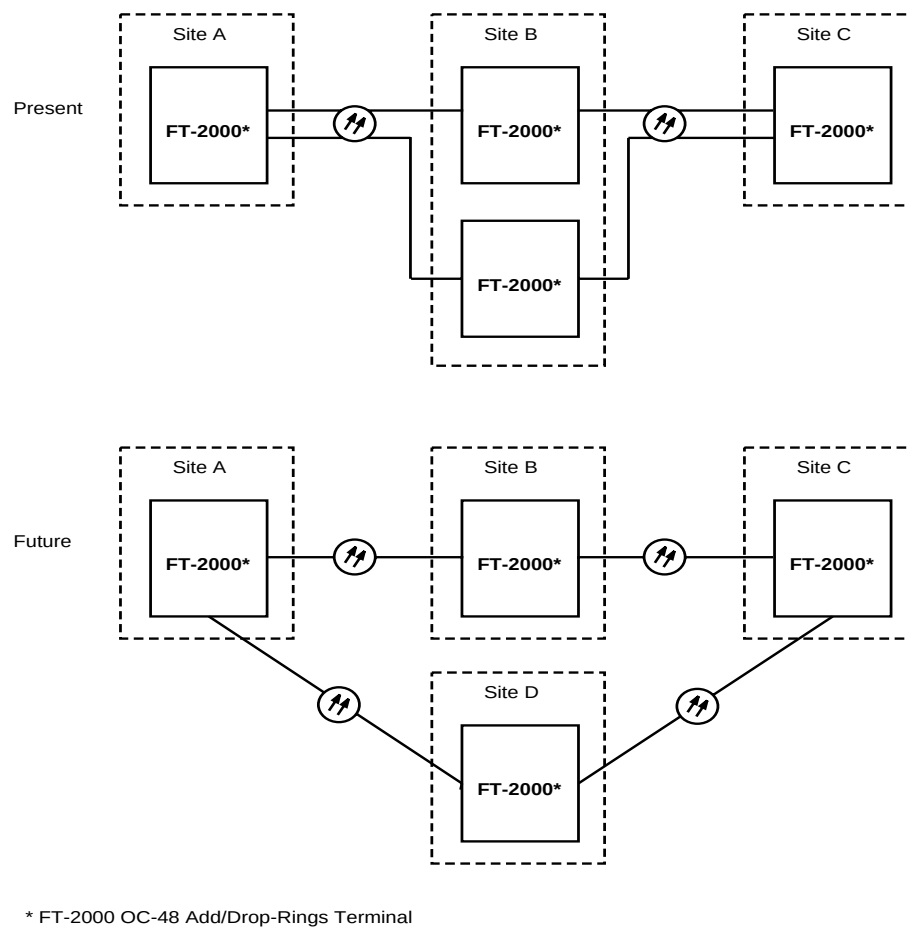


Figure 2-3. Folded Rings Evolving to Full Ring Networks

End-to-End Interface Mixing

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 4.0.0-ADR or later software supports a mix of low-speed interfaces in a network. The mix may include any, or all, of the following:

Table 2-1. Low-Speed Interface Mixing by Release

Software Release	Low-Speed Interface Mix Supported
Release 4.0.0-ADR and Release 4.1.0-ADR	DS3 and EC-1
Release 5.0.1-ADR or later	DS3, EC-1, and OC-3
Release 7.1-ADR or later	DS3, EC-1, OC-3, IS-3, and OC-12

Mixing is supported within an FT-2000 OC-48 Add/Drop-Rings Terminal and between FT-2000 OC-48 Add/Drop-Rings Terminals. This capability offers more efficient network evolution and lets planners improve their equipment deployment based on the needs of the particular application. For example, the asynchronous (DS3) low-speed interfaces at one FT-2000 OC-48 Add/Drop-Rings Terminal in a network can be upgraded in service to synchronous (EC-1, IS-3, OC-3 or OC-12) low-speed interfaces without any changes at the other FT-2000 OC-48 Add/Drop-Rings Terminal terminating the circuit (Figure 2-4).

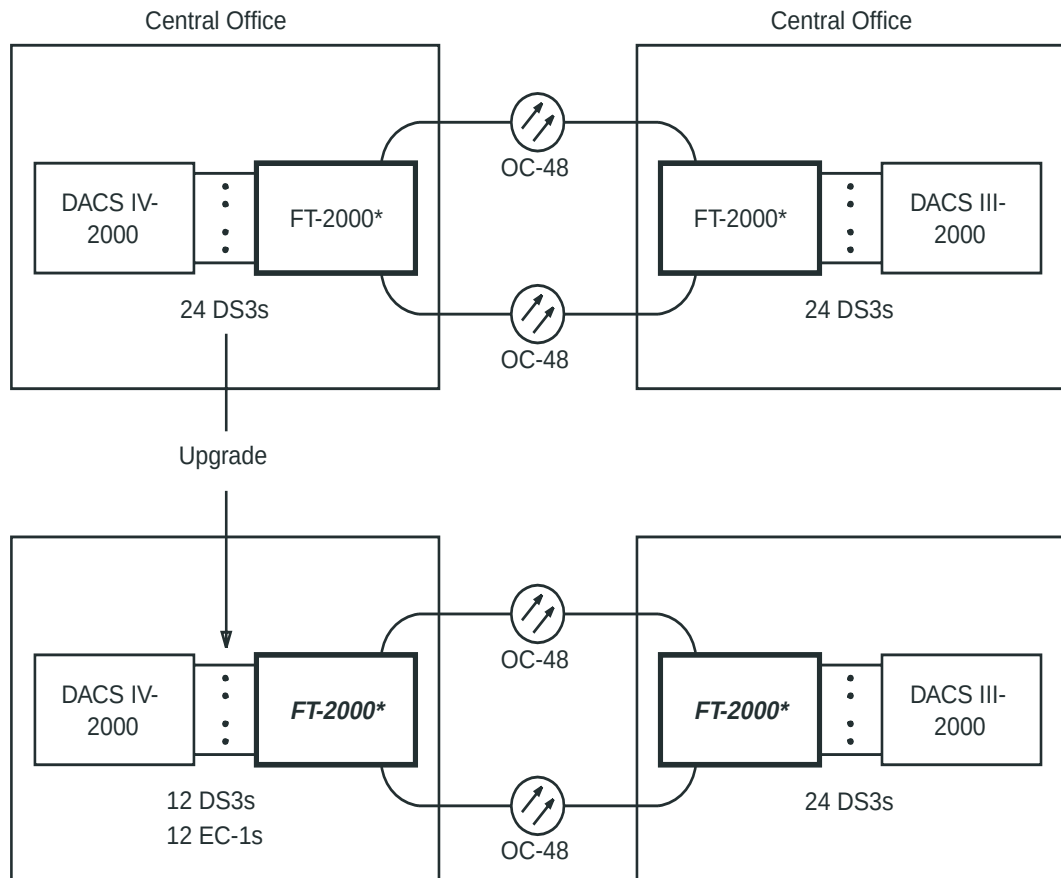


Figure 2-4. End-to-End Interface Mixing Application

The FT-2000 OC-48 Add/Drop-Rings Terminal supports the following combinations of low-speed interfaces concurrently between nodes in a network:

- Three DS3 or EC-1 signals at one end (or from various nodes) and one OC-3 signal at the other end
- Three DS3 signals at one end (or from various nodes) and three EC-1 signals at the other end (or at various nodes)
- Four OC-3s at one end and one OC-12 at the other end
- One OC-3 at one end and one IS-3 at the other end

Limited STS-1 cross-connections allow three DS3 or EC-1 circuits from various nodes in a network to be routed to a single destination and output as an OC-3 signal. In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal also has an STS-1 TSA capability that allows STS-1 cross-connections. The STS-1 cross-connections allow individual DS3 or EC-1 circuits from various nodes in a network to be routed to a single destination node. For more information about cross-connections, refer to Volume I, Section 8, "Administration and Provisioning."

OC-48 Hubbing

The FT-2000 OC-48 Add/Drop-Rings Terminal supports OC-48 hubbing. An FT-2000 OC-48 Add/Drop-Rings Terminal can serve a cluster of DDM-2000 OC-3 Multiplexers located at remote sites in the loop environment. Figure 2-5 shows an FT-2000 OC-48 Add/Drop-Rings Terminal serving as an OC-48 hub. The OC-48 hub supplies OC-3 optical extensions and local DS3 and/or EC-1 low-speed interfaces. All the traffic for the DDM-2000 OC-3 Multiplexers passes through the OC-48 hub using the OC-3 optical extensions.

The FT-2000 OC-48 Add/Drop-Rings Terminal provides single-ended operation capabilities through the OC-3 data communications channel (DCC). The OC-12 also provides this same DCC capability. For more information about the single-ended operation capabilities, refer to the "Operations Interworking" application in this section. For more information about operations interworking between the FT-2000 OC-48 Lightwave System and the DDM-2000 Multiplexers, refer to 824-102-147, *2000 Product Family Operations Interworking Guide*.

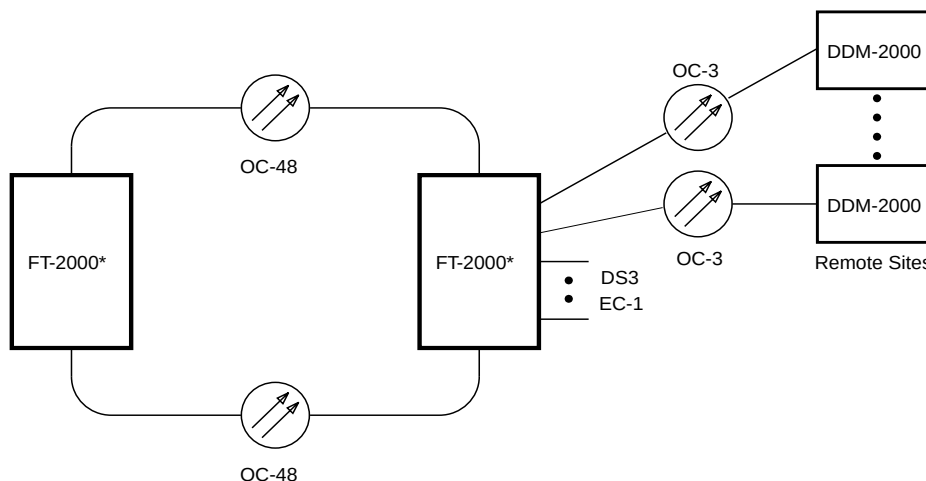


Figure 2-5. OC-48 Hubbing Application

Dual Ring Interworking (DRI)

The DRI application consists of two ring networks that are interconnected by two nodes in each ring. Figure 2-6 shows an example of the DRI application with Ring A and Ring B.

- Ring A is a line-switched ring equipped with FT-2000 OC-48 Add/Drop-Rings Terminals.
- Ring B may be any of the following:
 - Another line-switched ring equipped with FT-2000 OC-48 Add/Drop-Rings Terminals
 - A path-switched ring equipped with DDM-2000 OC-3 Multiplexers
 - A path-switched ring equipped with DDM-2000 OC-12 Multiplexers
 - A path-switched ring equipped with *SLC*[®]-2000 Access System OC-3 Multiplexers.

The two rings may be electrically connected (DS3 and/or EC-1) and/or optically connected using OC-3 or OC-12 interfaces.

The DRI application protects a circuit with one termination in one ring and the other termination in another ring if one of the interconnecting nodes fails or a connection between two interconnecting nodes fail. For each circuit with a termination in both rings, one FT-2000 OC-48 Add/Drop-Rings Terminal is designated as the primary node and one FT-2000 OC-48 Add/Drop-Rings Terminal is designated as the secondary node.

The primary and secondary nodes may be designated on an STS-3 or STS-1 tributary basis. The primary node for one tributary may be the secondary node for another tributary. A DRI circuit must be established using STS-3 or STS-1 cross-connections so that traffic is routed from the source node in the ring to the primary node. From the primary node, the traffic is bridged to the secondary node. For example, in Figure 2-6, if traffic is routed from Node 0 in Ring A to Node 5 and bridged to Node 4, Node 5 is the primary node and Node 4 is the secondary node. Different types of cross-connections are required at the primary and secondary nodes. (The primary and secondary nodes are not required to be adjacent nodes.) For more information about the cross-connections required at the primary and secondary nodes for DRI protection switching, refer to Volume I, Section 9, "Maintenance Description."

In a DRI application with two line-switched rings equipped with FT-2000 OC-48 Add/Drop-Rings Terminals, the primary node in one ring may be connected to the primary node in the other ring. For example, in Figure 2-6, Node 5 is the primary node in Ring A and Node 1 is the primary node in Ring B.

**NOTE:**

If the primary node in one ring is connected to the secondary node in the other ring, the DRI-provisioned circuit will be protected against a failure of the low-speed cross-connected circuit. However, the DRI-provisioned circuit may not be protected if one of the DRI-provisioned nodes becomes isolated from the ring (for example, node power failure or open fibers on both sides).

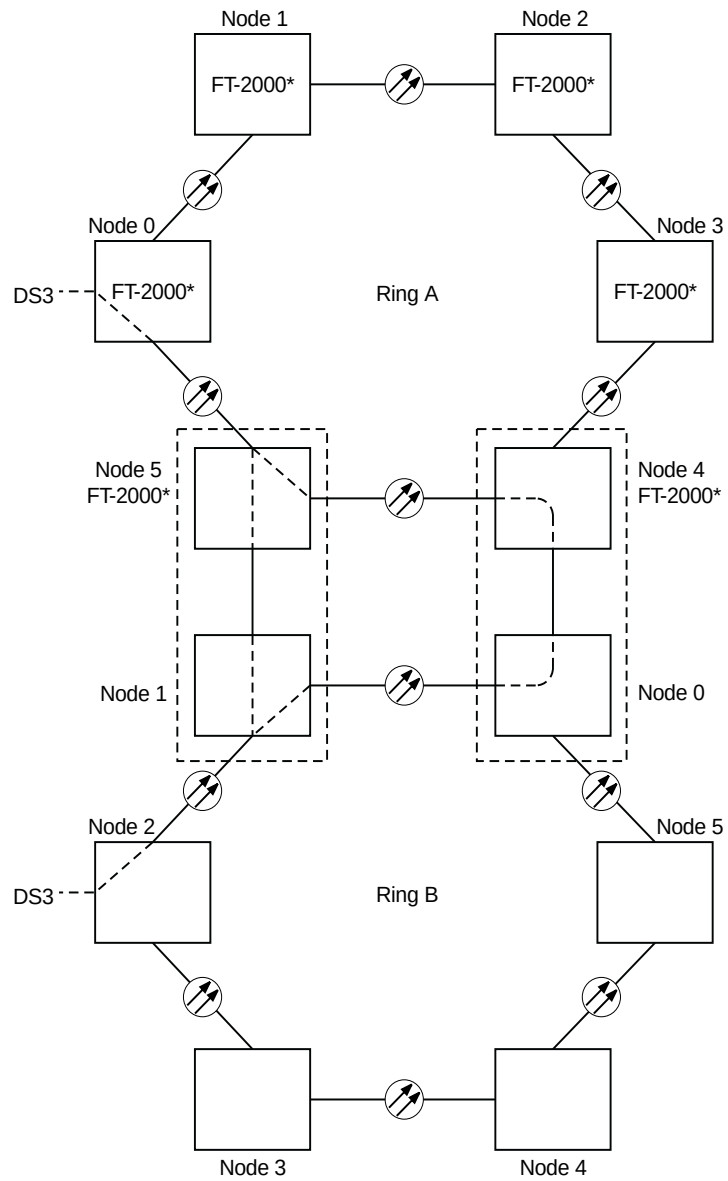


Figure 2-6. Dual Ring Interworking Application

**CAUTION:**

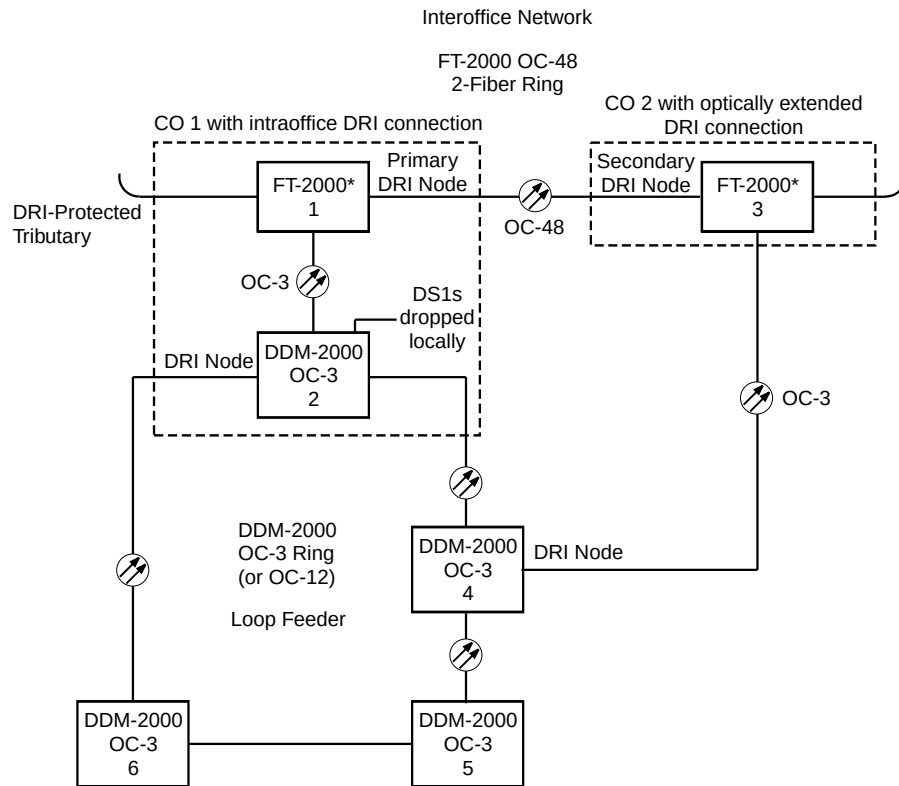
The primary node should be connected directly to a node in the other ring. No intervening transmission equipment (for example, DACS III-2000 Cross-Connect System) should be present. If intervening transmission equipment is present and a loss-of-signal (LOS) condition occurs, the intervening equipment may transmit alarm indication signals (AIS) toward the primary node. In Releases 7.0 and earlier, an incoming AIS will falsely NOT trigger a DRI protection switch causing service to be dropped. With Releases 7.1 and later, an AIS will cause a DRI protection switch. The secondary node can be connected through intervening transmission equipment to a node in the other ring.

Figure 2-7 shows a DRI application using OC-3 interfaces between a line-switched ring equipped with FT-2000 OC-48 Add/Drop-Rings Terminals and a path-switched ring equipped with DDM-2000 OC-3 Multiplexers. There is a direct intra-office connection between the primary node in the FT-2000 OC-48 two-fiber ring and Node 2 in the DDM-2000 ring. There is also a direct interoffice connection, using an OC-3 optical extension, between the secondary node in the FT-2000 OC-48 two-fiber ring and Node 4 in the DDM-2000 ring.

The DRI application supports synchronous optical network (SONET) path connectivity between rings with EC-1 and OC-3 interfaces. For example, a DS3 signal may enter a node in a ring, be transported from the primary node to the other ring as a DS3-based EC-1 signal, then emerge from a node in the other ring in the original DS3 format. A line-switched ring equipped with FT-2000 OC-48 Add/Drop-Rings Terminals may also accept VT1.5-based EC-1 signals from a path-switched ring equipped with DDM-2000 Multiplexers. This allows VT1.5 level cross-connections in the path-switched ring. The OC-3 interfaces may be used between rings to transport DS3-based and Virtual Terminal (VT)1.5-based EC-1 signals, or STS-3c signals in broadband service applications. DS3 interfaces may be used between rings; however, DS3 interfaces do not support SONET path connectivity between rings. DS3 interfaces terminate the SONET path.

A circuit with one termination in the line-switched ring and the other termination in the other ring (path-switched ring or another line-switched ring) is protected by two identical input low-speed signals and two identical output low-speed signals. In the line-switched ring, the primary node and secondary node accept an identical low-speed signal from the other ring. The primary node selects the low-speed signal from the other ring as the active low-speed signal and the low-speed signal from the secondary node as the standby low-speed signal.

The primary and secondary nodes of the line-switched ring also provide two identical output low-speed signals to the other ring. The primary node bridges the STS-3 or STS-1 tributary using its drop and continue capability to the secondary node.



★ FT-2000 OC-48 Add/Drop-Rings Terminal (Release 5 and later)

nc-ft2000-029

Figure 2-7. Dual Ring Interworking Application with the DDM-2000 OC-3 Multiplexer

In Release 4.1 and later releases, if an incoming DS3 or EC-1 LOS is detected at the primary node as a result of an incoming signal failure or an interconnecting node failure in the other ring, the primary node performs an automatic DRI protection switch. The primary node selects the low-speed signal from the secondary node as the active low-speed signal, completing the circuit between the secondary node and the terminating node. In Release 5 and later releases, an unprotected incoming OC-3 LOS or loss of frame (LOF) also triggers a DRI protection switch. With Release 7.1 and later releases, an AIS-P will result in a DRI protection switch.

In Releases 4.1 through 6.0.2, the DRI cross-connection switch takes place at the STS-3 level. Therefore, if one of the three incoming DS3 or EC-1 low-speed signals in an STS-3 tributary fails, the DRI protection switch affects all three low-speed signals of an STS-3 tributary. In Release 7 and later releases, the DRI protection switch may take place at the STS-1 level. Therefore, if one of the three incoming DS3 or EC-1 low-speed signals fails in an STS-3 tributary, the DRI protection switch will only affect the failed STS-1 tributary.

The primary node does not monitor the condition of the incoming low-speed signal at the secondary node before performing a DRI protection switch. In Release 6 and later releases, the DRI protection switch time is less than 100 milliseconds for unprotected (0x1) OC-3 interfaces and less than 200 milliseconds for unprotected failures in 1+1 protected OC-3 interfaces. For more information about DRI protection switching, refer to Volume I, Section 9, "Maintenance Description."

If the primary node in the line-switched ring fails, the FT-2000 OC-48 Add/Drop-Rings Terminals adjacent to the primary node perform two-fiber ring protection switching. Traffic is rerouted to/from the termination node in the line-switched ring through the secondary node to the other ring.

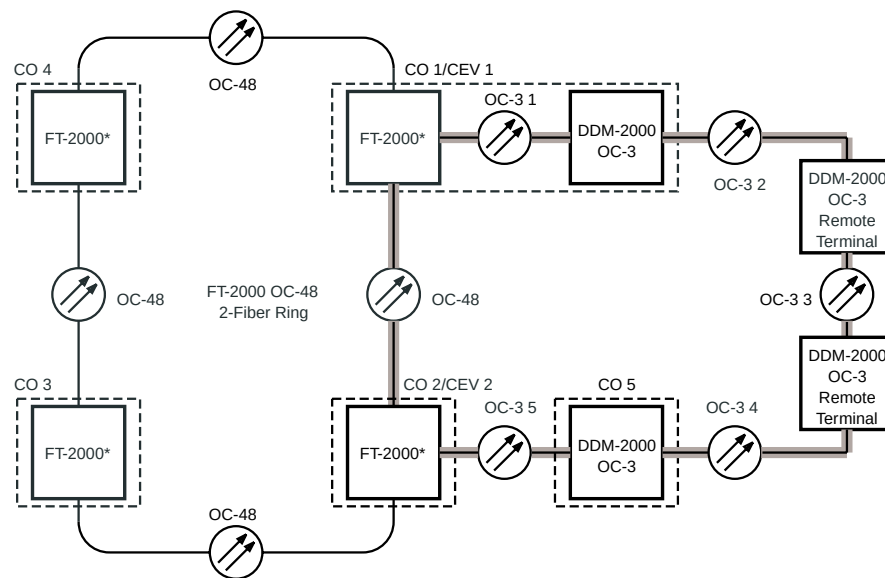
If the secondary node in the line-switched ring fails, the FT-2000 OC-48 Add/Drop-Rings Terminals adjacent to the secondary node perform two-fiber ring protection switching. However, traffic continues to be routed to/from the termination node in the line-switched ring through the primary node to the other ring. For information about two-fiber ring protection switching, refer to Volume I, Section 9, "Maintenance Description."

Cross-connections can be made, using the CIT or message-based operations system interface. The message-based operations system interface uses X.25 protocol and TL1 messages. For more information about cross-connections, refer to Volume I, Section 8, "Administration and Provisioning."

DDM-2000 OC-3 Ring Transport

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 5.0.1-ADR or later software can provide transport within a DDM-2000 OC-3 Ring. A DDM-2000 OC-3 Ring is an end-to-end self-healing network providing path-switched protection of DS1, DS3, EC-1, or OC-3 signals.

Figure 2-8 shows an OC-48 Ring equipped with FT-2000 OC-48 Add/Drop-Rings Terminals that are used to close (or complete) a DDM-2000 OC-3 Ring. The OC-48 Ring provides the needed bandwidth (three STS-1 tributaries) between CO 1 and CO 2. The DDM-2000 OC-3 Ring has four DDM-2000 OC-3 nodes and four OC-3 signals (OC3 1, OC3 2, OC3 3, OC3 4) connecting them. The OC-3 1 signal is transported between CO 1 and CO 2 by the FT-2000 OC-48 Add/Drop-Rings Terminals. One or more OC-48 Ring segments or rings may be used as segments of a DDM-2000 OC-3 Ring.



The grey shading shows the extent of the OC-3 ring.

Figure 2-8. DDM-2000 OC-3 Ring Transport Application

The FT-2000 OC-48 Add/Drop-Rings Terminals at CO 1 and CO 2 are equipped with OC-3 low-speed interfaces. The OC-3 low-speed interfaces may be used as intra-office interfaces or interoffice optical extensions.

In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal provides single-ended operation capabilities through the OC-3 DCC. For more information about the single-ended operation capabilities, refer to the “Operations Interworking” application in this section. For more information about operations interworking between the FT-2000 OC-48 Lightwave System and the DDM-2000 Multiplexers, refer to 824-102-147, *2000 Product Family Operations Interworking Guide*.

DDM-2000 OC-12 Ring Transport

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 7.1-ADR or later software can provide transport within a DDM-2000 OC-12 Ring. A DDM-2000 OC-12 Ring is an end-to-end self-healing network providing path-switched protection of OC-12 signals. Functionally, a DDM-2000 OC-12 Ring is identical to an OC-3 ring, except for 0x1 DCC support and capacity of the ring.

Dual-Wire Center Architecture

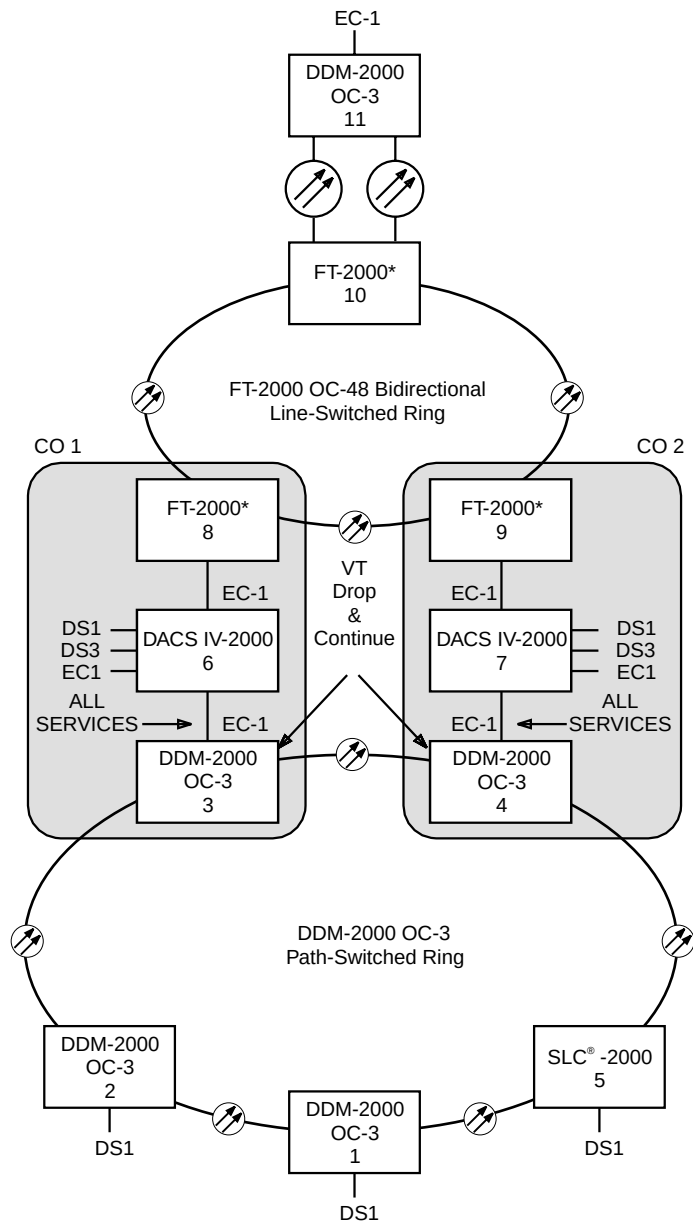
The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 4.0.0-ADR or 4.1.0-ADR software supports the dual-wire center architecture with EC-1 interfaces. The FT-2000 OC-48 ADD/Drop-Rings Terminal equipped with Release 5.0.1-ADR or later software supports the dual-wire center architecture with EC-1 and OC-3 interfaces (Figure 2-9).

A dual-wire center architecture is a self-healing network architecture that provides end-to-end survivable DS1 and DS3 business access services. Figure 2-9 shows two nodes in an OC-3 path-switched ring connected to two nodes in an OC-48 bidirectional line-switched ring with EC-1 interfaces and DACS IV-2000 Cross-Connect Systems. The EC-1 signals can contain a mixture of VT1.5 channels for all types of services. The capability to mix services, instead of having application-specific assignments for STS-1 tributaries, conserves the bandwidth of OC-3 access rings.

Traffic is routed from the terminating nodes in the DDM-2000 OC-3 path-switched ring to the interconnecting DDM-2000 OC-3 Multiplexers using two-way VT1.5 or STS-1 cross-connections. Drop and continue cross-connections are required at the interconnecting DDM-2000 OC-3 Multiplexers. Each interconnecting DDM-2000 OC-3 Multiplexer provides an EC-1 signal to a DACS IV-2000 Cross-Connect System.

Within the dual-wire center architecture, dual-wire center services and non-dual-wire center services can be mixed on an EC-1 signal from the DDM-2000 OC-3 Multiplexers (nodes 3 and 4) and sent to a DACS IV-2000 Cross-Connect System (nodes 6 and 7) to be VT-groomed (reassigned) for their destination. VT-grooming between rings by a DACS IV-2000 Cross-Connect System can optimize interoffice bandwidth usage. VT1.5 channels coming in to a DACS IV-2000 Cross-Connect System are groomed so that the outgoing facility

has a uniform and efficient fill. The DACS IV-2000 Cross-Connect System may groom VT1.5 channels from multiple OC-3 access rings. The DACS IV-2000 Cross-Connect System can also provide outputs for local traffic.



* FT-2000 OC-48 Add/Drop-Rings Terminal (Release 5 and later)

nc-ft2000-030

Figure 2-9. Dual-Wire Center Architecture Application

Each interconnecting FT-2000 OC-48 Add/Drop-Rings Terminal accepts an EC-1 signal from a DACS IV-2000 Cross-Connect System and routes the EC-1 signal to the terminating node. For example, in Figure 2-9, two separate STS-3 (Release 4.0 and later) or STS-1 (Release 7 and later) signals are routed from the interconnecting nodes (nodes 8 and 9) to the terminating node (node 10). In Release 4.0 and later releases, two separate STS-3 signals are routed using two-way STS-3 cross-connections at the FT-2000 OC-48 Add/Drop-Rings Terminals. In Release 7 and later releases, two separate STS-1 signals may be routed using two-way STS-1 cross-connections at the FT-2000 OC-48 Add/Drop-Rings Terminals. Cross-connections can be made, using the CIT or message-based operations system interface. The message-based operations system interface uses X.25 protocol and TL1 messages. For more information about cross-connections, refer to Volume I, Section 8, "Administration and Provisioning."

The dual-wire center architecture protects against facility failures, node failures, and interconnection failures. A failure in one ring or a single simultaneous failure in each ring is automatically healed and service is reestablished in ≤ 60 milliseconds (includes detection and switching times). This results in a highly reliable network.

Any failures on the OC-48 bidirectional line-switched ring are protected by the FT-2000 OC-48 Add/Drop-Rings Terminal using two-fiber ring protection switching. For more information about two-fiber ring protection switching, refer to Volume I, Section 9, "Maintenance Description."

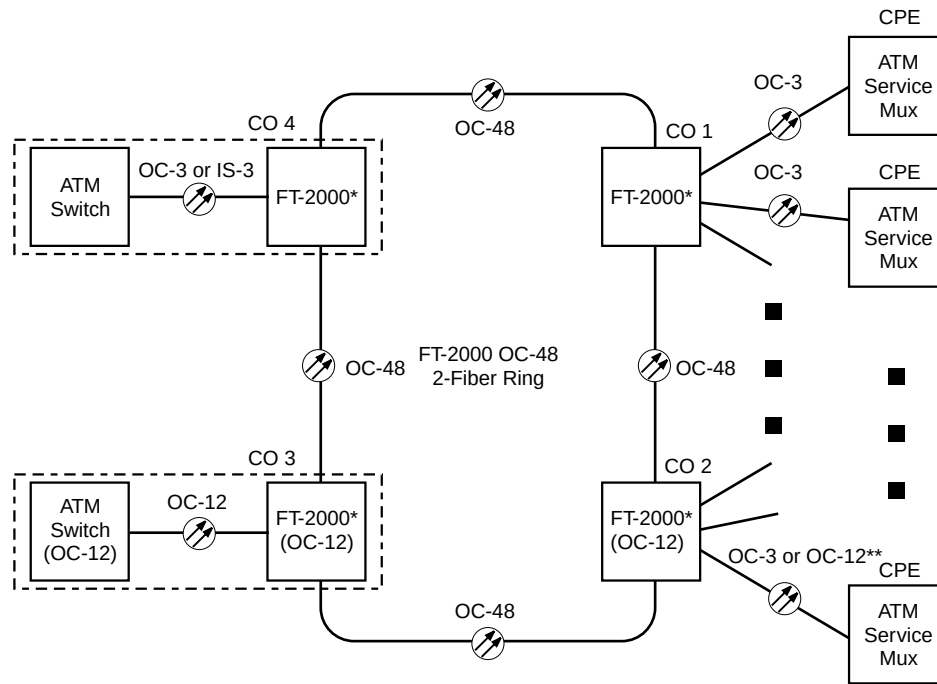
Any failures in the OC-3 path-switched rings or in the interconnections between the rings (for example, a DACS IV-2000 Cross-Connect System failure) are protected by the DDM-2000 Multiplexers using path-protection switching. For more information about the DDM-2000 Multiplexers, VT1.5 and STS-1 cross-connections, and path-protection switching, refer to 363-206-200, *DDM-2000 Multiplexer, Applications, Planning, and Ordering Guide*.

In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal provides single-ended operation capabilities through the OC-3 DCC. For more information about the single-ended operation capabilities, refer to the "Operations Interworking" application in this section. For more information about operations interworking between the FT-2000 OC-48 Lightwave System and the DDM-2000 Multiplexers, refer to 824-102-147, *2000 Product Family Operations Interworking Guide*.

Broadband Service Transport — STS-3c/STS-12c

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 5.0.1-ADR or later software and OC-3 low-speed interfaces supports the broadband service transport application. This allows the FT-2000 OC-48 Add/Drop-Rings Terminal to transport STS-3c payloads generated by asynchronous transfer mode (ATM) products and nonswitched services such as medical imaging. With Release 7.1-ADR software, OC-12 low-speed interfaces, and high-speed optics with STS-1 granularity, STS-12c payloads may be transported.

Figure 2-10 shows a two-fiber bidirectional ring equipped with FT-2000 OC-48 Add/Drop-Rings Terminals. The FT-2000 OC-48 Add/Drop-Rings Terminals are connected to ATM service multiplexers and switches with OC-3 low-speed interfaces. The FT-2000 OC-48 Add/Drop-Rings Terminals at CO 1 and CO 2 are optical hubs that provide OC-3/OC-12 optical extensions to ATM service multiplexers. The STS-3c payload may pass through other transmission equipment before reaching the FT-2000 OC-48 Add/Drop-Rings Terminal (for example, a DDM-2000 Multiplexer). It is anticipated that an STS-12c signal(s) will not pass through other transmission equipment before reaching the FT-2000 OC-48 ADR Terminal.



* FT-2000 OC-48 Add/Drop-Rings Terminal (Release 5 and later)

** FT-2000 OC-48 Add/Drop-Rings Terminal (Release 7.1.0 or later)

If an STS-12c signal is to be transported - then ALL nodes on the ring
MUST be equipped with (x5 or 4x) TRMTRs

nc-ft2000-032

Figure 2-10. Broadband Service Transport Application

The two-fiber ring is used to transport the ATM traffic from the optical hubs to the ATM switches at CO 3 and CO 4. The two-fiber ring is also used to interconnect the ATM switches. The STS-3c payload is routed through the OC-48 ring using STS-3 cross-connections (Release 5 and later) or STS-1 (Release 7 and later) cross-connections. (The three STS-1 cross-connections must be to the same STS-3 tributary.)

**CAUTION:**

Exercise caution when using STS-1 cross-connections to provision STS-3c/STS-12c circuits. It is possible to provision circuits with STS-1 cross-connections that do not properly carry STS-3c/STS-12c signals and are not reported or alarmed. For example, if the second and third STS-1 signals in an STS-3c signal are transposed at an add cross-connection or at a drop cross-connection, the resulting STS-3c signal will be corrupted but no conditions are reported or alarmed.

For more information about the STS-3c/STS-12c concatenated signals, refer to the "Concatenated Mode" part of Appendix A, "A SONET Overview." For more information about cross-connections, refer to Volume I, Section 8, "Administration and Provisioning."

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with OC-3 low-speed interfaces can also be provisioned to transport SONET STS-3 and STS-3c payloads or synchronous digital hierarchy (SDH) STM-1 AU-4 mapped payloads. This allows a direct interface to SDH equipment over an STM-1 optical connection. The OC3 circuit pack can automatically support transport of STS-3, STS-3c, or STM-1 signals carrying AU-4 mapped signals. For more information about provisioning an OC-3 low-speed interface, refer to the information describing the **CONFIGURATION - Set - OC3** input in Volume I, Section 8, "Administration and Provisioning."

Similarly, and in summary, the OC-12 low-speed interface can transport a single SONET STS-12c signal, a combination of STS-1 and STS-3c signals, or an SDH type STM-4 signal.

In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal provides single-ended operation capabilities through the OC-3 DCC. For more information about the single-ended operation capabilities, refer to the "Operations Interworking" application in this section. For more information about operations interworking between the FT-2000 OC-48 Lightwave System and the DDM-2000 Multiplexers, refer to 824-102-147, *2000 Product Family Operations Interworking Guide*.

STS-1/STS-3 Non-Preemptible Protection Access

This feature is available with FT-2000 OC-48 ADR Release 7.1 and later software. More specifically, this feature is applicable for FT-2000 OC-48 ADR Terminal ring configurations that are equipped with STS-1 granularity hardware (for example, 739 transmitter and 839B5 receiver) at all ring nodes.



NOTE:

For rings that are not equipped to support STS-1 granularity—then non-preemptible protection access is supported at the STS-3 level. This means that three STS-1 tributaries must be selected at a time. On a comparative basis (STS-1 versus STS-3) it should be evident that the STS-1 level gives the ability to use the available bandwidth more efficiently. Bandwidth efficiency is even more important when mixing signals from different services.

This feature permits the use of idle protection capacity to carry “extra traffic” on an unprotected/non-preemptible basis. The traffic carried by non-preemptible tributaries is not dropped (preempted) by any ring loopback protection switch. For a two-fiber ring it is important to realize that whenever a protection tributary is selected for non-preemptible protection access, the corresponding STS-3 tributary of service capacity on ALL spans of the ring becomes unprotected. This results in two unprotected/non-preemptible tributaries on all spans of the ring; both the service tributary (STS-3 #n) and the protection tributary (STS-3 #n+8).



NOTE:

The capability to select non-preemptible protection access on a per individual span basis is made very useful in four-fiber rings by their ability to provide per-span protection switching. Service on other spans can still be protected. This gives four-fiber rings an advantage over two-fiber rings in all applications where the need for non-preemptible capacity is to carry add/drop traffic on individual spans, part-of-the-way around the ring.

In terms of user administration, there are two “protection access states” associated with a tributary being used for Non-Preemptible Protection Access. These two “protection access states” are:

- Temporary, and
- Permanent.



NOTE:

With these two additions there are now a total of three protection access states (preemptible, temporary, and permanent).

It should be emphasized that the primary need for this feature is to increase the span capacity and thus increase bandwidth efficiency for “Path-in-Line” applications. As a review, a “Path-in-Line” application carries path-switched ring traffic within the FT-2000 line-switched ring. Tributaries of the FT-2000 ring provide transport between DDM-2000 OC-3 and/or SLC-2000 ring nodes. The path-switched ring protects traffic with dual-fed signals. The node that originates the traffic sends it in both directions (East and West) from the node where the traffic enters the ring. The node where the traffic exits the ring selects one of the two received signals.

“Path-in-Line” applications are always administered as “Permanent”. Other examples of these types of applications include:

- **Real-Time Networks**—that support time-sensitive control functions.
- **ATM and Bridge/Router Traffic**—for this application customers may already provide their own rerouting, in case of failure, and may not need FT-2000 protection switching.
- **Video Transport**—which sometimes requires non-preemptible transport without protection.
- **Dual-Wire Center Architecture**—a self healing network that provides end-to-end survivable DS1 and DS3 business access services.

An application that uses the “Temporary” protection access state is in support of DACS-based network restoration. For this application the service traffic is temporarily made unprotected so that a restoration can use protection access that is non-preemptible.

One-Way Video Distribution

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 5.0.1-ADR or later software can be used in 1-way video distribution applications. The FT-2000 OC-48 Add/Drop-Rings Terminal is used to distribute video signals from one or more video servers to video serving areas (Figure 2-11).

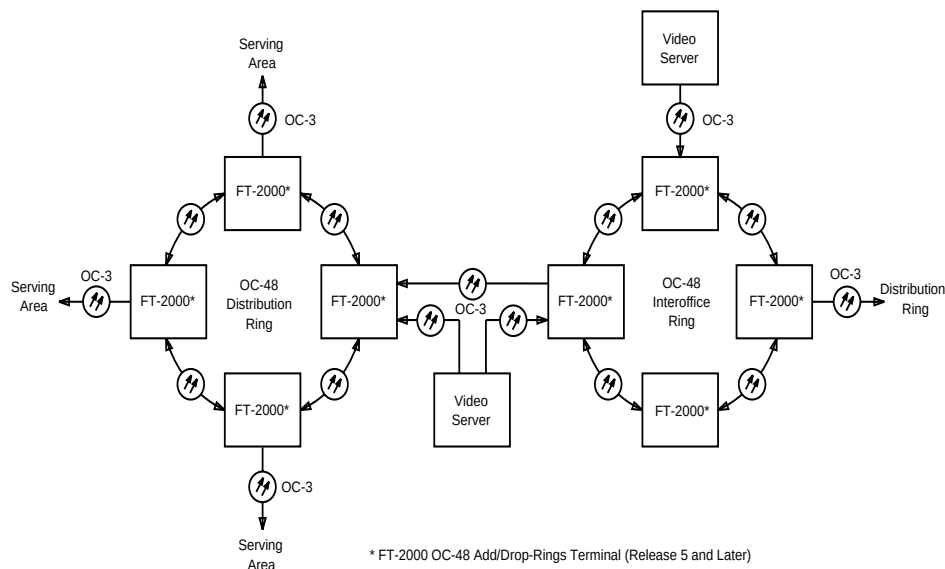


Figure 2-11. One-Way Video Distribution Application

The FT-2000 OC-48 Add/Drop-Rings Terminal accepts up to 16 unprotected (0x1) OC-3c signals carrying broadcast television and interactive multimedia television (IMTV) services for transport to the serving areas. The OC-3c signals are fully protected within the two-fiber bidirectional ring. Up to eight OC-3c signals may be cross-connected to OC-48 high-speed line 1E, and up to eight OC-3c signals may be cross-connected to OC-48 high-speed line 1W. For more information about two-fiber ring protection switching, refer to Volume I, Section 9, "Maintenance Description."

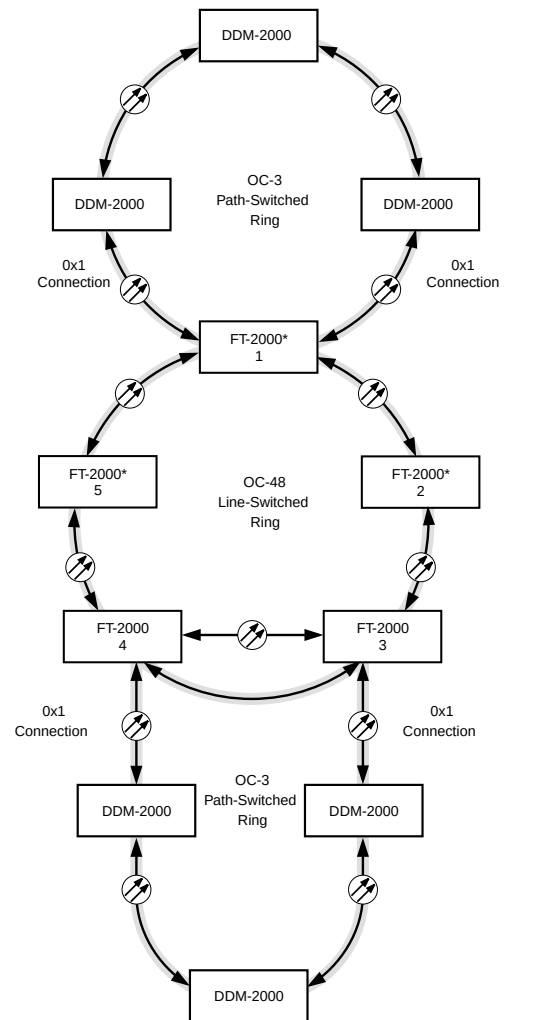
All cross-connections in this video application are 1-way STS-3 cross-connections from the video server to the customer. Because the corresponding return path has no input signal, the FT-2000 OC-48 Add/Drop-Rings Terminal reports a trouble condition. In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal may be provisioned to disable reporting this trouble condition. For more information about provisioning, refer to Volume I, Section 8, "Administration and Provisioning," and the "Operation and Maintenance (TOP)" section (Volume II).

In Release 5, the OC-3 interface should be used where the OC-3 section DCC and integrated single-ended operations, via an OC-3 line, are not required. In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal supports the OC-3 section DCC and integrated single-ended operations with other FT-2000 OC-48 Add/Drop-Rings Terminals, via an OC-3 line. For more information about the single-ended operation capabilities, refer to the "Operations Interworking" application in this section.

For more information about operations interworking between the FT-2000 OC-48 Lightwave System and the DDM-2000 Multiplexers, refer to 824-102-147, *Lucent Technologies 2000 Product Family Operations Interworking Guide*.

Path-In-Line Architecture

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 6.0.0-ADR or later software supports the path-in-line architecture (Figure 2-12). The path-in-line architecture allows part of the bandwidth of an OC-48 bidirectional line-switched ring to be part of an OC-3 path-switched ring. Similarly, with Release 7.1 or later software, this architecture allows part of the bandwidth of an OC-48 bidirectional line-switched ring to be part of an OC-12 path-switched ring.



* FT-2000 OC-48 Add/Drop-Rings Terminal (Release 6 and later)
The grey shading indicates the extent of OC-3 ring.

nc-ft2000-033

Figure 2-12. Path-In-Line Architecture

The path-in-line architecture is a self-healing network architecture that provides end-to-end survivable DS1 and DS3 services. Figure 2-12 shows an OC-3 path-switched ring using part of the bandwidth of an OC-48 bidirectional line-switched ring. The OC-3 interfaces can contain a mixture of VT1.5 and STS-1 channels for all types of services. The capability to mix services, instead of having application-specific assignments for STS-1 tributaries, conserves the bandwidth of OC-3 access rings.

Traffic is routed from the terminating node in the DDM-2000 OC-3 path-switched ring to the interconnecting DDM-2000 OC-3 Multiplexers using two-way add/drop and pass-through cross-connections. Unprotected 0x1 connections are supported between the OC-3 path-switched rings and the OC-48 bidirectional line-switched ring.

Each interconnecting FT-2000 OC-48 Add/Drop-Rings Terminal accepts one OC-3 signal or two OC-3 signals. Traffic is routed through the OC-48 bidirectional line-switched ring using two-way STS-3 cross-connections at the FT-2000 OC-48 Add/Drop-Rings Terminals.

Cross-connections can be made using the CIT or message-based operations system interface. The message-based operations system interface uses X.25 protocol and TL1 messages. For more information about cross-connections, refer to Volume I, Section 8, "Administration and Provisioning."

The path-in-line architecture protects against facility failures, node failures, and interconnection failures. A failure in one ring or a single simultaneous failure in each ring is automatically healed and service is reestablished in ≤ 60 milliseconds (includes detection and switching times). This results in a highly reliable network.

Any failures on the OC-48 bidirectional line-switched ring are protected by the FT-2000 OC-48 Add/Drop-Rings Terminal using two-fiber ring protection switching. For more information about two-fiber ring protection switching, refer to Volume I, Section 9, "Maintenance Description."

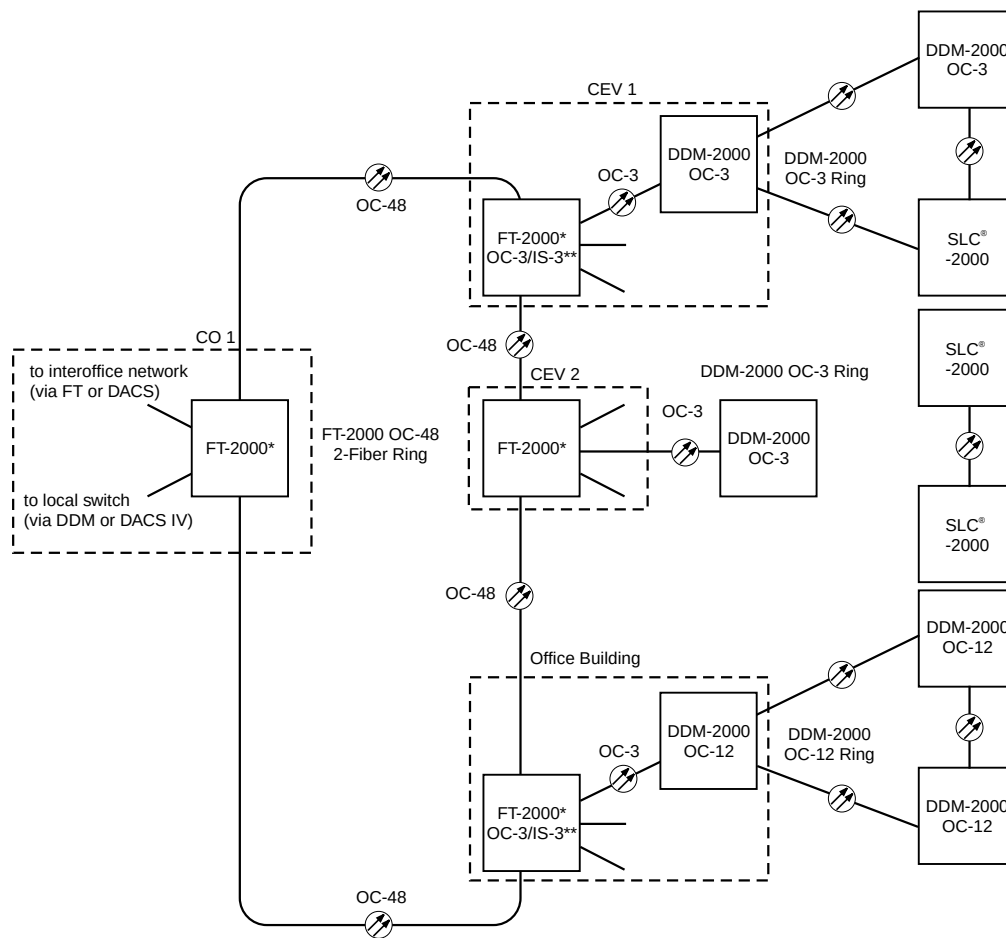
Any failures in the OC-3 path-switched rings or in the interconnections between the rings are protected by the DDM-2000 Multiplexers using path-protection switching. For more information about the DDM-2000 Multiplexers, VT1.5 and STS-1 cross-connections, and path-protection switching, refer to 363-206-200, *DDM-2000 Multiplexer, Applications, Planning, and Ordering Guide*.

In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal provides single-ended operation capabilities through the OC-3 DCC. For more information about the single-ended operation capabilities, refer to the "Operations Interworking" application in this section.

For more information about operations interworking between the FT-2000 OC-48 Lightwave System and the DDM-2000 Multiplexers, refer to 824-102-147, *Lucent Technologies 2000 Product Family Operations Interworking Guide*.

Loop Feeder Network

The FT-2000 OC-48 Add/Drop-Rings Terminal, equipped with Release 5.0.1-ADR or later software, can be used in a loop feeder network with OC-3 low-speed interfaces. Figure 2-13 shows a two-fiber ring used to connect one or more central offices to one or more DDM-2000 Multiplexers at remote sites.



* FT-2000 OC-48 Add/Drop-Rings Terminal (Release 5 and later)

** FT-2000 ADR Release 7.1.0 and later support the use of IS-3

nc-t2000-031

Figure 2-13. Loop Feeder Network Application

The FT-2000 OC-48 Add/Drop-Rings Terminal must be located in a controlled environment facility (for example, a controlled environment vault [CEV], temperature controlled hut, or central office).

Figure 2-13 shows the following OC-3 connections between the DDM-2000 Multiplexers and an FT-2000 OC-48 Add/Drop-Rings Terminal equipped with OC-3 low-speed interfaces:

- OC-3/IS-3 intra-office connection to a local DDM-2000 OC-3 Multiplexer (CEV 1)
- OC-3 interoffice connection to a remote DDM-2000 OC-3 Multiplexer (CEV 2)
- OC-3/IS-3 intra-office connection to a local DDM-2000 OC-12 Multiplexer (office building).

An STS-3 payload can enter the two-fiber ring via an OC-3 interface and exit the two-fiber ring via an OC-3 interface or a mix of DS3 and EC-1 interfaces at another node.

The OC-3 signal or the available DS1 timing outputs and inputs can be used to distribute network timing from the FT-2000 OC-48 Add/Drop-Rings Terminal to the DDM-2000 Multiplexers and SLC[®]-2000 access systems.

In Release 5, the OC-3 interface should be used where the OC-3 section DCC and integrated single-ended operations with the DDM-2000 Multiplexers are not required. In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal supports the OC-3 section DCC and integrated single-ended operations with the DDM-2000 Multiplexers. For more information about the single-ended operation capabilities, refer to the "Operations Interworking" application in this section.

For more information about operation interworking between the FT-2000 OC-48 Lightwave System and the DDM-2000 Multiplexers, refer to 824-102-147, *2000 Product Family Operations Interworking Guide*.

Extra Traffic for Restoration and Revenue Generation

The FT-2000 OC-48 Add/Drop-Rings Terminal allows access to the idle protection STS-3 tributaries for low priority traffic or for restoration purposes.

Figure 2-14 shows how the FT-2000 OC-48 Add/Drop-Rings Terminal can be used with a DACS III-2000 and/or DACS IV-2000 Cross-Connect System and the *DACScan*®-2000 controller for restoration purposes. If a failure occurs between central offices, the *DACScan*®-2000 controller receives alarms and sends cross-connect commands to the DACS III-2000 and/or DACS IV-2000 Cross-Connect Systems in the network to reroute traffic around the failure. At the FT-2000 OC-48 Add/Drop-Rings Terminals, the idle protection STS-3 tributaries carry rerouted traffic until the failure is fixed and the regular service route is restored.

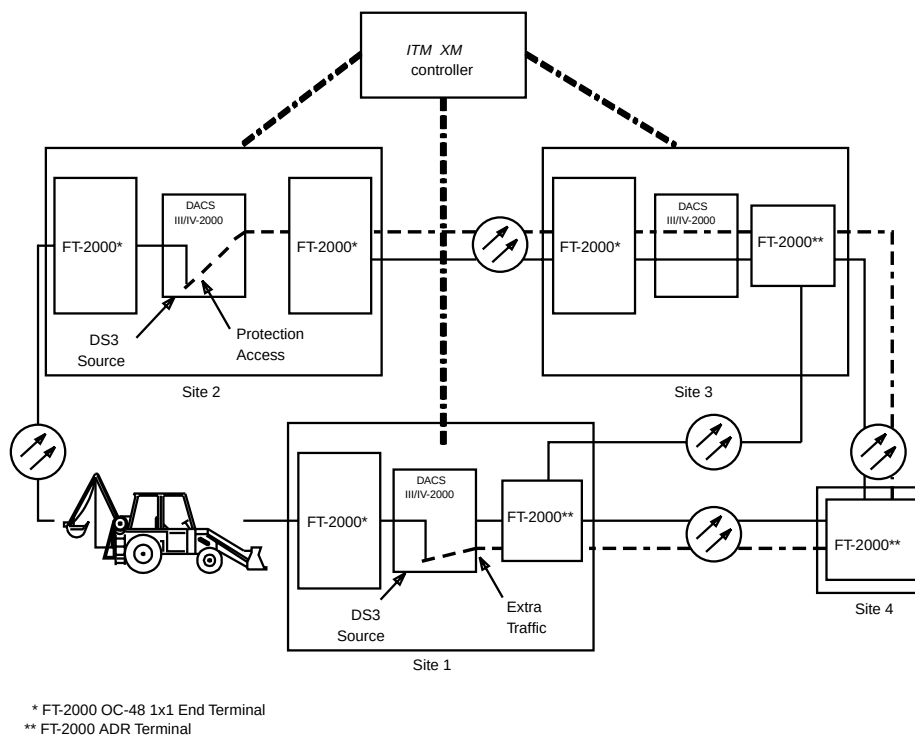


Figure 2-14. Extra Traffic (Protection Line Access) for Restoration and Revenue Generation Application

Extra traffic is established by cross-connecting low-speed service slots to protection STS-3 tributaries, using the CIT (locally or remotely). For the FT-2000 ADR Releases 1 through 7, "Extra traffic" can only be established on a preemptible basis.

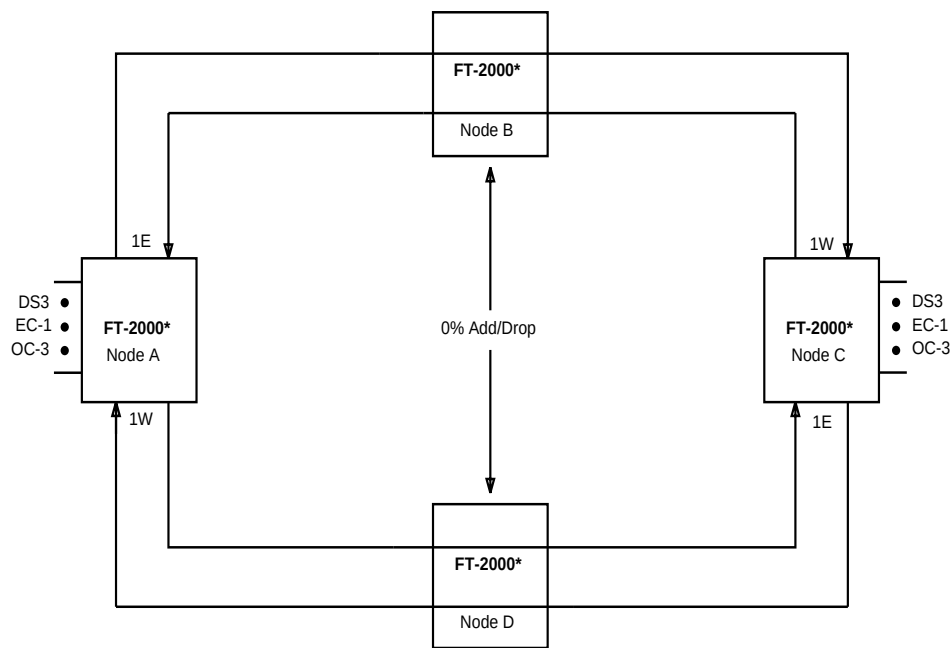
Preemptible means that a signal fail or signal degrade condition on the service STS-3 tributaries causes traffic to be automatically switched to the protection STS-3 tributaries, preempting extra traffic.

For the FT-2000 ADR Terminal Release 7.1.0 and later software, "Extra traffic" can be administered as either preemptible or non-preemptible. Non-preemptible means the "Extra traffic" carried on a protection slot cannot be preempted/dropped by a failure of one of the service slots. For more information about cross-connections and extra traffic, refer to Volume I, Section 8, "Administration and Provisioning."

0% Add/Drop

Figure 2-15 shows the FT-2000 OC-48 Add/Drop-Rings Terminal in a 0% add/drop application. In the 0% add/drop application, an FT-2000 OC-48 Add/Drop-Rings Terminal is not equipped with low speed interface circuit packs and acts as a repeater. Only 1-way and two-way through cross-connections are used at the 0% add/drop node.

Low-speed interface circuit packs may be added to the 0% add/drop node if add/drop traffic is required in the future. The FT-2000 OC-48 Add/Drop-Rings Terminal also allows access to the OC-48 DCC to perform operations, administration, maintenance, and provisioning at remote FT-2000 OC-48 Add/Drop-Rings Terminals in the ring. The FT-2000 OC-48 Repeater Bay does not allow access to the OC-48 DCC.



* FT-2000 OC-48 Add/Drop-Rings Terminal

Figure 2-15. 0% Add/Drop Application

FT-Lightwave Booster Amplifier (FT-LBA) for Extended Distances

The FT-LBA can be used with the FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 5.0.1-ADR or later software to extend the distance between sites (Figure 2-16). The FT-LBA is a miscellaneous-mounted shelf that provides optical amplifiers. The optical amplifiers supply 9, 12, or 16 dBm of optical output power.

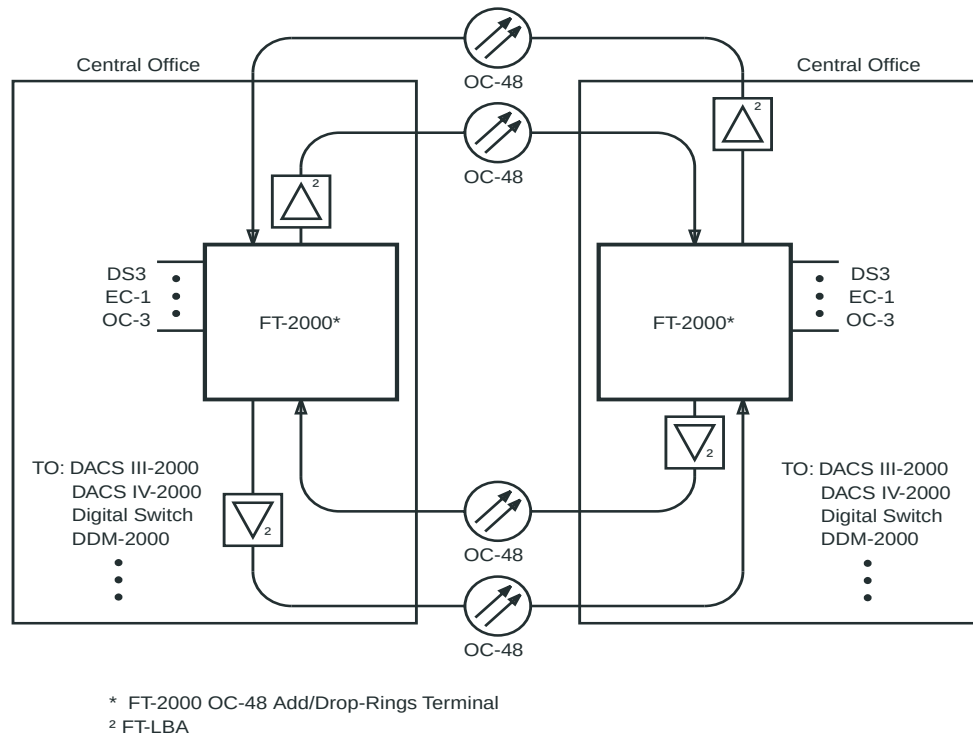


Figure 2-16. Operation with FT-Lightwave Booster Amplifier

The FT-2000 OC-48 Add/Drop-Rings Terminal and FT-LBA support span lengths up to 140 km using standard fiber or 160 km using dispersion-shifted fiber. This capability reduces the number of repeater sites on long routes. This capability also allows the FT-2000 OC-48 Add/Drop-Rings Terminal and FT-LBA to be used with poor fiber or fiber that has frequent, high-loss splices (for example, in urban areas) or with dispersion-shifted fiber and terrestrial electronics (for example, underwater/sea applications).

The FT-2000 OC-48 Add/Drop-Rings Terminal must be equipped with the OC48 Transmitter, Add/Drop — 1.5 μ m LBA Standard Performance (OC48 TRMTR [A/D 1.5 LBA]) circuit pack or the OC48 Transmitter, Add/Drop — 1.5 μ m Standard Performance (OC48 TRMTR [A/D 1.5 STD]) circuit packs when operating with the FT-LBA. In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal may be equipped with the OC48 Transmitter, Add/Drop — 1.5 μ m LBA Wavelength 1 (OC48 TRMTR [A/D 1.5 LBA W1]) and the OC48 Transmitter, Add/Drop — 1.5 μ m LBA Wavelength 2 (OC48 TRMTR [A/D 1.5 LBA W2]) circuit packs when operating with the FT-LBA.

In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal may be equipped with the OC48 TRMTR (A/D STS1 1.5 LBA), the OC48 TRMTR (A/D STS1 1.5 LBA, W1), and the OC48 TRMTR (A/D STS1 1.5 LBA, W2) circuit packs when operating with the FT-LBA.

In Release 6 and later releases, the FT-Lightwave Booster Amplifier (FT-LBA) can also be used with the FT-2000 OC-48 Repeater Bay equipped with Release 6.0.0-RPTR software to extend the distance between sites. The FT-2000 OC-48 Repeater Bay must be equipped with the OC48 Regenerator — 1.5 μ m Standard Performance [OC48 REGENR (1.5 LBA)], OC48 Regenerator — 1.5 μ m Standard Performance (OC48 REGENR [1.5 LBA W1]), or the OC48 Regenerator — 1.5 μ m Standard Performance (OC48 REGENR [1.5 LBA W2]) circuit packs when operating with the FT-LBA.

The integrated operation of the FT-2000 OC-48 Add/Drop-Rings Terminal and the FT-LBA is further supported by additional cabling to transport conditions from the FT-LBA to the FT-2000 OC-48 Add/Drop-Rings Terminal using the miscellaneous discrete interface. For more information about the miscellaneous discrete interface, refer to Volume I, Section 6, "Operations Interfaces."

For more information about loss budgets and transmission distances when operating with the FT-LBA, refer to Volume I, Section 10, "Technical Specifications." For more information about the FT-LBA, refer to 365-565-500, *Digital Transmission Systems FT-Lightwave Booster Amplifier Description, Engineering, Installation, and Maintenance*.

Wavelength Division Multiplexing (WDM)

The circuit-carrying capacity of lightguide cable can be increased by transmitting two signals of different wavelengths simultaneously over a single fiber. WDM is the combination of two lightwave signals at different wavelengths.

The device used to implement WDM for the FT-2000 OC-48 Lightwave System is called a WDM filter. Figure 2-17 is a high level diagram depicting how the WDM filters multiplex and demultiplex a single-mode 1.3 μm wavelength signal and a single-mode 1.5 μm wavelength signal, enabling the two signals to operate on the same fiber pair. Thus, one FT-2000 OC-48 Lightwave System and one FT Series G 1.7 Gb/s system or two FT-2000 OC-48 Lightwave Systems at the same location, each equipped for lightwave transmission at different wavelengths, can share the same fiber pair.

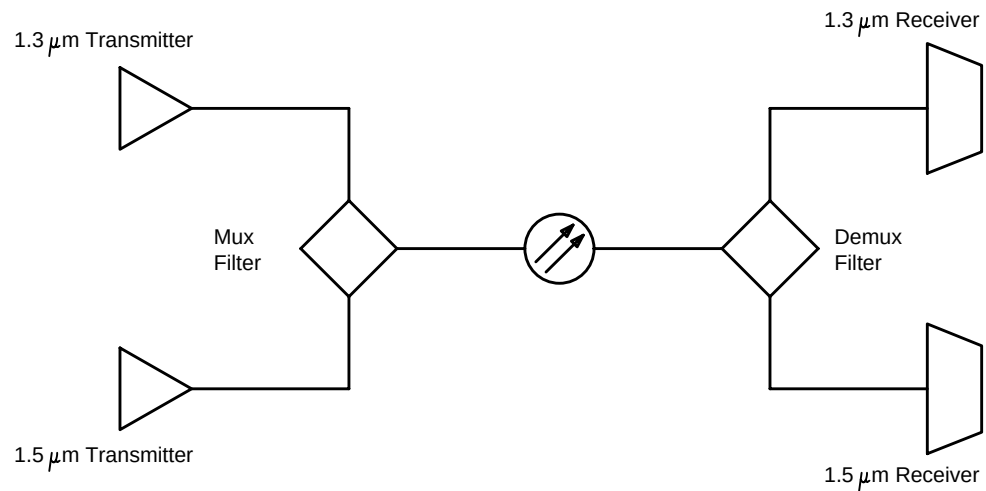


Figure 2-17. Wavelength Division Multiplexing Application (1.3 μm /1.5 μm)

Figure 2-18 shows WDM used in repeater sections.

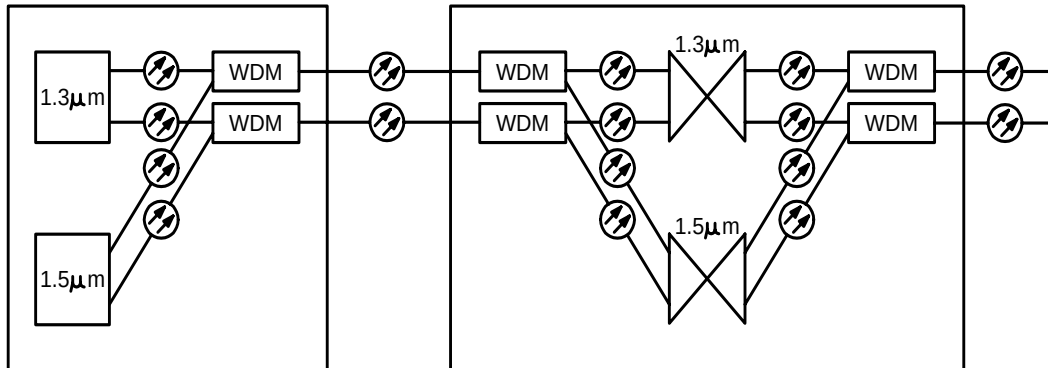


Figure 2-18. Section by Section Wavelength Division Multiplexing (1.3 μm/1.5 μm)

In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal may be equipped with the OC48 Transmitter, Add/Drop(A/D) — 1.5μm LBA Wavelength 1 (OC48 TRMTR [A/D 1.5 LBA W1]) and the OC48 Transmitter, Add/Drop(A/D) — 1.5μm LBA Wavelength 2 (OC48 TRMTR [A/D 1.5 LBA W2]) circuit packs in WDM applications. In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal may be equipped with the OC48 TRMTR (A/D STS1 1.5 LBA, W1) and the OC48 TRMTR (A/D STS1 1.5 LBA, W2) circuit packs in WDM applications. The 7A WDM Filter Assembly is recommended for use with these OC48 TRMTR circuit packs.

Figure 2-19 shows how the WDM filters multiplex and demultiplex two single-mode 1.5 μm wavelength signals with slightly different wavelengths. Thus, two FT-2000 OC-48 Lightwave Systems at the same location, each equipped for lightwave transmission at different wavelengths, can share the same fiber pair.

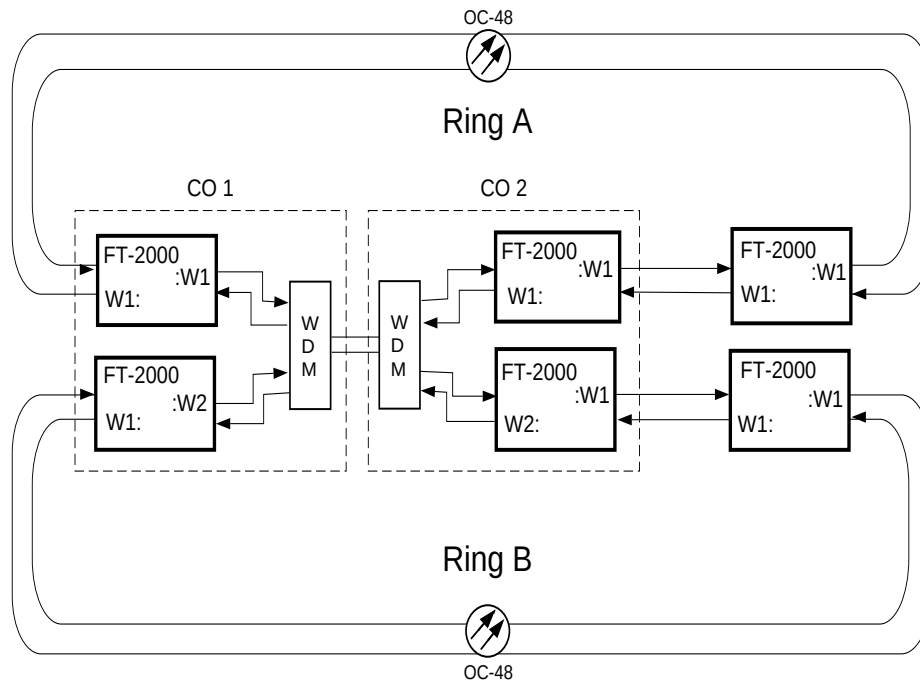


Figure 2-19. Wavelength Division Multiplexing Application (1.5 μm /1.5 μm)

In Release 6 and later releases, the FT-2000 OC-48 Repeater Bay may be equipped with the OC48 REGENR — 1.5 μm Standard Performance (OC48 REGENR [1.5 LBA W1]) and the OC48 Regenerator — 1.5 μm Standard Performance (OC48 REGENR [1.5 LBA W2]) circuit packs when using wavelength division multiplexing. The OC48 REGENR (1.5 LBA W1) circuit pack operates at the same wavelengths as the OC48 TRMTR (A/D 1.5 LBA W1) and OC48 TRMTR (A/D STS1 1.5 LBA, W1) circuit packs. The OC48 REGENR (1.5 LBA W2) circuit pack operates at the same wavelengths as the OC48 TRMTR (A/D 1.5 LBA W2) and OC48 TRMTR (A/D STS1 1.5 LBA, W2) circuit packs.

Figure 2-20 shows WDM used in repeater sections with FT-LBA.

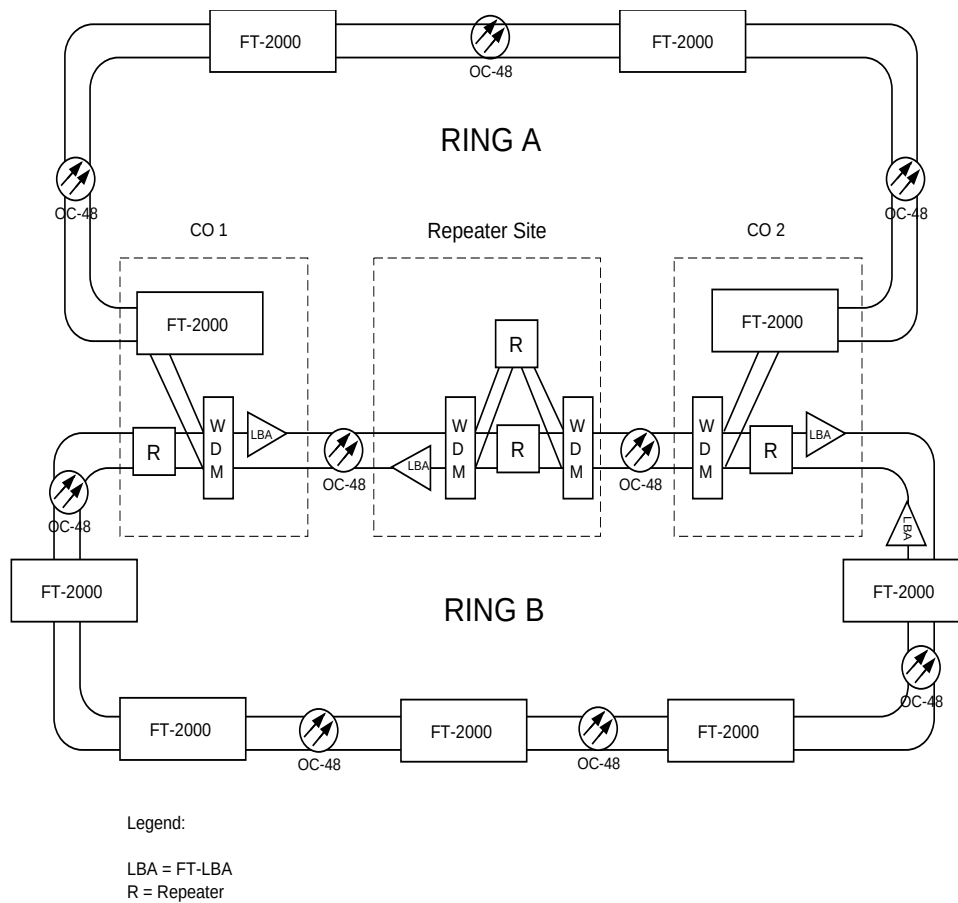


Figure 2-20. Section by Section WDM (1.5 μm /1.5 μm)



CAUTION:

In WDM applications with bidirectional OC-48 signal transmission over a single fiber, unprotected fiber or coupler failures are possible. If a fiber break or coupler failure occurs, sufficient light may be reflected from the break to the local receiver and cause the local receiver to frame on the signal transmitted by the local transmitter (optical loopback). If an optical loopback occurs, the failure would not be detected or alarmed and not protected. The probability of this condition occurring depends on the type of fiber break and the distance between the break and receiver.

When WDM is planned for a future addition to a span, the insertion loss of the WDM devices must be properly allocated in each outside plant repeater section cable loss design. For more information about loss budgets and WDM specifications, refer to Volume I, Section 10, "Technical Specifications."

For more information about operation with the FT-LBA, refer to the "FT-Lightwave Booster Amplifier (FT-LBA) for Extended Distances" application in this section.

Dense Wavelength Division Multiplexing (EON* Compatible)

DWDM is a technology that meets the customer's need for transporting more capacity per fiber. Specifically, this technology permits two or more OC-48 signals (each with a different wavelength) to be multiplexed together and simultaneously transmitted over a single fiber.

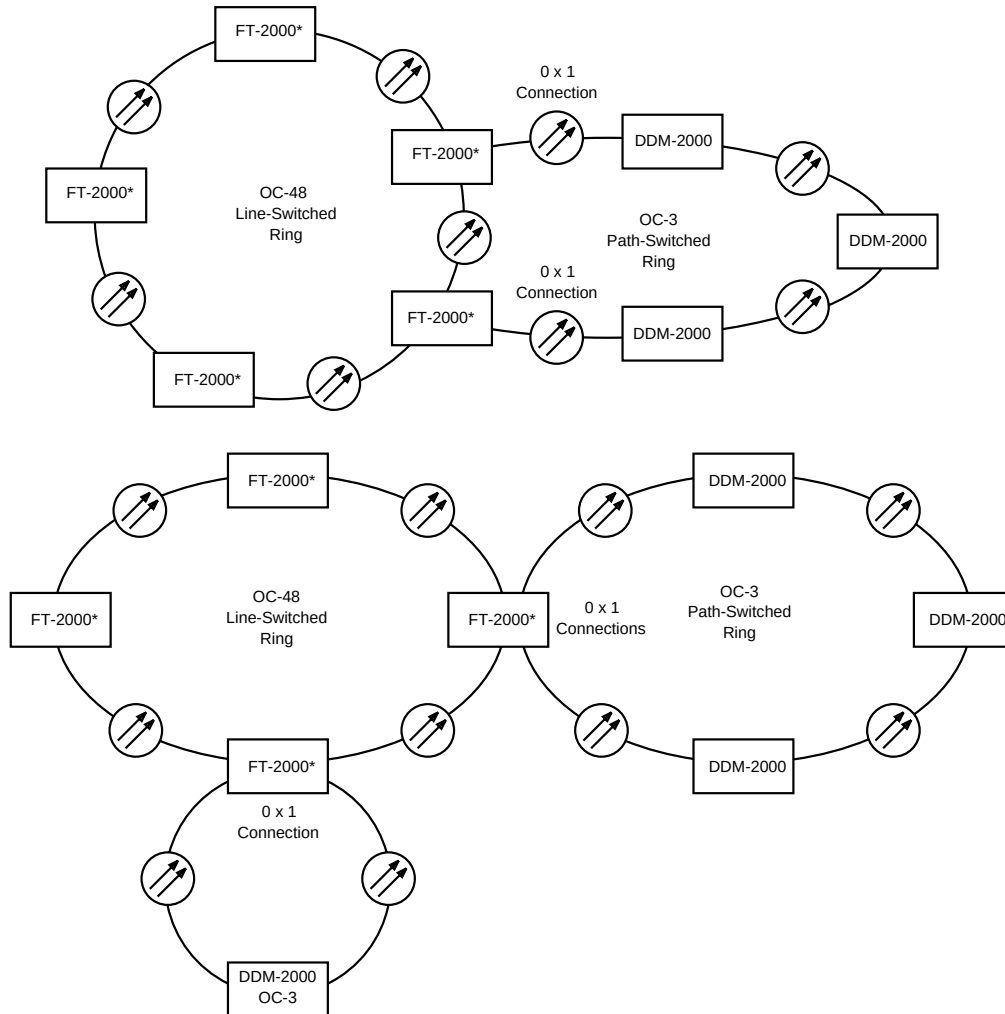
The actual multiplexing is accomplished within the Optical Line System (OLS). On the FT-2000 side, the 739E[1-16] transmitter circuit packs provide a direct interface to the OLS product. For FT-2000 Release 7.2.0, sixteen different wavelengths (739E1 through 739E16) are supported. This will permit the design of fiber routes that operate up to 40 Gb/s per fiber.

*

Metropolis Enhanced Optical Networking.

Operations Interworking

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 6.0.0-ADR or later software supports operations interworking applications with DDM-2000 Multiplexers (Figure 2-21).



* FT-2000 OC-48 Add/Drop-Rings Terminal (Release 6 and later)

Figure 2-21. Operations Interworking Application

Figure 2-21 shows two subnetworks with DCC connectivity within the subnetwork. The OC-3 path-switched rings use part of the bandwidth of the OC-48 bidirectional line-switched rings.

Operations interworking is available among systems that are connected through an optical interface. The following systems support operations interworking applications:

- FT-2000 OC-48 Add/Drop-Rings Terminal (Release 6 and later)
- DDM-2000 OC-3 Multiplexer (Releases 7.2 and 9 — Rings)
- DDM-2000 OC-3 Multiplexer (Release 8 — Linear)
- DDM-2000 OC-12 Multiplexer (Release 5 and later)
- DDM-2000 FiberReach (Release 1 and later).

For operations interworking between the FT-2000 OC-48 Add/Drop-Ring Terminal (Releases 6 and 7) and the DDM-2000 OC-3 Multiplexer, the routing domain and area fields of the network service access point (NSAP) code at the DDM-2000 OC-3 Multiplexer must be 0 (default value). In Releases 6 and 7, the routing domain and area fields of the FT-2000 OC-48 Add/Drop-Rings Terminal have a fixed value (0) and cannot be user provisioned. For any operations interworking application to work, the network elements (NE) participating in the application must have the same routing domain and area fields.

If the routing domain and area fields of the DDM-2000 OC-3 Multiplexer are provisioned to some value other than the default (0), the DDM-2000 OC-3 Multiplexers will not show up in the **CONFIGURATION-Retrieve-Map-Network** report and no FT-2000 OC-48 Add/Drop-Rings Terminal can be used as a TL1 gateway network element (GNE) for the DDM-2000 OC-3 Multiplexers.

Operations interworking applications allow users to access, operate, provision, and administer remote network elements (NE) from any site in a network or from a centralized operations system through the OC-3 DCC. This reduces the number of dispatches required to perform operation, administration, maintenance, and provisioning functions at remote NE. In Release 6 and later releases, the operations interworking application supports the following single-ended operations:

- **Remote login:** A user logged in locally to an FT-2000 OC-48 Add/Drop-Rings Terminal can log in remotely to a DDM-2000 Multiplexer via the OC-3 DCC. A user logged in locally to a DDM-2000 Multiplexer can also log in remotely to any other DDM-2000 Multiplexer within the same subnetwork via the OC-3 and OC-48 DCC. However, a user logged in locally to a DDM-2000 Multiplexer cannot log in remotely to the FT-2000 OC-48 Add/Drop-Ring Terminal. For more information about remote access using the DCC, refer to Volume I, Section 6, "Operations Interfaces".

- **GNE operation:** One or more FT-2000 OC-48 Add/Drop Rings Terminals can serve as a single interface to the local X.25 message-based operations system for the DDM-2000 Multiplexers through the OC-3 DCC. The GNE also serves as a single interface for all FT-2000 OC-48 Add/Drop-Rings Terminals in the same ring network and in the same subnetwork. For more information about GNE operations, refer to the “Gateway Network Element” application in this section.
- **RNE status:** One or more FT-2000 OC-48 Add/Drop-Rings Terminals can receive and transport alarm and status information from other FT-2000 OC-48 Add/Drop-Rings Terminals and DDM-2000 Multiplexers that are in the same subnetwork and alarm group. Within a subnetwork, NEs in the same alarm group (provisionable) exchange alarm and status information through at least one alarm gateway network element (AGNE). When an AGNE receives alarm and status information, it rebroadcasts the information to all the other NEs in the alarm group. The AGNEs reduce the number of technicians required to monitor equipment at remote sites. When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 of the DDM-2000 OC-3 Multiplexer, an alarm group supports up to 24 NEs. When Release 7 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 of the DDM-2000 OC-3 Multiplexer, an alarm group supports up to 32 NEs. When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 9 of the DDM-2000 OC-3 Multiplexer, an alarm group supports up to 24 NEs. When Release 7 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 9 of the DDM-2000 OC-3 Multiplexer, an alarm group supports up to 32 NEs. For more information about provisioning alarm groups and AGNEs, refer to Volume I, Section 8, “Administration and Provisioning.”

The remote alarm and status information is reported at the AGNE using remote office alarms, far-end parallel telemetry, a far-end active light emitting diode (LED), and a network alarms report. For more information about office alarms, parallel telemetry, and the far-end activity LED on the user panel, refer to Volume I, Section 6, “Operations Interfaces”. For more information about the network alarms report, refer to the **FAULT-Retrieve-Network Alarms** input in Section 11, “Craft Interface Terminal Usage.”

- **Remote software download and copy:** An FT-2000 OC-48 Add/Drop-Rings Terminal can download (copy) software to remote FT-2000 OC-48 Add/Drop-Rings Terminals in the same network and across a DDM-2000 network to another FT-2000 OC-48 Add/Drop-Rings Terminal in the same subnetwork. For more information about software upgrades, refer to Volume I, Section 8, "Administration and Provisioning."

When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 of the DDM-2000 OC-3 Multiplexer in subnetworks with less than 16 NEs, the same NE may serve as the TL1 GNE and the alarm GNE. However, in subnetworks with more than 16 NEs, the network element serving as the TL1 GNE cannot serve as the alarm GNE. The TL1 GNE and alarm GNE functions must be at separate NEs in the subnetwork.

The FT-2000 OC-48 Add/Drop-Rings Terminal must be equipped with the OC3 Optical Interface — 1.3 μ m Standard Performance (OC3 [1.3 STD]) circuit pack and the Overhead Controller — Tributary (TOHCTL) circuit pack to access the OC-3 DCC. A single TOHCTL circuit pack can access up to eight OC-3 DCCs. Low-speed interface slots must be assigned to an OC-3 DCC channel. For more information about entering OC-3 DCC channel assignments for single-ended operations, refer to Volume I, Section 8, "Administration and Provisioning."

For more information about operations interworking between the FT-2000 OC-48 Lightwave System and the DDM-2000 Multiplexers, refer to 824-102-147, *2000 Product Family Operations Interworking Guide*.

Gateway Network Element (GNE)

In Releases 4.1 and 5, the FT-2000 OC-48 Add/Drop-Rings Terminal may operate as a GNE that serves as a single interface to the local X.25 message-based operations system for all the FT-2000 OC-48 Add/Drop-Rings Terminals in a ring network. In releases prior to Release 4.1, each FT-2000 OC-48 Add/Drop-Rings Terminal in a ring network has an interface to the local X.25 message-based operations system.

The GNE receives operations information from all the FT-2000 OC-48 Add/Drop-Rings Terminals in the ring network through the DCC and reports that information, along with its own, to the X.25 message-based operations system (Figure 2-22). The operations information is in the form of TL1 messages. The operations system can also send TL1 commands through the GNE to any FT-2000 OC-48 Add/Drop-Rings Terminal in the ring network. All the FT-2000 OC-48 Add/Drop-Rings Terminals in the ring network must be equipped with Release 4.1.0-ADR or 5.0.1-ADR software.

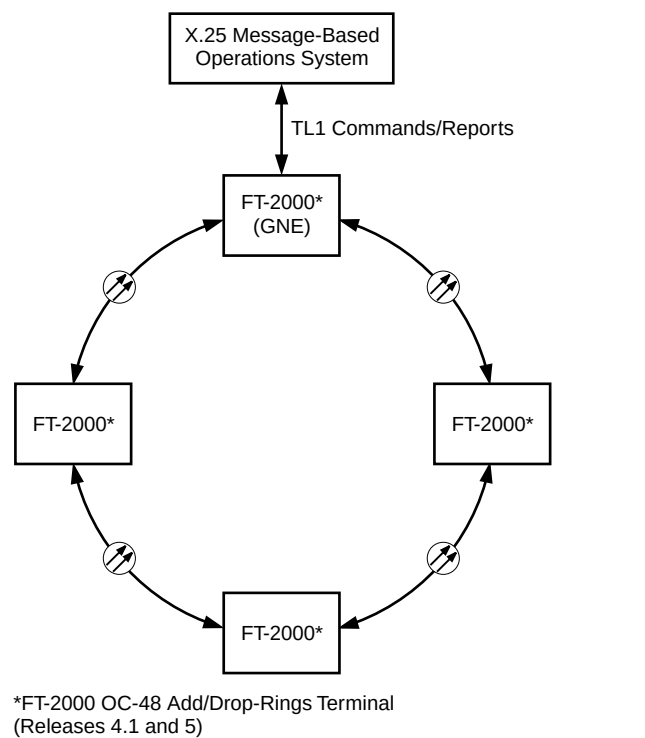


**Figure 2-22. Gateway Network Element Application
(Releases 4.1 and 5)**

In Release 4.1, one or more FT-2000 OC-48 Add/Drop-Rings Terminals in a ring network may be provisioned as a GNE using the CIT. In Release 5 and later releases, the GNE function is no longer CIT provisionable. Any FT-2000 OC-48 Add/Drop-Rings Terminal in a ring network will automatically operate as a GNE if it receives TL1 messages through the local X.25 interface that are for another node in the ring network.

In Release 6 and later releases, one or more FT-2000 OC-48 Add/Drop-Rings Terminals can serve as a single interface to the local X.25 message-based operations system for the DDM-2000 Multiplexers through the OC-3 DCC (Figure 2-23). The GNE also serves as a single interface for all FT-2000 OC-48 Add/Drop-Rings Terminals in the same ring network and in the same subnetwork. When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 or Release 9 of the DDM-2000 OC-3 Multiplexer, the recommended maximum subnetwork size is 24 NEs. When Release 7 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 or Release 9 of the DDM-2000 OC-3 Multiplexer, the recommended maximum subnetwork size is 32 NEs.

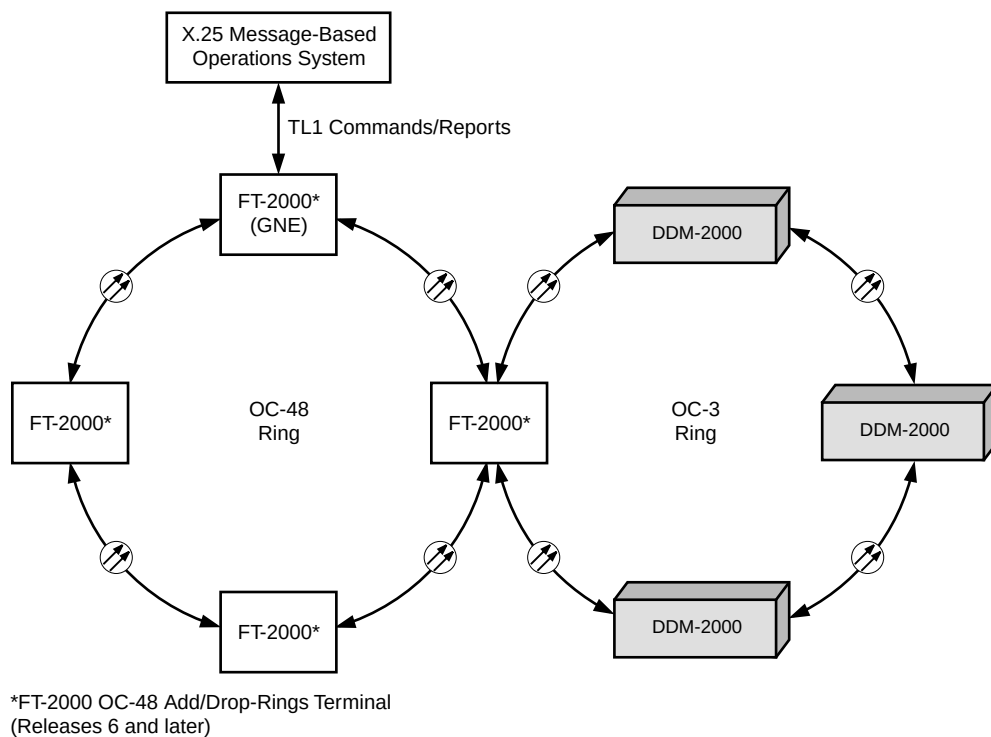


Figure 2-23. Gateway Network Element Application (Release 6 and Later)

When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 of the DDM-2000 OC-3 Multiplexer in subnetworks with less than 16 NEs, the same NE may serve as the TL1 GNE and the alarm GNE. However, in subnetworks with more than 16 NEs, the NE serving as the TL1 GNE cannot serve as the alarm GNE. The TL1 GNE and alarm GNE functions must be at separate NEs in the subnetwork.

The FT-2000 OC-48 Add/Drop-Rings Terminal must be equipped with the OC3 Optical Interface — 1.3 μ m Standard Performance (OC3 [1.3 STD]) circuit pack and the Overhead Controller — Tributary (TOHCTL) circuit pack to access the OC-3 DCC. A single TOHCTL circuit pack can access up to eight OC-3 DCCs. Low-speed interface slots must be assigned to an OC-3 DCC channel.

For more information about entering OC-3 DCC channel assignments for single-ended operations, refer to Volume I, Section 8, "Administration and Provisioning."

The number of GNEs in a ring network is based on operational needs. For example, to reduce the number of X.25 links between the ring network and the operations system, one FT-2000 OC-48 Add/Drop-Rings Terminal may operate as the GNE. If redundancy is desired, two or more FT-2000 OC-48 Add/Drop-Rings Terminals may operate as GNEs. For more information about provisioning a GNE, refer to Volume I, Section 8, "Administration and Provisioning."

Synchronization Messaging

Synchronization messages are a method by which nodes within an FT-2000 ring can communicate their synchronization status with other nodes in the ring. This message set is transmitted in the S1 byte on an OC-N signal.

The messages in Table 2-2 indicate the stratum level to which the timing on an incoming OC-N line is traceable.

Table 2-2. Synchronization Status Messages

Description	Acronym	Quality Level	S1 bits 5678	K2 bits 123
PRS Traceable	PRS	1	001	111
Synchronized - traceability unknown	STU	2	000	110
Traceable Stratum 2 Holdover	ST2	3	0111	101
Traceable Stratum 3 Holdover	ST3	4	1010	100
Traceable SONET clock self-timed	SIC	5	1100	N/A
DON'T USE for Sync	DUS	7	111	000

The message DUS (Don't Use for Synchronization) is used to indicate

- a link on which timing has been looped back, in order to prevent timing loops
- an invalid message

Synchronization messages are exchanged

- between FT-2000 nodes over OC-48 interfaces via bits 5, 6, 7, and 8 of the S1 byte
- from FT-2000 nodes to DDM-2000 nodes over OC-3 and OC-12 interfaces via proprietary message set on bits 1, 2, and 3 of the K2 byte.

On the OC-48 interface, synchronization messages are both transmitted and processed. On the IS-3, OC-3, and OC-12 interfaces, synchronization messages are supported in the direction to distribute timing to DDM-2000. The table below shows two ring environments available:

- interoffice
- customer location.

Table 2-3 explains ring environments and the use of synchronization.

Table 2-3. FT-2000 Ring Environments

IF the ring environment is...	THEN the synchronization is located in...	AND...
interoffice	central offices with BITS clocks	synchronization messages are not needed.
loop (customer location)	customer sites without BITS clocks	DS1 output will be used to provide timing to the equipment in the customer location. Note: if the timing is not traceable to the timing network, insert DS1 AIS.

Table of Contents

Overview	3-1
Introduction	3-1
FT-2000 OC-48 Add/Drop-Rings Terminal (E-Bay)	3-2
■ Equipment Packages	3-2
■ Shelf Descriptions	3-4
Power Distribution and Fuse Panel	3-4
Low-Speed Shelf — System Controller	3-4
High-Speed Shelf	3-17
Enhanced High-Speed Shelf	3-24
Heat Baffle	3-31
PC Tray	3-31
Screen	3-31
FT-2000 OC-48 Repeater (R-Bay)	3-32
■ Equipment Packages	3-32
■ Shelf Descriptions	3-35
Power Distribution and Fuse Panel	3-35
Repeater Shelf — System Controller	3-36
Heat Baffle	3-44
PC Tray	3-44
Screen	3-44
■ Upgrades	3-44
FT-2000 OC-48 Dual ADR Terminal (D-Bay)	3-45
■ Equipment Packages	3-45

Table of Contents

■ Shelf Descriptions	3-47
Power Distribution and Fuse Panel	3-47
Low-Speed Shelf — System Controller	3-47
Condensed High-Speed Shelf	3-57
Fan Assembly	3-63

Overview

This section introduces the FT-2000 OC-48 Lightwave System platforms and provides a more detailed view of the FT-2000 OC-48 Lightwave System physical design.

Introduction

The FT-2000 OC-48 Lightwave System is a flexible high-capacity lightwave system that transports digitally encoded information through single-mode optical fibers at the synchronous optical network (SONET) OC-48 signal level of 2.5 Gb/s (up to 32,256 voice channels). The FT-2000 OC-48 Lightwave System consists of several platforms. A platform is a family of equipment and software configurations designed to support a particular set of applications. These platforms include the:

- FT-2000 OC-48 Add/Drop-Rings Terminal (E-Bay)
- FT-2000 OC-48 Dual Lightwave Terminating Bay (D-Bay)
- FT-2000 OC-48 Repeater (R-Bay)

These platforms enable the FT-2000 OC-48 Lightwave System to offer flexible and upgradable features for different applications in high-capacity loop, interoffice, outstate, and long-haul environments, and to provide different sets of features. The FT-2000 OC-48 Lightwave System software enables the different platforms to be upgraded with new features as they become available.

FT-2000 OC-48 Add/Drop-Rings Terminal (E-Bay)

Equipment Packages

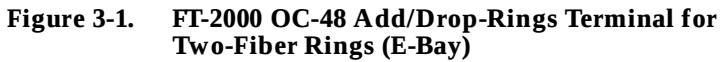
The FT-2000 OC-48 Add/Drop-Rings Terminal is available in the following equipment package:

- **FT-2000 OC-48 Add/Drop-Rings Terminal for two-Fiber Rings:** The FT-2000 OC-48 Add/Drop-Rings Terminal for two-Fiber Rings (Figure 3-1) consists of the J68974E FT-2000 OC-48 Enhanced Lightwave Terminating Bay (or the J68974A FT-2000 OC-48 Lightwave Terminating Bay) and Releases 3.0.1-ADR*, 4.0.0-ADR*, 4.1.0-ADR*, 5.0.1-ADR, 5.0.3-ADR, 6.0.1-ADR, 6.0.2-ADR, 7.0.0-ADR, 7.1.0-ADR, or 7.2.X software.

In this equipment package, the J68974E FT-2000 OC-48 Enhanced Lightwave Terminating E-Bay consists of a power distribution and fuse panel, one Low-Speed Shelf — System Controller, a personal computer (PC) tray, and one Enhanced High-Speed Shelf mounted in a single seismic network bay framework.

This equipment package provides OC-48 high-speed lines 1W and 1E. The low-speed interface service capacity of an FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 3.0.1-ADR software is 48 DS3 signals. The low-speed interface capacity of an FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 4.0.0-ADR or 4.1.0-ADR software is any combination of 48 DS3 and/or EC-1 signals. The low-speed interface capacity of an FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 5.0.1-ADR, 5.0.3-ADR, 6.0.1-ADR, 6.0.2-ADR, 7.0.0-ADR, 7.1.0-ADR, or 7.2.0 software is 48 DS3 and/or EC-1 signals, or eight 1+1 protected OC-3 signals (16 unprotected), 2 protected OC-12 signals (4 unprotected), or a mix of DS3, EC-1, OC-3, and OC-12 signals.

* Software releases 3.0.1-ADR through 7.1.0-ADR have been discontinued — effective June 1996.



Shelf Descriptions

Power Distribution and Fuse Panel

The power distribution and fuse panel (Figure 3-2) is located at the top of each FT-2000 OC-48 Lightwave System bay. It measures 2 inches high by 21.5 inches wide by 12 inches deep. The power distribution and fuse panel has a removable front cover that allows front access for shelf power connections. It accepts two 8-gauge power feeder stubs and distributes power to the shelves in the bay. The power distribution and fuse panel is also equipped with six 10-amp fuses (two for each shelf in the bay). Three fuses are for power feeder A and three fuses are for power feeder B. For more information about power distribution, refer to Volume I, Section 4, "Power."

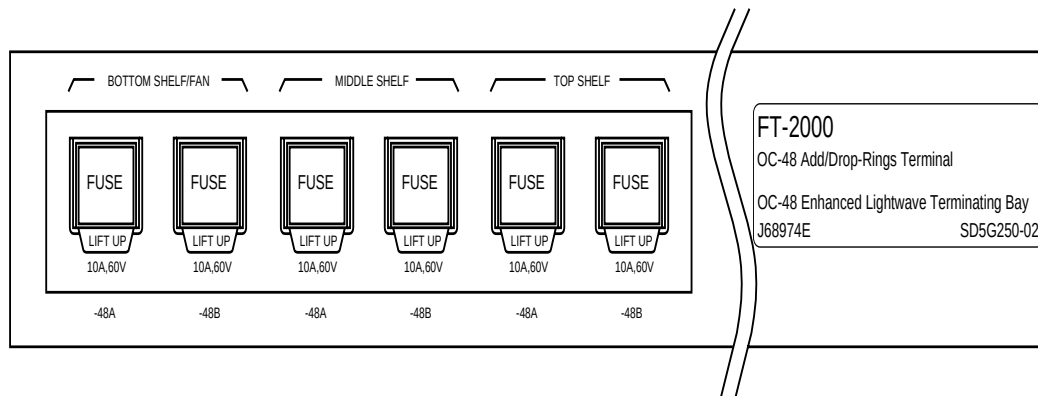


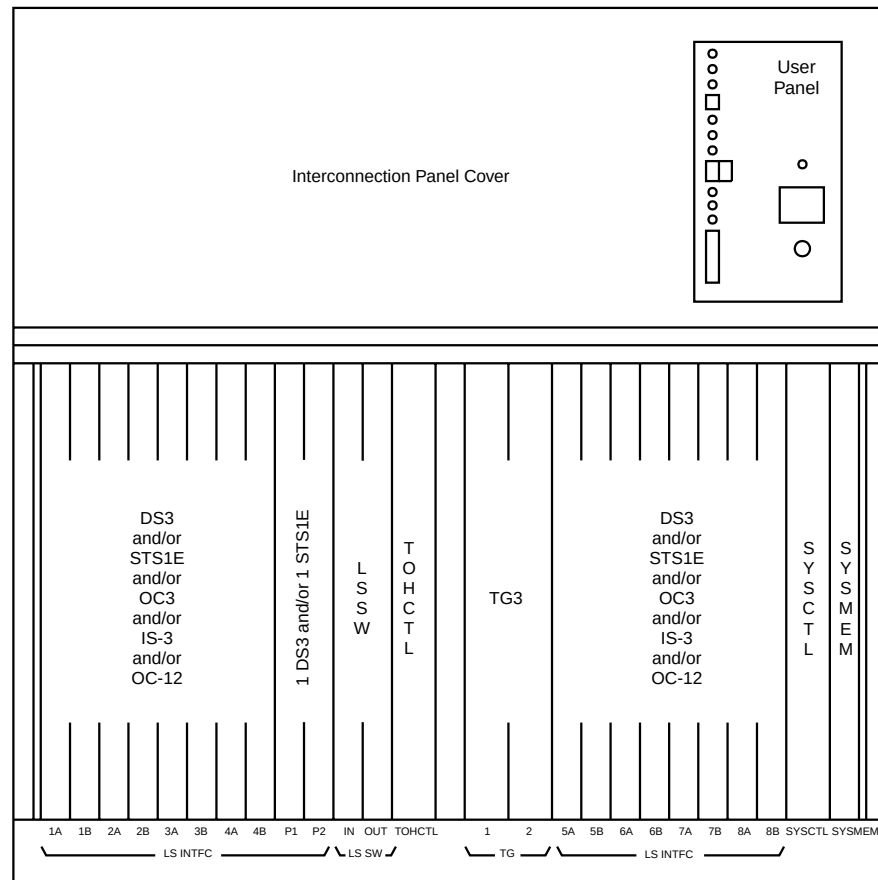
Figure 3-2. Power Distribution and Fuse Panel

Low-Speed Shelf — System Controller

Purpose of Low-Speed Shelf — System Controller

The Low-Speed Shelf — System Controller provides high-level system control functions and serves as an interface for up to 48 service DS3 and/or EC-1 signals, eight 1+1 protected OC-3 signals (16 unprotected), two 1+1 protected OC-12 signals, or a mix of DS3, EC-1, OC-3 and OC-12 signals. In releases prior to Release 7, the low-speed signals can be cross-connected to OC-48 high-speed line 1W or 1E in groups of three DS3 signals, three EC-1 signals, or one OC-3 signal. In Release 7 and later releases, a single DS3 or EC-1 signal can also be cross-connected to OC-48 high-speed line 1W or 1E.

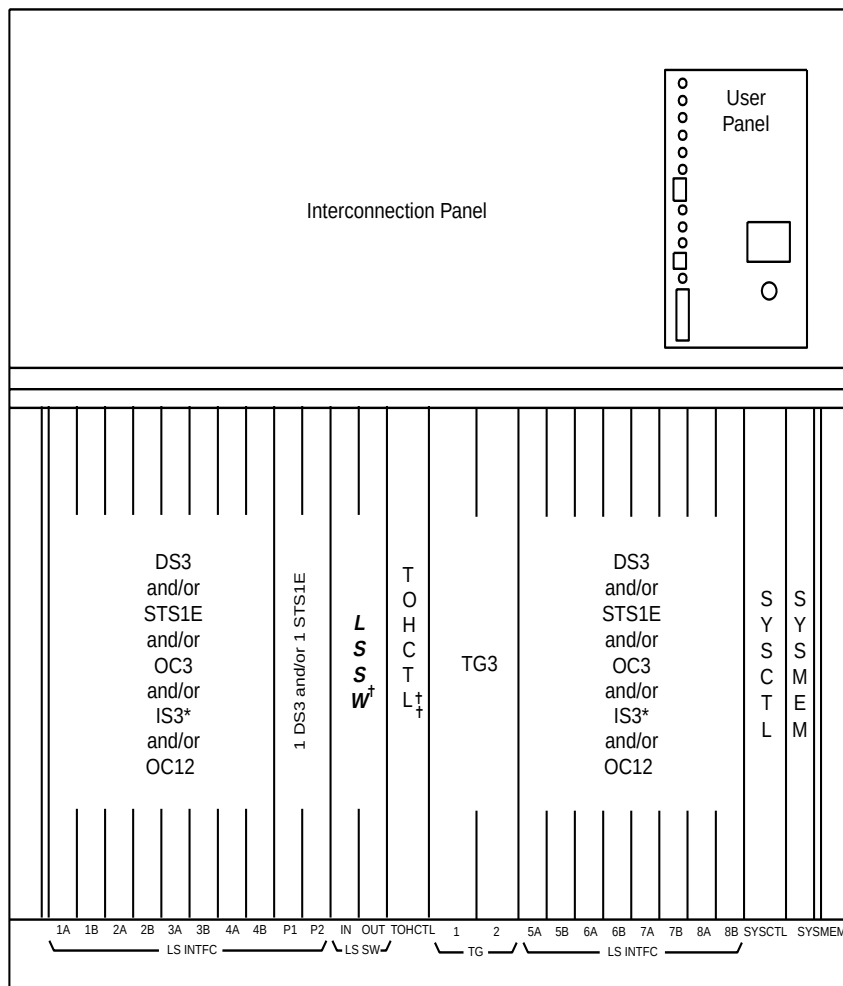
The Low-Speed Shelf — System Controller (Figure 3-3) consists of a fully connectorized interconnection panel, a USER PANEL, and 25 circuit pack slots. It measures 21.5 inches high by 21.5 inches wide by 12 inches deep and fits in a standard 23-inch wide seismic network bay framework.



nc-ft2000-039

Figure 3-3. Low-Speed Shelf — System Controller (Without Circuit Pack Cover)— ED6G999-31, G5

The Low-Speed Shelf — System Controller (Figure 3-4) is functionally identical to Figure 3-3 and is its new replacement. The main distinguishing differences are: (1) a slightly modified User Panel, and (2) a fully integrated backplane.



* IS3 circuit pack is for intraoffice applications only.

† LSSW is required only if you use DS3 and/or STS1E circuit packs with low speed protection (low speed protection is strongly recommended).

† TOHCTL is required only if you use OC3 or OC12 circuit packs and want DCC capabilities.

adr04008.01eJK040997

Figure 3-4. Low-Speed Shelf — System Controller (Without Circuit Pack Cover) — ED6G999-32, G3

Low-Speed Shelf — System Controller Circuit Packs

The Low-Speed Shelf — System Controller (Figure 3-3) provides the following circuit pack slots:

■ **LS INTFC 1A - 8B:** Sixteen slots are provided for the following circuit packs.

- LAA2/LAA2B DS3 Interface (DS3) (S1:1 or S4:4) circuit pack (service): The S1:1 DS3 circuit pack has been discontinued and replaced with the S4:4 DS3 circuit pack. The S1:1 DS3 circuit pack is compatible with Release 3.0.1-ADR software only. The S4:4 DS3 circuit pack is compatible with Release 3.0.1-ADR or later software.
- LAA4 STS1 Electrical Interface (STS1E) circuit pack (service): The STS1E circuit pack is compatible with Release 4.0.0-ADR or later software.
- LAA10/LAA10B OC3 Optical Interface — 1.3µm Standard Performance (OC3 [1.3 STD]) circuit pack (service and protection): The OC3 (1.3 STD) circuit pack is compatible with Release 5.0.1-ADR or later software. For optional 1+1 nonrevertive protection, two OC3 (1.3 STD) circuit packs must be placed in adjacent "A" and "B" LS INTFC slots (for example, LS INTFC slots "2A" and "2B").
- LAA5 IS-3 Optical Interface — The IS-3 circuit pack is compatible with Release 7.1.0 and later software. For intraoffice applications, it is a cost effective alternative to the OC-3 Optical Interface. The IS-3 terminates one input signal, generates one output signal, and supports the transport of both STS-3 and STS-3c formats. It supports interconnection distances up to 2000-feet using multimode fiber.



NOTE:

For both 1+1 nonrevertive protection applications and transport of STS-3c, two IS-3 circuit packs must be placed in adjacent "A" and "B" LS INTFC slots (for example, LS INTFC slots "2A" and "2B").

- T939A OC-12 Optical Interface 1.3µm Standard Performance circuit pack (service and protection): The OC-12 circuit pack is compatible with Release 7.1.0 and later software. It may be used for either intraoffice or interoffice applications. The OC-12 terminates one STS-12/STS-12c input signal and generates one STS-12/STS-12c output signal.

The OC-12 is a "quad-size" circuit pack and thus requires four adjacent low-speed slots as shown in Table 3-1.

Table 3-1. Permitted Slot Locations for the Quad Circuit Pack

OC-12 Quad	Slot Locations	Command Slot	Extension Slots	OC-12 Pack AID
1	1A-1B-2A-2B	1B	1A, 2A, 2B	ls-1b
2	3A-3B-4A-4B	3B	3A, 4A, 4B	ls-3b
3	5A-5B-6A-6B	5B	5A, 6A, 6B	ls-5b
4	7A-7B-8A-8B	7B	7A, 8A, 8B	ls-7b



NOTE:

Each quad location consists of one command slot and three extension slots. The command slot address is used in the OC-12 provisioning commands and for reporting any circuit pack and OC-12 signal-related alarm conditions. The extension slots are used to gain access to the STS-3 signals, in conjunction with cross-connect commands, and for reporting tributary related alarms.

Each low-speed shelf can support a maximum capacity of up to four OC-12 circuit packs. The four circuit packs may be configured to operate independently in an 0x1 unprotected mode or as pairs in a 1+1 protected mode. In a protected mode, the Service and Protection OC-12 packs must be inserted in quad pairs as follows:

- Quads 1 and 2, or
- Quads 3 and 4.



NOTE:

Quads 2 and 3, or 1 and 4 will not protect each other.

In an all 0x1 configuration, the FT-2000 Release 7.1.0 has the capacity to add/drop four OC-12 equivalents of traffic. In an all 1+1 protected configuration, the FT-2000 provides the capacity to add/drop two OC-12 equivalents of traffic. In a mixed 1+1/0x1 configuration, the capacity is three OC-12 equivalents of traffic.



NOTE:

For Release 7.1.0, OC-12 1+1 links do not support the DCC channel. With Release 7.2.0, OC-12 1+1 links do support the DCC channel.

- **LS INTFC P1 and P2:** Two slots are provided for one protection LAA2/LAA2B DS3 circuit pack and/or one protection LAA4 STS1E circuit pack. Two DS3 or two STS1E circuit packs cannot be used. The protection DS3 and STS1E circuit packs can be placed in slot P1 or P2. The S1:1 DS3 circuit pack has been discontinued and replaced with the S4:4 DS3 circuit pack. The LAA2 DS3 (S1:1) circuit pack is compatible with

| Release 3.0.1-ADR software only. The LAA2/LAA2B DS3 (S4:4) circuit pack is compatible with Release 3.0.1-ADR or later software. The STS1E circuit pack is compatible with Release 4.0.0-ADR or later software.

- **LSSW IN and OUT:** Two slots are provided for the LAA12B Low Speed Switch (LSSW) circuit packs. The LSSW circuit packs are required when DS3 and STS1E low-speed protection is desired. The LSSW circuit packs are not required if the LS INTFC slots are equipped with OC3 (1.3 STD) and/or OC12 circuit packs.
- **TOHCTL:** One slot is provided for the LAA26 Tributary - Overhead Controller (TOHCTL) circuit pack. The TOHCTL circuit pack is required when an LS INTFC slot is equipped with an OC3 circuit pack and single-ended operation is desired through the OC-3 data communications channel (DCC). The TOHCTL circuit pack is compatible with Release 6.0.0-ADR or later software.
- **TG 1 and 2:** Two slots are provided for the LAA17 or LAA18 Timing Generator, Stratum 3 — DS1 (TG3 [DS1]) circuit packs. The LAA17 TG3 (DS1) circuit pack has been discontinued and replaced with the LAA18 TG3 (DS1) circuit pack.
- **SYSCTL:** One slot is provided for the LAA23B System Controller (SYSCTL) circuit pack.
- **SYSTEMEM:** One slot is provided for the LAA25 System Memory (SYSTEMEM) circuit pack.

Table 3-2 also provides circuit pack slot/circuit pack code/software compatibility information.

Table 3-2. Low-Speed Shelf — System Controller Circuit Pack Slot/Circuit Pack Code/Software Compatibility

Circuit Pack Slot	Supported Circuit Pack Codes	Compatible Software (Note)							
		R3.01 of ADR	R4.0.0 of ADR	R4.1.0 of ADR	R5.0. of ADR	R6.0. of ADR	R701.0 of ADR	R7.1.0 of ADR	R7.2.0 of ADR
LS INTFC 1A-8B	LAA2 DS3, S1:1*	X							
	LAA2/LAA2B DS3 [‡]	X	X	X	X	X	X	X	X
	LAA4 STS1E		X	X	X	X	X	X	X
	LAA10/LAA10B OC3 (1.3 STD)				X	X	X	X	X
	LAA5 IS-3							X	X
	T939A OC-12							X	X
LS INTFC P1 and P2	LAA2 DS3, S1:1*	X							
	LAA2/LAA2B DS3	X	X	X	X	X	X	X	X
	LAA4 STS1E		X	X	X	X	X	X	X
LSSW in and OUT	LAA12B LSSW ^{††}	X	X	X	X	X	X	X	X
TOHCTL	LAA26 TOHCTL					X	X	X	X
TG 1 and 2	LAA17 TG3 (DS1) [†]	X	X	X	X	X	X	X	
	LAA18 TG3 (DS1)	X	X	X	X	X	X	X	X
SYSCTL	LAA23B/LAA23C SYSCTL	X	X	X	X	X	X	X	X
SYSTEMEM	LAA25 SYSTEMEM	X	X	X	X	X	X	X	X

Note: Software releases 3.0.1-ADR, 4.0.0-ADR, and 4.1.0-ADR were discontinued in June 1996.

* The S1:1 DS3 circuit pack has been discontinued and replaced with the S4:4 DS3 circuit pack.

† The LAA17 TG3 (DS1) circuit pack has been discontinued and replaced with the LAA18 TG3 (DS1) circuit pack.

†† The LAA12 circuit pack has been manufacture discontinued and replaced with the LAA12B circuit pack.

‡ The LAA2 circuit pack has been manufacture discontinued and replaced with the LAA2B circuit pack.

Circuit pack keying prevents circuit packs from being accidentally inserted into incorrect slots.

For detailed circuit pack descriptions and software compatibility information, refer to Volume I, Section 7, "Circuit Pack Descriptions."

Low-Speed Shelf — System Controller Interfaces—ED6G999-31,G5

Front access cabling is provided through the interconnection panel (Figure 3-5). The interconnection panel provides the following interfaces:

- **LS INTFC 1A — 8B:** These connectors are cabled to the low-speed interface slots 1A through 8B. For example, the DS3 circuit pack in slot 1A receives three DS3 signals through the three IN connectors (1, 2, and 3) on the interconnection panel associated with slot 1A. The DS3 circuit pack in slot 1A also transmits three DS3 signals through the three OUT connectors (1, 2, and 3) associated with slot 1A. The 9A through 16B connector designations are not used on this shelf. Any LS INTFC 1A — 8B connector cabled to a slot equipped with an OC3/OC12 circuit pack is not used. For more information, refer to SD-5G250-02, *FT-2000 OC-48 Enhanced Lightwave Terminating Bay Application Schematic* (or SD-5G250-01, *FT-2000 OC-48 Lightwave Terminating Bay Application Schematic*).
- **X.25:** This connector provides an interface to a message-based operations system that uses X.25 protocol and the TL1 message language. For more information about this message-based operations system interface, refer to Volume I, Section 6, "Operations Interfaces."
- **SER TLM 1 and SER TLM 2:** These connectors provide a serial telemetry interface to a remote maintenance center that uses telemetry byte-oriented serial (TBOS) protocol. For more information about this serial telemetry interface, refer to Volume I, Section 6, "Operations Interfaces."
- **OFFICE ALMS:** This connector provides an interface to the local office audible and visible alarms. For more information about this office alarm interface, refer to Volume I, Section 6, "Operations Interfaces."
- **PAR TLM:** This connector provides an interface to an operations center for alarm information. For more information about this parallel telemetry (PAR TLM) interface, refer to Volume I, Section 6, "Operations Interfaces."
- **MISC DSCRT 1 and MISC DSCRT 2:** These connectors provide an interface to external equipment via a terminal strip. This allows an operations system to monitor and control equipment collocated with the FT-2000 OC-48 Lightwave System. For more information about this user-settable miscellaneous discrete interface, refer to Volume I, Section 6, "Operations Interfaces." For detailed information about terminal strip wiring, refer to 365-575-115, *FT-2000 OC-48 Lightwave System, Releases 3, 4, 5, 6, and 7 Installation Manual* and SD-5G250-02, *FT-2000 OC-48 Enhanced Lightwave Terminating Bay Application Schematic* (or SD-5G250-01, *FT-2000 OC-48 Lightwave Terminating Bay Application Schematic*).
- **CIT (DTE):** This connector provides an interface to a modem that allows dial-up access. For more information about the craft interface terminal (CIT) DTE interface, refer to Volume I, Section 6, "Operations Interfaces."

- **TIMING IN PRI and TIMING IN SECY:** These connectors provide a primary (PRI) and secondary (SEC) interface to an external DS1 timing source that is required for synchronous operation. For more information about synchronization interfaces, refer to Volume I, Section 5, "Control, Transmission, and Synchronization Interfaces."
- **TIMING OUT 1 and TIMING OUT 2:** These connectors provide an interface to other central office equipment that requires a DS1 timing reference signal. For more information about synchronization interfaces, refer to Section 5, "Control, Transmission, and Synchronization Interfaces."
- **CIT (DCE):** This connector provides an interface to the CIT (DCE) port on the user panel (USER PNL) that allows local access. For more information about the CIT (DCE) interface, refer to Section 6, "Operations Interfaces."
- **USER PNL DISPLAY:** This connector provides an interface to the alarm and status indicators, and display on the user panel. For information about the user panel display and indicators, refer to Section 6, "Operations Interfaces."
- **PWR:** These connectors provide an interface to the –48 V power filters on the user panel. For more information about power, refer to Section 4, "Power."

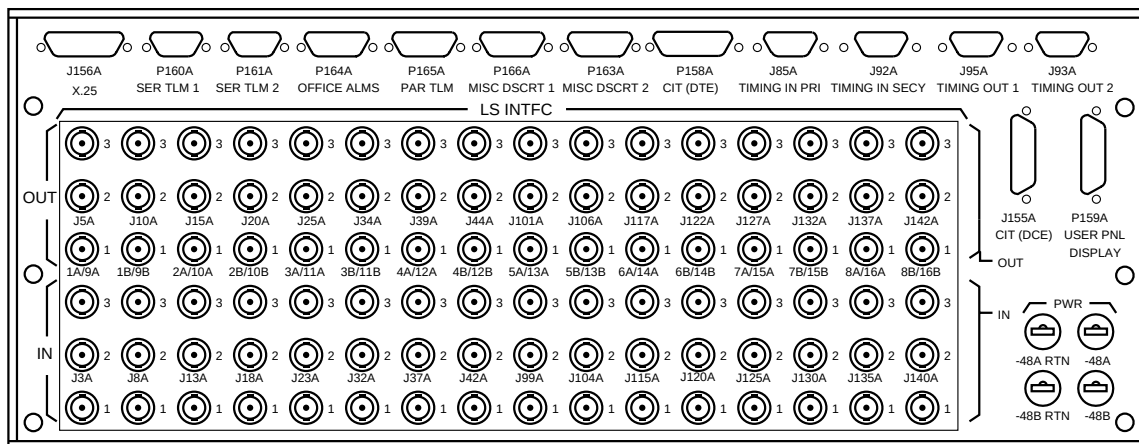


Figure 3-5. Interconnection Panel (Low-Speed Shelf — System Controller)

Except for power, all cabling entering the interconnection panel is connectorized with the following industry standard connectors:

- **BNC connectors:** The BNC connectors are used on all cables for electrical low-speed interfaces.
- **D-subminiature connectors:** The D-subminiature connectors are used on all cables for the operation system and central office interfaces.

The power cables are connected to radio frequency (RF) filters on the interconnection panel using push-on type connectors.

Low-Speed Shelf — System Controller Interconnect Panel — ED6G999-32, G3

The new (G3) panel provides the same basic functionality as the older (G5) panel. However, the placement of the power indicator LEDs has been rearranged. Also, the CIT and User Panel connectors have been eliminated. Figure 3-6 shows the G3 Interconnect Panel.

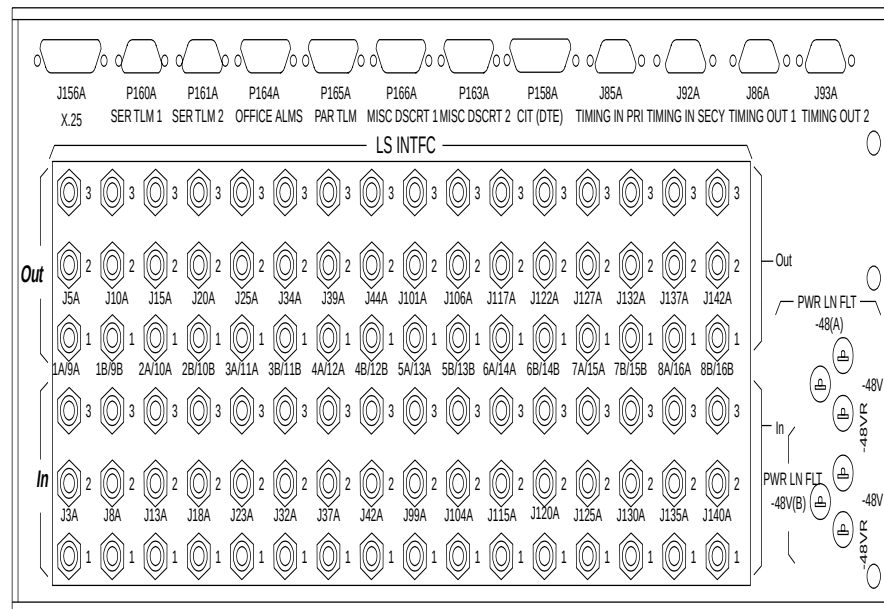


Figure 3-6. Interconnection Panel (Low-Speed Shelf — System Controller) —ED6G999-32, G3

The user panel (Figure 3-7) provides –48 V power filters, a power on (PWR ON) LED, an electrostatic discharge jack, and system level information. For more information about the user panel, refer to Section 6, "Operations Interfaces."

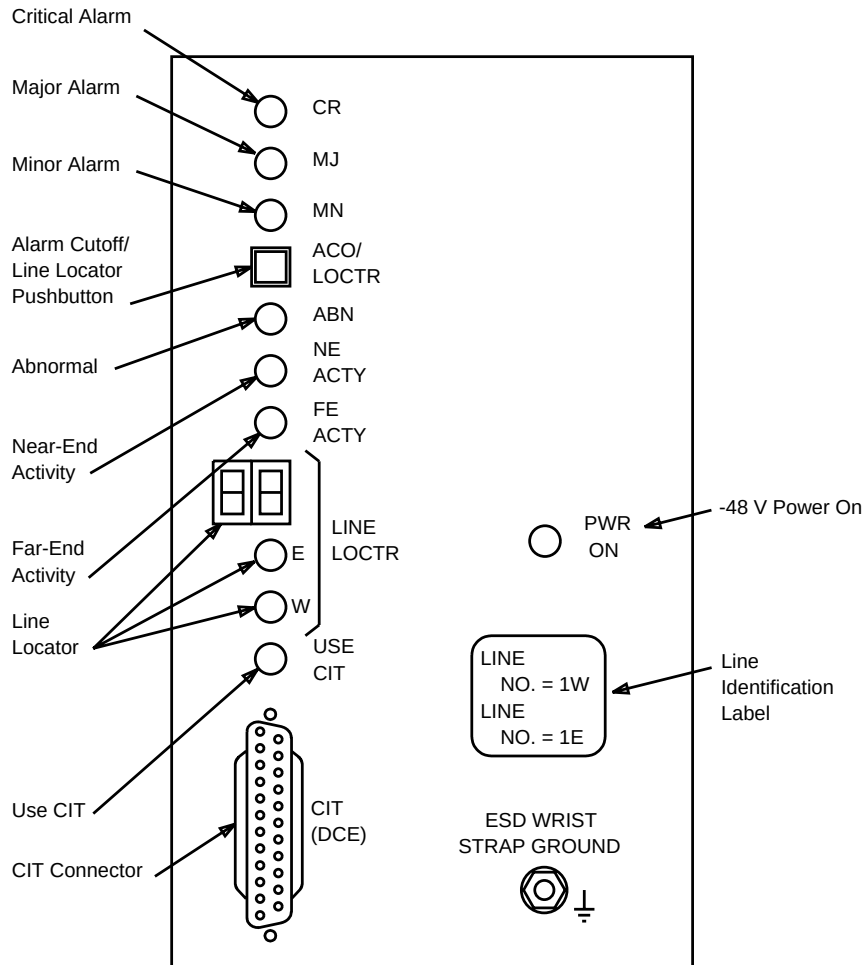


Figure 3-7. User Panel (Enhanced) 6G999-31, G5

Functionally, the (newer) G3 User Panel is the same as the (older) G5 panel. However, there are minor physical differences. Figure 3-8 depicts the G3 user panel.

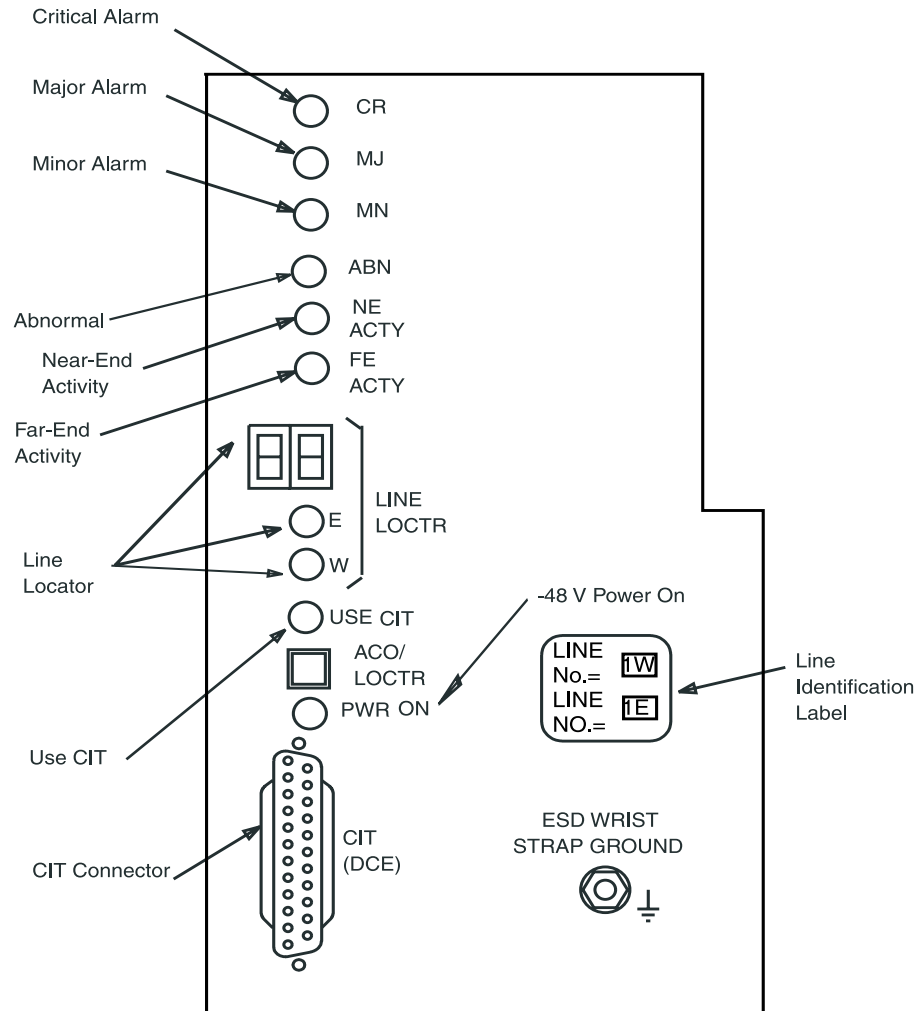


Figure 3-8. User Panel 6G999-32, G3

The Low-Speed Shelf — System Controller has two front covers (Figure 3-9). One cover covers the interconnection panel and the other covers the circuit packs. Openings are provided in the top cover that allow the user panel to show through and provide access to the (CIT) port. The covers are hinged and can be easily removed for maintenance access.

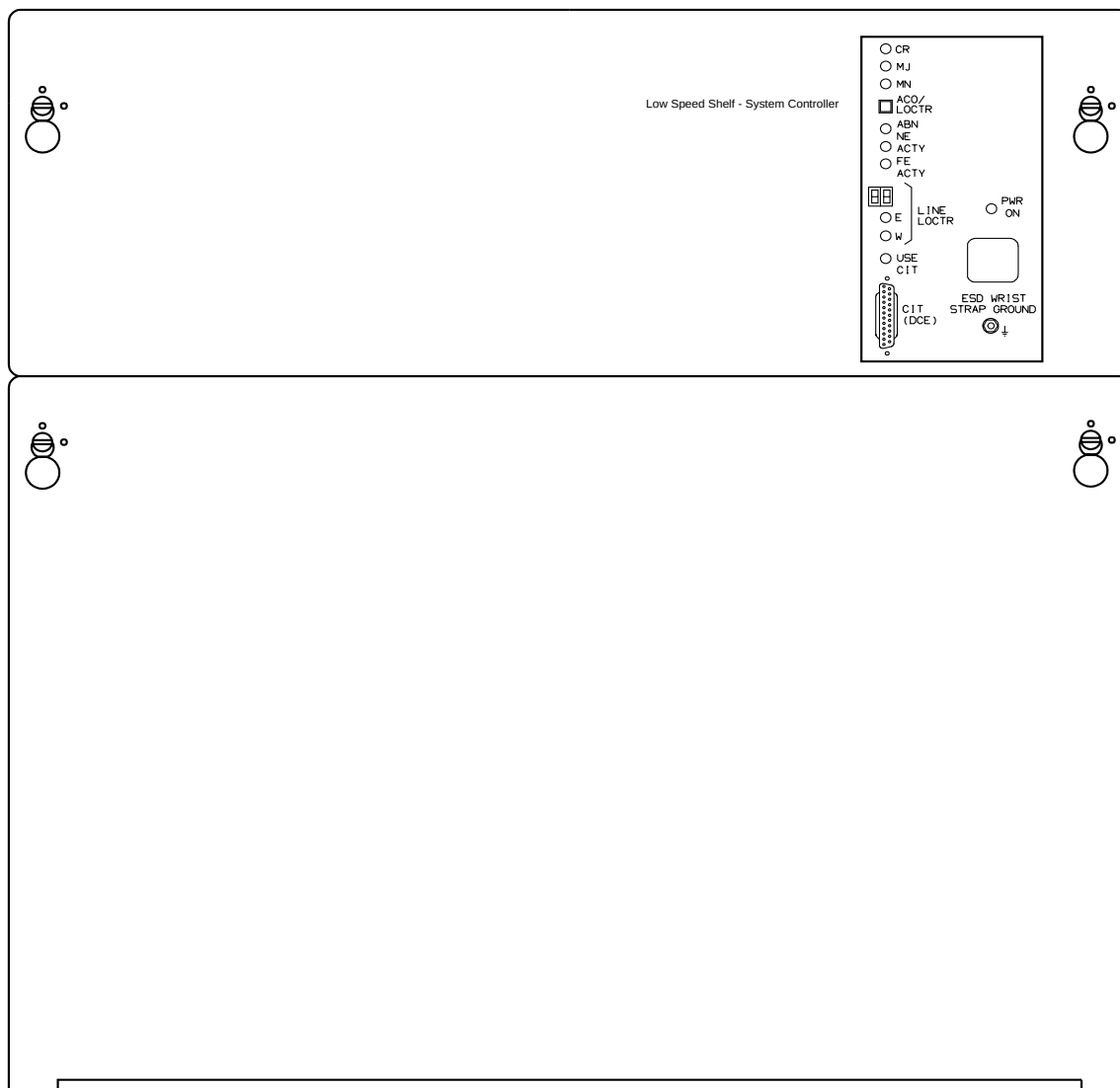


Figure 3-9. Low-Speed Shelf — System Controller (with Covers)

High-Speed Shelf

Purpose of High-Speed Shelf

The High-Speed Shelf* serves as an interface for the OC-48 high-speed lines 1W and 1E. The High-Speed Shelf (Figure 3-10) consists of the fully connectorized interconnection panel, a filter panel, and eight circuit pack slots. It measures 21.5 inches high by 21.5 inches wide by 12 inches deep and fits in a standard 23-inch wide network bay framework.

High-Speed Shelf Circuit Packs

The High-Speed Shelf (Figure 3-10) provides the following circuit pack slots:

- **TRMTR 1W:** One slot is provided for the 739B2[†], 739C2[†], 739E[1-16], 739H[1-16], 739J2[†], 739J3[†], 739R2[†], 739S2[†], 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, or 739S5 OC48 Transmitter, Add/Drop [OC48 TRMTR (A/D)] circuit pack for OC-48 high-speed line 1W. The 739J3[†] OC48 TRMTR (A/D) circuit pack is compatible with Release 5.0.1-ADR or later software. The 739R2[†] and 739S2[†] OC48 TRMTR (A/D) circuit packs are compatible with Release 6.0.0-ADR or later software. The 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 739E[1-16] circuit packs are compatible with Release 7.2.0 and later software.
- **RCVR 1W:** One slot is provided for the 839B2[†], 839B4B, 839B5, or 839E5 OC48 Receiver, Add/Drop (OC48 RCVR [A/D]) circuit pack for OC-48 high-speed line 1W. The 839B4B and 839B5 OC48 RCVR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 839E5 circuit pack is compatible with Release 7.2.0 and later software.
- **LNCTL:** One slot is provided for the LAA28 Line Controller — 4 Megabyte (LNCTL [4 MEG]) circuit pack.
- **OHCTL 1W:** One slot is provided for the LAA22 Overhead Controller — Terminal (OHCTL [TERM]) circuit pack.
- **OHCTL 1E:** One slot is provided for the LAA21 Overhead Controller — Terminal [OHCTL (TERM)] circuit pack.

TRMTR 1E: One slot is provided for the 739B2[†], 739C2[†], 739E[1-16], 739H[1-16], 739J2[†], 739J3[†], 739R2[†], 739S2[†], 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, or 739S5 OC48 Transmitter, Add/Drop [OC48 TRMTR (A/D)] circuit pack for OC-48 high-speed line 1E. The 739J3 OC48 TRMTR (A/D) circuit pack is compatible with Release 5.0.1-ADR or later software. The 739R2[†] and 739S2[†] OC48 TRMTR (A/D) circuit packs are compatible with

* The High-Speed Shelf is used only on an FT-2000 OC-48 Add/Drop-Rings Terminal for two-Fiber Rings equipped with the J68974A FT-2000 OC-48 Lightwave Terminating Bay.

† These transmitters and receivers are DA effective 06/30/97.

Release 6.0.0-ADR or later software. The 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 739E[1-16] circuit packs are compatible with Release 7.2.0 and later software.

- **RCVR 1E:** One slot is provided for the 839B2, 839B4B, 839B5, or 839E5 OC48 Receiver, Add/Drop (OC48 RCVR [A/D]) circuit pack for OC-48 high-speed line 1E. The 839B4B and 839B5 OC48 RCVR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 839E RCVR (A/D) circuit pack is compatible with Release 7.2.0 and later software.

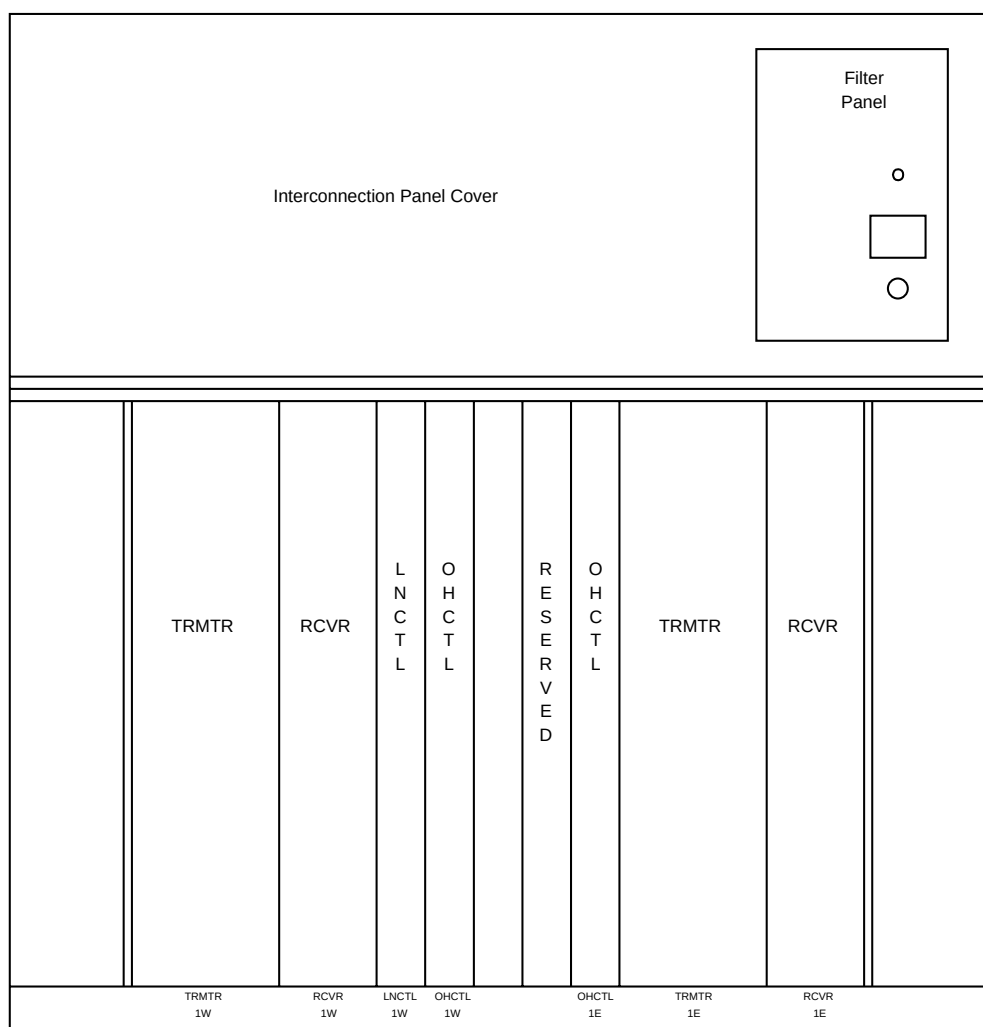


Figure 3-10. High-Speed Shelf (Without Circuit Pack Cover)

Table 3-3 also provides circuit pack slot/circuit pack code/software compatibility information.

Table 3-3. High-Speed Shelf Circuit Pack Slot/Circuit Pack Code/Software Compatibility

Circuit Pack Slot	Supported Circuit Pack Codes	ADR Compatible Software (Note)							
		R3.0.1	R4.0.0	R4.1.0	R5.0._	R6.0._	R7.0.0	R7.1.0	R7.2.0
TRMTR 1W	739B2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739C2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739E[1-16] OC48 TRMTR								X
	739H[1-16] OC48 TRMTR								X
	739J2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739J3† OC48 TRMTR				X	X	X	X	DA
	739R2† OC48 TRMTR					X	X	X	DA
	739S2† OC48 TRMTR					X	X	X	DA
	739B4 OC48 TRMTR						X	X	X
	739J4 OC48 TRMTR						X	X	X
	739B5 OC48 TRMTR						X	X	X
	739C5 OC48 TRMTR						X	X	X
	739J5 OC48 TRMTR						X	X	X
	739P5 OC48 TRMTR						X	X	X
	739R5 OC48 TRMTR						X	X	X
	739S5 OC48 TRMTR						X	X	X
RCVR 1W	839B2† OC48 RCVR	X	X	X	X	X	X	X	DA
	839B4B OC48 RCVR						X	X	X
	839B5 OC48 RCVR						X	X	X
	839E5 OC48 RCVR								X
LNCTL	LAA28 LNCTL	X	X	X	X	X	X	X	X
OHCTL 1W	LAA22 OHCTL (TERM)	X	X	X	X	X	X	X	X
OHCTL 1E	LAA21 OHCTL (TERM)	X	X	X	X	X	X	X	X
TRMTR 1E	739B2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739C2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739E[1-16] OC48 TRMTR								X
	739H[1-16] OC48 TRMTR								X
	739J2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739J3† OC48 TRMTR				X	X	X	X	DA
	739R2† OC48 TRMTR					X	X	X	DA
	739S2† OC48 TRMTR					X	X	X	DA
	739B4 OC48 TRMTR						X	X	X
	739J4 OC48 TRMTR						X	X	X
	739B5 OC48 TRMTR						X	X	X
	739C5 OC48 TRMTR						X	X	X
	739J5 OC48 TRMTR						X	X	X
	739P5 OC48 TRMTR						X	X	X
	739R5 OC48 TRMTR						X	X	X
	739S5 OC48 TRMTR						X	X	X
RCVR 1E	839B2† OC48 RCVR	X	X	X	X	X	X	X	DA
	839B4B OC48 RCVR						X	X	X
	839B5 OC48 RCVR						X	X	X
	839E5 OC48 RCVR								X



NOTE:

Releases 3.0.1-ADR, 4.0.0-ADR, and 4.1.0-ADR software were discontinued in June 1996.

Circuit pack keying prevents circuit packs from being accidentally inserted in incorrect slots.

For detailed circuit pack descriptions and software compatibility information, refer to Section 7, "Circuit Pack Descriptions."

High-Speed Shelf Interfaces

Front access cabling is provided through the interconnection panel (Figure 3-11).

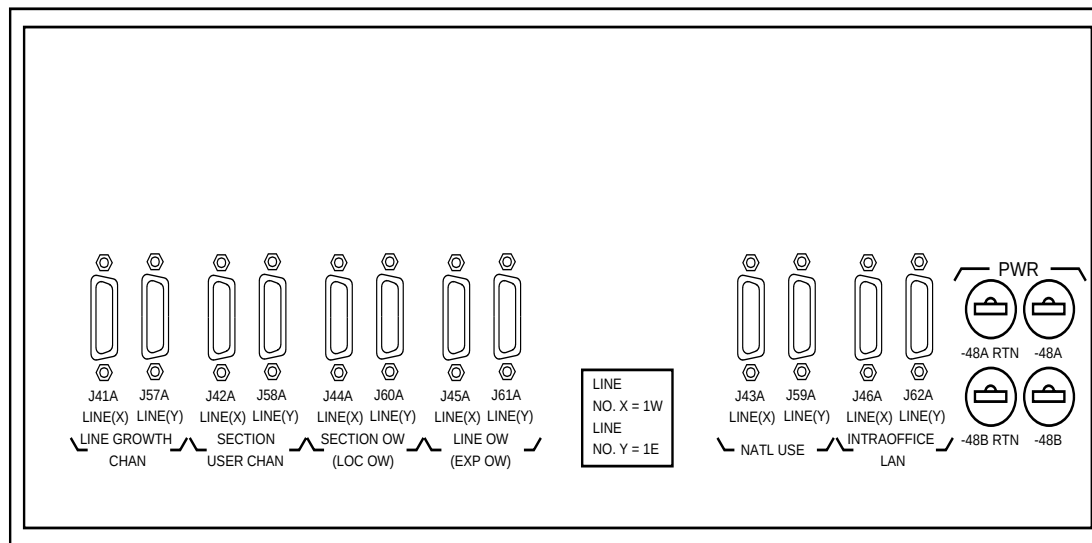


Figure 3-11. Interconnection Panel (High-Speed Shelf)

The interconnection panel provides the following interfaces:

- **LINE GROWTH CHAN LINE (X) and LINE (Y):** These connectors provide an interface that allows access to the line growth channel (Z1 and Z2 bytes) of the OC-48 high-speed lines. These connectors are reserved for use in a future release.
- **SECTION USER CHAN LINE (X) and LINE (Y):** These connectors provide an interface that allows access to the section user channel (F1 byte) of the service and protection OC-48 high-speed lines. These connectors are reserved for use in a future release.

- **SECTION OW (LOC OW) LINE (X) and LINE (Y):** These connectors provide an interface that allows an orderwire (OW) set access to the section orderwire (E1 byte) for voice communications. For more information about the orderwire interface, refer to Section 6, "Operations Interfaces."
- **LINE OW (EXP OW) LINE (X) and LINE (Y):** These connectors provide an interface that allows an orderwire set access to the line orderwire (E2 byte) for voice communications. For more information about the orderwire interface, refer to Section 6, "Operations Interfaces."
- **NATL USE LINE (X) and LINE (Y):** These connectors are not used on this shelf.
- **INTRAOFFICE LAN LINE (X) and LINE (Y):** These connectors are reserved for use in a future release.
- **PWR:** These connectors provide an interface to the –48 V power filters on the filter panel. For more information about power, refer to Section 4, "Power."

Except for power, all the cabling entering the interconnection panel is connectorized with D-subminiature connectors. The power cables are connected to RF filters on the interconnection panel using push-on type connectors.

The filter panel (Figure 3-12) provides –48 V power filters, a power on (PWR ON) LED, and an electrostatic discharge jack for the High-Speed Shelf. For more information about the filter panel, refer to Section 6, "Operations Interfaces."

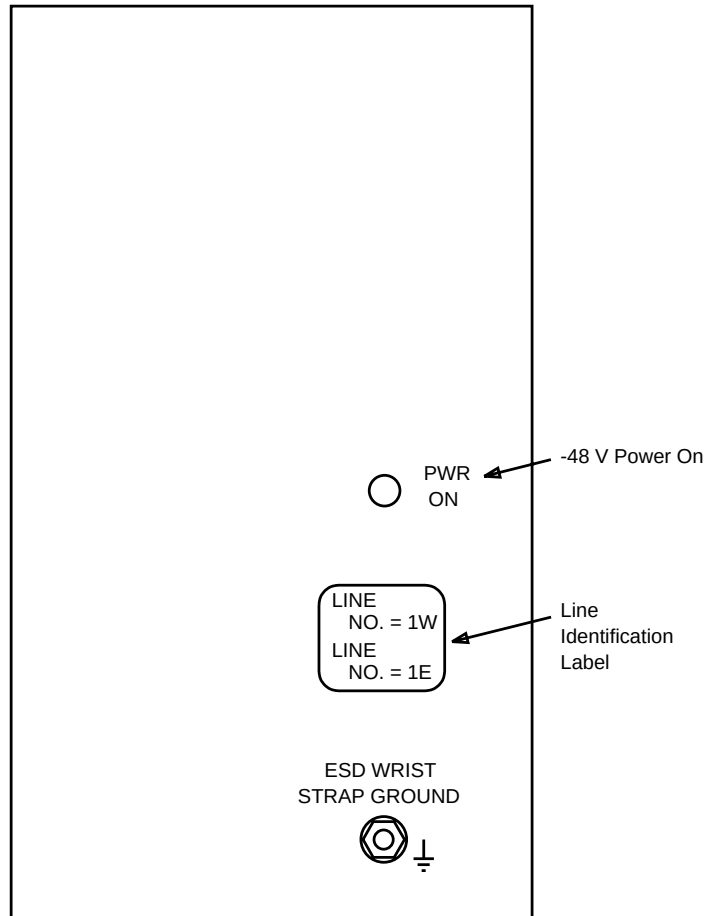


Figure 3-12. Filter Panel (High-Speed Shelf and Enhanced High-Speed Shelf)

The High-Speed Shelf has two front covers (Figure 3-13). One cover covers the interconnection panel and the other covers the circuit packs. Openings are provided in the top cover that allow the filter panel to show through. The covers are hinged and can be easily removed for maintenance access.

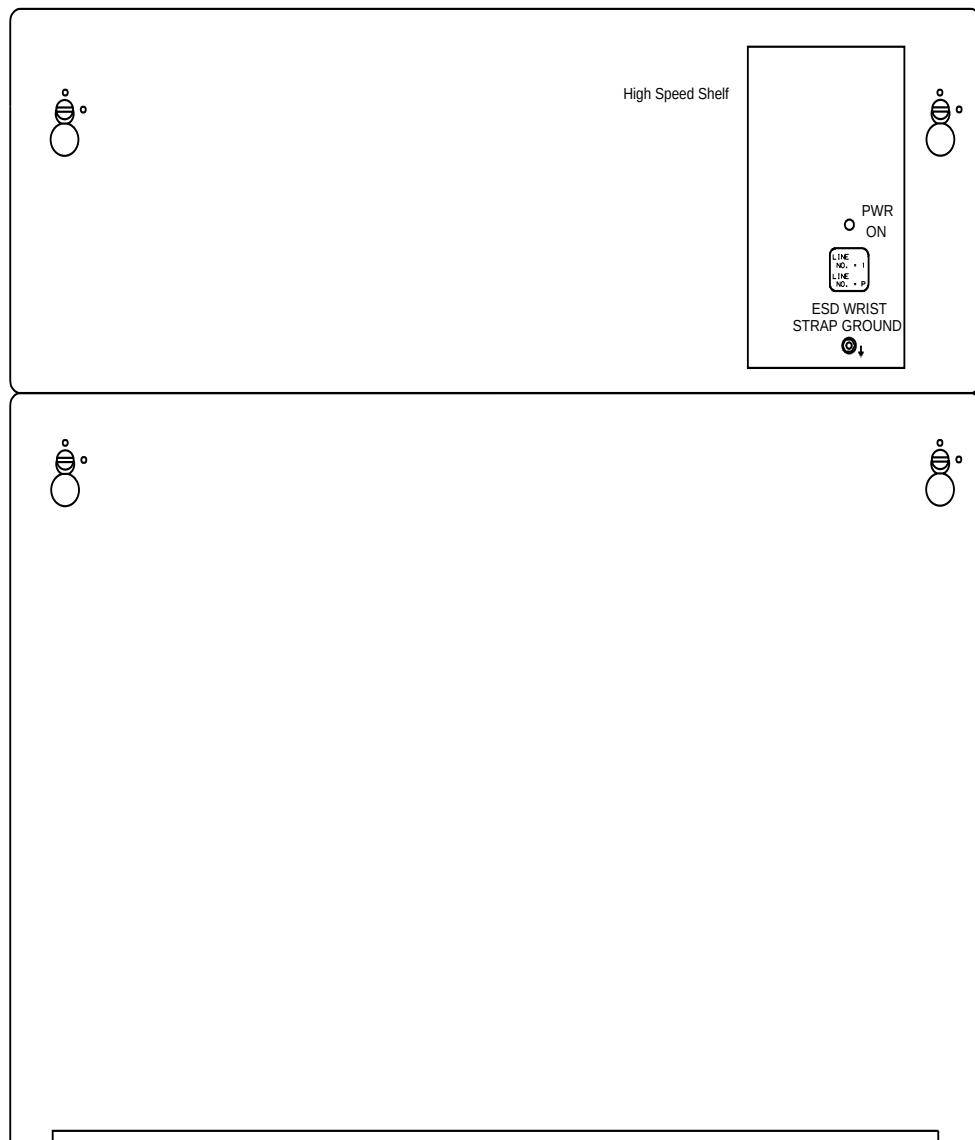


Figure 3-13. High-Speed Shelf (with Covers)

Enhanced High-Speed Shelf

Purpose of Enhanced High-Speed Shelf

The Enhanced High-Speed Shelf* serves as an interface for OC-48 high-speed lines 1W and 1E. The Enhanced High-Speed Shelf (Figure 3-13) consists of the fully connectorized interconnection panel, a filter panel, and 12 circuit pack slots. It measures 21.5 inches high by 21.5 inches wide by 12 inches deep and fits in a standard 23-inch wide network bay framework.

Enhanced High-Speed Shelf Circuit Packs

The Enhanced High-Speed Shelf (Figure 3-14) provides the following circuit pack slots:

- **TRMTR 1W:** One slot is provided for the 739B2, 739C2, 739E[1-16], 739H[1-66], 739J2, 739J3, 739R2, 739S2 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, or 739S5 OC48 Transmitter, Add/Drop (OC48 TRMTR [A/D]) circuit pack for OC-48 high-speed line 1W. The 739J3 OC48 TRMTR (A/D) circuit pack is compatible with Release 5.0.1-ADR or later software. The 739R2 and 739S2 OC48 TRMTR (A/D) circuit packs are compatible with Release 6.0.0-ADR or later software. The 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 739E[1-16] circuit packs are compatible with Release 7.2.0-ADR and later software.
- **RCVR 1W:** One slot is provided for the 839B2, 839B4B, 839B5, or 839E5 OC48 Receiver, Add/Drop (OC48 RCVR [A/D]) circuit packs for OC-48 high-speed line 1W. The 839B4B and 839B5 OC48 RCVR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 839E5 circuit packs are compatible with Release 7.2.0 and later software.
- **TRMTR 1E:** One slot is provided for the 739B2, 739C2, 739E[1-16], 739H[1-16], 739J2, 739J3, 739R2, 739S2, 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, or 739S5 OC48 Transmitter, Add/Drop (OC48 TRMTR [A/D]) circuit pack for OC-48 high-speed line 1E. The 739J3 OC48 TRMTR (A/D) circuit pack is compatible with Release 5.0.1-ADR or later software. The 739R2 and 739S2 OC48 TRMTR (A/D) circuit packs are compatible with Release 6.0.0-ADR or later software. The 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 739E[1-16] circuit packs are compatible with Release 7.2.0 and later software.

* The Enhanced High-Speed Shelf is used only on FT-2000 OC-48 Add/Drop-Rings Terminal for two-Fiber Rings equipped with the J68974E FT-2000 OC-48 Enhanced Lightwave Terminating Bay.

- **RCVR 1W:** One slot is provided for the 839B2, 839B4B, 839B5, or 839E5 OC48 Receiver, Add/Drop (OC48 RCVR [A/D]) circuit packs for OC-48 high-speed line 1E. The 839B4B and 839B5 OC48 RCVR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 839E5 circuit pack is compatible with Release 7.2.0 and later software.
- **LNCTL:** One slot is provided for the LAA28 Line Controller — 4 Megabyte (LNCTL [4 MEG]) circuit pack.
- **OHCTL 1W:** One slot is provided for the LAA21 Overhead Controller — Terminal (OHCTL [TERM]) circuit pack for OC-48 high-speed line 1W.
- **OHCTL 1E:** One slot is provided for the LAA21 OHCTL (TERM) circuit pack for OC-48 high-speed line 1E.
- **LOHCTL 1W and 1E:** Two slots are reserved for the Overhead Controller - LAN (LOHCTL) circuit packs in a future release.

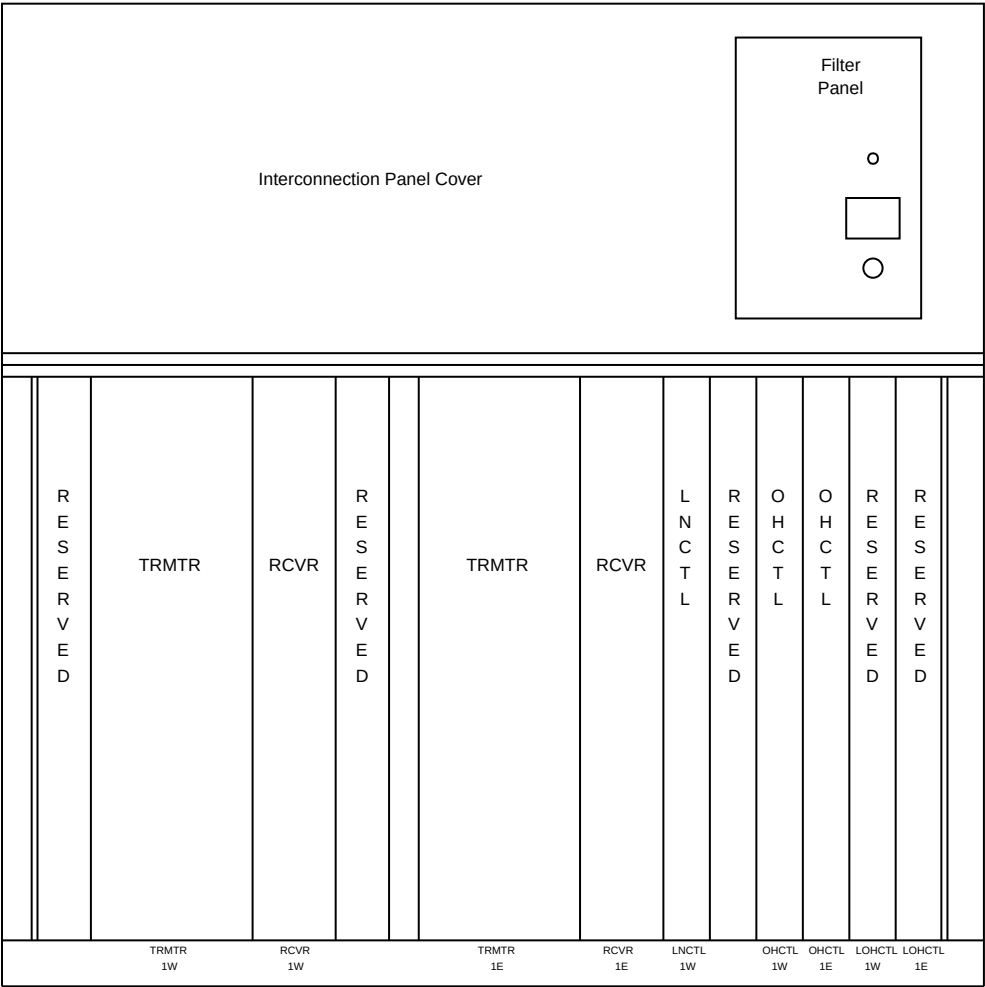


Figure 3-14. Enhanced High-Speed Shelf (Without Circuit Pack Cover)

Table 3-4 also provides circuit pack slot/circuit pack code/software compatibility information.

Table 3-4. Enhanced High-Speed Shelf Circuit Pack Slot/Circuit Pack Code/Software Compatibility

Circuit Pack Slot	Supported Circuit Pack Codes	ADR Compatible Software (Note)							
		R3.0.1	R4.0.0	R4.1.0	R5.0._	R6.0._	R7.0.0	R7.1.0	R7.2.0
TRMTR 1W	739B2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739C2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739E[1-16] OC48 TRMTR								X
	739H[1-16] OC48 TRMTR								X
	739J2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739J3† OC48 TRMTR				X	X	X	X	DA
	739R2† OC48 TRMTR					X	X	X	DA
	739S2† OC48 TRMTR					X	X	X	DA
	739B4 OC48 TRMTR						X	X	X
	739J4 OC48 TRMTR						X	X	X
	739B5 OC48 TRMTR						X	X	X
	739C5 OC48 TRMTR						X	X	X
	739J5 OC48 TRMTR						X	X	X
	739P5 OC48 TRMTR						X	X	X
	739R5 OC48 TRMTR						X	X	X
	739S5 OC48 TRMTR						X	X	X
RCVR 1W	839B2† OC48 RCVR	X	X	X	X	X	X	X	DA
	839B4B OC48 RCVR						X	X	X
	839B5 OC48 RCVR						X	X	X
	839E5 OC48 RCVR								X
LNCTL	LAA28 LNCTL	X	X	X	X	X	X	X	X
OHCTL 1W	LAA22 OHCTL (TERM)	X	X	X	X	X	X	X	X
OHCTL 1E	LAA21 OHCTL (TERM)	X	X	X	X	X	X	X	X
TRMTR 1E	739B2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739C2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739E[1-16] OC48 TRMTR								X
	739H[1-16] OC48 TRMTR								X
	739J2† OC48 TRMTR	X	X	X	X	X	X	X	DA
	739J3† OC48 TRMTR				X	X	X	X	DA
	739R2† OC48 TRMTR					X	X	X	DA
	739S2† OC48 TRMTR					X	X	X	DA
	739B4 OC48 TRMTR						X	X	X
	739J4 OC48 TRMTR						X	X	X
	739B5 OC48 TRMTR						X	X	X
	739C5 OC48 TRMTR						X	X	X
	739J5 OC48 TRMTR						X	X	X
	739P5 OC48 TRMTR						X	X	X
	739R5 OC48 TRMTR						X	X	X
	739S5 OC48 TRMTR						X	X	X
RCVR 1E	839B2† OC48 RCVR	X	X	X	X	X	X	X	DA
	839B4B OC48 RCVR						X	X	X
	839B5 OC48 RCVR						X	X	X
	839E5 OC48 RCVR								X

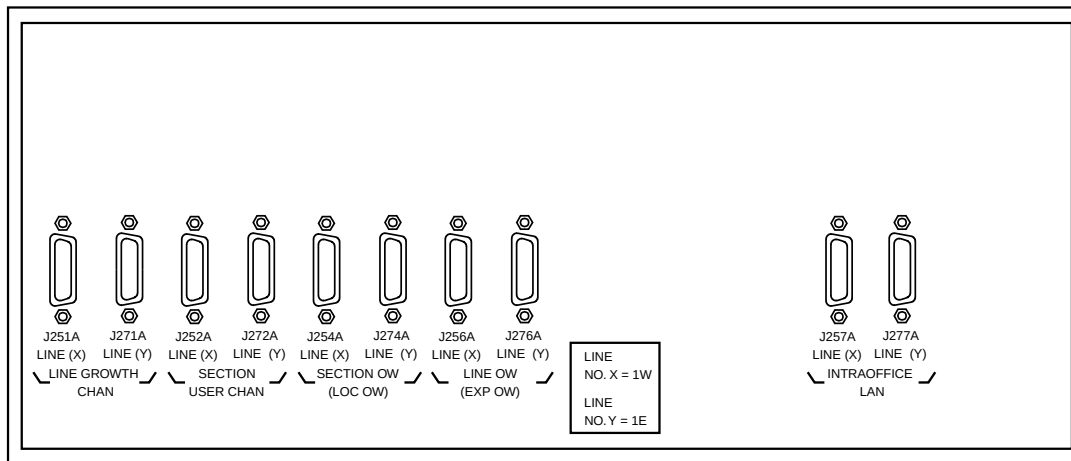
Note: Releases 3.0.1-ADR, 4.0.0-ADR, and 4.1.0-ADR software were discontinued in June 1996.

Circuit pack keying prevents circuit packs from being accidentally inserted in incorrect slots.

For detailed circuit pack descriptions and software compatibility information, refer to Section 7, "Circuit Pack Descriptions."

Enhanced High-Speed Shelf Interfaces

Front access cabling is provided through the interconnection panel (Figure 3-15).



nc-ft2000-040

Figure 3-15. Interconnection Panel (Enhanced High-Speed Shelf)

The interconnection panel provides the following interfaces:

- **LINE GROWTH CHAN LINE (X) and LINE (Y):** These connectors provide an interface that allows access to the line growth channel (Z1 and Z2 bytes) of the OC-48 high-speed lines. These connectors are reserved for use in a future release.
- **SECTION USER CHAN LINE (X) and LINE (Y):** These connectors provide an interface that allows access to the section user channel (F1 byte) of the service and protection OC-48 high-speed lines. These connectors are reserved for use in a future release.
- **SECTION OW (LOC OW) LINE (X) and LINE (Y):** These connectors provide an interface that allows an orderwire set access to the section orderwire (E1 byte) for voice communications (local orderwire). For more information about the orderwire interface, refer to Volume I, Section 6, "Operations Interfaces."
- **LINE OW (EXP OW) LINE (X) and LINE (Y):** These connectors provide an interface that allows an orderwire set access to the line orderwire (E2 byte) for voice communications (express orderwire). For more information about the orderwire interface, refer to Volume I, Section 6, "Operations Interfaces."
- **INTRAOFFICE LAN LINE (X) and LINE (Y):** These connectors are reserved for use in a future release.
- **PWR:** These connectors provide an interface to the –48 V power filters on the filter panel. For more information about power, refer to Volume I, Section 4, "Power."

Except for power, all the cabling entering the interconnection panel is connectorized with D-subminiature connectors. The power cables are connected to RF filters on the interconnection panel using push-on type connectors.

The filter panel (Figure 3-12) provides –48 V power filters, a PWR ON LED, and an electrostatic discharge jack for the Enhanced High-Speed Shelf. For more information about the filter panel, refer to Volume I, Section 6, "Operations Interfaces."

The Enhanced High-Speed Shelf has two front covers (Figure 3-16). One cover covers the interconnection panel and the other covers the circuit packs. Openings are provided in the top cover that allow the filter panel to show through. The covers are hinged and can be easily removed for maintenance access.

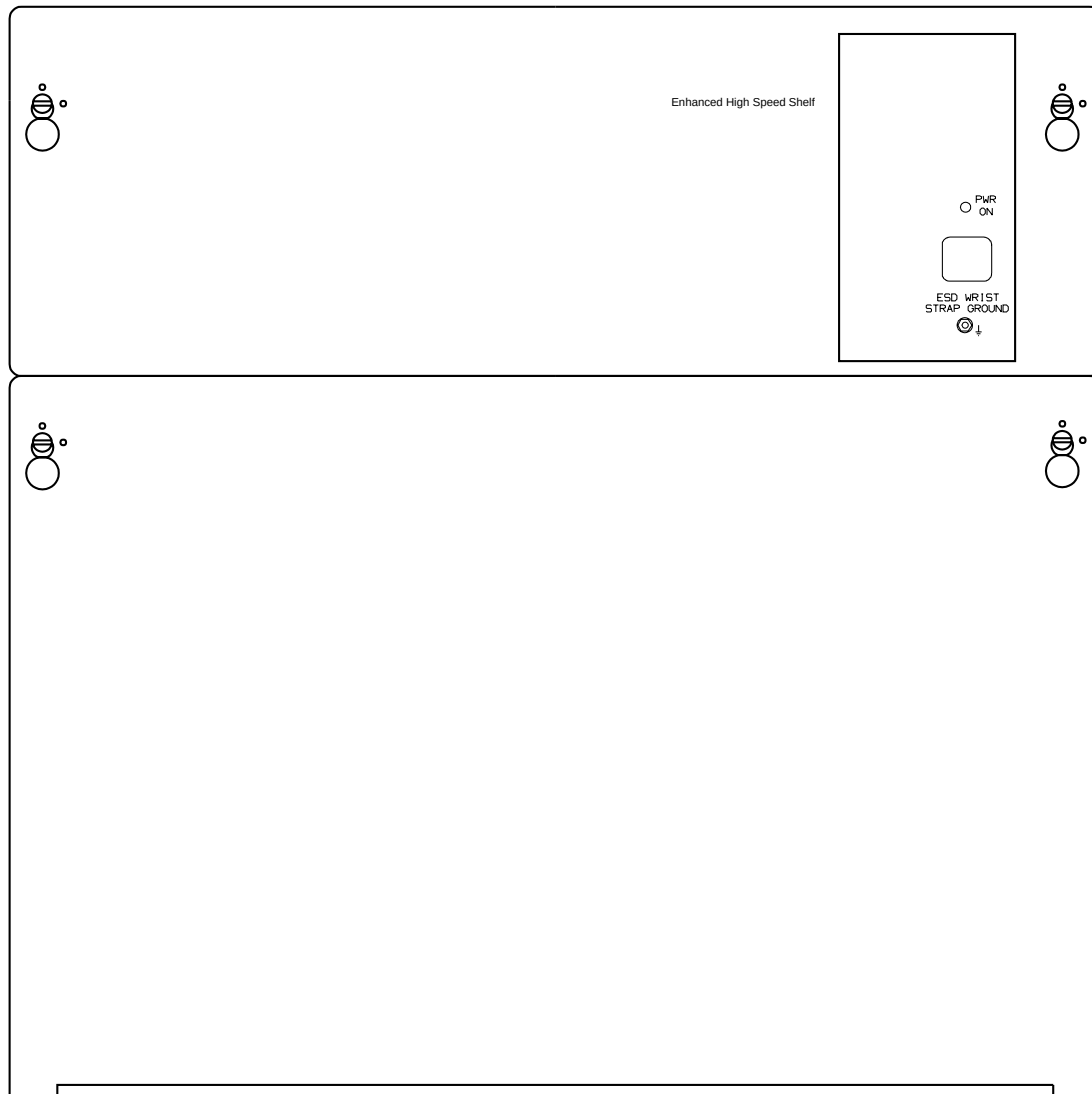


Figure 3-16. Enhanced High-Speed Shelf (with Covers)

Heat Baffle

The heat baffles prevent heat rising from the shelf below from entering the shelf above. A front cover is provided to maintain a flush appearance.

PC Tray

The PC tray is a combination PC tray and heat baffle. It provides a place to hold a portable PC (for example, the *NCR** 3170 computer) weighing up to 25 pounds and measuring up to 20 inches wide and 12 inches deep. It can also serve as a writing tray. This fixture folds into the heat baffle when not in use. A front cover is also provided to maintain a flush appearance. *

Screen

The screen provides a filter panel at the bottom of the bay and an air intake plenum for cooling purposes.

* Trademark of NCR Corporation.

FT-2000 OC-48 Repeater (R-Bay)

Equipment Packages

The FT-2000 OC-48 Repeater Bay is available in the following equipment packages:

- **FT-2000 OC-48 Repeater Bay:** The FT-2000 OC-48 Repeater Bay (Figure 3-17) consists of the J68974R FT-2000 OC-48 Repeater Bay and Release 2.1.0-RPTR*, Release 4.0.0-RPTR, or Release 6.0.0-RPTR software.

The J68974R FT-2000 OC-48 Repeater Bay consists of a power distribution and fuse panel, up to three Repeater Shelf — System Controllers, and a PC tray mounted in a single seismic network bay framework.

This equipment package supports up to six OC-48 high-speed lines.

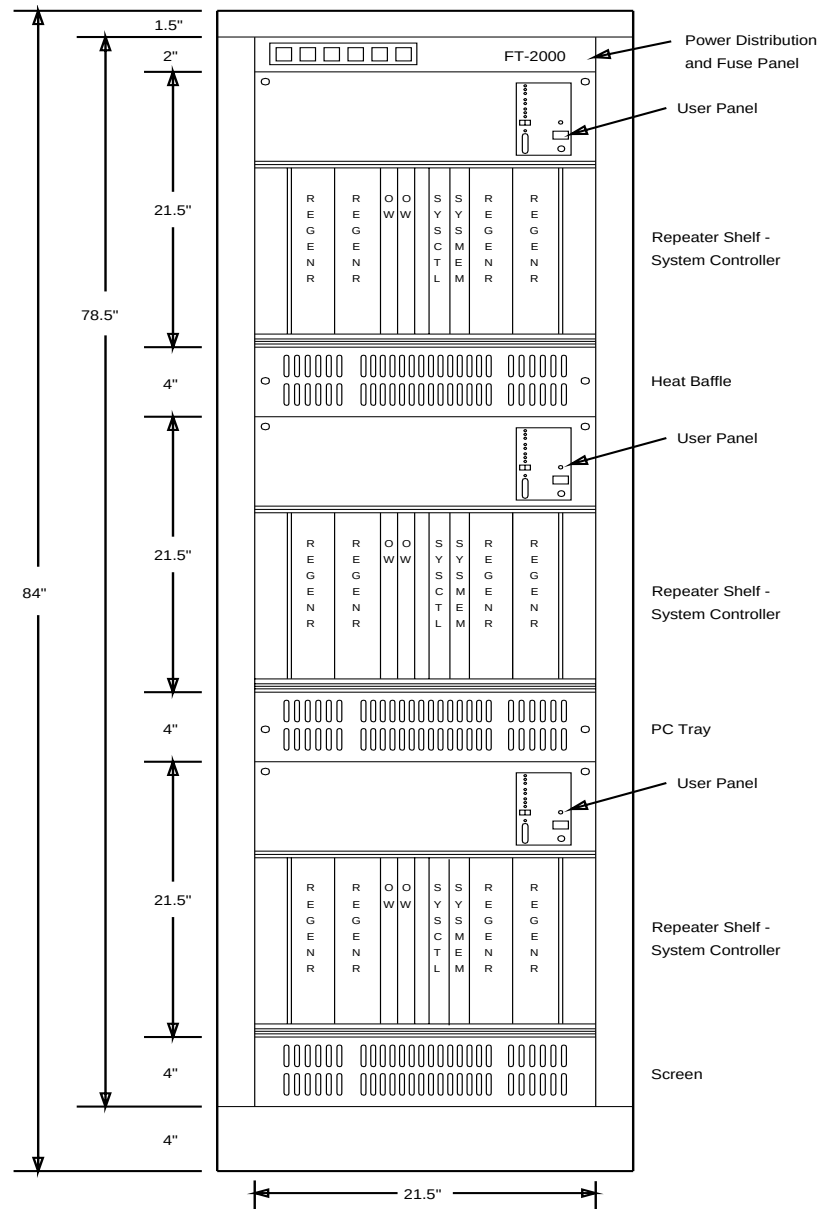
- **FT-2000 OC-48 Repeater Shelf:** The FT-2000 OC-48 Repeater Shelf (Figure 3-18) consists of a miscellaneous mounted shelf arrangement and Release 2.1.0-RPTR*, Release 4.0.0-RPTR, or Release 6.0.0-RPTR software.

This shelf arrangement consists of one Repeater Shelf — System Controller, a PC tray, and a user panel equipped with two 10-amp fuses. (Power fusing is performed on the shelf level.) It is possible to mount the FT-2000 OC-48 Repeater Shelf in several types of network bay frameworks and certain *ESS*[™] switch frameworks. This shelf arrangement may be used in existing bays where space is limited or in cross route locations.

This equipment package supports two OC-48 high-speed lines.

*

* Release 2.1.0-RPTR software has been discontinued.



tpa 814210/01

Figure 3-17. FT-2000 OC-48 Repeater Bay

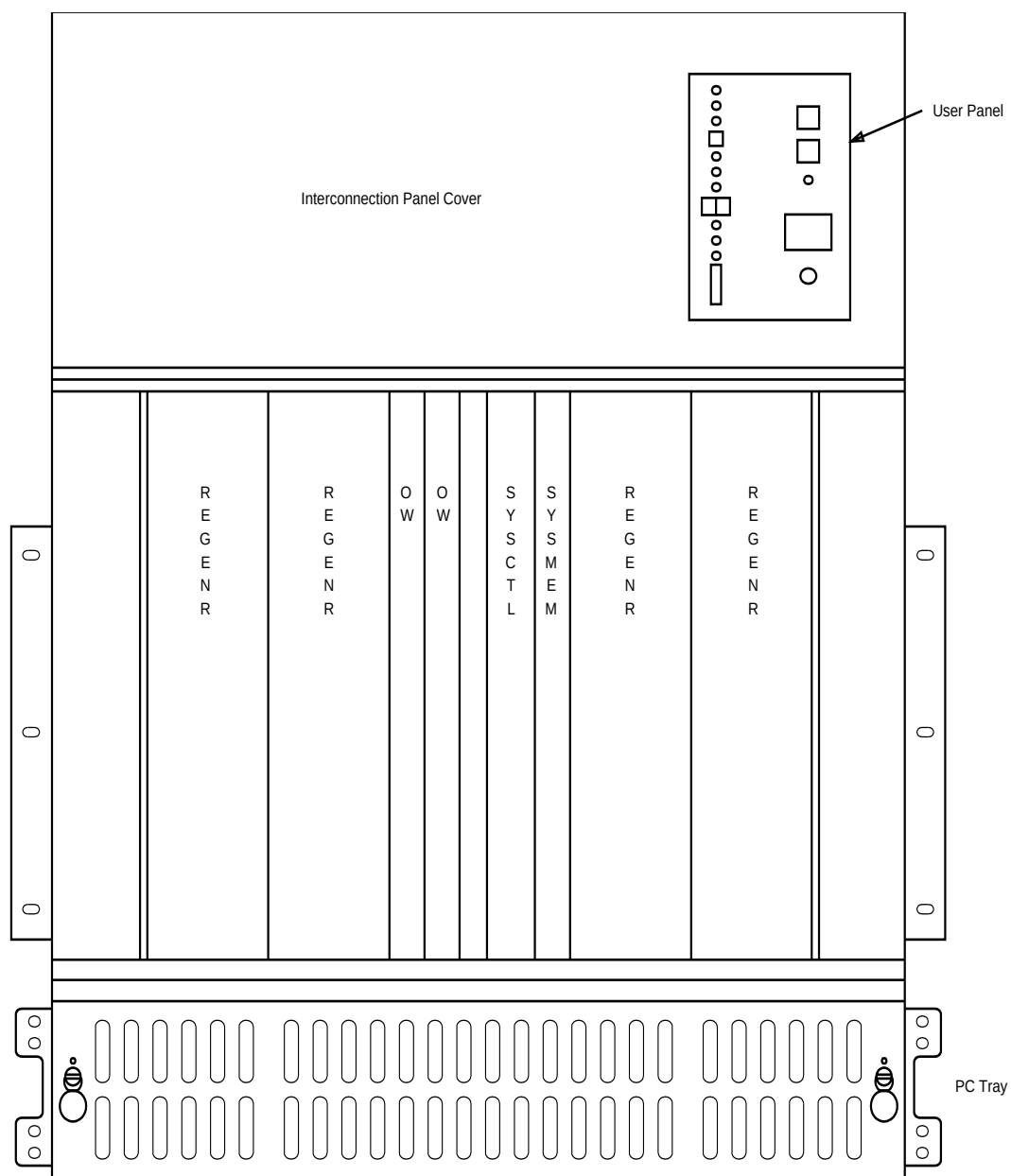


Figure 3-18. FT-2000 OC-48 Repeater Shelf (Miscellaneously Mounted)

Shelf Descriptions

Power Distribution and Fuse Panel

The power distribution and fuse panel (Figure 3-19) is located at the top of each FT-2000 OC-48 Lightwave System bay. It measures 2 inches high by 21.5 inches wide by 12 inches deep. The power distribution and fuse panel has a removable front cover that allows front access for shelf power connections. It accepts two 8-gauge power feeder stubs and distributes power to the shelves in the bay. The power distribution and fuse panel is also equipped with six 10-amp fuses (two for each shelf in the bay). Three fuses are for power feeder A and three fuses are for power feeder B. For more information about power distribution, refer to Volume I, Section 4, "Power."

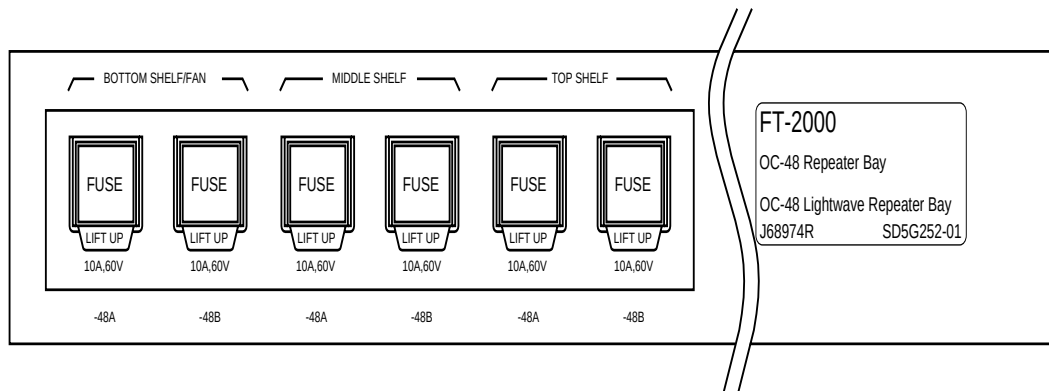


Figure 3-19. Power Distribution and Fuse Panel

Repeater Shelf — System Controller

Purpose of Repeater Shelf — System Controller

The Repeater Shelf — System Controller provides high-level system control functions and regenerates OC-48 signals for two OC-48 high-speed lines. The Repeater Shelf — SYSCTL (Figure 3-20) consists of the fully connectorized interconnection panel, a user panel, and eight circuit pack slots. It measures 21.5 inches high by 21.5 inches wide by 12 inches deep and fits in a standard 23-inch wide seismic network bay framework. The miscellaneous mounted version can also be mounted in certain *ESS* switch frameworks.

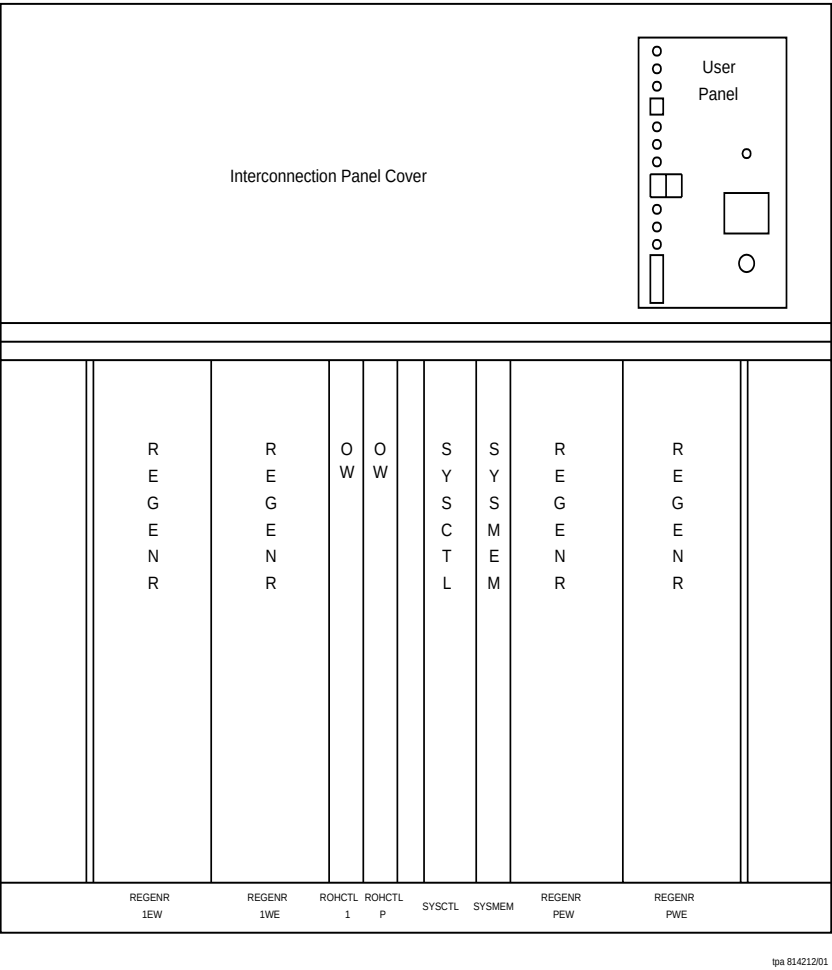


Figure 3-20. Repeater Shelf — System Controller (Without Circuit Pack Cover)

Repeater Shelf — System Controller Circuit Packs

The Repeater Shelf — System Controller (Figure 3-20) provides the following circuit pack slots:

- **REGENR 1EW and 1WE:** Two slots are provided for the 39B1, 39C1, 39B2, 39C2, 39J2, 39R2, or 39S2 OC48 Regenerator (OC48 REGENR) circuit packs for one OC-48 high-speed line. The 39B1 and 39C1 OC48 REGENR circuit packs were discontinued and replaced with the 39B2 and 39C2 OC48 REGENR circuit packs, respectively. The 39J2 OC48 REGENR circuit pack is compatible with Release 4.0.0-RPTR or later software. The 39R2 and 39S2 OC48 REGENR circuit packs are compatible with Release 6.0.0-RPTR or later software.
- **ROHCTL 1 and ROHCTL P:** Two slots are provided for the LAA14 Orderwire (OW) circuit pack. The LAA14 OW circuit pack is required to access the OC-48 section overhead bytes for orderwire. The LAA14 OW circuit pack is compatible with Release 2.1.0-RPTR* or later software.
- **SYSCTL:** One slot is provided for the LAA23 or LAA23B System Controller (SYSCTL) circuit pack. The LAA23 SYSCTL circuit pack has been discontinued and replaced with the LAA23B SYSCTL circuit pack. The LAA23 and LAA23B SYSCTL circuit packs are compatible with Release 2.1.0-RPTR* or later software.
- **SYSTEMEM:** One slot is provided for the LAA25 System Memory (SYSTEMEM) circuit pack.
- **REGENR PEW and PWE:** Two slots are provided for the 39B1, 39C1, 39B2, 39C2, 39J2, 39R2, or 39S2 OC48 Regenerator (OC48 REGENR) circuit packs for one OC-48 high-speed line. The 39B1 and 39C1 OC48 REGENR circuit packs were discontinued and replaced with the 39B2 and 39C2 OC48 REGENR circuit packs, respectively. The 39J2 OC48 REGENR circuit pack is compatible with Release 4.0.0-RPTR or later software. The 39R2 and 39S2 OC48 REGENR circuit packs are compatible with Release 6.0.0-RPTR or later software.

*

Release 2.1.0-RPTR software has been discontinued.

Table 3-5 also provides circuit pack slot/circuit pack code/software compatibility information.

Table 3-5. Repeater Shelf — System Controller Circuit Pack Slot/Circuit Pack Code/Software Compatibility

Circuit Pack Slot	Supported Circuit Pack Codes	Compatible Software (Note)		
		R2.1.0-RPTR	R4.0.0-RPTR	R6.0.0-RPTR
REGENR 1EW and 1WE	39B1 OC48 REGENR*	X	X	X
	39B2 OC48 REGENR	X	X	X
	39C1 OC48 REGENR*	X	X	X
	39C2 OC48 REGENR	X	X	X
	39J2 OC48 REGENR		X	X
	39R2 OC48 REGENR			X
	39S2 OC48 REGENR			X
ROHCTL 1 and P	LAA14 OW	X	X	X
SYSCTL	LAA23 SYSCTL†	X	X	X
	LAA23B SYSCTL	X	X	X
SYSMEM	LAA25 SYSMEM	X	X	X

Note: Release 2.0.1-RPTR has been discontinued.

* The 39B1 and 39C1 OC48 REGENR circuit packs were discontinued in December 1995.

† The LAA23 SYSCTL circuit pack has been discontinued and replaced with the LAA23B SYSCTL circuit pack.

Circuit pack keying prevents circuit packs from being accidentally inserted in incorrect slots.

For detailed circuit pack descriptions and software compatibility information, refer to Volume I, Section 7, "Circuit Pack Descriptions."

Repeater Shelf — System Controller Interfaces

Front access cabling is provided through the interconnection panel (Figure 3-21).

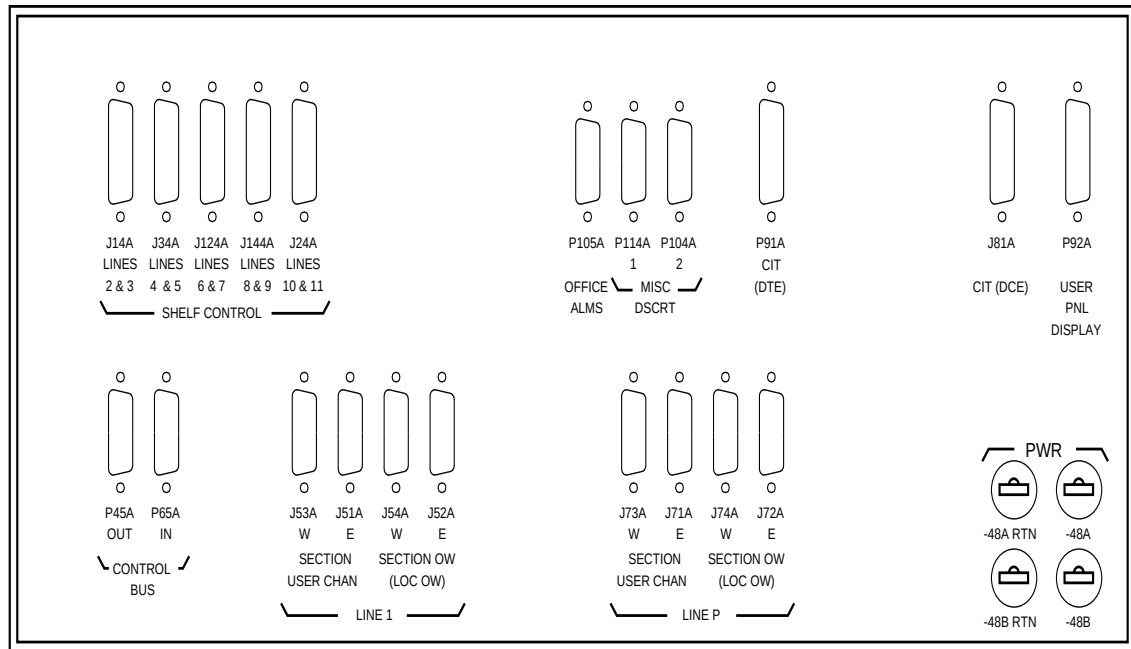


Figure 3-21. Interconnection Panel (Repeater Shelf — System Controller)

The interconnection panel provides the following interfaces:

- **SHELF CONTROL:** These connectors are reserved for use in a future release. (These connectors and the associated cabling are not provided on the miscellaneous mounted FT-2000 OC-48 Repeater Shelf.)
- **OFFICE ALMS:** This connector provides an interface to the local office audible and visible alarms. For more information about this office alarm interface, refer to Volume I, Section 6, "Operations Interfaces."
- **MISC DSCRT 1 and 2:** These connectors provide an interface that allows an operations system to monitor equipment collocated with the FT-2000 OC-48 Repeater Bay. For more information about this user-settable miscellaneous discrete interface, refer to Volume I, Section 6, "Operations Interfaces."
- **CIT (DTE):** This connector provides an interface to a modem that allows dial-up access. For more information about the CIT (DTE) interface, refer to Volume I, Section 6, "Operations Interfaces."
- **CIT (DCE):** This connector provides an interface to the CIT (DCE) port on the user panel that allows local access. For more information about the CIT (DCE) interface, refer to Volume I, Section 6, "Operations Interfaces."

- **USER PNL DISPLAY:** This connector provides an interface to the alarm and status indicators, and display on the user panel. For more information about the user panel display and indicators, refer to Volume I, Section 6, "Operations Interfaces."
 - **CONTROL BUS OUT and IN:** These connectors are reserved for use in a future release. (These connectors and the associated cabling are not provided on the miscellaneous mounted FT-2000 OC-48 Repeater Shelf.)
 - **SECTION USER CHAN LINE 1 and LINE P:** These connectors are reserved for use in a future release.
 - **SECTION OW (LOC OW) LINE 1 and LINE P:** These connectors provide an interface that allows an orderwire set access to the section orderwire (E1 byte) for voice communications. For more information about the orderwire interface, refer to Volume I, Section 6, "Operations Interfaces."
- PWR:** These connectors provide an interface to the –48 V power filters on the user panel. For more information about power, refer to Volume I, Section 4, "Power."

Except for power, all the cabling entering the interconnection panel is connectorized with D-subminiature connectors. The power cables are connected to RF filters on the interconnection panel using push-on type connectors.

The user panel provides -48 V power filters, a PWR ON LED, an electrostatic discharge jack, and system level information. Figure 3-22 shows the user panel for the miscellaneously mounted FT-2000 OC-48 Repeater Shelf. Figure 3-7 shows the user panel for the FT-2000 OC-48 Repeater Bay except the line identification label is the same as shown in Figure 3-22. For more information about the user panel, refer to Volume I, Section 6, "Operations Interfaces."

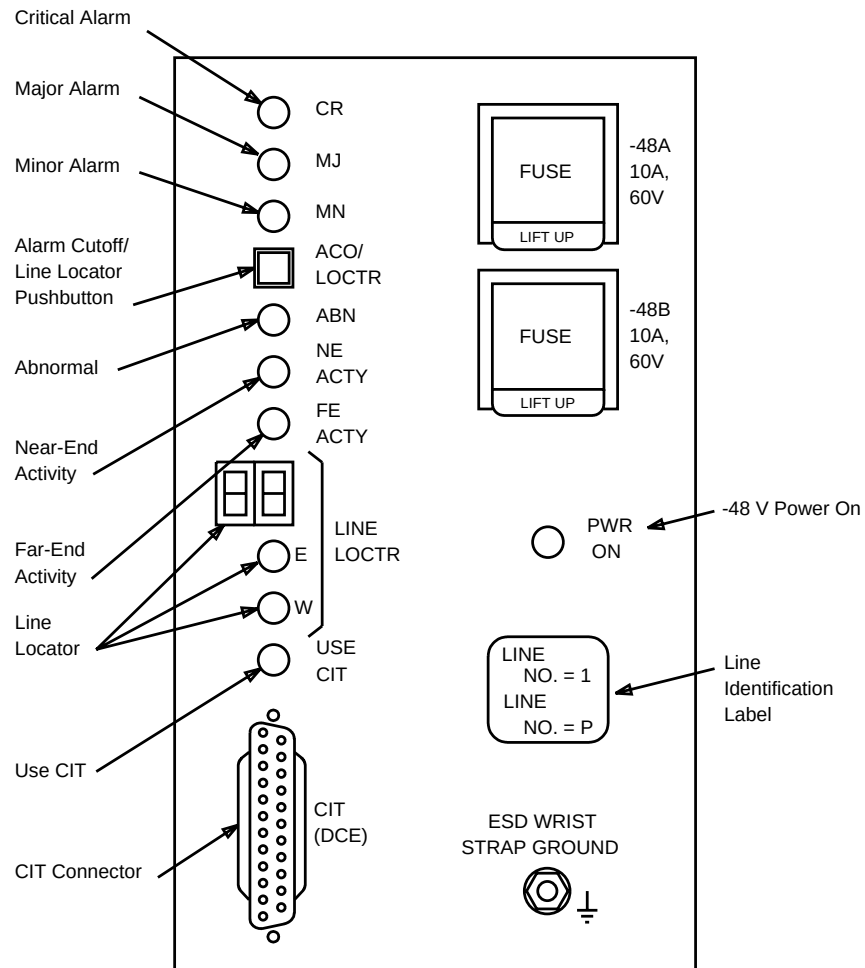


Figure 3-22. User Panel (FT-2000 OC-48 Repeater Shelf) for Miscellaneous Mounting Only

The Repeater Shelf — System Controller has two front covers. One covers the interconnection panel and the other covers the circuit packs. Openings are provided in the top cover that allow the user panel to show through and provide access to the CIT port. The covers are hinged and can be easily removed for maintenance access. Figure 3-23 shows the Repeater Shelf — System Controller with covers for the FT-2000 OC-48 Repeater Bay. Figure 3-24 shows the Repeater Shelf — System Controller with covers for the miscellaneously mounted FT-2000 OC-48 Repeater Shelf.

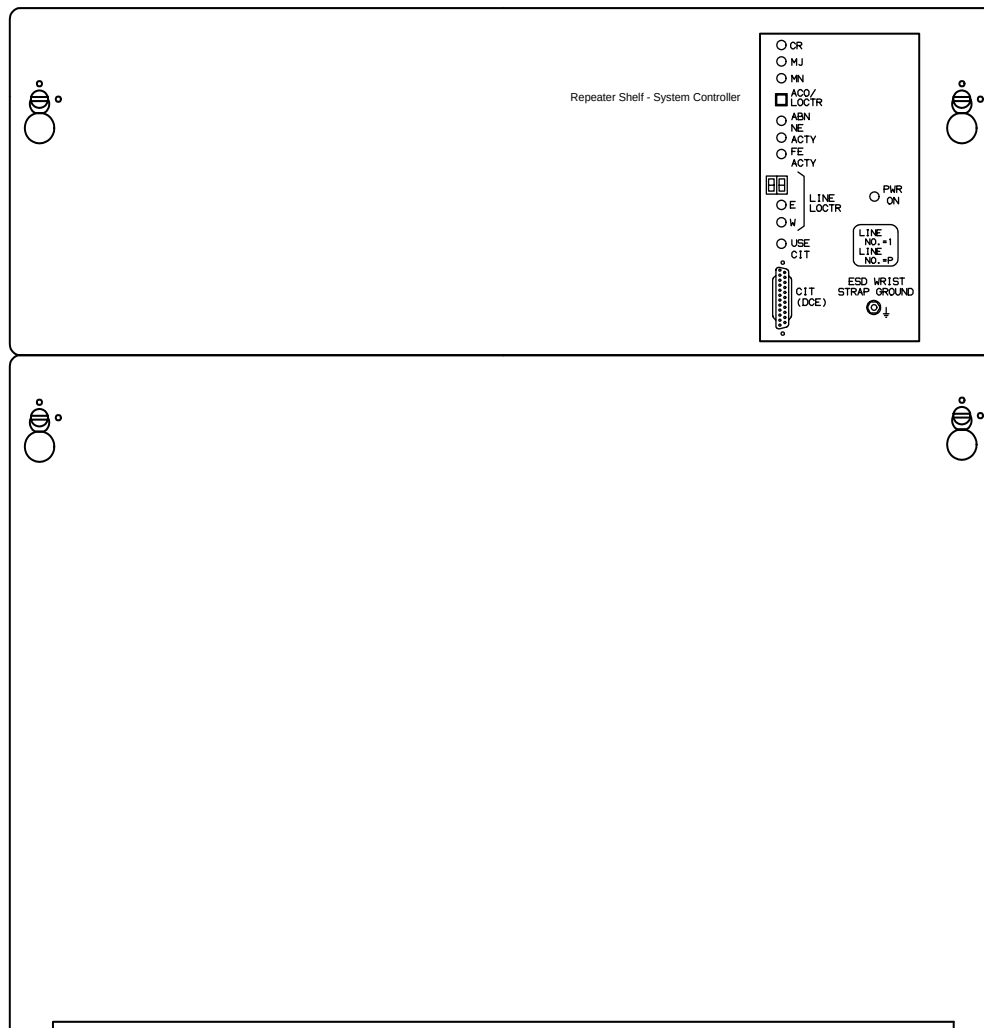


Figure 3-23. Repeater Shelf — System Controller with Covers (FT-2000 OC-48 Repeater Bay)

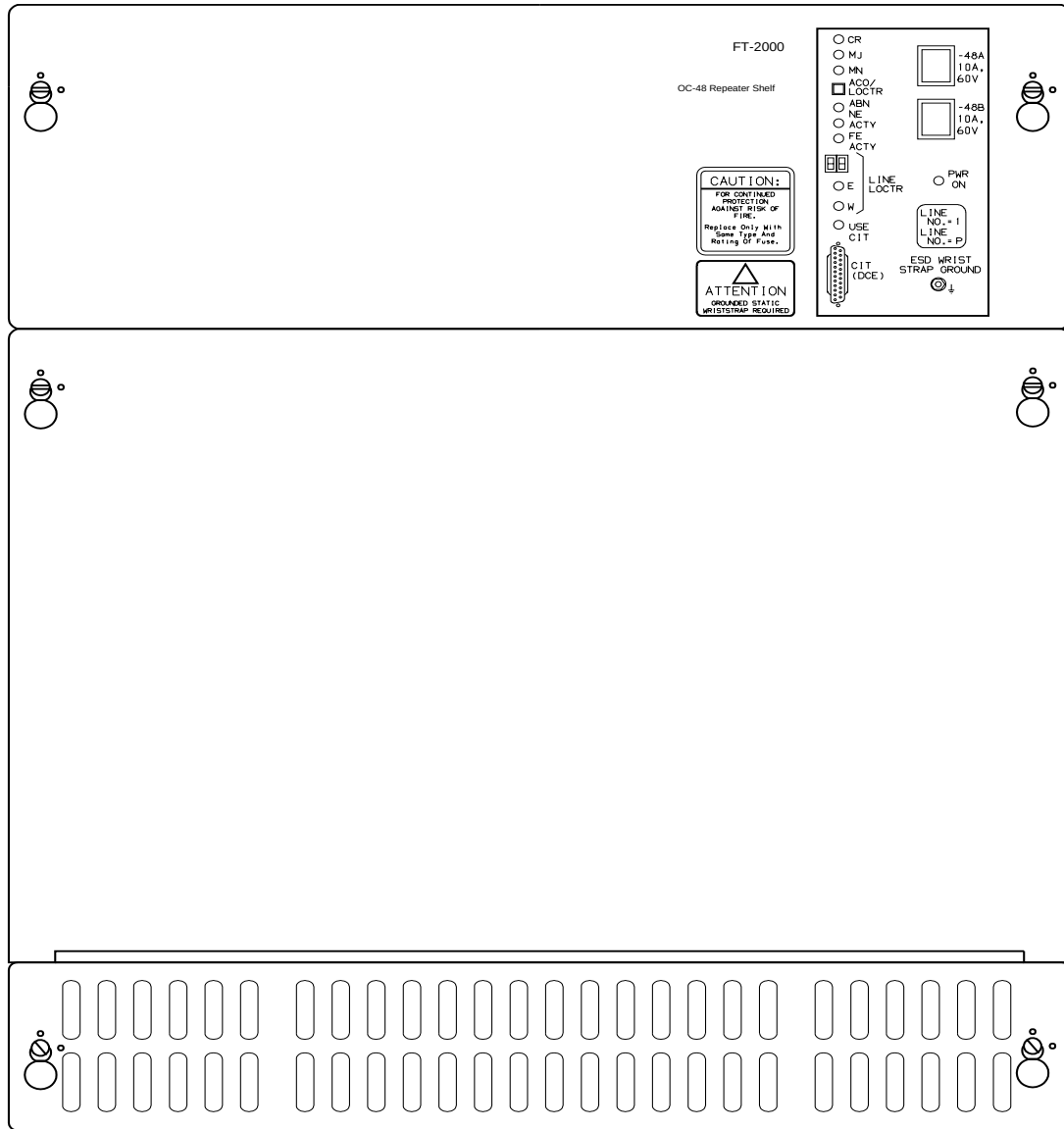


Figure 3-24. Repeater Shelf — System Controller with Covers (FT-2000 OC-48 Repeater Shelf)

Heat Baffle

The heat baffles prevent heat rising from the shelf below from entering the shelf above. A front cover is provided to maintain a flush appearance.

PC Tray

The PC tray is a combination PC tray and heat baffle. It provides a place to hold a portable PC (for example, the *NCR 3170* computer) weighing up to 25 pounds and measuring up to 20 inches wide and 12 inches deep. It can also serve as a writing tray. This fixture folds into the heat baffle when not in use. A front cover is also provided to maintain a flush appearance.

Screen

The screen provides a filter panel at the bottom of the bay and an air intake plenum for cooling purposes.

Upgrades

The FT-2000 OC-48 Repeater Bay equipped with one Repeater Shelf — System Controller can be upgraded in service with up to two additional Repeater Shelf — System Controllers.

For more information about possible upgrades, consult your local Account Executive.

For more information about the FT-2000 OC-48 Lightwave System features release plan and the availability of features, refer to 365-575-100, *FT-2000 OC-48 Lightwave System, Applications, Planning, and Ordering Guide*.

FT-2000 OC-48 Dual ADR Terminal (D-Bay)

Equipment Packages

This equipment package is identified as J68974D. Conceptually, the D-Bay is very similar to the E-Bay. The main differences are:

- A modified power distribution and fuse panel
- A more compact arrangement of the circuit pack shelves
- A new high-speed shelf—which is identified as the "Condensed High-Speed Shelf"
- A new fan assembly—for forced air circulation

This configuration includes two Low-Speed/High-Speed shelf pairs and supports two independent two-fiber ring systems. This means that the bay provides a total of 96 slots. Thus, whenever the D-Bay is fully equipped, it is functionally equivalent to two single OC-48 E-Bays. Figure 3-25 functionally depicts the D-Bay.



NOTE:

In terms of operational features, there are no differences between the E-Bay and the D-Bay. They both require the same circuit packs and same generic software—FT-2000 ADR Release 7 or later.

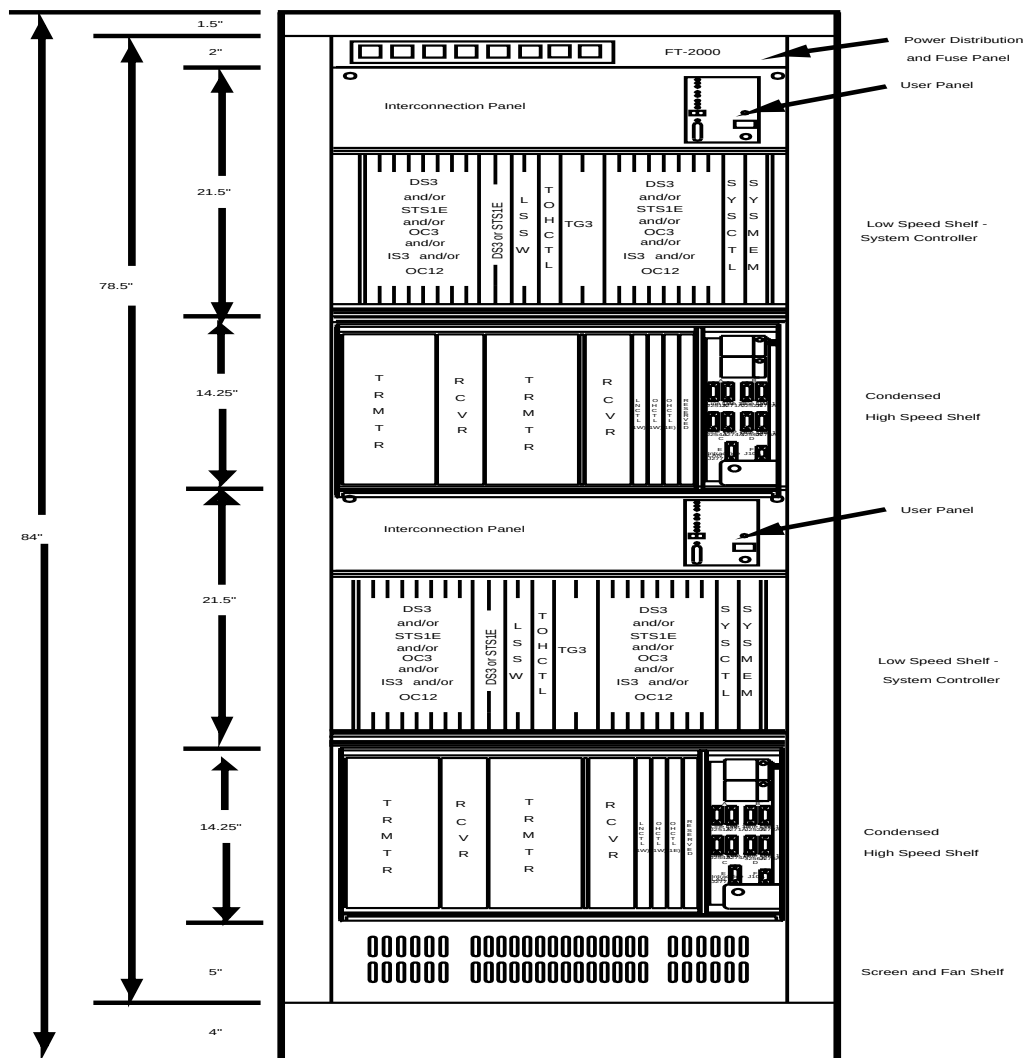


Figure 3-25. FT-2000 OC-48 Dual ADR Terminal for Two-Fiber Rings (D-Bay)

Shelf Descriptions

Power Distribution and Fuse Panel

The power distribution and fuse panel (Figure 3-26) is located at the top of each FT-2000 OC-48 D-Bay. It measures 2 inches high by 21.5 inches wide by 12 inches deep. The power distribution and fuse panel has a removable front cover that allows for shelf power connections. It accepts two 8-gauge power feeder stubs and distributes power to the shelves in the bay. The power distribution and fuse panel is also equipped with eight 10-amp fuses (two for each shelf in the bay). Four fuses are for power feeder A and four fuses are for power feeder B. For more information about power distribution, refer to Volume I, Section 4, "Power."

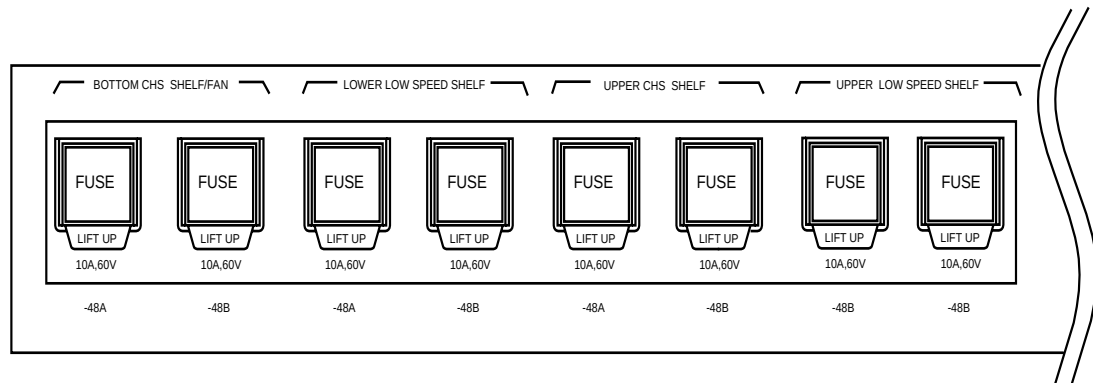


Figure 3-26. FT-2000 OC-48 D-Bay Power Distribution and Fuse Panel

Low-Speed Shelf — System Controller

Purpose of Low-Speed Shelf — System Controller

There are no differences between the Low-Speed Shelf(s) used in the E-Bay and D-Bay. Each low-speed shelf — System Controller provides high-level system control functions and serves as an interface for up to 48 service DS3 and/or EC-1 signals, eight 1+1 protected OC-3 signals (16 unprotected), two 1+1 protected OC-12 signals, or a mix of DS3, EC-1, OC-3 and OC-12 signals. In Release 7.1.0 and later releases, IS-3 and OC-12 low-speed signals can be interfaced. All of these Low-Speed signals can be cross-connected to either the 1W or 1E high-speed line.

The Low-Speed Shelf — System Controller (Figure 3-27) consists of a fully connectorized interconnection panel, a user panel, and 25 circuit pack slots. It measures 21.5 inches high by 21.5 inches wide by 12 inches deep and fits in a standard 23-inch wide seismic network bay framework.

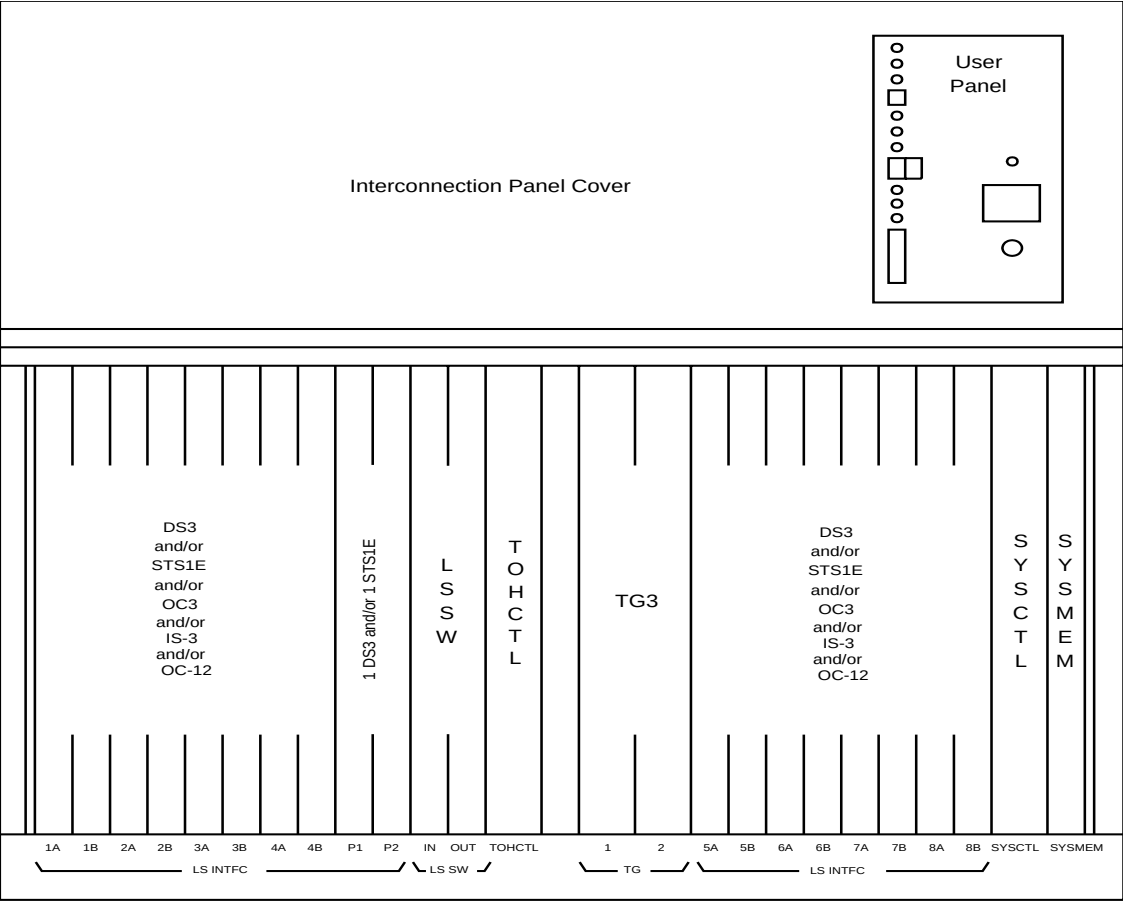
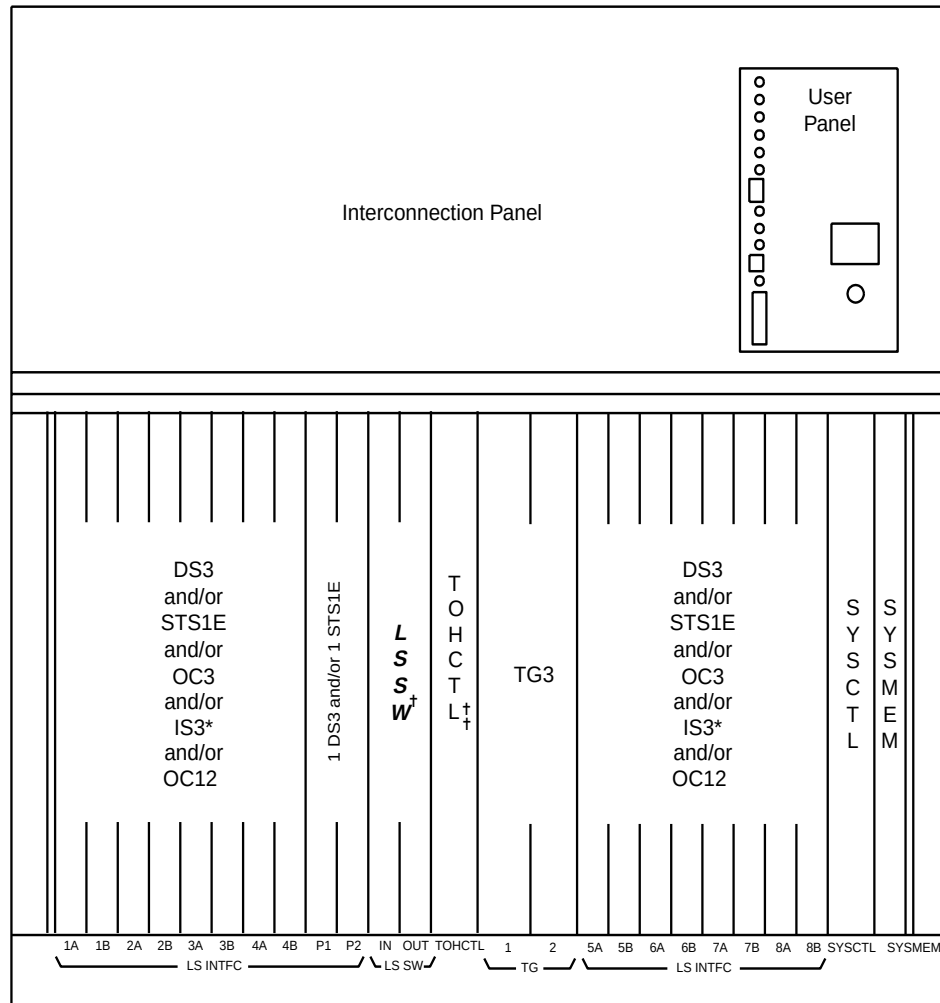


Figure 3-27. Low-Speed Shelf — System Controller (Without Circuit Pack Cover) — ED6G999-31, G5

The Low-Speed Shelf — System Controller (Figure 3-28) is functionally identical to Figure 3-27 and is its new replacement. The main distinguishing differences are: (1) a slightly modified User Panel and (2) a fully integrated backplane.



* IS3 circuit pack is for intraoffice applications only.

† LSSW is required only if you use DS3 and/or STS1E circuit packs with low speed protection (low speed protection is strongly recommended).

† TOHCTL is required only if you use OC3 or OC12 circuit packs and want DCC capabilities.

adr04008.01eJK040997

Figure 3-28. Low-Speed Shelf — System Controller (Without Circuit Pack Cover) — ED6G999-32, G3

Low-Speed Shelf — System Controller Circuit Packs

The Low-Speed Shelf — System Controller (Figure 3-28) provides the following circuit pack slots:

- **LS INTFC 1A - 8B:** Sixteen slots are provided for the following circuit packs.
 - LAA2/LAA2B DS3 Interface (DS3) (S4:4) circuit pack (service):
 - LAA4 STS1 Electrical Interface (STS1E) circuit pack (service):
 - LAA10/LAA10B OC3 Optical Interface — 1.3μm Standard Performance (OC3 [1.3 STD]) circuit pack (service and protection): For optional 1+1 nonrevertive protection, two OC3 (1.3 STD) circuit packs must be placed in adjacent "A" and "B" LS INTFC slots (for example, LS INTFC slots "2A" and "2B").
 - LAA5 IS-3 Optical Interface — The IS-3 circuit pack is compatible with Release 7.1.0 and later software. For intraoffice applications, it is a cost effective alternative to the OC-3 Optical Interface. The IS-3 terminates one input signal, generates one output signal, and supports the transport of both STS-3 and STS-3c formats. It supports interconnection distances up to 2000-feet using multimode fiber.



NOTE:

For both 1+1 nonrevertive protection applications and transport of STS-3c, two IS-3 circuit packs must be placed in adjacent "A" and "B" LS INTFC slots (for example, LS INTFC slots "2A" and "2B").

- T939A OC-12 Optical Interface 1.3μm Standard Performance circuit pack (service and protection): The OC-12 circuit pack is compatible with Release 7.1.0 and later software. It may be used for either intraoffice or interoffice applications. The OC-12 terminates one STS-12/STS-12c input signal and generates one STS-12/STS-12c output signal.

The OC-12 is a "quad-size" circuit pack and thus requires four adjacent low-speed slots as shown in Table 3-6.

Table 3-6. Permitted Slot Locations for the Quad Circuit Pack

OC-12 Quad	Slot Locations	Command Slot	Extension Slots	OC-12 Pack AID
1	1A-1B-2A-2B	1B	1A, 2A, 2B	Is-1b
2	3A-3B-4A-4B	3B	3A, 4A, 4B	Is-3b
3	5A-5B-6A-6B	5B	5A, 6A, 6B	Is-5b
4	7A-7B-8A-8B	7B	7A, 8A, 8B	Is-7b

**NOTE:**

Each quad location consists of one command slot and three extension slots. The command slot address is used in the OC-12 provisioning commands and for reporting any circuit pack and OC-12 signal-related alarm conditions. The extension slots are used to gain access to the STS-3 signals, in conjunction with cross-connect commands, and for reporting tributary related alarms.

Each low-speed shelf can support a maximum capacity of up to four OC-12 circuit packs. The four circuit packs may be configured to operate independently in an 0x1 unprotected mode or as pairs in a 1+1 protected mode. In a protected mode, the Service and Protection OC-12 packs must be inserted in quad pairs as follows:

- Quads 1 and 2, or
- Quads 3 and 4.

**NOTE:**

Quads 2 and 3, or 1 and 4 will not protect each other.

I

In an all 0x1 configuration, the FT-2000 running Release 7.1.0 and later has the capacity to add/drop four OC-12 equivalents of traffic. In an all 1+1 protected configuration, the FT-2000 provides the capacity to add/drop two OC-12 equivalents of traffic. In a mixed 1+1/0x1 configuration, the capacity is three OC-12 equivalents of traffic.

- **LS INTFC P1 and P2:** Two slots are provided for one protection LAA2 DS3 circuit pack (S4:4) and/or one protection LAA4 STS1E circuit pack. Two DS3 or two STS1E circuit packs cannot be used. The protection DS3 and STS1E circuit packs can be placed in slot P1 or P2.
- **LSSW IN and OUT:** Two slots are provided for the LAA12 LSSW circuit packs. The LSSW circuit packs are required when DS3 and STS1E low-speed protection is desired. The LSSW circuit packs are not required if the LS INTFC slots are equipped with only OC3 (1.3 STD) circuit packs.
- **TOHCTL:** One slot is provided for the LAA26 Tributary - Overhead Controller (TOHCTL) circuit pack. The TOHCTL circuit pack is required when an LS INTFC slot is equipped with an OC3 circuit pack and single-ended operation is desired through the OC-3 data communications channel (DCC). The TOHCTL circuit pack is compatible with Release 6.0.0-ADR or later software.
- **TG 1 and 2:** Two slots are provided for the LAA17 or LAA18 Timing Generator, Stratum 3 - DS1 (TG3 [DS1]) circuit packs. The LAA17 TG3 (DS1) circuit pack has been discontinued and replaced with the LAA18 TG3 (DS1) circuit pack.

- **SYSCTL**: One slot is provided for the LAA23B System Controller (SYSCTL) circuit pack.
- **SYSMEM**: One slot is provided for the LAA25 System Memory (SYSMEM) circuit pack.

Table 3-7 also provides circuit pack slot/circuit pack code/software compatibility information.

Table 3-7. Low-Speed Shelf — System Controller Circuit Pack Slot/Circuit Pack Code/Software Compatibility

Circuit Pack Slot	Supported Circuit Pack Codes	Compatible Software	
		R7.0.0 of ADR	R7.1.0&R7.2.0 of ADR
LS INTFC 1A-8B	LAA2/LAA2B DS3, S4:4	X	X
	LAA4 STS1E	X	X
	LAA10/LAA10B OC3 (1.3 STD)	X	X
	LAA5 IS-3		X
	T939A OC-12		X
LS INTFC P1 and P2	LAA2/LAA2B DS3, S4:4	X	
	LAA4 STS1E	X	X
LSSW IN and OUT	LAA12B LSSW	X	X
TOHCTL	LAA26 TOHCTL	X	X
TG 1 and 2	LAA17 TG3 (DS1)	X	X
	LAA18 TG3 (DS1)	X	X
SYSCTL	LAA23B SYSCTL	X	X
SYSMEM	LAA25 SYSMEM	X	X

Circuit pack keying prevents circuit packs from being accidentally inserted into incorrect slots.

For detailed circuit pack descriptions and software compatibility information, refer to Volume I, Section 7, "Circuit Pack Descriptions."

Low-Speed Shelf — System Controller Interfaces

Front access cabling is provided through the interconnection panel (Figure 3-29). The interconnection panel provides the following interfaces:

- **LS INTFC 1A - 8B:** These connectors are cabled to the low-speed interface slots 1A through 8B. For example, the DS3 circuit pack in slot 1A receives three DS3 signals through the three IN connectors (1, 2, and 3) on the interconnection panel associated with slot 1A. The DS3 circuit pack in slot 1A also transmits three DS3 signals through the three OUT connectors (1, 2, and 3) associated with slot 1A. The 9A through 16B connector designations are not used on this shelf. Any LS INTFC 1A - 8B connector cabled to a slot equipped with an OC3 (1.3 STD) circuit pack is not used. For more information, refer to SD-5G250-02, *FT-2000 OC-48 Enhanced Lightwave Terminating Bay Application Schematic* (or SD-5G250-01, *FT-2000 OC-48 Lightwave Terminating Bay Application Schematic*).
- **X.25:** This connector provides an interface to a message-based operations system that uses X.25 protocol and the TL1 message language. For more information about this message-based operations system interface, refer to Volume I, Section 6, "Operations Interfaces."
- **SER TLM 1 and SER TLM 2:** These connectors provide a serial telemetry interface to a remote maintenance center that uses telemetry byte-oriented serial (TBOS) protocol. For more information about this serial telemetry interface, refer to Volume I, Section 6, "Operations Interfaces."
- **OFFICE ALMS:** This connector provides an interface to the local office audible and visible alarms. For more information about this office alarm interface, refer to Volume I, Section 6, "Operations Interfaces."
- **PAR TLM:** This connector provides an interface to an operations center for alarm information. For more information about this parallel telemetry interface, refer to Volume I, Section 6, "Operations Interfaces."
- **MISC DSCRT 1 and MISC DSCRT 2:** These connectors provide an interface to external equipment via a terminal strip. This allows an operations system to monitor and control equipment collocated with the FT-2000 OC-48 Lightwave System. For more information about this user-settable miscellaneous discrete interface, refer to Volume I, Section 6, "Operations Interfaces." For detailed information about terminal strip wiring, refer to 365-575-115, *FT-2000 OC-48 Lightwave System, Releases 3, 4, 5, 6, and 7 Installation Manual*, and SD-5G250-02, *FT-2000 OC-48 Enhanced Lightwave Terminating Bay Application Schematic* (or SD-5G250-01, *FT-2000 OC-48 Lightwave Terminating Bay Application Schematic*).
- **CIT (DTE):** This connector provides an interface to a modem that allows dial-up access. For more information about the CIT (DTE) interface, refer to Volume I, Section 6, "Operations Interfaces."
- **TIMING IN PRI and TIMING IN SECY:** These connectors provide a PRI and SEC interface to an external DS1 timing source that is required for synchronous operation. For more information about synchronization interfaces, refer to Volume I, Section 5, "Control, Transmission, and Synchronization Interfaces."

- **TIMING OUT 1 and TIMING OUT 2:** These connectors provide an interface to other central office equipment that requires a DS1 timing reference signal. For more information about synchronization interfaces, refer to Volume I, Section 5, "Control, Transmission, and Synchronization Interfaces."
- **CIT (DCE):** This connector provides an interface to the CIT (DCE) port on the user panel that allows local access. For more information about the CIT (DCE) interface, refer to Volume I, Section 6, "Operations Interfaces."
- **USER PNL DISPLAY:** This connector provides an interface to the alarm and status indicators, and display on the user panel. For information about the user panel display and indicators, refer to Volume I, Section 6, "Operations Interfaces."
- **PWR:** These connectors provide an interface to the –48 V power filters on the user panel. For more information about power, refer to Volume I, Section 4, "Power."

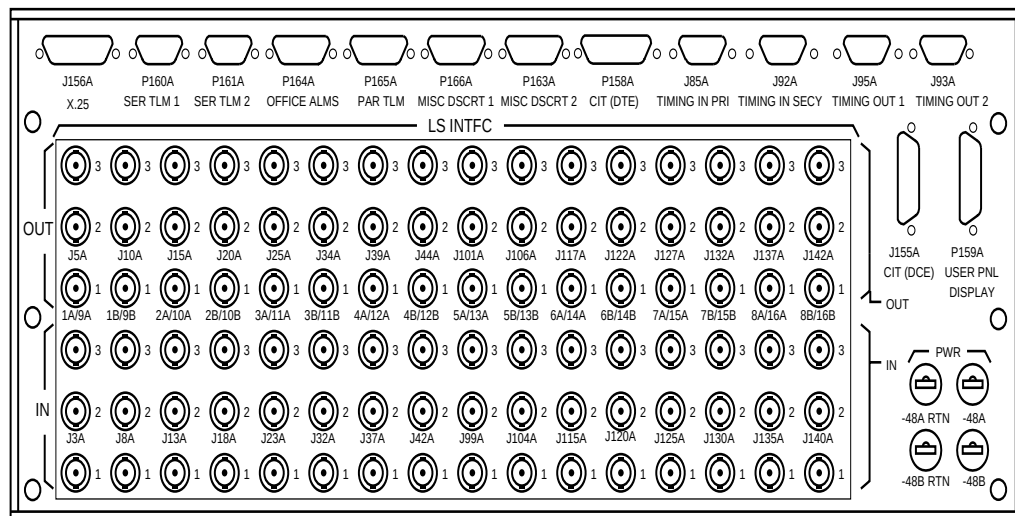


Figure 3-29. Interconnection Panel (Low-Speed Shelf - System Controller)—ED6G999-31,G5

Except for power, all cabling entering the interconnection panel is connectorized with the following industry standard connectors:

- **BNC connectors:** The BNC connectors are used on all cables for electrical low-speed interfaces.
- **D-subminiature connectors:** The D-subminiature connectors are used on all cables for the operation system and central office interfaces.

The power cables are connected to radio frequency (RF) filters on the interconnection panel using push-on type connectors.

The user panel (Figure 3-30) provides –48 V power filters, a power on (PWR ON) LED, an electrostatic discharge jack, and system level information. For more information about the user panel, refer to Volume I, Section 6, "Operations Interfaces."

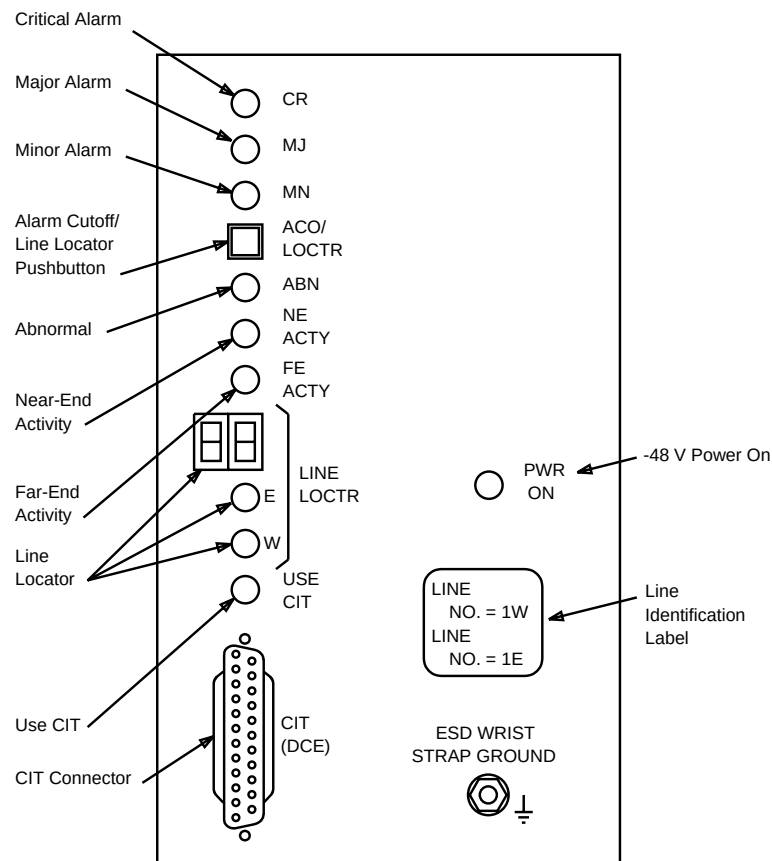


Figure 3-30. User Panel (Enhanced)

The Low-Speed Shelf - System Controller has two front covers (Figure 3-31). One covers the interconnection panel and the other covers the circuit packs. Openings are provided in the top cover that allow the user panel to show through and provide access to the CIT port. The covers are hinged and can be easily removed for maintenance access.

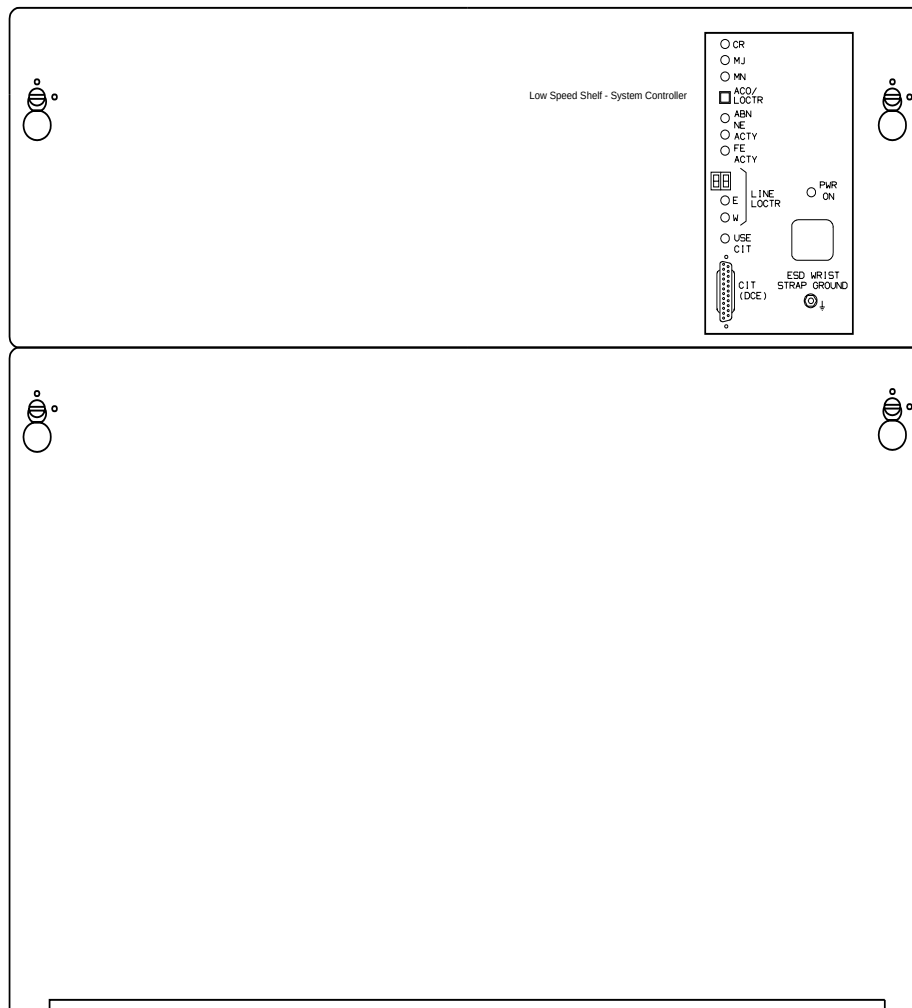


Figure 3-31. Low-Speed Shelf - System Controller (with Covers)

Condensed High-Speed Shelf

Purpose of Condensed High-Speed Shelf

Each Condensed High-Speed Shelf* serves as an interface for OC-48 high-speed lines 1W and 1E. The Condensed High-Speed Shelf (Figure 3-32) consists of the fully connectorized interconnection panel, a filter panel, and 12 circuit pack slots. It measures 14.25 inches high by 21.5 inches wide by 12 inches deep and fits into a standard 23-inch wide network bay framework.

Condensed High-Speed Shelf Circuit Packs

The Condensed High-Speed Shelf (Figure 3-32) provides the following circuit pack slots:

- **TRMTR 1W:** One slot is provided for the 739B2[†], 739C2[†], 739E[1-16], 739H[1-16], 739J2[†], 739J3[†], 739R2[†], 739S2[†], 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, or 739S5 OC48 Transmitter, Add/Drop (OC48 TRMTR [A/D]) circuit pack for OC-48 high-speed line 1W. The 739J3 OC48 TRMTR (A/D) circuit pack is compatible with Release 5.0.1-ADR or later software. The 739R2 and 739S2 OC48 TRMTR (A/D) circuit packs are compatible with Release 6.0.0-ADR or later software. The 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 739E[1-16] circuit packs are compatible with Release 7.2.0-ADR and later software.
- **RCVR 1W:** One slot is provided for the 839B2[†], 839B4B, or 839B5 OC48 Receiver, Add/Drop (OC48 RCVR [A/D]) circuit packs for OC-48 high-speed line 1W. The 839B4B and 839B5 OC48 RCVR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 839E5 circuit pack is compatible with Release 7.2.0 and later software.
- **TRMTR 1E:** One slot is provided for the 739B2[†], 739C2[†], 739E[1-16], 739H[1-16], 739J2[†], 739J3[†], 739R2[†], 739S2[†], 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, or 739S5 OC48 Transmitter, Add/Drop (OC48 TRMTR [A/D]) circuit pack for OC-48 high-speed line 1E. The 739B4, 739J4, 739B5, 739C5, 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR (A/D) circuit packs are compatible with Release 7.0.0-ADR or later software. The 739E[1-16] circuit packs are compatible with Release 7.2.0 and later software.

* The Condensed High-Speed Shelf is used only on FT-2000 OC-48 Add/Drop-Rings Terminal for two-Fiber Rings equipped with the J68974D FT-2000 OC-48 Enhanced Lightwave Terminating Bay.

† These transmitters and receivers are rated discontinued availability (DA) as of 06/97.

- **RCVR 1E:** One slot is provided for the 839B2[†], 839B4B, or 839B5 OC48 Receiver, Add/Drop (OC48 RCVR [A/D]) circuit packs for OC-48 high-speed line 1E. The 839B4B and 839B5 OC48 RCVR (A/D) circuit Packs are compatible with Release 7.0.0-ADR or later software. The 839E5 circuit pack is compatible with Release 7.2.0 and later software.
- **LNCTL:** One slot is provided for the LAA28 Line Controller — 4 Megabyte [LNCTL (4 MEG)] circuit pack.
- **OHCTL 1W:** One slot is provided for the LAA21 Overhead Controller — Terminal (OHCTL [TERM]) circuit pack for OC-48 high-speed line 1W.
- **OHCTL 1E:** One slot is provided for the LAA21 OHCTL (TERM) circuit pack for OC-48 high-speed line 1E.
- **RESERVED:** One slot is reserved for the Overhead Controller — LAN (LOHCTL) circuit packs in a future release.

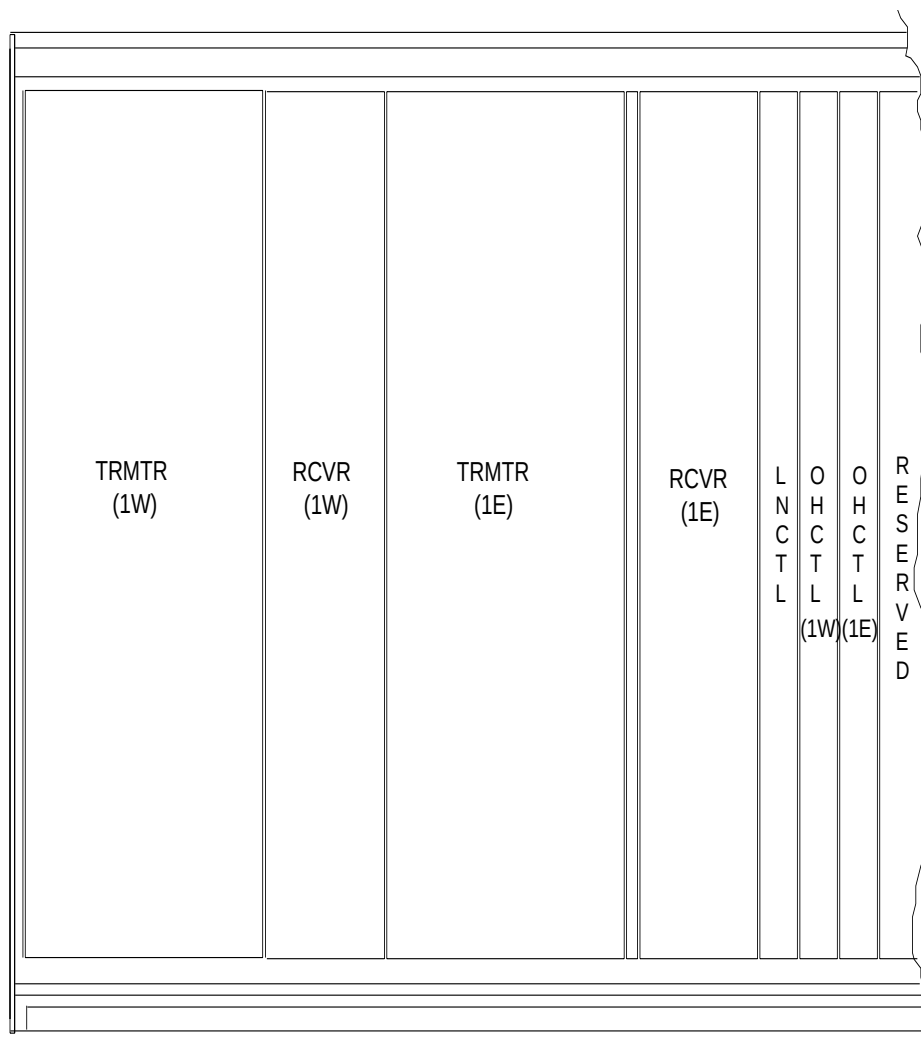


Figure 3-32. Condensed High-Speed Shelf (Without Circuit Pack Cover)

Table 3-8 also provides circuit pack slot/circuit pack code/software compatibility information.

Table 3-8. Enhanced High-Speed Shelf Circuit Pack Slot/Circuit Pack Code/Software Compatibility

Circuit Pack Slot	Supported Circuit Pack Codes	ADR Compatible Software		
		R7.0.0	R7.1.0	R7.2.0
TRMTR 1W	739B2 [†] OC48 TRMTR	X	X	DA
	739C2 [†] OC48 TRMTR	X	X	DA
	739E[1-16] OC48 TRMTR			X
	739H[1-16] OC48 TRMTR			X
	739J2 [†] OC48 TRMTR	X	X	DA
	739J3 [†] OC48 TRMTR	X	X	DA
	739R2 [†] OC48 TRMTR	X	X	DA
	739S2 [†] OC48 TRMTR	X	X	DA
	739B4 OC48 TRMTR	X	X	X
	739J4 OC48 TRMTR	X	X	X
	739B5 OC48 TRMTR	X	X	X
	739C5 OC48 TRMTR	X	X	X
	739J5 OC48 TRMTR	X	X	X
	739P5 OC48 TRMTR	X	X	X
	739R5 OC48 TRMTR	X	X	X
	739S5 OC48 TRMTR	X	X	X
RCVR 1W	839B2 [†] OC48 RCVR	X	X	DA
	839B4B OC48 RCVR	X	X	X
	839B5 OC48 RCVR	X	X	X
	839E5 OC48 RCVR			X
TRMTR 1E	739B2 [†] OC48 TRMTR	X	X	DA
	739C2 [†] OC48 TRMTR	X	X	DA
	739E[1-16] OC48 TRMTR			X
	739H[1-16] OC48 TRMTR			X
	739J2 [†] OC48 TRMTR	X	X	DA
	739J3 [†] OC48 TRMTR	X	X	DA
	739R2 [†] OC48 TRMTR	X	X	DA
	739S2 [†] OC48 TRMTR	X	X	DA
	739B4 OC48 TRMTR	X	X	X
	739J4 OC48 TRMTR	X	X	X
	739B5 OC48 TRMTR	X	X	X
	739C5 OC48 TRMTR	X	X	X
	739J5 OC48 TRMTR	X	X	X
	739P5 OC48 TRMTR	X	X	X
	739R5 OC48 TRMTR	X	X	X
	739S5 OC48 TRMTR	X	X	X

Table 3-8. Enhanced High-Speed Shelf Circuit Pack Slot/Circuit Pack Code/Software Compatibility (Cont'd)

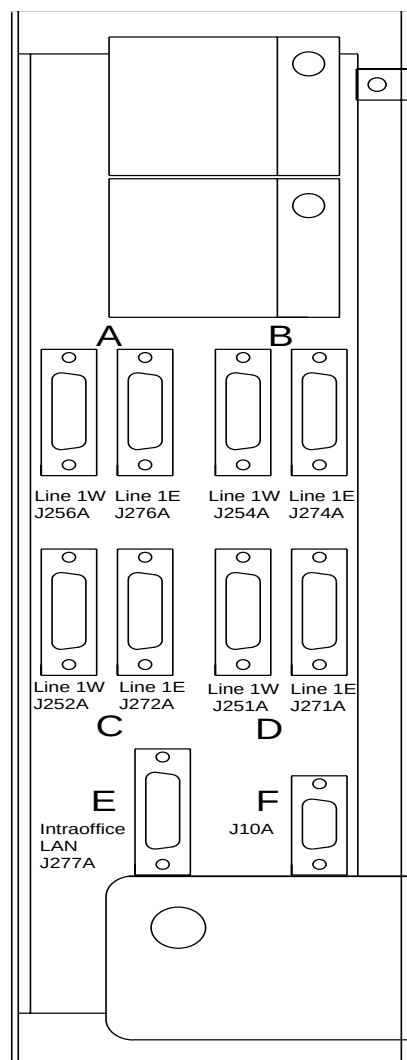
Circuit Pack Slot	Supported Circuit Pack Codes	ADR Compatible Software		
		R7.0.0	R7.1.0	R7.2.0
RCVR 1E	839B2 OC48 RCVR	X	X	X
	839B4B OC48 RCVR	X	X	X
	839B5 OC48 RCVR	X	X	X
	839E5 OC48 RCVR			X
LNCTL	LAA28 LNCTL	X	X	X
OHCTL 1W	LAA21 OHCTL (TERM)	X	X	X
OHCTL 1E	LAA21 OHCTL (TERM)	X	X	X

Circuit pack keying prevents circuit packs from being accidentally inserted in incorrect slots.

For detailed circuit pack descriptions and software compatibility information, refer to Volume I, Section 7, "Circuit Pack Descriptions."

Condensed High-Speed Shelf Interfaces

Front access cabling (for ordering and power) is provided through the Interconnection and Filter Panel (Figure 3-33).



adr05005.00eJF0081896r7.1

Figure 3-33. Interconnection and Filter Panel (Condensed High-Speed Shelf)

The interconnection and filter panel is divided into six segments. They are labeled A-through-F and each interface is described as follows:

- **A - *SECTION OW (LOC OW) LINE (1W) and LINE (1E)*:** These connectors provide an interface that allows an orderwire set access to the section orderwire (E1 byte) for voice communications (local orderwire). For more information about the orderwire interface, refer to Volume I, Section 6, "Operations Interfaces."
- **B - *LINE OW (EXP OW) LINE (1W) and LINE (1E)*:** These connectors provide an interface that allows an orderwire set access to the line orderwire (E2 byte) for voice communications (express orderwire). For more information about the orderwire interface, refer to Volume I, Section 6, "Operations Interfaces."
- **C - *LINE GROWTH CHAN LINE (1W) and LINE (1E)*:** These connectors provide an interface that allows access to the line growth channel (Z1 and Z2 bytes) of the OC-48 high-speed lines. These connectors are reserved for use in a future release.
- **D - *SECTION USER CHAN LINE (1W) and LINE (1E)*:** These connectors provide an interface that allows access to the section user channel (F1 byte) of the service and protection OC-48 high-speed lines. These connectors are reserved for use in a future release.
- **E - *INTRAOFFICE LAN*:** This connector is reserved for use in a future release.
- **F -** This connector provides an interface to the –48 V power filters on the filter panel. This is a factory-wired connection.

Except for power, all the cabling entering the interconnection panel is connectorized with D-subminiature connectors. The power cables are connected to RF filters on the interconnection panel using push-on type connectors.

Fan Assembly

The fan assembly is located in the bottom of the D-Bay. It consists of three six-inch fans. The fan controller senses the incoming air temperature and provides a signal enabling the fans to operate at different speeds. Cooling air is taken in from the bottom front of the bay. It passes through a user-replaceable air filter and is forced up through all four shelves and exhausted through a top rear grill.



NOTE:

Normally, the fans will be operating at medium speed. However, with a higher ambient air temperature the fans will operate at high-speed. These modes of operation should result in increased fan life and reduced operating noise.

The fan assembly contains two status LEDs. These are:

- Power-On
- Alarm.

The alarm signal is brought from a connector located on the left side of the fan assembly and terminated on the "miscellaneous discrete alarm inputs connector" which is located on the lower low-speed shelf.

Table of Contents

Overview	4-1
General	4-1
Power Distribution	4-2
Power Dissipation	4-6
LEDs	4-7

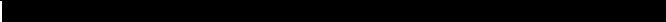


Table of Contents

Overview

This section describes the power distribution and dissipation of the FT-2000 OC-48 Lightwave System.

General

The FT-2000 OC-48 Lightwave System power distribution philosophy is based on individual rather than bulk power supplies. Each circuit pack contains DC-to-DC converters that change the office battery voltages to the voltages required. This leads to improved system reliability, since heat is dissipated uniformly across the system avoiding "hot spots."

The FT-2000 OC-48 Lightwave System is powered by –48 V DC. The voltage range for all the components is –42.75 to –60 V DC as measured at the power feeder inputs and returns at the bay.

Power Distribution

The FT-2000 OC-48 Lightwave System accepts two –48 V DC office power feeders for feeder redundancy from the battery distribution and fuse bay (BDFB) or equivalent. Figure 4-1 shows how power is distributed in an FT-2000 OC-48 Lightwave System bay. The power feeders are 4.5-foot, 8-gauge power feeder stubs gutter-tapped to the feeder cables going to the BDFB. There are also 8.5- and 15-foot, 8-gauge power feeders available for overhead high bay cable racking and raised-floor applications, respectively. Power feeders can also be ordered to length.

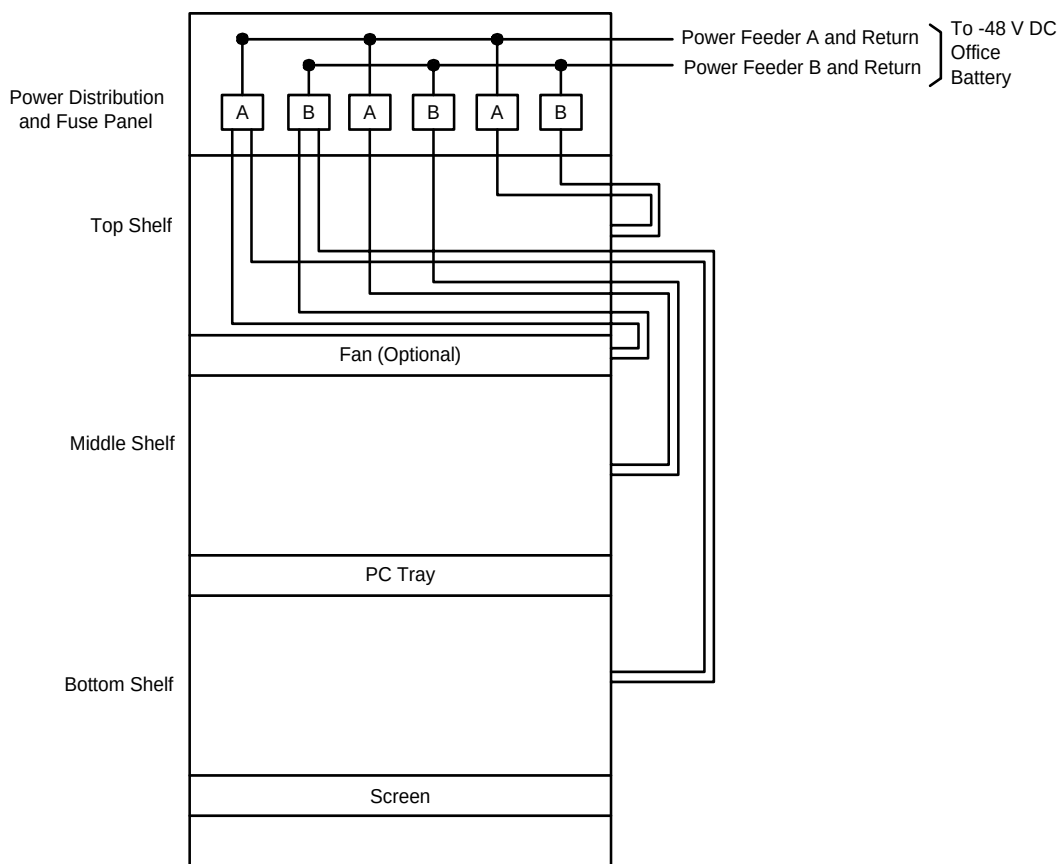


Figure 4-1. Bay Power Distribution

The 8-gauge power feeder stubs (power feeder A and power feeder B) are terminated at the bay power distribution and fuse panel. Within the power distribution and fuse panel, the power feeders are routed with 10-gauge wire to six 10-amp fuses (red lamp is lighted when blown). Three fuses are for power feeder A and three fuses are for power feeder B. The bottom shelf and the fan assembly (if equipped) share the first pair of fuses. The second pair of fuses is assigned to the middle shelf. The third pair of fuses is assigned to the top shelf.

The power feeders are cabled, using 14-gauge wire, from the fuses down the right side of the bay (when viewed from the front) to the shelves and the fan assembly (if equipped).

Figure 4-2 shows an example of how power is distributed to a miscellaneous mounted FT-2000 OC-48 Repeater Shelf. The 8-gauge power feeder stubs are terminated at two 10-gauge power connectors that are connected to the user panel. The user panel is equipped with two 10-amp fuses: one fuse for power feeder A and one fuse for power feeder B (red lamp is lighted when blown).

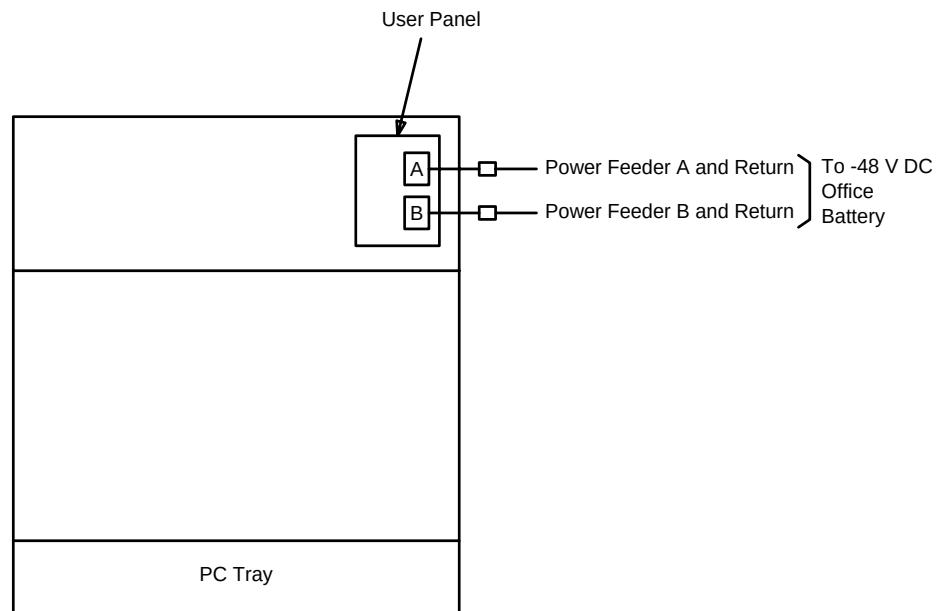


Figure 4-2. Miscellaneously-Mounted FT-2000 OC-48 Repeater Shelf Power Distribution

The two independent –48 volt office power feeders (A and B) enter each shelf via a power interface circuit and are distributed to the circuit packs (Figure 4-3) via the printed backplane. The power interface circuit consists of a green PWR ON LED, two power filter assemblies, and two radio-frequency (RF) filters.

The green PWR ON LED (located on the user panel and filter panel) shows that the shelf is receiving fused –48 volt power. The green PWR ON LED will remain lighted as long as either –48 volt power feeder is supplying power to the shelf.

Each power filter assembly smooths the input current and reduces the converter switching noise to the allowed limit. Each power feeder also passes through an RF filter at the interconnection panel to remove any RF energy before going to the backplane.

From the RF filters, the power feeders are connected to the backplane and distributed to the circuit packs on the shelf. Each backplane power feeder connects to the circuit pack via four backplane pins. One pin is longer than the other three power pins and connects to the circuit pack first. This long pin feeds a surge resistor to limit in-rush currents and prevents voltage dropouts on the power feeders.

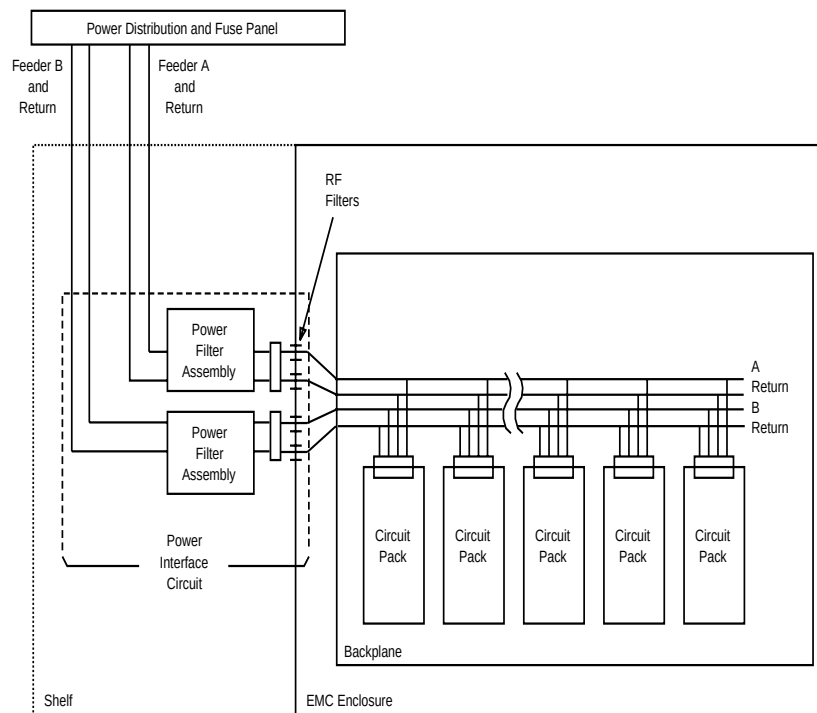


Figure 4-3. Shelf Power Distribution

Each circuit pack is equipped with diodes and fuses that protect the power feeders (Figure 4-4). A filtering section follows the fused input. Modular DC-to-DC power converters on each circuit pack convert the -48 V to the voltages required on the circuit pack.

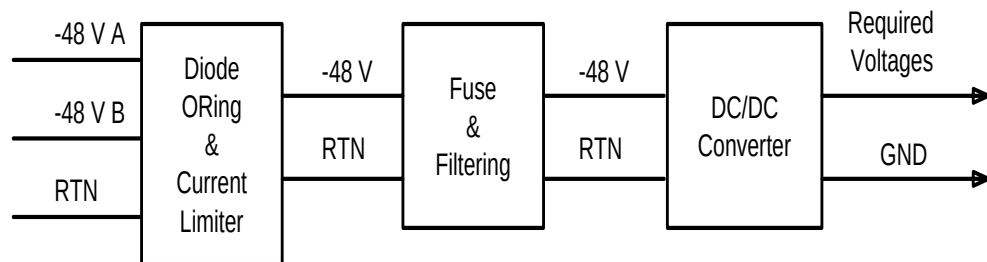


Figure 4-4. Circuit Pack Power Distribution

Power Dissipation

Table 4-1 shows the maximum power dissipation and current drains for the FT-2000 OC-48 Lightwave System.



NOTE:

Refer to the Floor Plan Data Sheet (FPD 804-911-168-[]) for complete information on power engineering for the FT-2000 OC-48 Lightwave System.

Table 4-1. Power Dissipation and Current Drains

Equipment Package	Maximum Power Dissipated		Current Drain (Amps) per Feeder (Two Feeders Required)	
	(Watts)	(Watts/ft ²)	Nominal (Note 1)	Maximum (Note 2)
FT-2000 OC-48 Add/Drop-Rings (E-Bay) Terminal for two-Fiber Rings	487	69	5.1	11.4
FT-2000 OC-48 Add/Drop-Rings (D-Bay) Terminal for two two-Fiber Rings	1142	162	11.0	26.7
FT-2000 OC-48 Repeater Bay with				
— One Repeater Shelf — System Controller	175	25	1.8	4.1
— Two Repeater Shelf — System Controllers	350	50	3.6	8.2
— Three Repeater Shelf — System Controllers	525	75	5.5	12.3
FT-2000 OC-48 Repeater Shelf	175	25	1.8	4.1



NOTE:

Nominal (List 1) current drains are used to size batteries and rectifiers. To size batteries and rectifiers, use twice the nominal current drain per feeder. These current drains represent the average busy-hour current at normal operating voltages. Nominal current drains occur at –48 V.



NOTE:

Maximum (List 2) current drains are used to size each feeder cable and fuse. To size feeder cables and fuses, use the maximum current drain per feeder. These current drains represent the peak current under worst-case operating conditions. Normally, the current for the system is shared equally by both feeders. If one feeder fails, the other feeder carries the total load for both feeders (feeder A + feeder B current). Maximum current drains occur at –42.75 V.

LEDs

The green PWR ON LED (located on the user panel and filter panel) and the circuit pack red FAULT LED (located on the circuit pack faceplates) are associated with shelf and circuit pack power. The green PWR ON LED shows that the shelf is receiving fused –48 volt power. The green PWR ON LED will remain lighted as long as either –48 volt power feeder is supplying power to the shelf.

The red FAULT LED shows circuit pack failures. If the fuse or DC-to-DC converter fails on a transmission circuit pack at an FT-2000 OC-48 Repeater Bay, the red FAULT LED will be operated via a separate power path from the System Controller (SYSCTL) circuit pack.

If the fuse or DC-to-DC converter fails on a transmission or synchronization circuit pack at an FT-2000 OC-48 Add/Drop-Rings Terminal, the red FAULT LED will be operated via a separate power path from the Line Controller (LNCTL) circuit pack. If the fuse or DC-to-DC converter fails on the LNCTL, Overhead Controller — Terminal (OHCTL [TERM]), or System Memory (SYSMEM) circuit pack, the red FAULT LED will be operated via a separate power path from the SYSCTL circuit pack. If the fuse or DC-to-DC converter fails on the SYSCTL circuit pack, the red FAULT LED will be operated via a separate power path from the SYSMEM circuit pack.

Table of Contents

Overview	5-1
Control	5-1
Transmission	5-5
■ FT-2000 OC-48 Add/Drop-Rings Terminal	5-5
■ FT-2000 OC-48 Repeater Bay	5-9
Synchronization Modes	5-10
■ Platform Synchronization	5-10
General	5-10
Free Running Mode	5-11
Line Timed Mode	5-12
Through Timed Mode	5-13
Loop Timed Mode—only for Point-to-Point Releases of FT-2000	5-15
Externally Timed Mode	5-17
■ Network Synchronization	5-19
General	5-19
OC-48 Point-to-Point Applications	5-21
Free Running/Free Running Configuration	5-21
Free Running/Through Timed Configuration	5-22
Externally Timed/Through Timed Configuration	5-23
Phase Locked/Phase Locked Configuration	5-24
Externally Timed/Externally Timed Configuration	5-26
Bidirectional Ring Applications	5-27
General	5-27

Table of Contents

Ring Applications Not Used to Distribute Timing	5-28
Synchronous Applications (With a Mixture of Asynchronous and Synchronous Low-Speed Interfaces)	5-30
Ring Applications Used to Distribute Timing	5-35
Summary of Timing Rules for Bidirectional Line-Switched Rings	5-43

Overview

This section describes the FT-2000 OC-48 Lightwave System architecture. The system control, transmission, and synchronization architecture are described down to the circuit pack level.

Control

The FT-2000 OC-48 Lightwave System has a robust, flexible, three-level system control architecture that supports all platforms. Figure 5-1 shows the system control architecture for the FT-2000 OC-48 Add/Drop-Rings Terminal. Figure 5-2 shows the system control architecture for the FT-2000 OC-48 Repeater Bay. The system control architecture distributes the monitoring and control functions down to the lowest level where a particular function (for example, switching or timing) is performed.

At the lowest level of the control hierarchy is the board controller that is located on each transmission, synchronization, and protection circuit pack. The board controller consists of a microprocessor and its supporting circuitry. The board controller is responsible for the real-time intensive monitoring and control functions of the transmission, timing, and protection switch circuitry on a circuit pack. It also controls the circuit pack FAULT LED and performs self-maintenance functions (for example, audit checks and routine diagnostics).

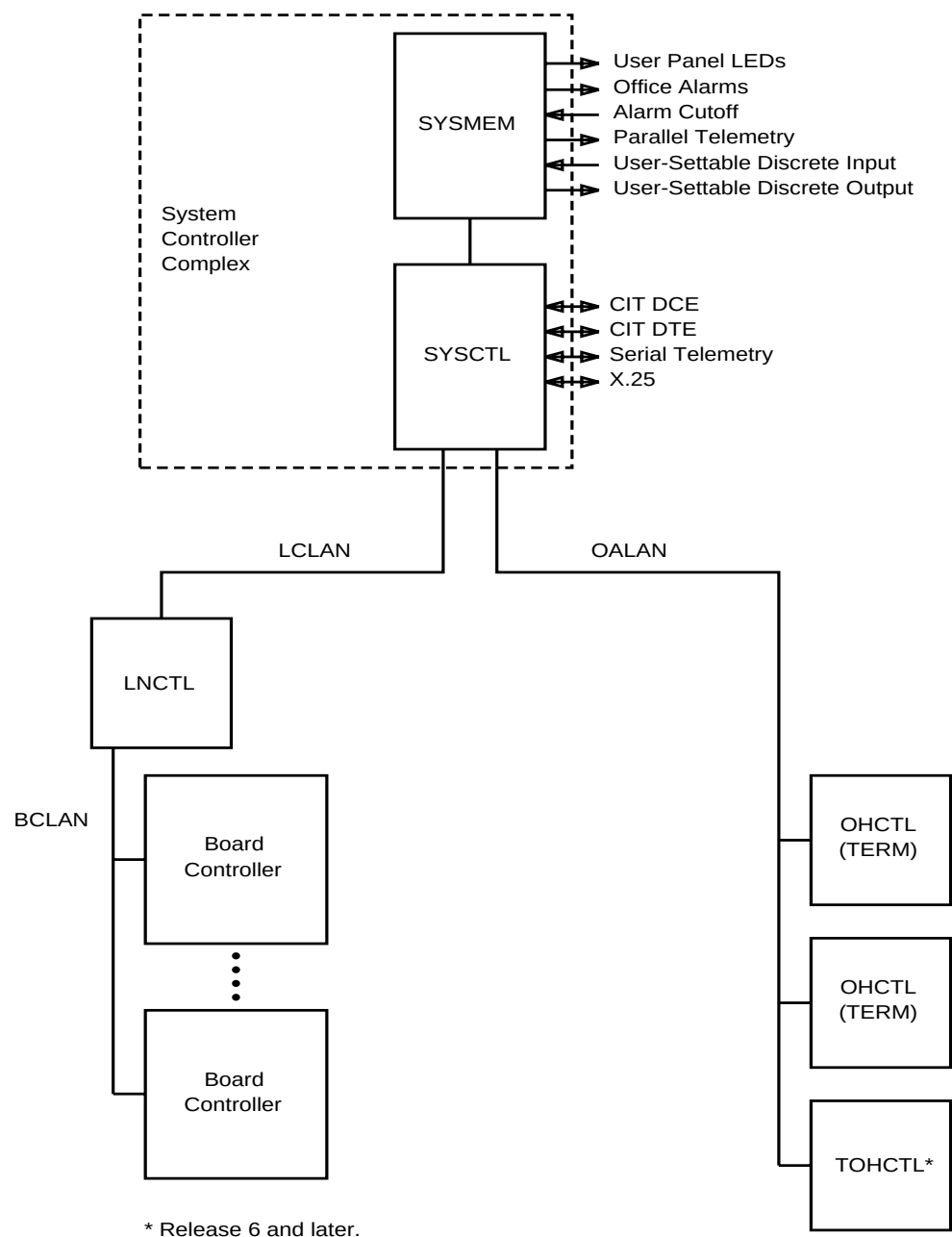
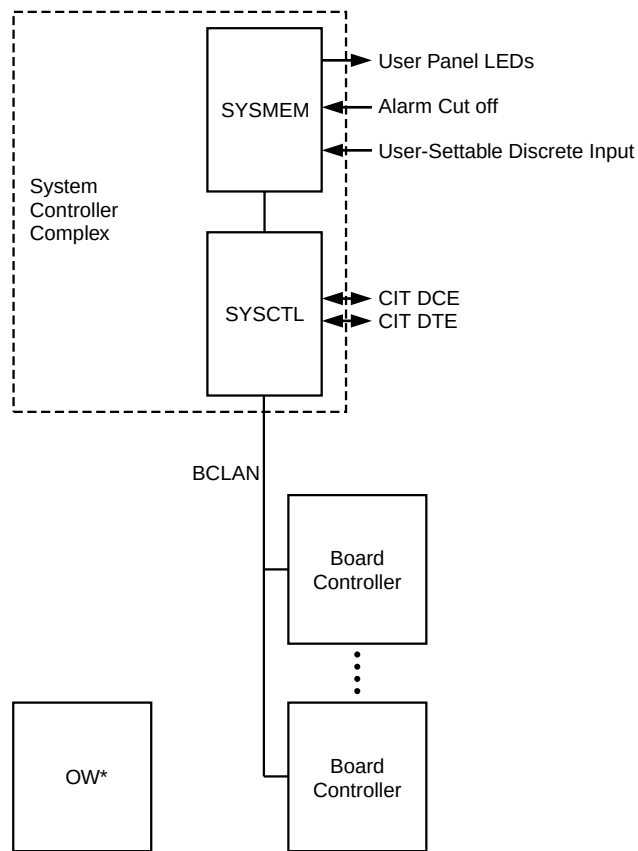


Figure 5-1. System Control Architecture for the FT-2000 OC-48 Add/Drop-Rings Terminal



* Release 6 and later

nc-ft2000-034

Figure 5-2. System Control Architecture for the FT-2000 OC-48 Repeater Bay

The middle level of the control hierarchy is the Line Controller (LNCTL) circuit pack. The LNCTL circuit pack is responsible for fault recovery. The LNCTL circuit pack communicates with the board controllers to activate protection switching. The middle level of the control hierarchy is not needed in the FT-2000 OC-48 Repeater Bay. Therefore, the FT-2000 OC-48 Repeater Bay does not require any LNCTL circuit packs.

The system controller complex is the highest level of the control hierarchy. The system controller complex consists of the System Controller (SYSCTL) circuit pack and the System Memory (SYSMEM) circuit pack. The system controller complex performs the non-real time intensive functions requiring overall system knowledge. The system controller complex also plays a major role in providing the extensive operations, administration, maintenance, and provisioning

functions. For more information about the operations, administration, maintenance, and provisioning functions, refer to Volume I, Section 6 ("Operations Interfaces"); Volume I, Section 8 ("Administration and Provisioning"); and Volume I, Section 9 ("Maintenance Description").

The Overhead Controller - Terminal [OHCTL (TERM)] circuit pack performs synchronous optical network (SONET) overhead access and processing functions. The OHCTL (TERM) circuit pack processes OC-48 section and line data communications channel (DCC) information and supports line and section orderwire (local and express orderwire, bytes E1 and E2), the user channel (F1), and the growth bytes (Z1, Z2). For more information about the overhead bytes, refer to Appendix A, "A SONET Overview."

In Release 6 and later releases, the Overhead Controller - Tributary (TOHCTL) circuit pack also performs SONET overhead access and processing functions at FT-2000 OC-48 Add/Drop-Rings Terminals. The TOHCTL circuit pack processes (OC-3/OC-12) section DCC information (bytes D1-D3).

In Release 6 and later releases, the Orderwire (OW) circuit pack performs SONET section overhead access functions at FT-2000 OC-48 Repeater Bays. The OW circuit pack provides access to the OC-48 section orderwire (E1) and user channel (F1) bytes.

The controllers within the control architecture communicate among the different hierarchical levels using internal local area networks (LANs). The board controllers and the LNCTL circuit pack of the same line communicate using the board controller local area network (BCLAN). The LNCTL circuit packs and the system controller complex communicate using the line controller local area network (LCLAN). The OHCTL (TERM) circuit packs, TOHCTL circuit pack, and system controller complex communicate using the overhead access local area network (OALAN). The OW circuit pack does not communicate with other controllers in the control architecture.

The FT-2000 OC-48 Lightwave System's extensive control features are available through several craft and operations system (OS) interfaces. In addition to accessing the local terminal, the craft and OS interfaces can reach a remote terminal using the data communications channel in the OC-48 signal. In Release 6 and later releases, craft and OS interfaces can reach remote terminals and DDM-2000 Multiplexers using the OC-3 data communications channel. These functions are provided by the SYSCTL, SYSMEM, LNCTL, OHCTL, TOHCTL, and OW circuit packs. For more information about craft and operations system interfaces, refer to Volume I, Section 6, "Operations Interfaces."

Transmission

FT-2000 OC-48 Add/Drop-Rings Terminal

The FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings allows groups of three DS3 signals, three EC-1 signals (available in Release 4 and later releases), one OC-3 signal (available in Release 5 and later releases), and/or GNE OC-12 (available in Release 7 and later) to be easily added/dropped while the through traffic is passed on without requiring low speed interface circuit packs. In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings also allows single DS3 and/or EC-1 signals to be added/dropped while the through traffic is passed on without requiring low speed interface circuit packs. A bidirectional ring capability allows the FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings to be used in 2-fiber bidirectional line-switched ring applications.

Figure 5-3 shows a transmission block diagram of the FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings.

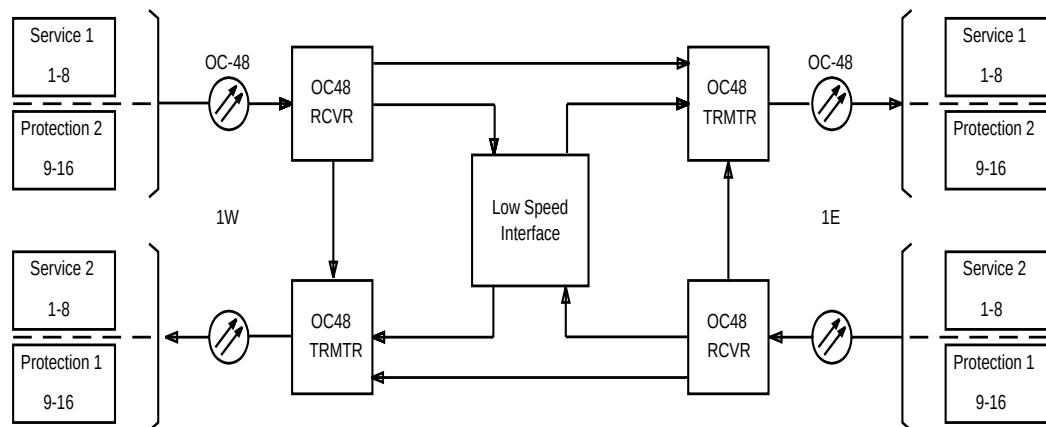


Figure 5-3. FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings Transmission Block Diagram

The FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings interfaces with up to 48 bidirectional DS3 equivalent signals (DS3, EC-1, and/or OC-3*) and 2 OC-48 high speed lines (1E and 1W). Each OC-48 high speed line consists of sixteen 155 Mb/s (STS-3) tributaries. The STS-3 tributaries 1 through 8 are the service tributaries, and STS-3 tributaries 9 through 16 are the protection tributaries. In Figure 5-3, the eastbound service STS-3 tributaries are identified as *Service 1* and the westbound service tributaries are identified as *Service 2*. The eastbound protection STS-3 tributaries are identified as *Protection 2* and the westbound protection STS-3 tributaries are identified as *Protection 1*.

If a failure occurs, the eastbound service STS-3 tributaries (Service 1) are protected by the westbound protection STS-3 tributaries (Protection 1). The westbound service STS-3 tributaries (Service 2) are protected by the eastbound protection STS-3 tributaries (Protection 2). For more information about 2-fiber ring protection switching, electrical low speed protection switching, and optical low speed protection switching, refer to Volume I, Section 9, "Maintenance Description."

Internally, the FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings employs standard synchronous multiplexing for add/drop and through traffic. (Refer to Appendix A, "A SONET Overview.") To add DS3 or EC-1 electrical low speed signals, each electrical low speed interface circuit pack (DS3 or STS1E) accepts up to three low speed signals (DS3 or EC-1) and maps them into 155 Mb/s signals (STS-3 tributaries). To add OC-3 optical low speed signals, each OC3 (1.3 STD) circuit pack accepts one OC-3 low speed signal and maps it into an STS-3 tributary. These add/drop STS-3 tributaries are combined with the through STS-3 tributaries, multiplexed to one STS-48 signal, and converted to an optical OC-48 signal for transmission over high speed line 1E or 1W.

To drop DS3, EC-1, OC-3, and/or OC-12 low speed signals, the incoming optical OC-48 signal is converted back to an electrical STS-48 signal and is demultiplexed to sixteen STS-3 tributaries. The STS-3 tributaries that are designated to be dropped are demultiplexed into DS3, EC-1, and/or OC-3 low speed signals. The STS-3 tributaries not designated to be dropped are the through traffic. The through traffic is combined with the add traffic for transmission in the opposite direction.

* One OC-3 signal is equivalent to three DS3 or EC-1 signals.



CAUTION:

All DS3 and EC-1 signal outputs from the FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings must be properly terminated at all times. Therefore, these outputs must be connected to cross-connect panels that have internal 75-ohm terminations (for example, Lucent Technologies cross-connect panels meet this requirement) or other working transmission equipment (for example, DACS III-2000 Cross-Connect System or DDM-2000 Multiplexer) where the associated low speed interfaces are equipped. When operating the FT-2000 OC-48 Add/Drop-Rings Terminal with cross-connect panels that do not automatically terminate the DS3 and EC-1 signal outputs, 75-ohm termination plugs must be placed at the cross-connect panel.

Care must be exercised during maintenance operations to ensure that the DS3 and EC-1 signal outputs are not inadvertently unterminated. In releases prior to Release 5, if DS3 and EC-1 signal outputs are not terminated properly, false trouble conditions could be reported by the FT-2000 OC-48 Add/Drop-Rings Terminal. In Release 5, it is still recommended that DS3 and EC-1 signal outputs be terminated properly. However, if DS3 and EC-1 signal outputs are not terminated properly and the unterminated DS3 and EC-1 ports in the drop direction (from OC-48 line) are in the NMON state, false trouble conditions will not be reported by the FT-2000 OC-48 Add/Drop-Rings Terminal.

The FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings has no fixed connection, or association, between the low speed interface circuit packs and the OC-48 high speed lines (add/drop traffic) or between high speed line 1E and 1W (through traffic). User specified cross-connections allow any low speed interface slot to be cross-connected to any STS-3 tributary of OC-48 high speed line 1E or 1W. They also allow any incoming STS-3 tributary of OC-48 high speed line 1E or 1W to be cross-connected to the same-numbered outgoing STS-3 tributary of the other OC-48 high speed line. The FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings offers an STS-3 time slot assignment capability that allows the following cross-connections:

- 1-way cross-connections (available in Release 4.1 and later releases)
- 1-way-DRI cross-connections (available in Release 4.1 and later releases)
- 2-way cross-connections (available in Release 3 and later releases).

Switching and bridging circuitry within the OC48 TRMTR, OC48 RCVR, and low speed interface circuit packs allow cross-connections to be specified. In the transmit direction, a low speed interface circuit pack bridges a 155 Mb/s signal to both OC48 TRMTR circuit packs. The user-specified cross-connections determine which OC48 TRMTR circuit pack selects the 155 Mb/s signal for transmission. In the receive direction, a low speed interface circuit pack selects a 155 Mb/s signal from one OC48 RCVR circuit pack based on the user specified cross-connections. For more information about the OC48 TRMTR, OC48 RCVR, and low speed interface circuit packs, refer to Volume I, Section 7, "Circuit Pack Descriptions."

In Release 5 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings also provides limited support for STS-1 cross-connections across an STS-3 tributary.

In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings also provides support for STS-1 cross-connections across an OC-48 signal.

The FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings also has the capability to provide *extra traffic*. Extra traffic allows access to the protection STS-3 tributaries for low priority traffic or for restoration purposes.

Cross-connections can be made using the CIT interface. In Release 4.1 and later releases, cross-connections can also be made through the message-based operations system interface using X.25 protocol and Transaction Language 1 (TL1) messages. For more information about cross-connections, refer to Volume 1, Section 8, "Administration and Provisioning."

FT-2000 OC-48 Repeater Bay

The FT-2000 OC-48 Repeater Bay regenerates optical OC-48 signals for up to six OC-48 high speed lines. Figure 5-4 shows a transmission block diagram of the FT-2000 OC-48 Repeater Bay for one 1x1 system.

Two OC48 Regenerator (OC48 REGENR) circuit packs make a repeater. One OC48 REGENR circuit pack receives an incoming OC-48 signal from high speed line 1W, for example, and transmits it toward high speed line 1E. The other OC48 REGENR circuit pack receives an incoming OC-48 signal from high speed line 1E and transmits it toward high speed line 1W.

Internally, the FT-2000 OC-48 Repeater Bay employs standard synchronous multiplexing. (Refer to Appendix A, "A SONET Overview.") For example, the incoming optical OC-48 signal from high speed line 1W is converted to an electrical STS-48 signal and demultiplexed to sixteen 155 Mb/s signals. This allows certain SONET section overhead bytes to be extracted, monitored, and controlled. The 155 Mb/s signals are then multiplexed to one STS-48 signal, and the SONET section overhead bytes are inserted. The STS-48 signal is then converted back to an optical OC-48 signal for transmission on high speed line 1E.

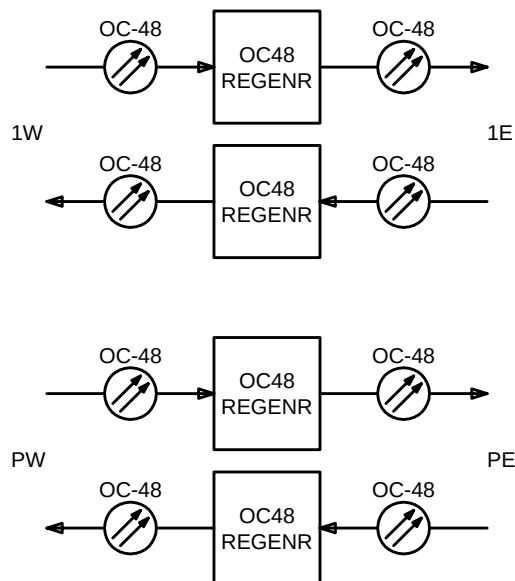


Figure 5-4. FT-2000 OC-48 Repeater Bay Transmission Block Diagram

Synchronization Modes

Platform Synchronization

General

The FT-2000 OC-48 Lightwave System platforms can be provisioned for the following synchronization modes:

- FT-2000 OC-48 Add/Drop-Rings Terminal
 - **Free running** from an internal oscillator
 - **Through timed** from an incoming OC-48 signal
 - **Line Timed** using sync-messages that are derived from the incoming OC-48 signal (Release 7.2.0 and later software)
 - **Externally timed** (phase locked in releases prior to Release 5) to the digital synchronization network using DS1 synchronization references (external timing)



NOTE:

The Release 7.1 generic software provides "Sync Messaging". For those OC-48 rings that connect to other rings (including a DDM-2000 ring) — this feature may be used to enhance network synchronization. More specifically, R7.1 supports messages STU, ST3 and DUS on the OC-48 and these messages are sent out on the OC-3 and OC-12 K-bytes on a provisionable basis.

- FT-2000 OC-48 Repeater Bay
 - **Through timed** from an incoming OC-48 signal.

As network needs evolve, the FT-2000 OC-48 Lightwave System can be reprovisioned easily in service to change synchronization modes as needed. If the synchronization mode is reprovisioned to the externally timed mode (phase locked mode in releases prior to Release 5), external DS1 synchronization references are required.

The FT-2000 OC-48 Lightwave System also provides DS1 synchronization output signals to the office building integrated timing supply (BITS) clock or other central office equipment.

Internal timing functions—reference interfaces, the on-board clock elements, and timing distribution—are located on the Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs. The TG3 (DS1) circuit packs distribute clock signals derived from the selected reference source to the transmission circuit packs.

Two TG3 (DS1) circuit packs are located on each Low Speed Shelf and support the synchronization needs of the FT-2000 OC-48 Lightwave System. The FT-2000 OC-48 Repeater Bay does not require TG3 (DS1) circuit packs. The TG3 (DS1) circuit packs are 1x1 (revertive) protected, protecting against circuit pack failures. The DS1 synchronization references are revertively protected. For more information about synchronization protection, refer to Volume I, Section 9, "Maintenance Description."

Free Running Mode

In the free running mode (Figure 5-5), the TG3 (DS1) circuit packs on the Low Speed Shelf - System Controller derive timing from a highly stable crystal oscillator. This oscillator has a long-term accuracy better than ± 4.6 ppm. The TG3 (DS1) circuit packs distribute 155.52 MHz timing signals to the low speed interface circuit packs on the Low Speed Shelf - System Controller and the OC48 Transmitter (OC48 TRMTR) circuit packs on the Enhanced High Speed Shelf (or High Speed Shelf).

In the free running mode, the DS1 reference input interface (TIMING IN PRI and TIMING IN SECY) and DS1 reference output interface (TIMING OUT 1 and TIMING OUT 2) located on the interconnection panel of the Low Speed Shelf - System Controller are not used.

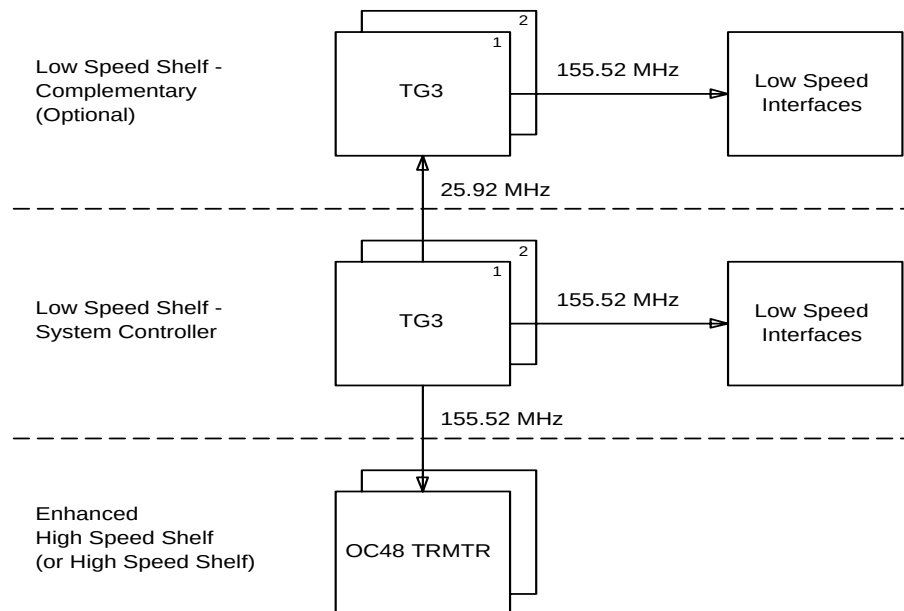


Figure 5-5. Free Running Synchronization

Line Timed Mode

The line timed mode of synchronization is available in FT-2000 OC-48 ADR Release 7.2 and later releases. It provides several benefits which make it superior to other modes—particularly with FT-2000 nodes that are located in sites without BITS clocks. More specifically, this mode uses status type messages that are referred to as "sync-messages." These "sync-messages" are a method by which nodes in an FT-2000 NE can communicate their synchronization status to other NEs in the ring. The main advantages provided by "sync-messages" are the following:

- The ability to automatically reconfigure how synchronization is obtained,
- Better reliability for timing distribution.

In terms of operation, the user may choose from which direction the timing is derived, East(E), West(W), or Auto. West is the default option. If Auto is optioned—then the particular received signal selected is determined by the sync-message algorithm. Simply put, the sync message algorithm allows each node to reconfigure its timing source to derive timing from the best available source. As a contrast to through timed, whenever line timed makes a switch/reconfiguration then that event is "non-revertive."

**NOTE:**

The line timed-auto mode can reconfigure for: (a) two transmission line failures, (b) a node failure, and (c) failure of an external timing source. The sync messages, via optical low speed interfaces, supports interworking with DDM-2000 rings. However, it should not be assumed that the 7.2.0 version's (timing messages or DCC communication) will interwork with other vendors products.

Table 5-1 lists each line timed "sync-message," its identifying acronym, and relative quality level.

Table 5-1. Sync-Messages—for Line Timed Mode

Description	Acronym	Quality Level
PRS Traceable	PRS	1
Synchronized - traceability unknown	STU	2
Traceable Stratum 2 Holdover	ST2	3
Traceable Stratum 3 Holdover	ST3	4
Traceable SONET clock self-timed*	SIC	5
DON'T USE for Sync	DUS	6
Reserved for Network Synchronization*	RES	7

* The SIC and RES messages are not expected in normal FT-2000 operations but must be supported for future planned interworking applications.

Through Timed Mode

In the through timed mode, an intermediate site (FT-2000 OC-48 Add/Drop-Rings Terminal or FT-2000 OC-48 Repeater Bay) derives timing from the incoming OC-48 signals.

In the FT-2000 OC-48 Add/Drop-Rings Terminal, the TG3 (DS1) circuit packs derive timing from the incoming OC-48 signals (Figure 5-6). The TG3 (DS1) circuit packs accept an OC-48 line reference signal (25.92 MHz) from the OC48 RCVR circuit packs and derive the internal synchronization used by the transmission circuit packs. For example, the TG3 (DS1) circuit pack that accepts the OC-48 line reference signal from high speed line 1E derives timing for the OC-48 signal transmitted on high speed line 1W. Also, the TG3 (DS1) circuit pack that accepts the OC-48 line reference signal from high speed line 1W derives timing for the OC-48 signal transmitted on high speed line 1E.

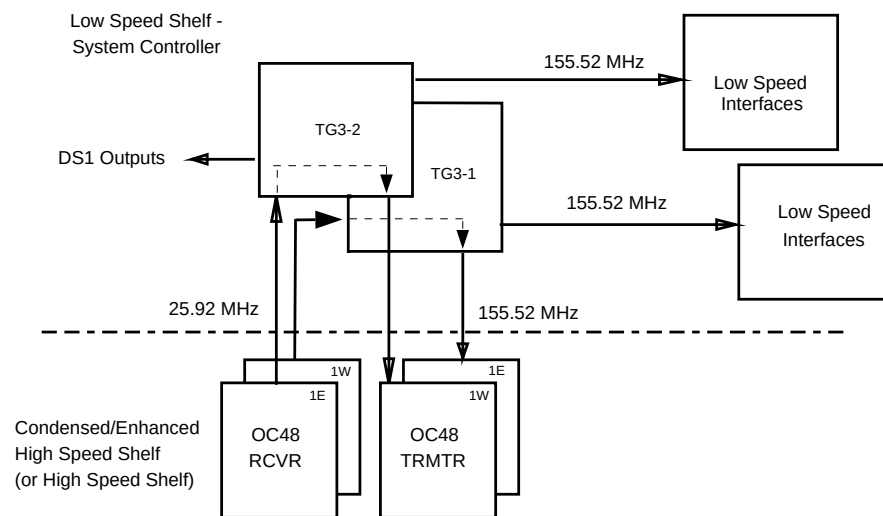


Figure 5-6. Through Timed Synchronization (FT-2000 OC-48 Add/Drop-Rings Terminal)

The OC-48 line reference signals are continuously monitored. If the primary OC-48 line reference signal becomes corrupted, the TG3 (DS1) circuit pack will select the secondary OC-48 line reference signal without causing service degradations. If the primary and secondary OC-48 line reference signals fail, the digital phase-locked loop (DPLL) circuit holds the on-board oscillator frequency at the last good reference sample while the references are repaired (holdover mode). This mechanism is provided so that operation with or without an external BITS clock can be easily accommodated. In the holdover mode, the on-board oscillator frequency will not degrade below the stratum 3 level.

In the through timed mode, the TG3 (DS1) circuit packs can also provide DS1 reference outputs to the office BITS clock or other central office equipment. The LAA17 TG3 (DS1) circuit pack derives the DS1 reference outputs from the internal oscillator. The LAA18 TG3 (DS1) circuit pack derives the DS1 reference outputs directly from the OC-48 line reference signals. The DS1 reference output interface (TIMING OUT 1 and TIMING OUT 2) is located on the interconnection panel of the Low Speed Shelf - System Controller. For more information about the physical location of the DS1 reference interfaces, refer to Volume I, Section 3, "Platform Descriptions."

**NOTE:**

The OC-12 low speed interface(s) can be provisioned to drop traffic from both the West and East directions. Thus, the OC-12 is provisioned with multiple cross-connections. With regard to synchronization (through timed mode only), the OC-12 interface selects the first cross-connection as its source for pass through synchronization.

In the FT-2000 OC-48 Repeater Bay, the OC48 Regenerator (OC48 REGENR) circuit packs derive timing from the incoming OC-48 signals and use it to time their outgoing OC-48 signals. For example, the OC48 REGENR circuit pack that accepts the incoming OC-48 signal from high speed line 1E derives timing for the OC-48 signal transmitted on high speed line 1W (Figure 5-7). Also, the OC48 REGENR circuit pack that accepts the incoming OC-48 signal from high speed line 1W derives timing for the OC-48 signal transmitted on high speed line 1E. The FT-2000 OC-48 Repeater Bay does not require TG3 (DS1) circuit packs.

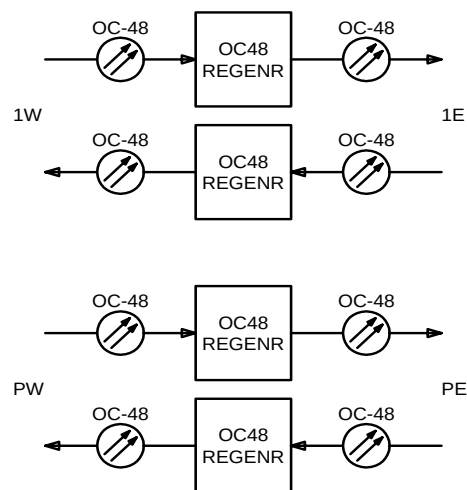


Figure 5-7. Through Timed Synchronization (FT-2000 OC-48 Repeater Bay)

Loop Timed Mode—only for Point-to-Point Releases of FT-2000

In the loop timed mode (Figure 5-8), the TG3 (DS1) circuit packs on the Low Speed Shelf - System Controller derive timing from the incoming OC-48 signals. The TG3 (DS1) circuit packs accept an OC-48 line reference signal (25.92 MHz) from the OC48 RCVR circuit packs and derive the internal synchronization used by the transmission packs.

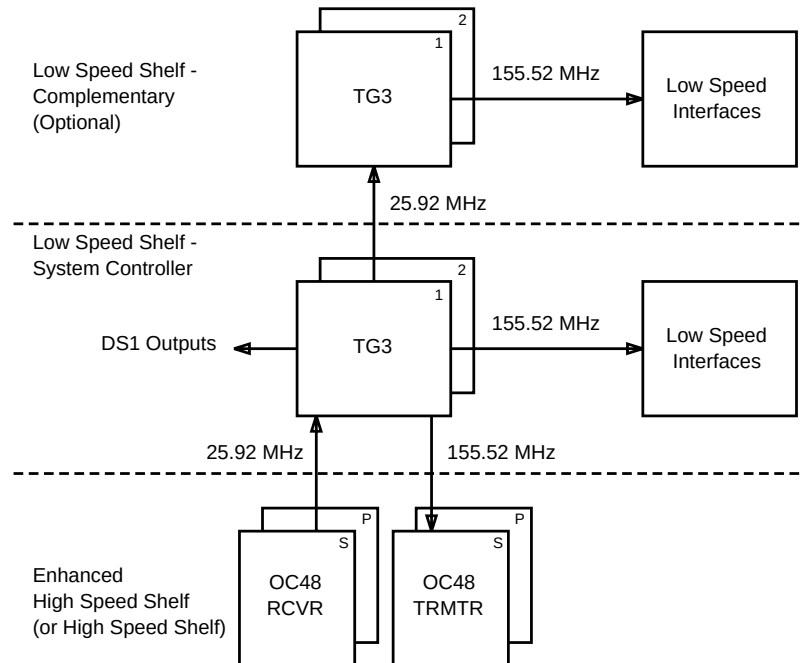


Figure 5-8. Loop Timed Synchronization

The OC-48 line reference signals are continuously monitored for error-free operation. If the primary OC-48 line reference signal becomes corrupted, the TG3 (DS1) circuit pack will select the secondary OC-48 line reference signal without causing service degradations. If both OC-48 line reference signals are corrupted, the digital phase-locked loop (DPLL) circuit will attempt to lock on to the primary DS1 synchronization input (if equipped). If the primary DS1 synchronization input is not equipped or fails, the DPLL circuit will attempt to lock on to the secondary DS1 synchronization input (if equipped). If all these references fail, the DPLL circuit holds the on-board oscillator frequency at the last good reference sample while the references are repaired (holdover mode). This mechanism is provided so that operation with or without an external BITS clock can be easily

accommodated. In the holdover mode, the on-board oscillator frequency will not degrade below the stratum 3 level.

The TG3 (DS1) circuit packs distribute 155.52 MHz timing signals to the low speed interface circuit packs on the Low Speed Shelf - System Controller and the OC48 TRMTR circuit packs on the Enhanced High Speed Shelf (or High Speed Shelf).

In the loop timed mode, the TG3 (DS1) circuit packs can also provide DS1 synchronization outputs to the office BITS clock or other central office equipment. The LAA17 TG3 (DS1) circuit pack derives the DS1 synchronization outputs from the internal oscillator. The LAA18 TG3 (DS1) circuit pack derives the DS1 synchronization outputs directly from the OC-48 line reference signal. If the OC-48 line reference signals fail, the TG3 (DS1) circuit pack transmits DS1 alarm indication signals (AIS) to the BITS clock to prevent any uncontrolled timing loops.

The DS1 reference input interface (TIMING IN PRI and TIMING IN SECY) and the DS1 reference output interface (TIMING OUT 1 and TIMING OUT 2) are located on the interconnection panel of the Low Speed Shelf - System Controller. For more information about the physical location of the DS1 reference interfaces, refer to Volume I, Section 3, "Platform Descriptions."

Externally Timed Mode

In the externally timed mode (phase locked mode in releases prior to Release 5) (Figure 5-9), each TG3 (DS1) circuit pack accepts a DS1 synchronization reference signal from an external stratum 3 or better clock. In bidirectional 2-fiber ring applications, the DS1 references synchronize the local FT-2000 OC-48 Add/Drop-Rings Terminal, and other FT-2000 OC-48 Add/Drop-Rings Terminals that are through timed, with other network equipment operating under the same primary clock source. Within the TG3 (DS1) circuit pack, a highly stable DPLL circuit removes any transient impairments on the DS1 reference for improved jitter performance.

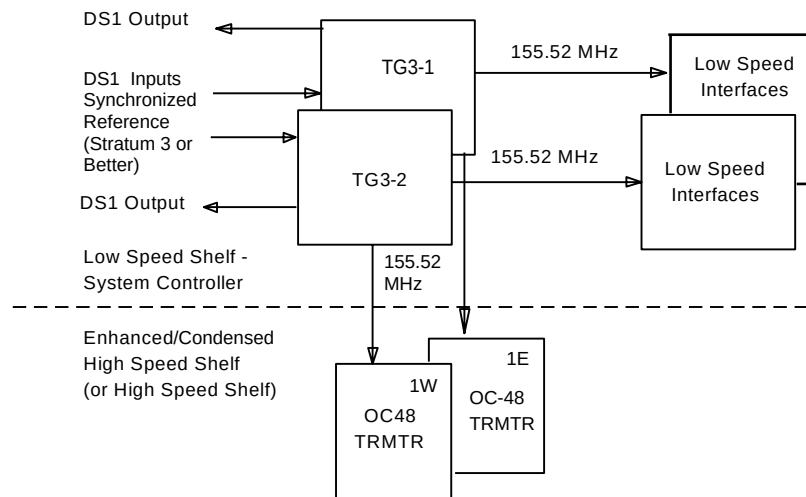


Figure 5-9. Externally Timed Synchronization

The DS1 references are continuously monitored. If the primary DS1 reference becomes corrupted, the TG3 (DS1) circuit pack will select the secondary DS1 reference without causing service degradations. If both DS1 references are corrupted, the DPLL circuit holds the on-board oscillator frequency at the last good reference sample while the DS1 references are repaired (holdover mode). In the holdover mode, the on-board oscillator frequency will not degrade below the stratum 3 level.

The TG3 (DS1) circuit packs distribute 155.52 MHz timing signals to the low speed interface circuit packs on the Low Speed Shelf - System Controller and the OC48 TRMTR circuit packs on the Enhanced High Speed Shelf (or High Speed Shelf).

In the externally timed mode, the LAA18 TG3 (DS1) circuit packs can also provide DS1 synchronization output signals to the office BITS clock or other central office equipment.

The DS1 reference input interface (TIMING IN PRI and TIMING IN SECY) and the DS1 reference output interface (TIMING OUT 1 and TIMING OUT 2) are located on the interconnection panel of the Low Speed Shelf - System Controller. Refer to Volume I, Section 3, "Platform Descriptions," for more information about the physical location of the DS1 reference interfaces.

Network Synchronization

General

Careful consideration should be given to proper design of the SONET network's synchronization. Proper synchronization engineering minimizes timing instabilities, maintains quality transmission network performance, and limits network degradation due to unwanted propagation of network synchronization faults. The synchronization features of the FT-2000 OC-48 Lightwave System are designed to complement existing and future network synchronization and, hence, allow it not only to make use of network synchronization but also to take an active role in facilitating network synchronization.

Several published sources give generic recommendations on setting up a synchronization network. The FT-2000 OC-48 Lightwave System is designed to operate in a network that complies with recommendations stated in GR-436-CORE, *Digital Synchronization Network Plan*, Issue 1, June 1994; GR-378-CORE, *Generic Requirements for Timing Signal Generators (TSG)*, Issue 1, June 1995; and ANSI* T1.101-1994, *American National Standard for Telecommunications - Synchronization Interface Standards*. The following are some key recommendations from these documents. For further detailed explanation, consult the sources directly.

- A network element can only receive the synchronization reference signal from another network element that contains a clock of equivalent or superior quality (stratum level).
- The facilities with the greatest availability (absence of outages) should be selected for synchronization facilities.
- Where possible, all primary and secondary synchronization facilities should be diverse, and synchronization facilities within the same cable should be minimized.
- The total number of network elements in series from the stratum 1 source should be minimized. For example, the primary synchronization network would ideally look like a star configuration with the stratum 1 source at the center. The network elements connected to the star would branch out in decreasing stratum level from the center.
- No timing loops may be formed in any combination of primary and secondary facilities.

The FT-2000 OC-48 Lightwave System supports the following applications (with or without the FT-2000 OC-48 Repeater Bay) and network synchronization configurations:

* Registered trademark of American National Standards Institute, Inc.

- OC-48 point-to-point applications with asynchronous (DS3) low speed interfaces
 - Free running/free running configuration
 - Free running/through timed configuration
 - Phase locked/phase locked configuration
- Bidirectional ring applications with asynchronous (DS3) and synchronous (EC-1, OC-3 and/or OC-12) low speed interfaces
 - Ring applications not used to distribute timing to BITS clocks
 - Ring applications used to distribute timing to BITS clocks.
 - Free running/free running configuration
 - Free running/through timed configuration
 - Phase locked/phase locked configuration

OC-48 Point-to-Point Applications

Free Running/Free Running Configuration

For initial SONET deployment with DS3 low speed interfaces, no external synchronization reference is needed; therefore, the expense of connecting to one is eliminated. The free running/free running configuration is ideal for linear networks with asynchronous interfaces. It allows the FT-2000 OC-48 Add/Drop-Rings Terminal to interface with the existing asynchronous equipment in a network and enables a smooth evolution to a synchronous network.

The free running/free running configuration applies to OC-48 point-to-point applications (with or without FT-2000 OC-48 Repeater Bays) with asynchronous (DS3) low speed interfaces only.

In the free running/free running configuration (Figure 5-10), both terminals are free running; that is, they require no external synchronization reference. The transmit and receive signals are asynchronous with respect to each other.

For more information about platform timing modes, refer to "Platform Synchronization" in this section.

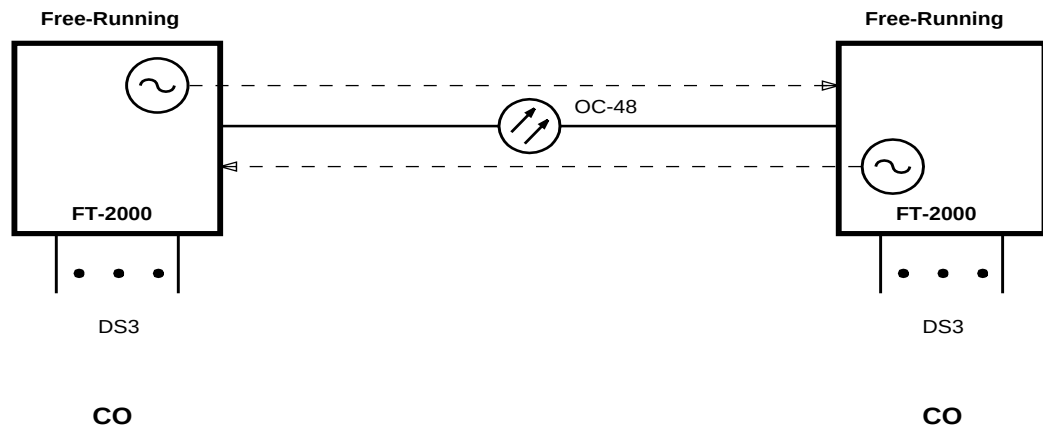


Figure 5-10. Free Running/Free Running Configuration

Free Running/Through Timed Configuration

The free running/through timed configuration may be used for initial asynchronous applications and reprovisioned later to the externally timed/through timed configuration by adding an external DS1 synchronization reference.

The free running/through timed configuration applies to OC-48 point-to-point applications (with or without FT-2000 OC-48 Repeater Bays) with asynchronous (DS3) low speed interfaces only.

In the free running/through timed configuration (Figure 5-11), one FT-2000 OC-48 Add/Drop-Rings Terminal times its transmitted signals from its free running internal oscillator. The internal oscillator at the other FT-2000 OC-48 Add/Drop-Rings Terminal uses the OC-48 line reference signal recovered from the incoming OC-48 signal to derive timing for its transmitted signals.

The FT-2000 OC-48 Add/Drop-Rings Terminal in the through timed mode also derives two DS1 synchronization outputs. The LAA18 TG3 (DS1) circuit pack derives the DS1 synchronization outputs directly from the timing signal recovered from the incoming OC-48 signal. The DS1 synchronization outputs are made available to the office BITS clock or other SONET network elements in the office.

For more information about platform timing modes, refer to "Platform Synchronization" in this section.

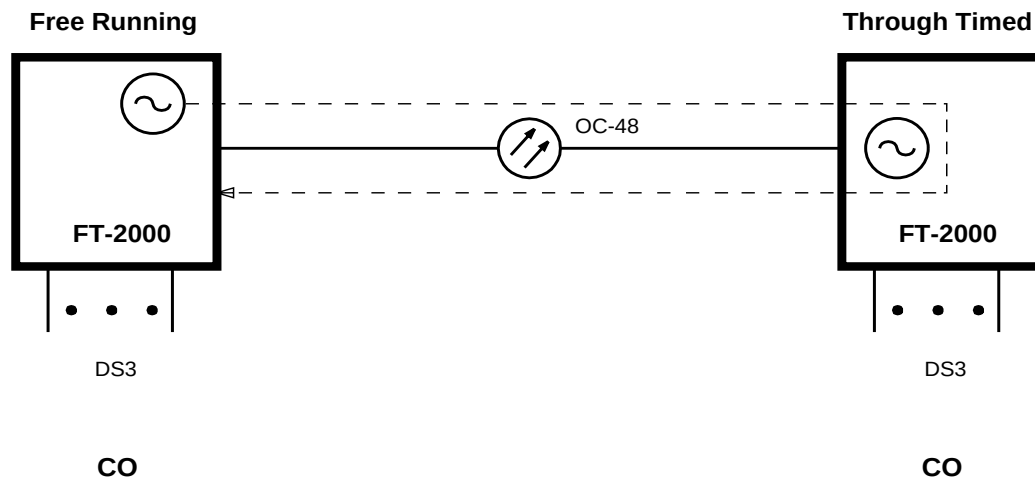


Figure 5-11. Free Running/Through Timed Configuration

Externally Timed/Through Timed Configuration

The externally timed/through timed configuration allows the FT-2000 OC-48 Add/Drop-Rings Terminal to synchronize to the digital synchronization network as required for synchronous operation by the SONET standard. The network is synchronized to a local central office clock via DS1 external synchronization references. The local office clock must be stratum 3 or better, with timing traceable to a primary reference source (stratum 1 clock). If synchronous (EC-1, OC-3, IS-3 and/or OC-12) low speed interfaces are present, external sources must be traceable to a stratum 1 clock.

The externally timed/through timed configuration applies to all OC-48 point-to-point applications (with or without the FT-2000 OC-48 Repeater Bay).

The FT-2000 OC-48 Add/Drop-Rings Terminal in the through timed mode may be equipped with two DS1 external synchronization references. If the OC-48 line reference signals fail, the TG3 (DS1) circuit pack holds the on-board oscillator frequency at the last good reference sample (holdover mode).

The FT-2000 OC-48 Add/Drop-Rings Terminal in the through timed mode can also provide two DS1 synchronization outputs to the office BITS clock or other SONET network elements. An FT-2000 OC-48 Add/Drop-Rings Terminal equipped with the LAA18 TG3 (DS1) circuit pack derives the DS1 synchronization outputs directly from the OC-48 line reference signals (Figure 5-12). For more information about platform timing modes, refer to "Platform Synchronization" in this section.

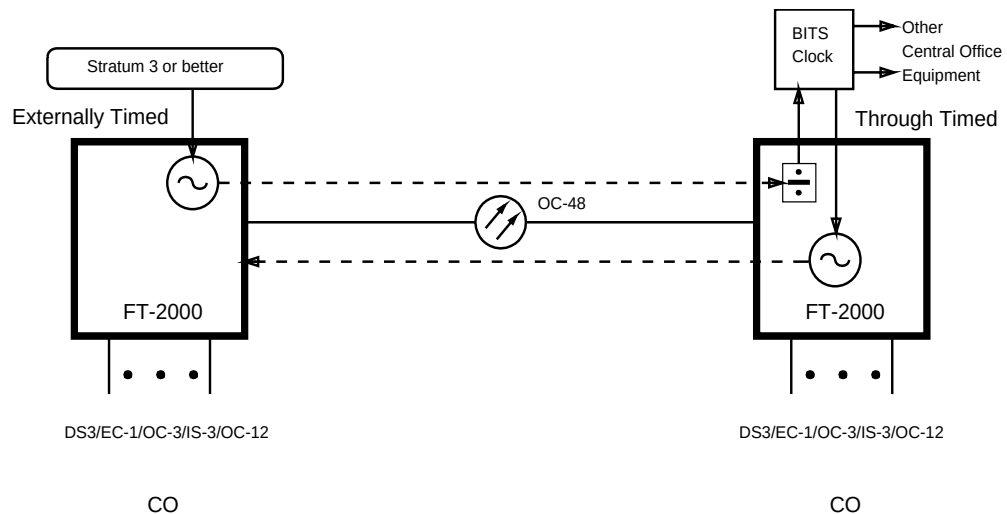


Figure 5-12. Externally Timed/Through Timed Configuration

Phase Locked/Phase Locked Configuration

In the phase locked/phase locked configuration, both FT-2000 OC-48 Point-to-Point or Add/Drop-Rings Terminals time their transmitted signals from internal oscillators that are locked to the DS1 external synchronization reference.

Since this configuration requires local office clocks at each end terminal, it is most suited to interoffice applications. The network may have all DS1 references traceable to a common primary reference source (for example, outstate trunking) or to multiple primary reference sources (for example, a carrier-to-carrier interface). The use of multiple primary reference sources is referred to as plesiochronous operation (Figure 5-13).

The phase locked/phase locked configuration applies to all OC-48 applications (with or without the FT-2000 OC-48 Repeater Bay).

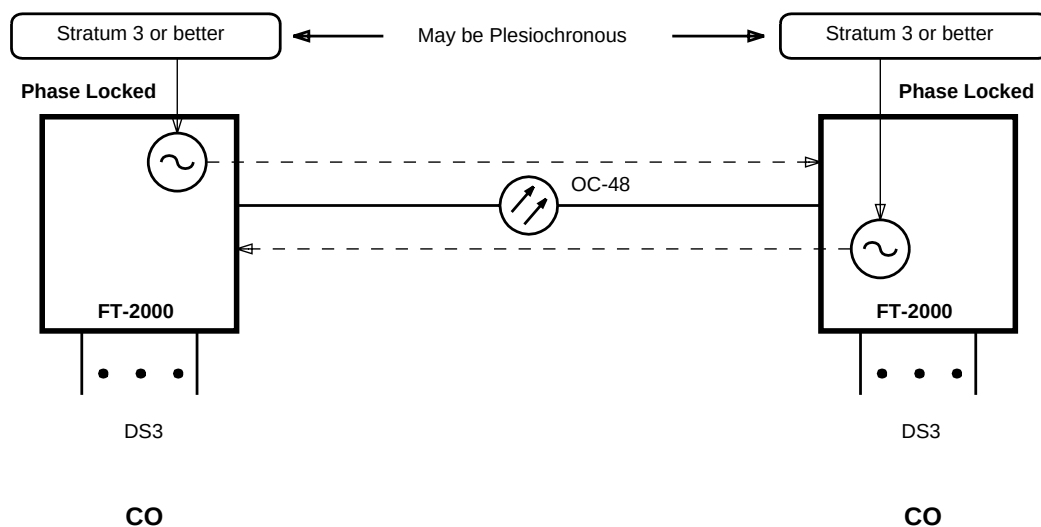


Figure 5-13. Phase Locked/Phase Locked Configuration

An FT-2000 OC-48 Add/Drop-Rings Terminal equipped with the LAA18 TG3 (DS1) circuit pack can also provide DS1 synchronization outputs to the office BITS clock or other central office equipment (Figure 5-14).

For more information about platform timing modes, refer to "Platform Synchronization" in this section.

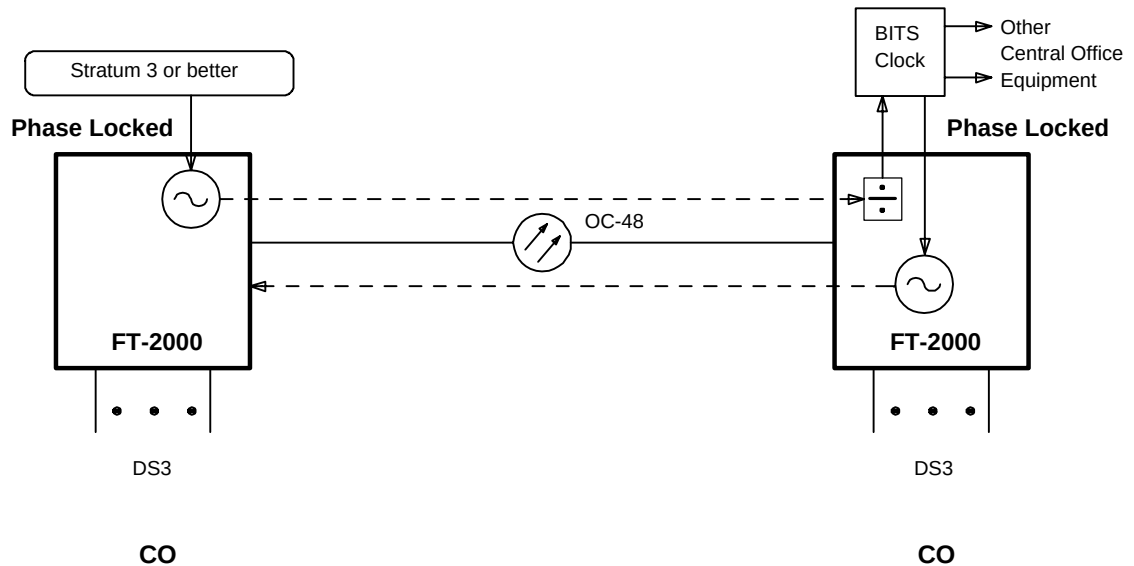


**Figure 5-14. Phase Locked/Phase Locked Configuration
[LAA18 TG3 (DS1) Circuit Pack]**

Externally Timed/Externally Timed Configuration

In the externally timed/externally timed configuration (Figure 5-15), both FT-2000 OC-48 Add/Drop-Rings Terminals time their transmitted signals from internal oscillators that are locked to the DS1 external synchronization reference.

Since this configuration requires local office clocks at each terminal, it is most suited to interoffice applications. The network must have all DS1 references traceable to a common primary reference source (for example, outstate trunking) or to multiple primary reference sources (for example, a carrier-to-carrier interface). The use of multiple primary reference sources is referred to as plesiochronous operation. If synchronous (EC-1, OC-3, IS-3 or OC-12) low speed interfaces are present, external sources must be traceable to a stratum 1 clock.

The externally timed/externally timed configuration applies to all OC-48 Add/Drop-Rings Terminal applications (with or without the FT-2000 OC-48 Repeater Bay).

The LAA18 TG3 (DS1) circuit pack can also provide DS1 synchronization outputs to the office BITS clock or other central office equipment. For more information about platform timing modes, refer to "Platform Synchronization" in this section.

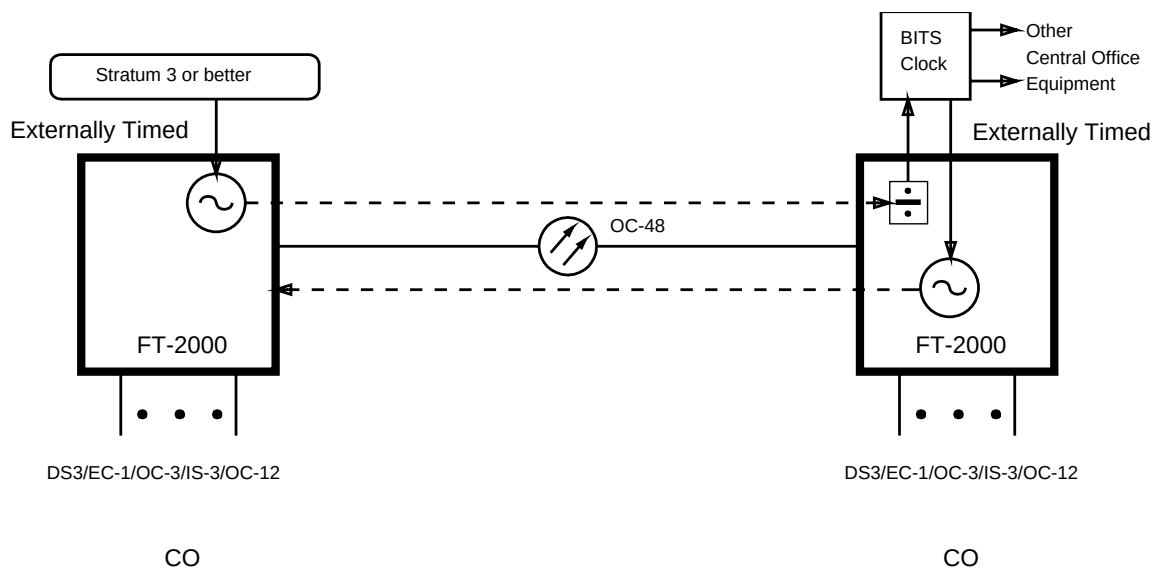


Figure 5-15. Externally Timed/Externally Timed Configuration

Bidirectional Ring Applications

General

A bidirectional ring consists of up to 16 nodes, and each node has several synchronization provisioning options possible. This section contains a complete set of rules for determining how the synchronization mode should be provisioned for each node.

In addition to providing high quality, protected transmission, an FT-2000 ring can also be used to distribute timing signals from BITS clocks to other BITS clocks using the OC-48 signal. Distributing timing signals to BITS clocks using the FT-2000 OC-48 Lightwave System is becoming an increasingly important additional application to the transmission function. This is because the OC-48 line is a higher quality medium than direct T1 lines currently used to distribute timing signals or T1 facilities carried in the payload of SONET systems. (SONET payloads carrying T1 facilities cannot be used to distribute timing in rings.) However, there are many good quality synchronization distribution networks in place using T1 facilities that users may not wish to convert. Therefore, the FT-2000 OC-48 Lightwave System may or may not be used for distributing timing signals. The user must decide if the FT-2000 ring will be used to distribute timing signals to BITS clocks. This distinction is necessary to ensure that the nodes in the ring are provisioned properly to prevent closed timing loops during protection switching operations.

Prior to FT-2000 OC-48 ADR Release 7.2, network configurations could only use: (a) free running, (b) external timed, or (c) through timed synchronization modes. With Release 7.2 line timed synchronization mode is available. Reconfiguring a customer's network to support this mode is optional, but it is recommended. A combination of through timed and line timed in the same ring should only be used temporarily during the upgrade/reconfiguration process. During normal operations the ring should be configured for either all through timed or line timed with 2 nodes externally timed.



NOTE:

Bidirectional line switched rings, as an option, may or may not be used to distribute timing.

Ring Applications Not Used to Distribute Timing

Asynchronous Applications (DS-3 Low Speed Interfaces Only) . In ring applications not used to distribute timing signals to BITS clocks, there are six possible synchronization configurations. The two asynchronous configurations are as follows:

- **One node provisioned free running; all other nodes provisioned through timed:** This configuration (Figure 5-16) is used when there are only asynchronous (DS3) low speed interfaces on every node in the ring. Only one node in the ring should be provisioned free running. This prevents cumulative pointer processing on transmission signals passing through a node.

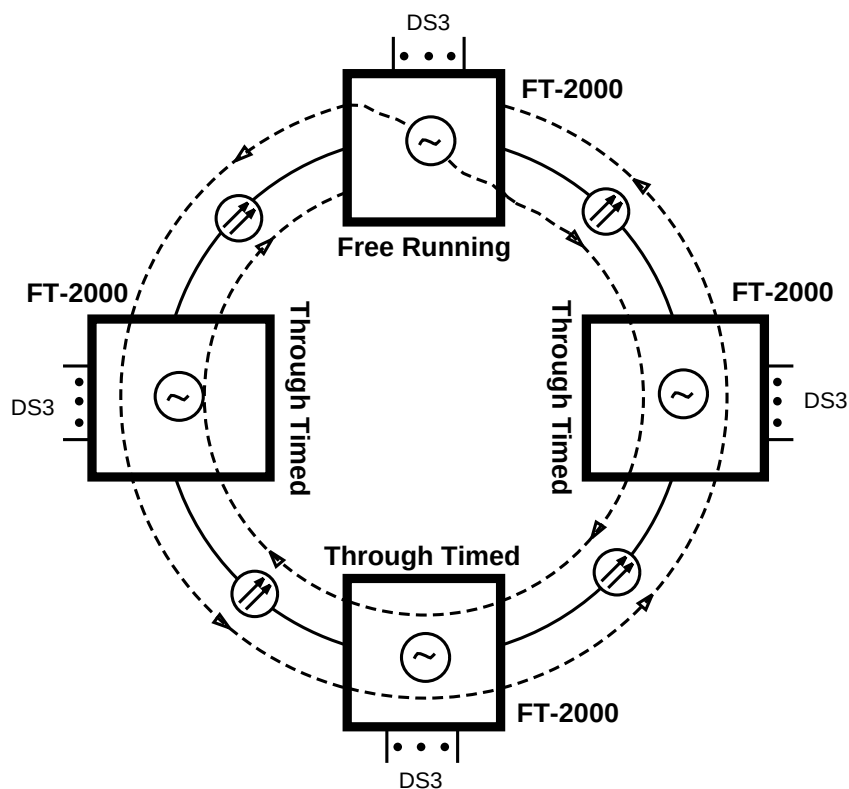


Figure 5-16. Example of a Ring Application with One Node Provisioned Free Running and All Other Nodes Provisioned Through Timed (Without Timing Drops)

- **One node provisioned free running; all other nodes provisioned Line Timed:** Refer to (Figure 5-17).

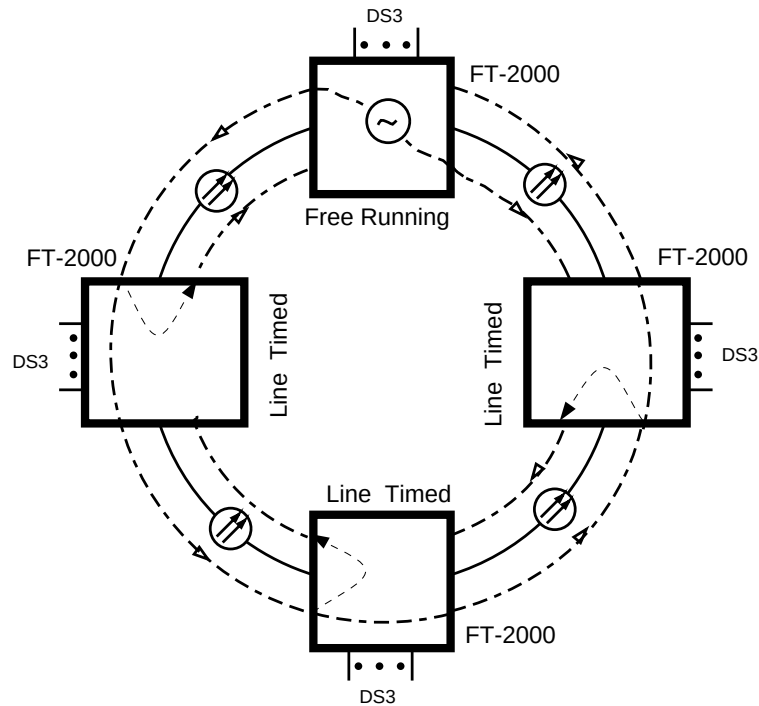


Figure 5-17. Example of a Ring Application with One Node Provisioned Free Running and All Other Nodes Provisioned Line Timed (Without Timing Drops)



NOTE:

If synchronous (EC-1, OC-3, IS-3 and/or OC-12) low speed interfaces are to be added to nodes of an asynchronous application, one node should be provisioned externally timed (phase locked in releases prior to Release 5), and all other nodes should be provisioned "through timed," "line timed," or "externally timed."

Synchronous Applications (With a Mixture of Asynchronous and Synchronous Low-Speed Interfaces)

- **One node provisioned externally timed; all other nodes provisioned through timed, line timed, or externally timed:** This configuration (Figure 5-18, Figure 5-19, Figure 5-20, and Figure 5-21) are used with asynchronous (DS3) or synchronous (EC-1, OC-3 and/or OC-12) low speed interfaces, or a mix of synchronous and asynchronous low speed interfaces. These configurations also prevent the need of having to reprovision a node when synchronous low speed interfaces are added. The timing signals used to synchronize the externally timed nodes (phase locked in releases prior to Release 5) should be traceable to a high quality source(s). If synchronous low speed interfaces are present, the timing signals must be traceable to a stratum 1 source. If only asynchronous low speed interfaces are present, the timing signals must be traceable to a stratum 3 or better source. The quality and source of the timing signals must be engineered appropriately. The guidelines for choosing the timing source(s) and the BITS clock configurations are beyond the scope of this document.

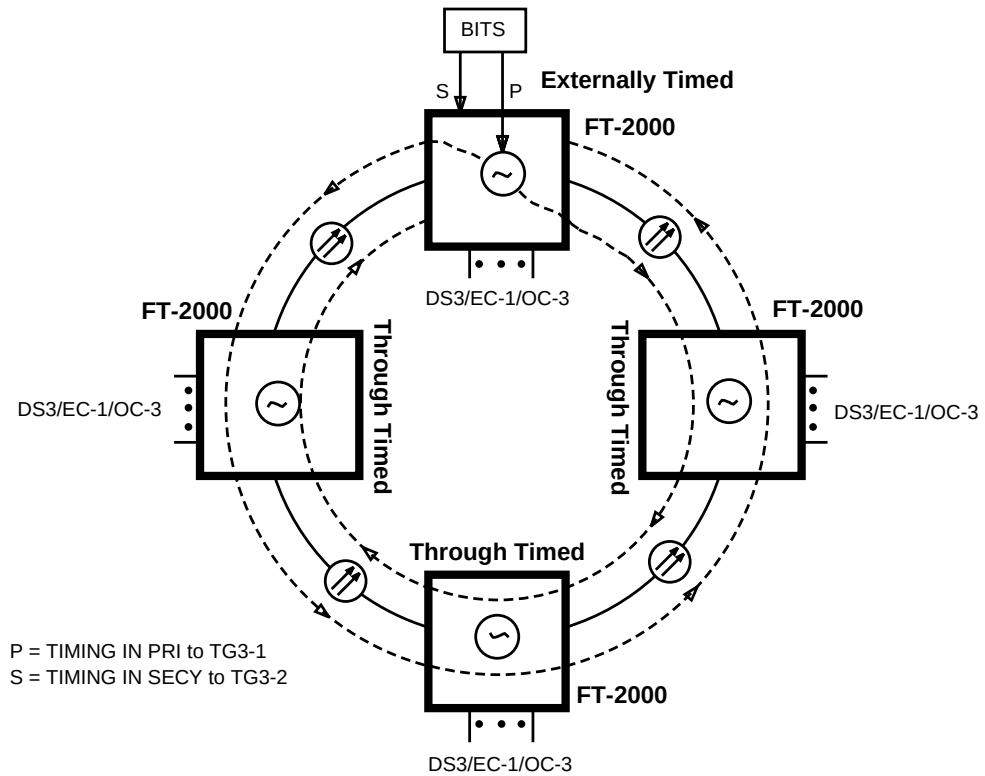


Figure 5-18. Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Through Timed (Without Timing Drops)

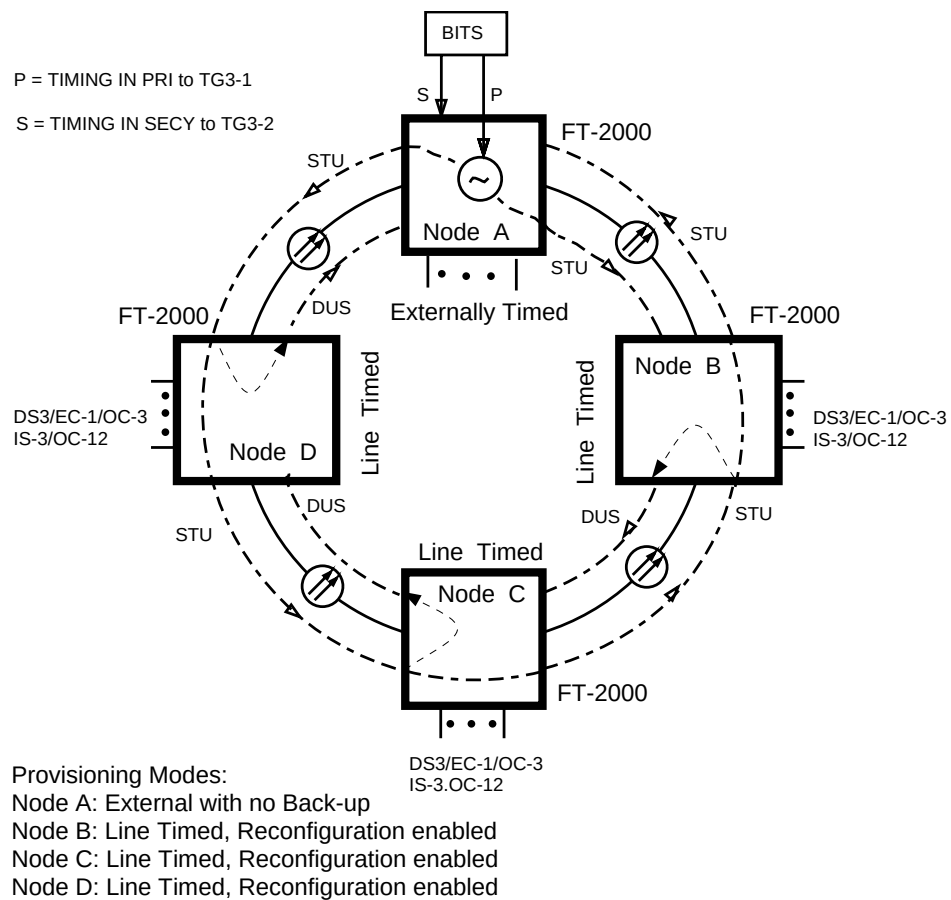


Figure 5-19. Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Line Timed (Without Timing Drops)



NOTE:

In (Figure 5-19) synchronization messages (DUS and STU) are shown as they are transmitted on the OC-48 line.

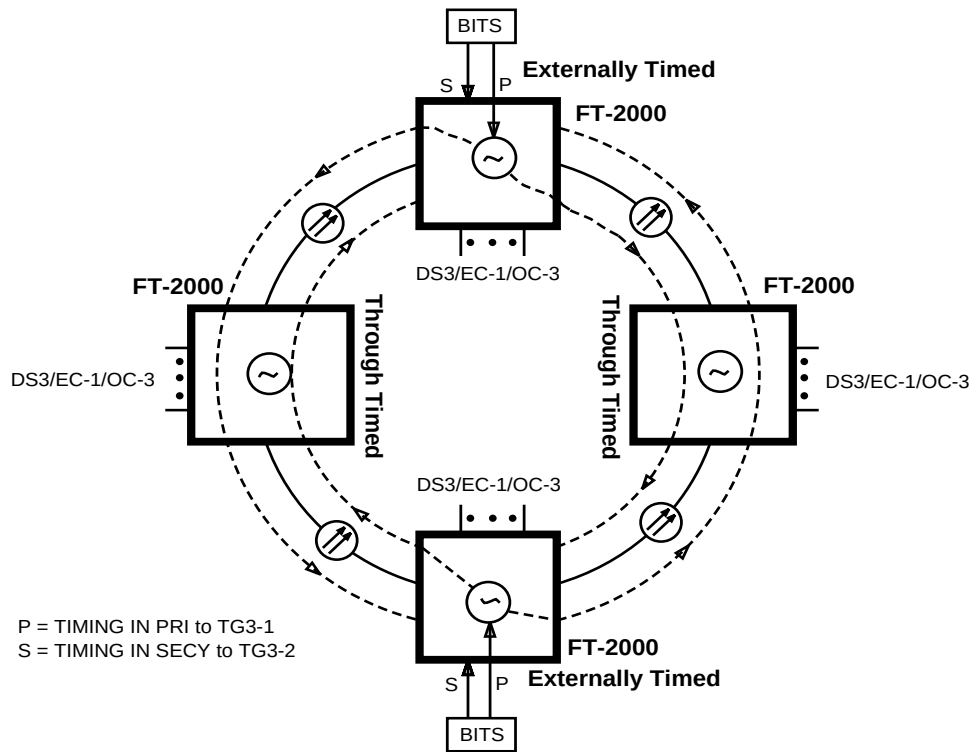


Figure 5-20. Example of a Ring Application with Two Nodes Provisioned Externally Timed and All Other Nodes Provisioned Through Timed or Externally Timed (Without Timing Drops)

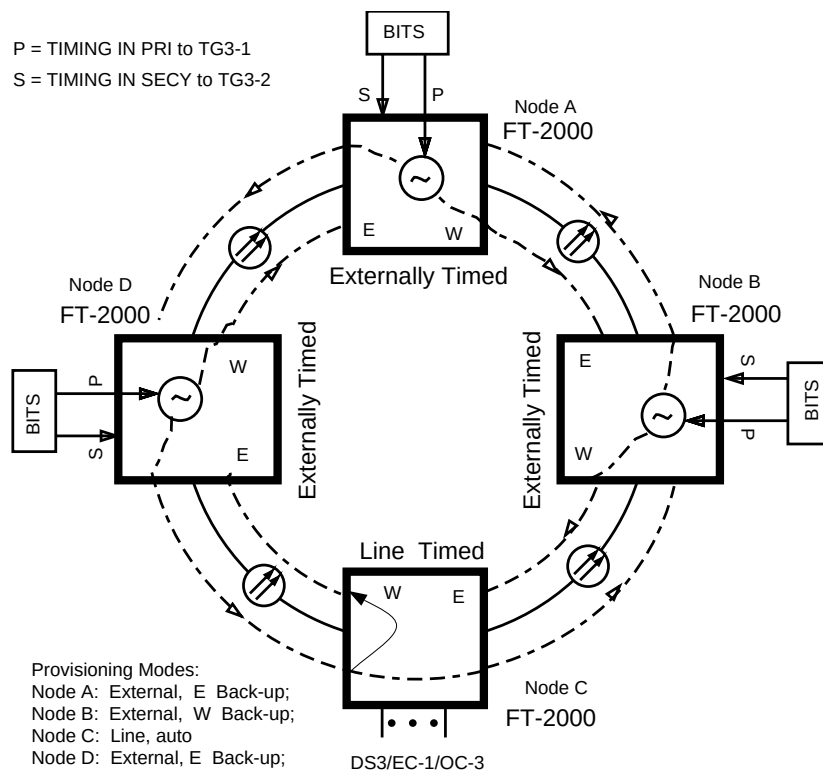


Figure 5-21. Example of a Ring Application with Three Nodes Provisioned Externally Timed and One Node Provisioned Line Timed (Without Timing Drops)

In rings with two or more external timing sources (for example, BITS clocks) available at more than one node, it is recommended to use the backup feature to provide reliability in the event of a failure. The provisioning modes, which are labeled in Figure 5-21, demonstrate this concept. Care should always be exercised to avoid timing loops.

For more information about platform timing modes, refer to "Platform Synchronization" in this section.

Ring Applications Used to Distribute Timing

For ring applications (FT-2000 Release 7.2.0 and later software) that are used to distribute timing, it is recommended that "Line Timed" mode of synchronization be used. Through timing is also (still) acceptable. In ring applications used to distribute timing signals to BITS clocks, there are two possible configurations. Considering all available releases of FT-2000, then there are three fundamentally different configurations. They are as follows:

- **One node provisioned externally timed; all other nodes provisioned line timed.** In this configuration (Figure 5-22), any one or all nodes that are provisioned line timed can derive and distribute timing.

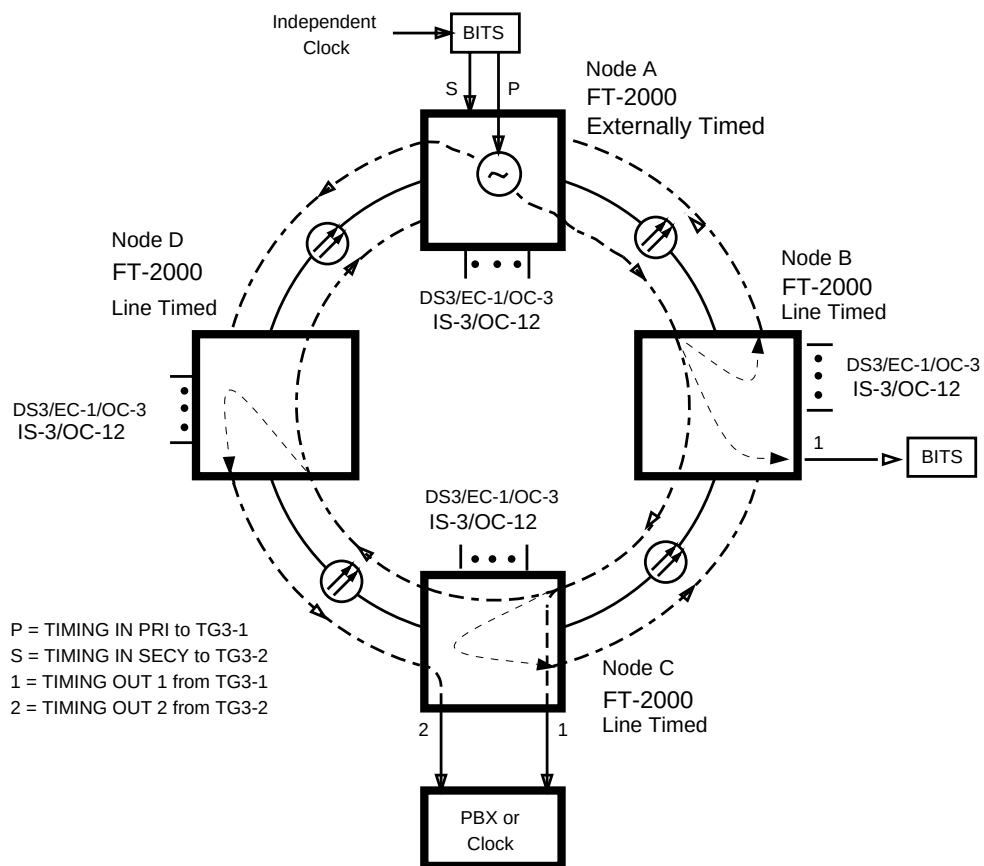


Figure 5-22. Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Line Timed (With Two Timing Drops)

- One node provisioned externally timed; all other nodes provisioned through timed or externally timed:** In this configuration (Figure 5-23 and Figure 5-24), at least one node should be provisioned externally timed (phase locked in releases prior to Release 5), and any nodes that drop timing signals should be provisioned through timed. All other nodes should be provisioned externally timed or through timed.

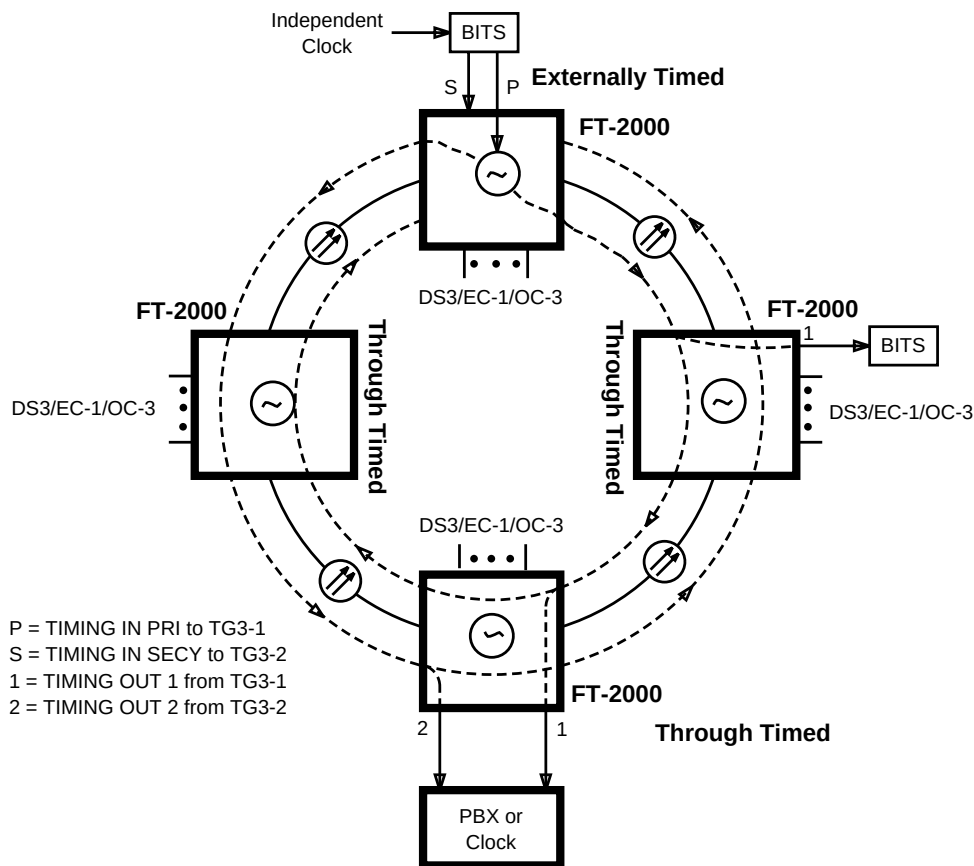


Figure 5-23. Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Through Timed (with Timing Drops)

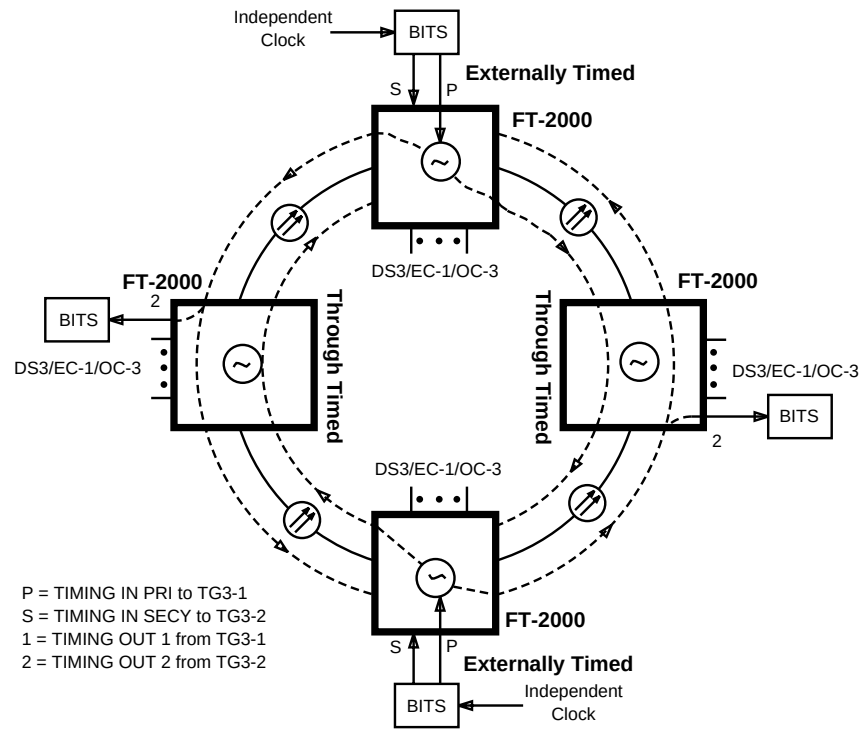


Figure 5-24. Example of a Ring Application with One Node Provisioned Externally Timed and All Other Nodes Provisioned Through Timed or Externally Timed (with Timing Drops)

At least one node should be provisioned externally timed to provide the timing signal that is distributed to some or all the remaining nodes. The timing signals used to synchronize the externally timed nodes should be traceable to a high quality timing source(s). If synchronous low speed interfaces are present, the timing signals must be traceable to a stratum 1 source. If only asynchronous low speed interfaces are present, the timing signals must be traceable to a stratum 3 or better source.

For the nodes that distribute the timing signal, the LAA18 TG3 (DS1) circuit pack should be used. The LAA18 TG3 (DS1) circuit pack provides a timing signal (DS1 synchronization output) derived directly from the OC-48 high speed line. This configuration supports either asynchronous or synchronous low speed interfaces or any combination. The quality and source of the timing signals must be engineered appropriately. The guidelines for choosing the timing source(s) and the BITS clock configurations are beyond the scope of this document.

For more information about platform timing modes, refer to "Platform Synchronization" in this section.

- Timing signal input/output interfaces must be consistent. All nodes with BITS clocks must use the primary timing interface (TIMING IN PRI and TIMING OUT 1), *or* all nodes with BITS clock must use the secondary timing interface (TIMING IN SECY and TIMING OUT 2). Figure 5-27 shows a ring application in this configuration with inconsistent timing interfaces and the resulting timing loop.

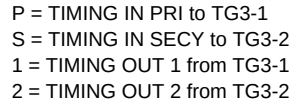


Figure 5-25. Example of a Ring Application with All Nodes Provisioned Externally Timed (with Timing Drops)

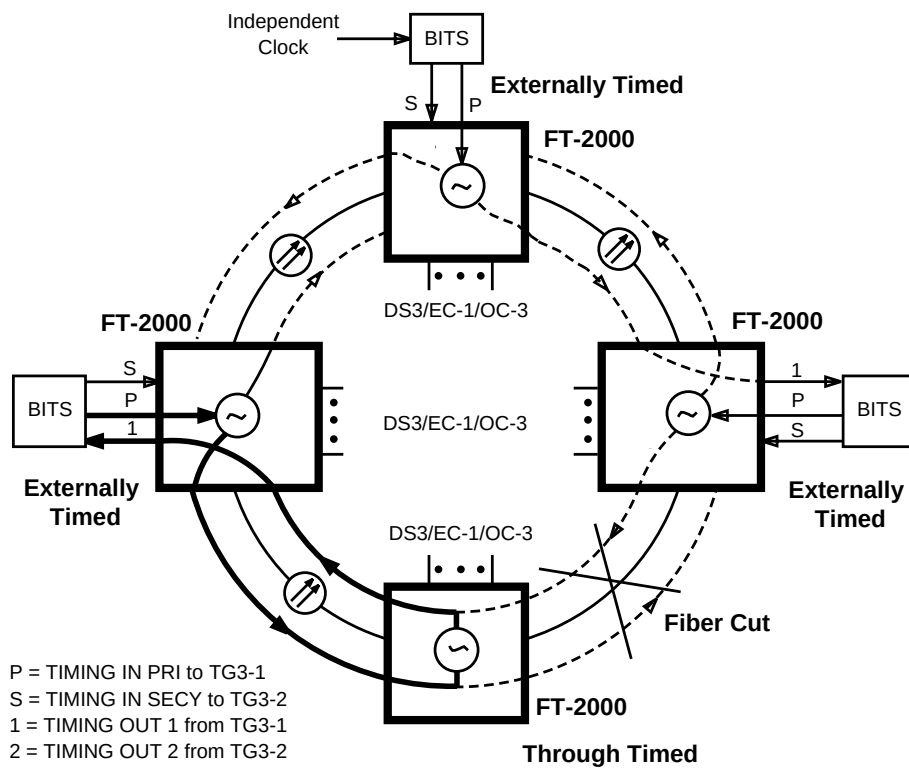


Figure 5-26. Example of a Timing Loop Resulting from Incorrect Provisioning

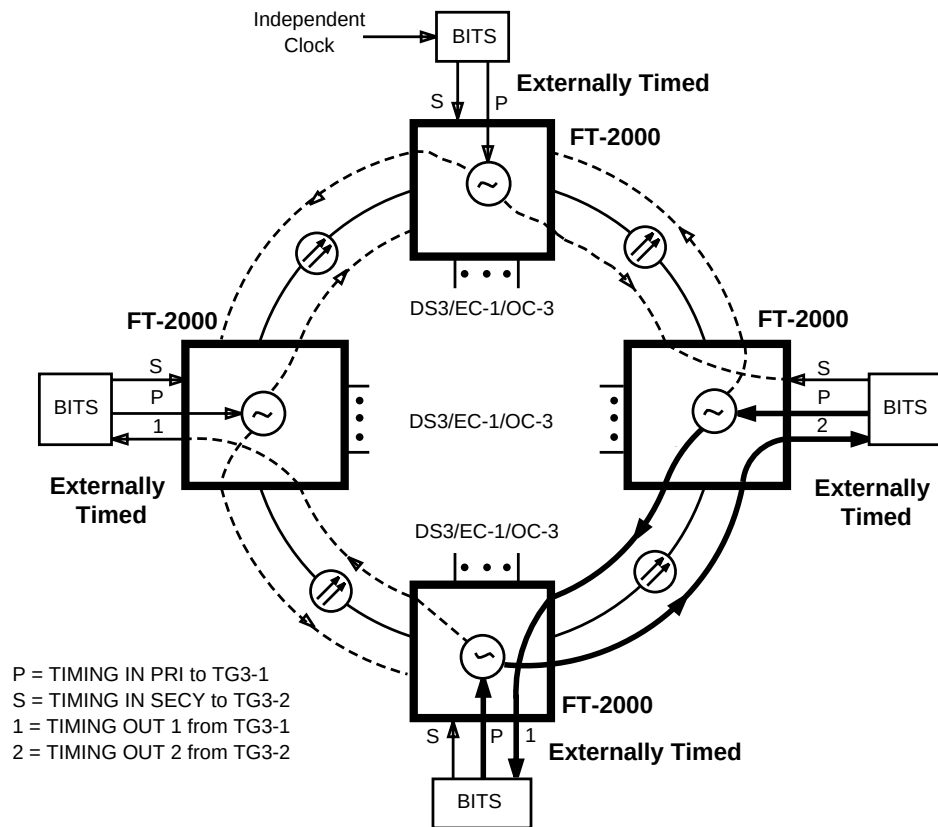


Figure 5-27. Example of a Timing Loop Resulting from Inconsistent DS1 Timing Interfaces

If a BITS or stratum 1 traceable clock is not available at a node and externally timed provisioning is desired, an external timing emulation (null BITS) cable may be used to emulate BITS operation (Figure 5-28). The external timing emulation cable connects TIMING OUT 1 to TIMING IN PRI, or TIMING OUT 2 to TIMING IN SECY at the node.

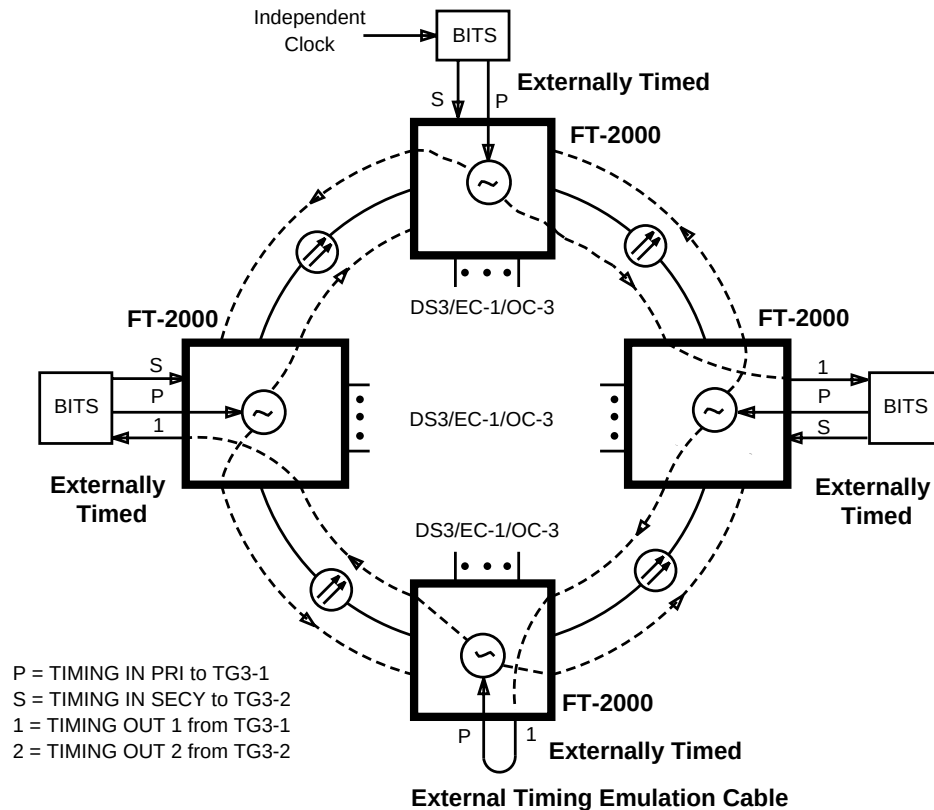


Figure 5-28. Example of a Ring Application with All Nodes Provisioned Externally Timed (with External Timing Emulation Cable)

For the nodes that distribute timing signals, the LAA18 TG3 (DS1) circuit pack should be used. The LAA18 TG3 (DS1) circuit pack provides a timing signal (DS1 synchronization output) derived directly from the OC-48 high speed line. This configuration supports either asynchronous or synchronous low speed interfaces or any combination. The quality and source of the timing signals must be engineered appropriately. The guidelines for choosing the timing source(s) and the BITS clock configurations are beyond the scope of this document.

For more information about platform timing modes, refer to "Platform Synchronization" in this section.

Summary of Timing Rules for Bidirectional Line-Switched Rings

Strategy 1

Strategy 1 is applicable for all ring topologies.

- At least one node must be externally timed from a stratum 1 traceable timing source that is not distributed by this ring.
- Nodes used as a timing drop must be through timed.
- Other nodes may be externally timed or through timed.

Strategy 2

Strategy 2 is applicable for rings that are used to distribute timing and where there is a BITS clock at every node. These are typically rings that connect central offices.

Externally timed nodes that are used to derive timing must obey the following rules to guarantee no timing loops:

- All nodes must be provisioned externally timed.
- At least one node must be timed from a stratum 1 traceable timing source that is not derived from this ring.
- Use only the Timing Out 1 signal. Alternately, it is possible to use only the Timing Out 2 signal—conditional that Timing Out 1 is not used.

Table of Contents

Overview	6-1
Craft Interface Terminal	6-1
■ General	6-1
■ PC with FT-2000 CIT-PC Software	6-2
■ PC with CPro-2000 Software	6-5
■ ASCII-Based Terminal	6-7
■ Local Access Through the CIT (DCE) Port	6-8
■ Dial-Up Access Using a Modem Through the CIT (DTE) Port	6-8
■ Remote Access Using the OC-48 Data Communications Channel (DCC)	6-10
■ Remote Access Using the OC-48 Section Orderwire	6-10
■ Remote Access Using the OC-3/OC-12 Data Communications Channel (DCC)	6-12
User Panel	6-13
Filter Panel	6-17
Office Alarms Interface	6-19
Parallel Telemetry Interface	6-20
User-Settable Miscellaneous Discrete Interface	6-20
Serial Telemetry Interface	6-22
Message-Based Operations System Interface	6-23
Orderwire Interface	6-30

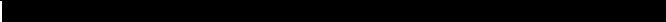


Table of Contents

Overview

This section presents the operations interfaces that allow access to the FT-2000 OC-48 Lightwave System and provide alarm and status information. Local operations interfaces include the craft interface terminal, user panel LEDs and controls, and the circuit pack faceplate LEDs. Other operations interfaces include office alarms, parallel telemetry, a user-settable miscellaneous discrete interface, serial telemetry, a message-based operations system interface, and orderwire.

Craft Interface Terminal

General

The craft interface terminal (CIT) provides full access to the administrative, maintenance, and provisioning capabilities of the FT-2000 OC-48 Lightwave System. The FT-2000 OC-48 Lightwave System supports CIT local access through the CIT (DCE) port, dial-up access using a modem through the CIT (DTE) port, and remote access using the OC-48 data communications channel (DCC). In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal supports remote access using the OC-3 DCC. In Release 7.1.0 and later releases, the FT-2000 OC-48 ADR Terminal supports remote access using the OC-12 DCC channel (0x1 links only). In Release 7.2.0 and later releases, remote access using the OC-12 DCC channel in 1+1 protected configurations is also supported.

The following functions are provided using the CIT:

- n Reports
- n Loopbacks and testing
- n Protection switching
- n Initializing performance-monitoring storage registers
- n Cross-connections
- n Provisioning.

The FT-2000 OC-48 Lightwave System also provides a security function to protect against unauthorized access to the CIT system functions (for example, provisioning). Logins, passwords, functional capabilities, authorization levels, and access to the system capabilities are provided. For more information about security, refer to Volume I, Section 8, "Administration and Provisioning."

The CIT may be one of the following:

- n A personal computer (PC) loaded with the FT-2000 CIT-PC software
- n A PC loaded with the CPro-2000 Graphical User Interface and Provisioning Tool (Release 5 and later)
- n An American Standard Code for Information Interchange (ASCII) based (dumb) terminal (Release 5 and later).

In Release 5 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal may be provisioned to accept a PC loaded with the CPro-2000 software or an ASCII-based (dumb) terminal as the CIT using the **SECURITY-Set - Security-Port** input or **ENT-CID-SECU** TL1 command in R7.2. For more information about provisioning and the **SECURITY-Set - Security-Port** input, refer to Volume I, Section 8, "Administration and Provisioning."

PC with FT-2000 CIT-PC Software

A PC loaded with FT-2000 CIT-PC software communicates with the FT-2000 OC-48 Lightwave System using the F-interface protocol. A PC loaded with FT-2000 CIT-PC software has a graphical user interface that uses pull-down menus. Extensive on-line help is also provided. A PC loaded with FT-2000 CIT-PC software must be used to install software in an FT-2000 OC-48 Add/Drop-Rings Terminal.

In Release 5.0.2 and later releases, a PC must meet the following minimum requirements to be loaded with FT-2000 CIT-PC software and used as a CIT:

- n 386SX *IBM*^{*} compatible computer (desktop or lap top) (25 MHz or greater clock)
- n Disk drive — one 1.44 megabytes (3.5 inches)
- n Hard disk with approximately 5 megabytes of free space
- n 4 megabytes RAM[†]
- n *MS-DOS*[‡] computer program, Version 5.0 or later
- n Serial port (EIA-232-D) — configured as COM1, COM2, COM3, or COM4
- n Parallel port — configured as LPT1
- n VGA color monitor.

In releases prior to Release 5.0.2, a PC must meet the following minimum requirements to be loaded with FT-2000 CIT-PC software and used as a CIT:

- n Any *IBM* compatible computer (desktop or lap top)
- n Disk drive — one 720 kilobytes (3.5 inches) or one 1.2 megabytes (5.25 inches)
- n Hard disk with approximately 5 megabytes of free space
- n 640 kilobytes RAM[†]
- n *MS-DOS* computer program, Version 3.1 or later
- n Serial port (EIA-232-D) — configured as COM1, COM2, COM3[§], or COM4[§]
- n Parallel port — configured as LPT1
- n 4.77 MHz clock speed or greater
- n Monochrome screen monitor — Standard-size ASCII 25 lines x 80 columns.

* Registered trademark of International Business Machines Corporation.

† No memory resident programs running in the background. 565 kilobytes of free conventional memory are required. In Release 7 and later releases, 2 megabytes of extended memory is also required. If less than 565 kilobytes of free conventional memory are available, a memory manager (for example, HIMEM.SYS with *MS-DOS* 5.0 and later) is required to increase the free conventional memory.

‡ Registered trademark of Microsoft Corporation.

§ Not supported in Releases 1 and 2.

When the PC is loaded with terminal emulation software (for example, *ProComm Plus*^{*} software), it can also be used to access a local DDM-2000 Multiplexer. In Release 6 and later releases, the PC does not require terminal emulation software to access a DDM-2000 Multiplexer. The CIT can access a remote DDM-2000 Multiplexer using the OC-3 data communications channel.

The CIT-PC software will run in a *Windows*[†] program environment. However, the CIT-PC software was not designed to operate with *Windows* and operation with *Windows* is not supported or recommended in Release 6 and earlier releases. Users can create multiple windows running the CIT-PC software to control the same FT-2000 OC-48 Add/Drop-Rings Terminal. The most recent user to log in supersedes all previous users. When this user logs out, all sessions are deleted.

When running the CIT-PC software in a *Windows* program environment, a program information file (PIF) must be created for the CIT. The PIF file should specify that the task be "exclusive" for 386 enhanced mode. If running in standard mode, users should select "directly modifies" for the COM port they will be using. In addition, users should select some of the other options (like no screen saver, no task switching, etc.) to reduce the amount of memory required. A CIT icon must also be defined and linked to the PIF file.

The following is an example of a CIT.PIF for the 386 enhanced mode:

PIF Editor	
Program Filename:	CIT.EXE
Window Title:	FT-2000 CIT Interface
Optional Parameters:	
Start-Up Directory:	Use your directory name!
Video Memory:	Text
Memory Requirements:	KB Required 540 KB Desired 640
EMS Memory (Release 6 and earlier):	KB Required 0 KB Desired 1024
EMS Memory (Release 7 and later):	MB Required 2 MB Desired 2
XMS Memory:	KB Required 0 KB Desired 1024
Display Usage:	Full Screen
Execution:	Exclusive
Close Window on Exit:	Yes

* Registered trademark of Datastorm Technologies, Inc.

† Trademark of Microsoft Corporation.

Advanced Options

Multitasking Options:

Background Priority:	50
Foreground Priority:	100
Detect Idle Time:	Yes

Memory Options:

Lock Application Memory:	Yes

Display Options:

Monitor Ports:	Emulate Text Mode

Other Options:

Allow Fast Paste:	Yes

The following is an example of a CIT.PIF for the standard mode:

Program Filename:	CIT.EXE
Window Title:	FT-2000 CIT Interface
Optional Parameters:	
Start-Up Directory:	Use your directory name!
Video Memory:	Text
Memory Requirements:	KB Required 540
XMS Memory:	KB Required 0 KB Desired 0
Directly Modifies:	COM1
Close Window on Exit:	Yes
No Screen Exchange:	Yes
Prevent Program Switch:	Yes
No Screen Save:	Yes

PC with CPro-2000 Software

In Release 5 and later releases, a PC loaded with Release 2 of the CPro-2000 Graphical User Interface and Provisioning Tool is compatible with the FT-2000 OC-48 Add/Drop-Rings Terminal. The CPro-2000 Graphical User Interface and Provisioning Tool is a *Windows* program based alternative to entering TL1 commands manually.

A PC equipped with the CPro-2000 Graphical User Interface and Provisioning Tool communicates with FT-2000 OC-48 Lightwave System using the same TL1

message set as the message-based operations system interface (X.25/TL1). In Release 5 and later releases, the TL1 message set consists of maintenance commands (excludes autonomous messages) and cannot be used to install software in an FT-2000 OC-48 Add/Drop-Rings Terminal. On-line help messages are not supported.

The CPro-2000 Graphical User Interface and Provisioning Tool simplifies and mechanizes operations of transmission network elements. It provides a common look and feel across the SONET subsystem of FT-2000, DDM-2000 OC-3 Multiplexer and the *SLC[®]-2000* Access System.

A PC must meet the following minimum requirements to be loaded with the CPro-2000 software and used as a CIT:

- 386SX *IBM* compatible computer (desktop or lap top) (25 MHz or greater clock)
- n Disk drive — one 1.44 megabytes (3.5 inches)
- n Hard disk with approximately 5 megabytes of free space
- n 8 megabyte RAM^{*}
- n *MS-DOS* computer program, Version 5.0 or later
- n *Windows* computer program, Version 3.1
- n Serial port (EIA-232-D) — configured as COM1 or COM2
- n Mouse
- n VGA color monitor.

The CPro-2000 Graphical User Interface and Provisioning Tool has been tested with *NCR[†]*, *NEC[‡]*, and *Gateway-2000[§]* personal computers. The CPro-2000 Graphical User Interface and Provisioning Tool has also been tested with a *SUN[¶] SPARC^{**}* station 10 workstation that has a SUNPC3.1 software environment set up on a PC emulator hardware and running windows in the enhanced mode. For more information about the CPro-2000 Graphical User Interface and Provisioning Tool, refer to 190-523-101, *Guide to Using CPro-2000*.

* No memory resident programs running in the background.

† Trademark of NCR Corporation.

‡ Registered trademark of Nippon Denki Kabushiki Kaisha.

§ Registered trademark of Gateway 2000, Inc.

¶ Registered trademark of SUN Microsystems Inc.

** Registered trademark of SPARC International, Inc., licensed exclusively to SUN Microsystems Inc.

ASCII-Based Terminal

In Release 5 and later releases, an ASCII-based terminal may be used to interface with the FT-2000 OC-48 Add/Drop-Rings Terminal. Users communicate with the FT-2000 OC-48 Add/Drop-Rings Terminal using an ASCII-based terminal and TL1 messages. The same TL1 message set is used by the message-based operations system interface (X.25/TL1). The TL1 messages must be entered manually by an experienced user.

In Release 5 and later releases, the TL1 message set consists of maintenance commands (excludes autonomous messages) and cannot be used to install software in an FT-2000 OC-48 Add/Drop-Rings Terminal. On-line help messages and menus are not supported. For detailed information about TL1 messages, refer to 824-102-151, *FT-2000 OC-48 Lightwave System Operations Systems Engineering Guide*.

An ASCII-based terminal must meet the following minimum requirements to be used as a CIT:

- n ASCII-based data communications terminal with an 80-column display
- n Scroll capability
- n Local echo
- n Serial port (EIA-232-D)
- n Data transmission speed should be from 1200 to 9600 baud
- n XON/XOFF flow control capability.

The ASCII-based terminal should be configured for byte serial data transmission of 8-bit bytes with one start bit and one stop bit. Parity should be "none" and the flow control should be "DC1/DC3." With flow control, entering **<Ct l-S>** stops the output (X/off state), and **<Ct l-Q><CR>**, **<Ct l-Q><Ct l-J>**, or **<Ct l-Q><Ct l-M>** resumes the output (X/on state).

Local Access Through the CIT (DCE) Port

The FT-2000 OC-48 Lightwave System supports CIT local access through the CIT (DCE) port on the user panel of the Low Speed Shelf - System Controller and the Repeater Shelf - System Controller. The CIT (DCE) port is configured as data circuit equipment (DCE) for direct CIT access. This port is also compatible with the ASCII EIA-232-D standard.

The CIT (DCE) port allows a PC running FT-2000 CIT-PC software to communicate with the FT-2000 OC-48 Lightwave System using the F-interface protocol. A PC running FT-2000 CIT-PC software automatically selects the highest usable baud rate (up to 9600 baud).

In Release 5 and later releases, the CIT (DCE) port can be provisioned using the **SECURITY-Set-Security-Port** input or **ENT-CID-SECU** TL1 command to allow a PC running CPro-2000 software or an ASCII-based terminal to access the FT-2000 OC-48 Lightwave System. A PC running CPro-2000 software or an ASCII-based terminal communicates with the FT-2000 OC-48 Lightwave System using the same TL1 message set (excluding autonomous messages) as the message-based operations system interface (X.25/TL1). For more information about provisioning and the **SECURITY-Set-Security-Port** input, refer to Volume I, Section 8, "Administration and Provisioning."

For more information about local access using the CIT (DCE), refer to the "Operation and Maintenance (TOP)" section (Volume II).

Dial-Up Access Using a Modem Through the CIT (DTE) Port

The FT-2000 OC-48 Lightwave System supports CIT dial-up access using a modem through the CIT (DTE) port on the interconnection panel of the Low Speed Shelf - System Controller and the Repeater Shelf - System Controller. The CIT (DTE) port is configured as data terminating equipment (DTE) to allow a permanent connection to a modem. The CIT (DTE) port also allows local access using a null modem adapter. This port is also compatible with the ASCII EIA-232-D standard.

The CIT (DTE) port allows a PC running FT-2000 CIT-PC software to communicate with the FT-2000 OC-48 Lightwave System using the F-interface protocol. A PC running FT-2000 CIT-PC software automatically selects the highest usable baud rate (up to 9600 baud).

In Release 5 and later releases, the CIT (DTE) port can be provisioned using the **SECURITY-Set-Security-Port** input or **ENT-CID-SECU** TL1 command to allow a PC running CPro-2000 software or an ASCII-based terminal to access the FT-2000 OC-48 Lightwave System. A PC running CPro-2000 software or an ASCII-based terminal communicates with the FT-2000 OC-48 Lightwave System using the same TL1 message set (excluding autonomous messages) as the

message-based operations system interface (X.25/TL1). For more information about provisioning and the **SECURITY-Set-Security-Port** input, refer to Volume I, Section 8, "Administration and Provisioning."

A pair of compatible modems allows remote dial-up access to the FT-2000 OC-48 Lightwave System from a CIT over the public switched telephone network. One modem is connected to the CIT (DTE) port at the FT-2000 OC-48 Lightwave System, and the other modem is connected to the serial port of the CIT. An internal modem in the CIT may be used instead of a stand-alone modem.

A digital data network may be used in place of the modem pair to provide remote dial-up access to the FT-2000 OC-48 Lightwave System from the CIT.

A compatible modem or digital data network must meet the following minimum requirements:

- n Full duplex
- n Asynchronous
- n Byte serial data transmission of eight bit bytes
- n One start bit
- n One stop bit
- n Data transmission speed should be from 1200 to 9600 baud.

The modem must comply with one of the following transmission standards depending on the desired baud rate. These standards apply to signaling used between modems.

Standard	Baud Rate(s)
Bell 212A	1200
V.22	1200
V.22 bis	2400
V.32	4800, 9600
V.32 bis	9600 (fax), 14400 (data)

For more information about establishing dial-up access using a modem and local access using a null modem adapter, refer to the "Operation and Maintenance (TOP)" section (Volume II).

Remote Access Using the OC-48 Data Communications Channel (DCC)

The FT-2000 OC-48 Lightwave System supports CIT remote access from the local terminal to a remote terminal using the OC-48 data communications channel (DCC). The FT-2000 OC-48 Lightwave System supports one incoming remote login session and one outgoing login session at a time. For example, a local user can gain remote access to a remote terminal at the same time as a remote user can gain remote access to the local terminal.

For more information about establishing remote access, refer to the "Operation and Maintenance (TOP)" section (Volume II).

Remote Access Using the OC-48 Section Orderwire

In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal supports CIT remote access using the OC-48 section orderwire. This allows the following:

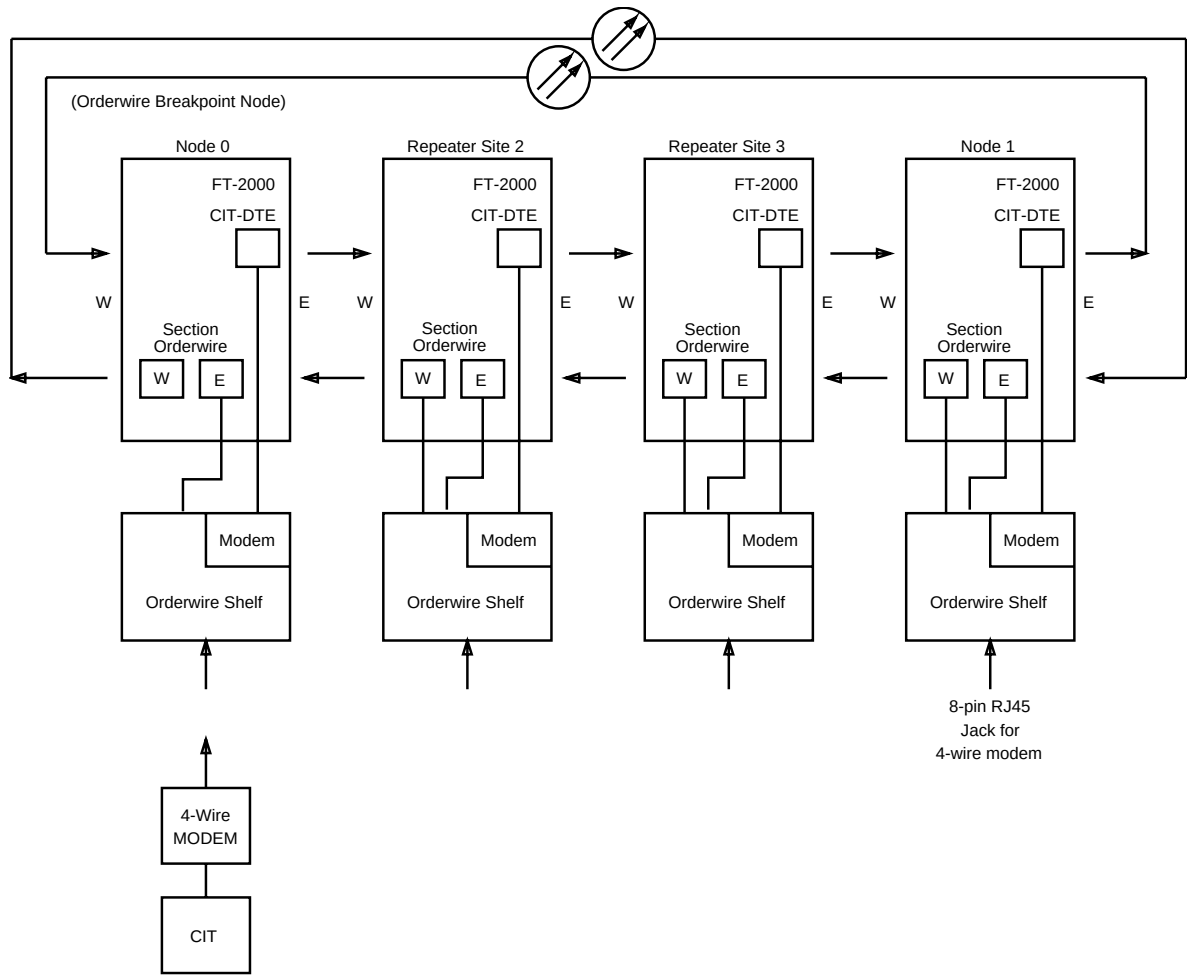
- n Remote access from a terminal to a repeater site
- n Remote access from a repeater site to a terminal
- n Remote access from a repeater site to another repeater site.

Figure 6-1 shows a 2-fiber OC-48 bidirectional, line-switched ring with two FT-2000 OC-48 Add/Drop-Rings Terminals and two FT-2000 OC-48 Repeater Bays. The FT-2000 OC-48 Add/Drop-Rings Terminals and FT-2000 OC-48 Repeater Bays must be equipped with the new *Dantel* orderwire assembly (A18-05547-01). At each node, the orderwire assembly has connections to the section orderwire [SECTION OW (LOC OW)] and the CIT (DTE) interfaces. The orderwire assembly cannot be used for voice communications and CIT remote access simultaneously.

The FT-2000 OC-48 Repeater Bays in the ring must be equipped with the Orderwire (OW) circuit pack to access the OC-48 section orderwire.

One node in the ring must be designated as the break point node (Node 0 in Figure 6-1) to avoid oscillating audio in the system. At the break point node, the orderwire assembly should only be connected to one section orderwire interface (east or west). The break point node should be in the central office where maintenance activity is most likely to be initiated.

A CIT and modem are required to establish a remote login. The CIT must be a PC equipped with FT-2000 CIT-PC software. For more information about establishing remote access, refer to the "Operation and Maintenance (TOP)" section (Volume II).



tpa 849475/01

Figure 6-1. Remote Access Using the OC-48 Section Orderwire

Remote Access Using the OC-3/OC-12 Data Communications Channel (DCC)

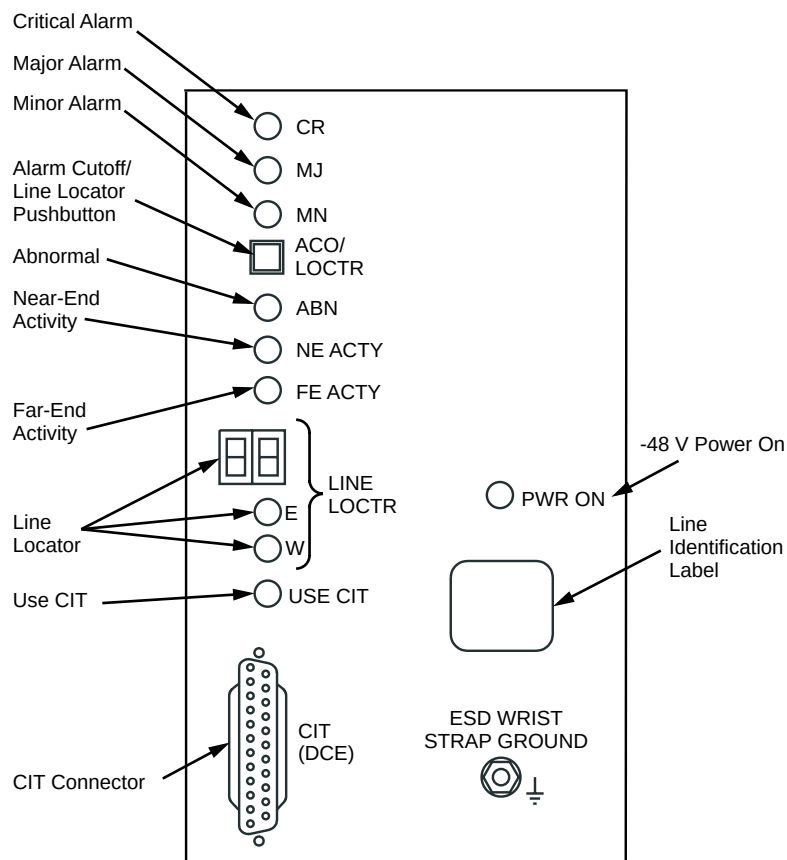
In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal supports CIT remote access using the OC-3 data communications channel (DCC). In Release 7.1.0 the FT-2000 OC-48 ADR Terminal supports CIT remote access (via the OC-12 DCC Channel) for 0x1 applications. This allows the local FT-2000 OC-48 Add/Drop-Rings Terminal to access a remote FT-2000 OC-48 Add/Drop-Rings Terminal or a remote DDM-2000 Multiplexer. A user logged in locally to a DDM-2000 Multiplexer can also log in remotely to any other DDM-2000 Multiplexer within the same subnetwork via the OC-3/OC-12 and OC-48 DCC. However, a DDM-2000 Multiplexer cannot access an FT-2000 OC-48 Add/Drop-Rings Terminal. The FT-2000 OC-48 Add/Drop-Rings Terminal supports one incoming remote login session (from another FT-2000 OC-48 Add/Drop-Rings Terminal) and one outgoing login session (to a DDM-2000 Multiplexer or another FT-2000 OC-48 Add/Drop-Rings Terminal) at a time. For example, a local user can gain remote access to a remote FT-2000 OC-48 Add/Drop-Rings Terminal or DDM-2000 Multiplexer at the same time as a remote user can gain remote access to the local terminal.

The CIT must be a PC equipped with FT-2000 CIT-PC software to establish a remote login to a DDM-2000 Multiplexer. For more information about establishing remote access, refer to the "Operation and Maintenance (TOP)" section (Volume II).

The FT-2000 OC-48 Add/Drop-Rings Terminal must be equipped with the OC3/OC-12 Optical Interface - 1.3 μ m Standard Performance [OC3/OC-12 (1.3 STD)] circuit pack and the Overhead Controller - Tributary (TOHCTL) circuit pack to access the OC-3/OC-12 DCC. A single TOHCTL circuit pack can access up to eight OC-3 DCCs or two OC-12 DCCs. Low speed interface slots must be assigned to an OC-3/OC-12 DCC channel. For more information about entering OC-3/OC-12 DCC channel assignments, refer to Section 8, "Administration and Provisioning."

User Panel

The user panel is a factory-installed unit mounted next to the right-hand flange on the Low Speed Shelf - System Controller, the Repeater Shelf - System Controller, and the FT-2000 OC-48 Repeater Shelf. Figure 6-2 and Figure 6-3 shows the user panel on the Low Speed Shelf - System Controller and the Repeater Shelf - System Controller. Figure 6-4 shows the user panel on the FT-2000 OC-48 Repeater Shelf. The user panel provides -48 V power filters, an electrostatic discharge jack, and local system-level information.



nc-ft2000-041

Figure 6-2. User Panel (Low Speed Shelf - System Controller and Repeater Shelf - System Controller) — Cabled Version (Before 06/97)

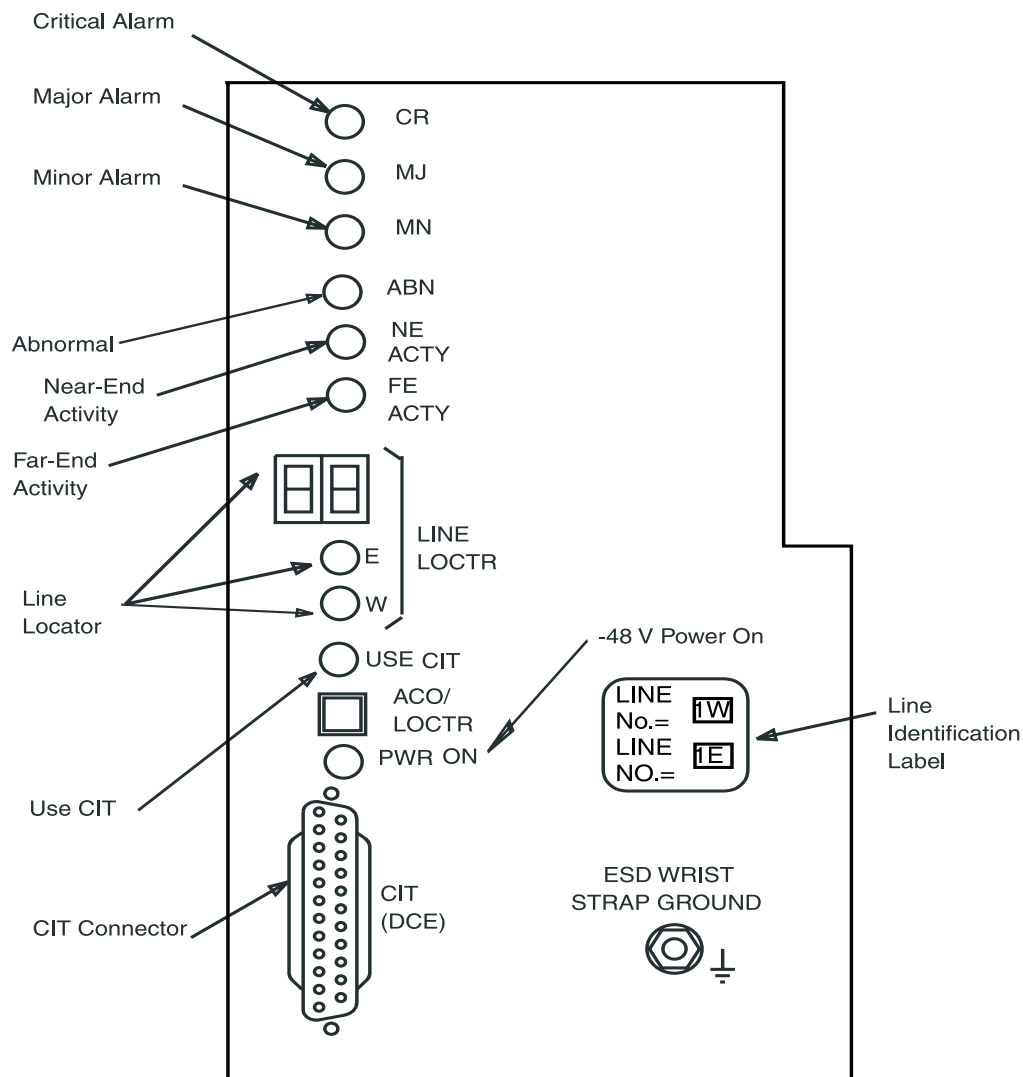


Figure 6-3. User Panel (Low Speed Shelf - System Controller and Repeater Shelf - System Controller) — Plug-In-Type (After 06/97)

The user panel LEDs show the following local system information:

- n The active alarm level is shown by red LEDs for critical (CR) and major (MJ) alarms and by a yellow LED for minor (MN) alarms.

- n A green alarm cutoff/locator (ACO/LOCTR) pushbutton with a built-in LED activates the alarm cutoff function and lights the LED. The ACO/LOCTR pushbutton also controls the line locator (LINE LOCTR) displays and LEDs, showing the shelf where a fault has occurred.
- n The yellow abnormal (ABN) LED is lighted when a temporary condition potentially affecting transmission exists; for example, a CIT-initiated protection switch or lockout, or loopback.
- n The yellow near-end activity (NE ACTY) LED is lighted when any alarm or status condition exists at the local terminal or local repeater site.
- n The yellow far-end activity (FE ACTY) LED is lighted when any alarm or status condition exists at a remote terminal or remote repeater site. In Release 6 and later releases, the FE ACTY LED is also lighted when any alarm or status condition exists at FT-2000 OC-48 Add/Drop-Rings Terminals and DDM-2000 Multiplexers in the same alarm group within a subnetwork. (The remote activity reporting parameter must be enabled, using the **SECURITY-Set-Far End Communications** CIT-PC command or in Release 7.2 and later the **ENT-CID-SECU** TL1 command.)
- n The LINE LOCTR consists of two 7-segment displays and two individual LEDs. (The original version, available in Release 1 only, had one 7-segment display and lacked the individual LEDs.)

The two 7-segment displays normally show the line number when a circuit pack fails. When the ACO/LOCTR pushbutton is pressed in and held, the shelf associated with the line containing the failed circuit pack is displayed.

The yellow east (E) and west (W) LEDs complete the number of the line containing the failed circuit pack. For example, if the failed circuit pack is associated with high speed line 1E, the right-hand 7-segment display will show the number "1" and the E LED will be lighted.
- n The yellow USE CIT LED is lighted when the CIT must be used to obtain more detailed information about system status. The USE CIT LED and the LINE LOCTR display are never lighted at the same time (except during an LED test).
- n The green power on (PWR ON) LED is lighted when the shelf is receiving –48 V power.

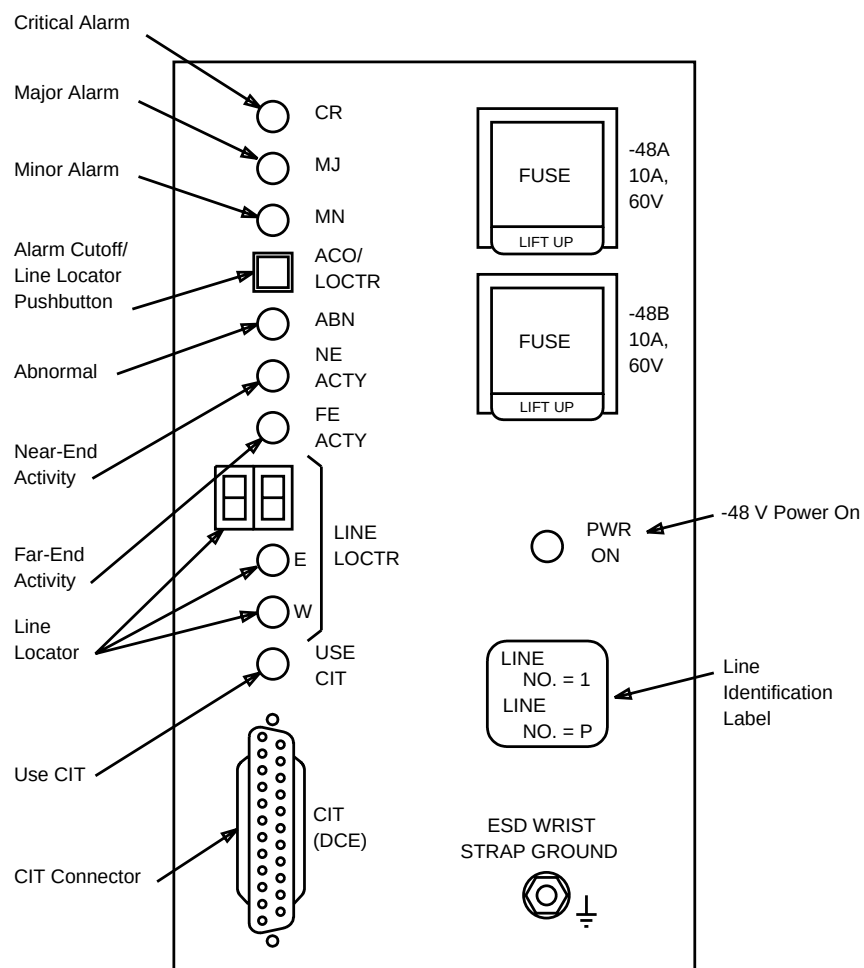


Figure 6-4. User Panel (FT-2000 OC-48 Repeater Shelf)

Filter Panel

The filter panel (Figure 6-5 and Figure 6-6) is a factory-installed unit mounted next to the right-hand flange on the Low Speed Shelf - Complementary, High Speed Shelf, Enhanced High Speed Shelf, and the Condensed High Speed Shelf. The filter panel has -48 V power filters, an electrostatic discharge jack, and a green PWR ON LED. The PWR ON LED shows that the shelf is receiving -48 V power.

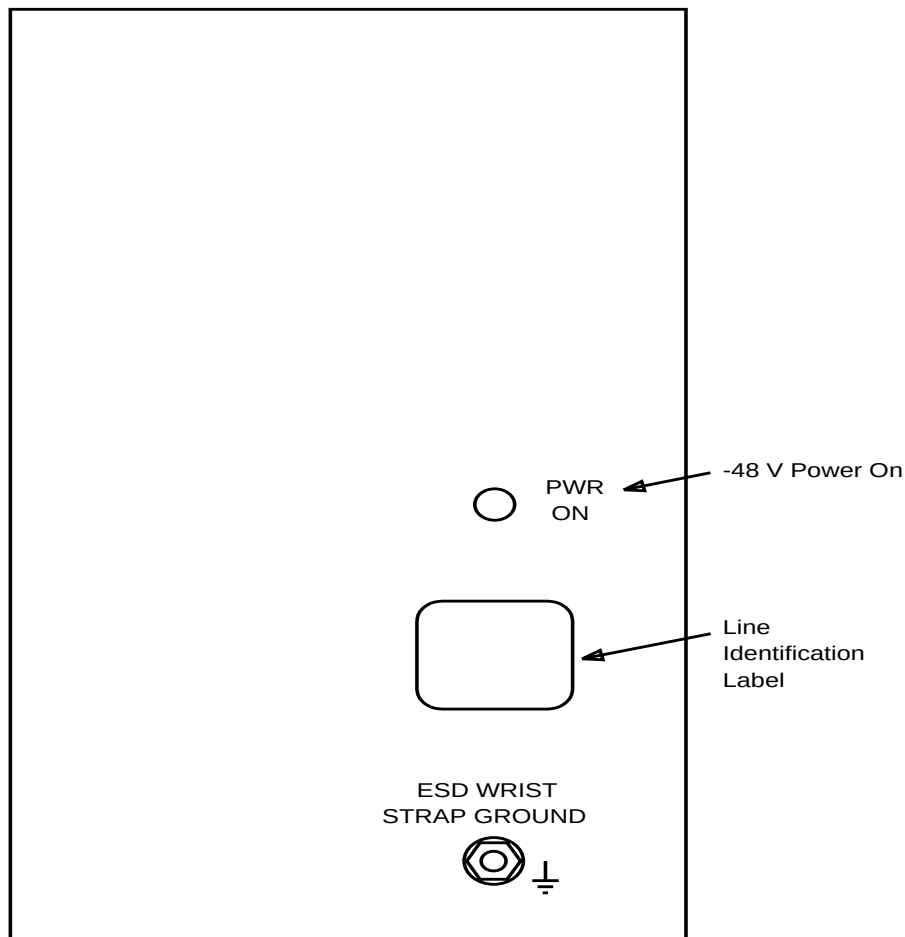


Figure 6-5. Filter Panel for the Enhanced High Speed Shelf

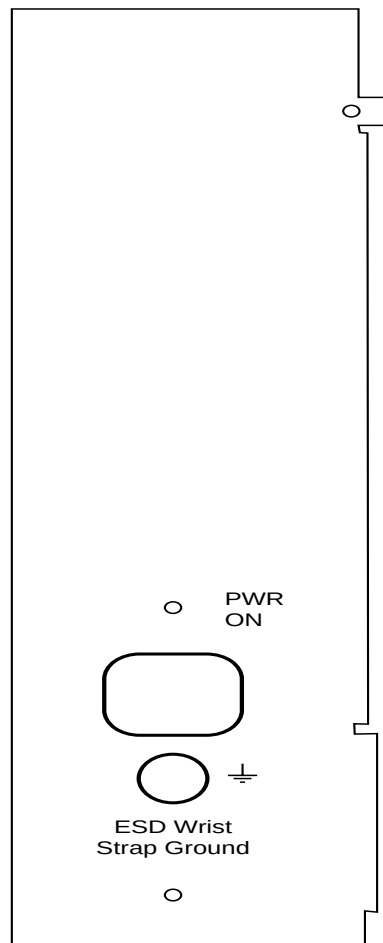


Figure 6-6. Power Indicating Panel for the Condensed High Speed Shelf

To supplement the user panel's system-level view, each circuit pack has a red FAULT LED on its faceplate. A continuously lighted FAULT LED means that the FT-2000 OC-48 Lightwave System has isolated a failure to this circuit pack. A flashing FAULT LED shows the following:

- A flashing FAULT LED on a transmission circuit pack shows that an incoming signal to that circuit pack has failed.
- A flashing FAULT LED on a synchronization circuit pack shows that an external timing reference input signal has failed.

- n A flashing FAULT LED on the System Memory circuit pack shows that the contents of the nonvolatile memory differ from the duplicate copy in the System Controller circuit pack. It can also indicate corrupted data in the nonvolatile memory of the System Memory circuit pack.

Office Alarms Interface

The FT-2000 OC-48 Lightwave System provides relays for connection to the local office audible and visible alarms. Relays are provided for each alarm condition: CR (critical), MJ (major), and MN (minor). The steady state current for office alarm connections must not exceed 0.9 A at 60 V or 1.8 A at 30 V. The maximum transient currents (20 msec duration) during initial contact closure must not exceed 9 A at 60 V or 18 A at 30 V.



CAUTION:

The office alarms interface of the FT-2000 OC-48 Repeater Bay is optional. If the office alarms interface of the FT-2000 OC-48 Repeater Bay is used, there is a certain class of single failures that causes the office alarms interface to be activated in TWO offices. If this occurs and technicians at each office react to the alarms, service could be dropped.

The audible office alarms are silenced through activation of the alarm cutoff (ACO) function. Visible alarms are not extinguished by the ACO function.

To prevent intermittent failures from causing unnecessary maintenance activity, a provisionable incoming signal alarm delay is provided. The office alarms will not be activated unless an incoming signal condition of greater duration than the alarm delay occurs. When a failure clears, an alarm clear delay prevents premature clearing of the alarm.

To accommodate Bell Operating Company (BOC) practices, the MJ and CR relay closures are designed to allow these office alarms to be OR'ed together and reported as an office MJ alarm.

As with the user panel LEDs, when multiple alarm conditions occur, the highest level office alarm (audible and visible) is activated. When the highest level alarm condition clears, the office alarm "bumps down" to the next highest level alarm condition.

If the ACO function has been activated to silence the active audible alarm and a "bump down" occurs, the audible alarm remains silent (that is, the lower level visible alarm is activated, but the corresponding audible alarm is not reactivated). If another alarmable condition occurs while the ACO is active, the highest level audible alarm is activated even if the new condition is a lower level. (For example, if an MJ alarm was active and silenced using the ACO function and an MN alarmable condition occurs, the MJ audible alarm will sound.)

The office alarms (OFFICE ALMS) interface is located on the interconnection panel of the Low Speed Shelf - System Controller. Refer to Volume I, Section 3, "Platform Descriptions," for more information about the physical location of the office alarm interface.

Parallel Telemetry Interface

The parallel telemetry interface at an FT-2000 OC-48 Add/Drop-Rings Terminal brings a minimum set of alarm information to an operations center. (The FT-2000 OC-48 Repeater Bay does not support a parallel telemetry interface.) Six alarm closures show critical, major, and minor alarms for the local and remote terminals. The appropriate contact is activated when an alarm condition is detected. In Release 6 and later releases, the alarm closures also show critical, major, and minor alarms for remote FT-2000 OC-48 Add/Drop-Rings Terminals and DDM-2000 Multiplexers in the same alarm group within a subnetwork.

The parallel telemetry interface is designed for a maximum open circuit voltage of 60 V and a maximum current of 35 mA.

As with the user panel LEDs and office alarms, when multiple alarms are active, the highest level parallel telemetry closure is activated. When the highest level alarm clears, the parallel telemetry output "bumps down" to the next highest level active alarm.

Since the parallel telemetry alarm closures reflect the office alarms, the alarm delay and alarm clear parameters set for office alarms also apply to parallel telemetry alarm closures.

The parallel telemetry (PAR TLM) interface is located on the interconnection panel of the Low Speed Shelf - System Controller. Refer to Volume I, Section 3, "Platform Descriptions," for more information about the physical location of the parallel telemetry interface.

Refer to 824-102-148, *FT-2000 OC-48 Lightwave System, Operations Systems Engineering Guide*, for detailed information about the parallel telemetry interface.

User-Settable Miscellaneous Discrete Interface

The user-settable miscellaneous discrete interface allows an operations system to monitor and control equipment collocated with the FT-2000 OC-48 Lightwave System through a set of input and output contact closures. Sixteen environmental inputs are provided to monitor environmental conditions (for example, open door, high temperature, etc.). The FT-2000 OC-48 Add/Drop-Rings Terminal also provides four control outputs to control external equipment (for example, pumps, generators, etc.).

Any external customer equipment to be monitored by the FT-2000 OC-48 Lightwave System must provide the electrical equivalent of a contact closure across the corresponding environmental input wiring pairs. The contact closure must be capable of passing at least 10 mA of drive current.

The power source to enable the FT-2000 OC-48 Lightwave System to control external customer equipment may be from a minimum of 3 V to a maximum of 72 V. The FT-2000 OC-48 Lightwave System provides a unidirectional opto-isolator connection across each corresponding control output wiring pair. The load current across each control output wiring pair must be limited by the external customer equipment and must not exceed 35 mA. For detailed wiring information, refer to 365-575-104, *FT-2000 OC-48 Lightwave System, Releases 1 and 2, Installation Manual*; 365-575-115, *FT-2000 OC-48 Lightwave System, Releases 3, 4, 5, 6, and 7 Installation Manual*; and SD-5G250-02, *FT-2000 OC-48 Enhanced Lightwave Terminating Bay Application Schematic* (or SD-5G250-01, *FT-2000 OC-48 Lightwave Terminating Bay Application Schematic*).

The environmental inputs are addressed by the numbers 1 through 16, and the control outputs are addressed by the numbers 1 through 4. The environmental inputs can be assigned in any order; however, if the bay is equipped with a fan assembly, the number 16 input is reserved for the fan alarm.

In Release 5 and later releases, the number 1, 2, 3, and 4 inputs are reserved for alarms from an FT-Lightwave Booster Amplifier. Table 6-1 shows the guidelines for mapping the miscellaneous discrete points from the FT-LBA to environmental inputs of the FT-2000 OC-48 Add/Drop-Rings Terminal.

Table 6-1. FT-LBA Miscellaneous Discrete Mapping

FT-LBA Miscellaneous Discrete	FT-2000 Environmental Input	Suggested Text
Fuse/Power Alarm	1	FT-LBA fuse/power failure
Fan Alarm	2	FT-LBA fan failure
1EW (Service) Failure	3	FT-LBA 1EW failure
1WE (Service) Failure	4	FT-LBA 1WE failure

Refer to Volume I, Section 8, "Administration and Provisioning," for information about provisioning a string of characters associated with an environmental input or control output.

Operations system access to individual environmental inputs and control outputs at an FT-2000 OC-48 Add/Drop-Rings Terminal is provided through the serial telemetry interface using the telemetry byte-oriented serial (TBOS) protocol. Access to environmental inputs is also provided through the X.25 interface.

Operations system access to the environmental inputs at an FT-2000 OC-48 Repeater Bay is provided through the serial telemetry and X.25 interfaces of an FT-2000 OC-48 Add/Drop-Rings Terminal. The serial telemetry interface has the capability to report that an environmental input at a repeater site is active. It does not have the capability to report the status of individual environmental inputs.

Local access to the environmental inputs is provided through the CIT interface using the **FAULT-Retrieve-Alarm (and Status)** input. For more information, refer to the **FAULT-Retrieve-Alarm (and Status)** input in Volume I, Section 11, "Craft Interface Terminal Usage." Local access to the control outputs is not provided.

The miscellaneous discrete (MISC DSCRT 1 and MISC DSCRT 2) interfaces are located on the interconnection panel of the Low Speed Shelf - System Controller and the Repeater Shelf - System Controller. The MISC DSCRT 1 and MISC DSCRT 2 interfaces each provide eight environmental inputs and two control outputs. Refer to Volume I, Section 3, "Platform Descriptions," for more information about the physical location of the miscellaneous discrete interface.

Serial Telemetry Interface

The FT-2000 OC-48 Lightwave System supports serial telemetry using the TBOS protocol. The serial telemetry interface transfers alarm, status, and control information to and from a remote maintenance center via a 2400-baud EIA-422 port. The serial telemetry link number 1 (SER TLM 1) interface on the interconnection panel of the Low Speed Shelf - System Controller supports maintenance at an FT-2000 OC-48 Add/Drop-Rings Terminal. The serial telemetry link number 2 (SER TLM 2) interface on the interconnection panel of the Low Speed Shelf - System Controller supports maintenance at an FT-2000 OC-48 Repeater Bay. Refer to Volume I, Section 3, "Platform Descriptions," for more information about the physical location of the serial telemetry interface.

For detailed information about display configurations and TBOS tables, refer to 824-102-148, *FT-2000 OC-48 Lightwave System Operations Systems Engineering Guide*.

Message-Based Operations System Interface

The FT-2000 OC-48 Lightwave System provides a message-based operations system interface to Telcordia's Network Monitoring and Analysis (*NMA*^{*}) and Lucent's Transvu II and SNC-2000 EMS operations systems. This interface uses an X.25 protocol and the transaction language 1 (TL1) message language. Different virtual circuits on the X.25/TL1 interface may be used to interface to different operations systems.

The FT-2000 OC-48 Add/Drop-Rings Terminal supports two permanent virtual circuits (PVCs) and up to six switched virtual circuits (SVCs). The OS type and the associated TL1 autonomous messages are provisionable on a virtual circuit basis using the **SECURITY-Set-Security-Port** CIT-PC command or in Release 7.2 and later, the **ENT-CID-SECU** TL1 command. The OS type is a filter that determines the types of TL1 autonomous messages that are transmitted on a virtual circuit to the OS. The calling address is also provisionable for each switched virtual circuit using the **SECURITY-Set-Security-Port** CIT-PC command or in Release 7.2 and later, the **ENT-CID-SECU** TL1 command. The calling address is used to determine the X.25 logical channel number for switched virtual circuits. There is a fixed association between permanent virtual circuits and X.25 logical channel numbers. The interface operates over a synchronous line at 1200 to 19,200 baud.

In Release 5 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal provides a security function for the X.25/TL1 interface to protect against unauthorized access to the system functions (for example, provisioning). Logins, passwords, functional capabilities, authorization levels, and access to the system capabilities are provided. For more information about security, refer to Volume I, Section 8, "Administration and Provisioning."

The FT-2000 OC-48 Lightwave System supports an X.25/TL1 interface to report alarm and status conditions and performance monitoring data to Telcordia's *NMA* and Lucent's Transvu II and SNC-2000 EMS operations systems. The X.25/TL1 interface provides significantly more detailed information, such as identifying specific circuit packs and facilities, than the parallel telemetry or serial telemetry (TBOS) interfaces. The TL1 messages are based on Telcordia's TR-TSY-000833, Issue 3, Rev. 1.[†]

In Release 4.1, the FT-2000 OC-48 Add/Drop-Rings Terminal may operate as a gateway network element (GNE) that serves as a single interface to the local X.25 message-based operations system for all the FT-2000 OC-48 Add/Drop-Rings Terminals in a ring network. One or more FT-2000 OC-48 Add/Drop-Rings

* NMA is a trademark of Telcordia Technologies, Inc.

† Where TR-TSY-000833, Issue 3, Rev. 1 was deficient, the FT-2000 OC-48 Lightwave System conforms to Telcordia's TR-NWT-000833, Issue 4.

Terminals in a ring network may be provisioned as the GNE. For more information about provisioning, refer to Volume I, Section 8, "Administration and Provisioning." In Release 5 and later releases, the GNE function is no longer CIT provisionable. Any FT-2000 OC-48 Add/Drop-Rings Terminal in a ring network will automatically operate as a GNE if it receives TL1 messages through the local X.25 interface that are for another node in the ring network.

In Release 6 and later releases, an FT-2000 OC-48 Add/Drop-Rings Terminal can also operate as a TL1 gateway network element for other FT-2000 OC-48 Add/Drop-Rings Terminals and DDM-2000 Multiplexers present in the subnetwork. When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 or Release 9 of the DDM-2000 OC-3 Multiplexer, the recommended maximum subnetwork size is 24 network elements. When Release 7 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 or Release 9 of the DDM-2000 OC-3 Multiplexer, the recommended maximum subnetwork size is 32 network elements. When Release 7.1.0 of the FT-2000 OC-48 ADR Terminal interworks with Release 9.1 of the DDM-2000 OC-3 and/or Release 5.1 of the DDM-2000 OC-12, the recommended maximum subnetwork size is 32 network elements.



CAUTION:

*In subnetworks with more than 16 NEs, the NE serving as the TL1 gateway NE CANNOT serve as the AGNE, nor can it serve as the DS-NE. **The AGNE, TL1 gateway NE, as well as the DS-NE functions must be distributed (i.e., at three separate NEs in the subnetwork).***

The following autonomous messages are supported:

REPORT ALARM (REPT ALM) — autonomous reports of alarm condition

REPORT ALARM ENVIRONMENT (REPT ALM ENV) —
autonomous reports of environmental alarm conditions

REPORT CONDITIONS (REPT COND)* — autonomous reports of active
status (nonalarmed) conditions

REPORT DATABASE CHANGES (REPT DBCHG)* — autonomous reports of
internal cross-connection changes and line (OC-48, OC-12, OC-3, EC-1,
and DS3) termination parameters

REPORT EVENT (REPT EVT) — autonomous reports of status conditions,
including performance-monitoring threshold-crossing alerts (TCAs)

REPORT SWITCH (REPT SW)[†] — autonomous reports of electrical low
speed interface circuit pack (DS3 and STS1E), or TG3 (DS1) circuit pack
protection switches.

* Available in Releases 5 and later.

† Available in Releases 4.1 through 7.1.

The following command/response messages are supported:

ACTIVATE-USER (**ACT-USER**)* — activates a TL1 access login session

ALLOW-MESSAGE (**ALW-MSG**)* — allows transmission of autonomous messages

CANCEL-USER (**CANC-USER**)* — terminates a TL1 login session

COPY-PROGRAM (**CPY-PRGM**)§§ — copies software from a network element to another network element

DELETE-ASSIGNMENT-DCC (**DLT-ASGNMT-DCC**)§§ — deletes an OC-3 or OC-12 DCC assignment

DELETE-CROSS-CONNECTION (**DLT-CRS-rr**)[†] — deletes an STS-3 or STS-1 (Releases 7 and later) cross-connection

DELETE-USER-SECURITY (**DLT-USER-SECU**)* — deletes the security parameters associated with a user

EDIT-DATE (**ED-DAT**)* — changes the system date and time

EDIT-PASSWORD ID (**ED-PID**)* — changes the user password

EDIT-USER-SECURITY (**ED-USER-SECU**)* — changes the login ID, password, and/or user privileges of any non-privileged user

ENTER-ASSIGNMENT-DCC (**ENT-ASGNMT-DCC**)§§ — enters an OC-3 or OC-12 DCC assignment

ENTER-CHANNEL IDENTIFIER-SECURITY (**ENT-CID-SECU**)§ —Release 7.2 and later sets system security for the CIT-PC, DCC and X.25 ports

ENTER-CROSS-CONNECTION (**ENT-CRS-rr**)[†] — establishes an STS-3 or STS-1 (Release 7 and later) cross-connection

ENTER-FAR-END-COMMUNICATION (**ENT-FECOM**)§§ — enables/disables remote access capabilities over the DCC and alarms associated with DCC failures.

ENTER-NETWORK-ELEMENT-SECURITY (**ENT-NE-SECU**)§§ —establishes the global security parameters of the system (e.g., password aging, login aging, etc).

ENTER-PROTECTION-LINE (**ENT-PRONT-LINE**)[‡] — sets the "wait-to-restore" line protection switching characteristic for the system.

ENTER-PROTECTION-LOW-SPEED (**ENT-PROTN-LS**)^{††} — sets the "wait-to-restore" low speed protection switching characteristics for the system.

ENTER-PROTECTION-STS3 (**ENT-PROT-rr**)§§ — enters protection for an STS-3 (NPPA).

ENTER-ROLL (**ENT-ROLL-rr**)† — performs a 1-way roll of an STS-3 or STS-1 (Release 7 and later) cross-connection

ENTER-SYNCHRONIZATION (**ENT-SYNCN**)§§ — provisions the synchronization mode.

ENTER-SYSTEM (**ENT-SYS**)† — sets network element name and other system level parameters

ENTER-STIS1 (**ENT-STIS1**)‡ — sets user-settable strings to the transmit and receive path trace fields

ENTER-USER-SECURITY (**ENT-USER-SECU**)* — enters the security parameters associated with a user

ENTER-rr (**ENT-rr**)† — provisions various parameters of low speed ports and slots

INHIBIT-MESSAGE (**INH-MSG**)* — inhibits transmission of all autonomous TL1 messages, office alarms, and/or parallel telemetry from a network element

INITIALIZE-REGISTER (**INIT-REG**)* — initializes all current day and/or current 15-minute performance-monitoring storage registers

INITIALIZE-SYSTEM (**INIT-SYS**)* — initializes (resets) provisionable parameters to their original value and restarts the network element software

OPERATE-ALARM CUTOFF (**OPR-ACO**)* — operates alarm cutoff to silence the audible office alarms

OPERATE-EXTERNAL CONTROL (**OPR-EXT-CONT**)§ — operates an external miscellaneous discrete control output

OPERATE-LOOPBACK (**OPR-LPBK-rr**)† — operates low speed (DS3, EC-1, OC-3, and OC-12) or high speed (OC-48) loopback

OPERATE-PROTECTION SWITCH-OC3 (**OPR-PROTNSW-OC3**)* — operates OC-3 line protection switching

OPERATE-PROTECTION SWITCH-OC12 (**OPR-PROTNSW-OC12**)§§ — operates OC-12 line protection switching

OPERATE-PROTECTION SWITCH-OC48 (**OPR-PROTNSW-OC48**)† — operates OC-48 line protection switching

OPERATE-PROTECTION SWITCH (**OPR-PROTNSW-rr**)† — operates STS-3 tributary protection switching

OPERATE-SYNCHRONIZATION SWITCH (**OPR-SYNCNSW**)† — operates synchronization reference protection switching

RELEASE-EXTERNAL CONTROL (**RLS-EXT-CONT**)§ — releases an external miscellaneous discrete control output

RELEASE-LOOPBACK (**RLS-LPBK-rr**)† — releases low speed (DS3, EC-1, and OC-3) or high speed (OC-3) loopback

RELEASE-PROTECTION SWITCH-OC3 (**RLS-PROTNSW-OC3**)* — releases OC-3 line protection switching

RELEASE-PROTECTION SWITCH-OC12 (**RLS-PROTNSW-OC12**)§§ — releases OC-12 line protection switching.

RELEASE-PROTECTION SWITCH-OC48 (**RLS-PROTNSW-OC48**)† — releases OC-48 line protection switching

RELEASE-PROTECTION SWITCH (**RLS-PROTNSW-rr**)† — releases STS-3 tributary protection switching

RELEASE-SYNCHRONIZATION SWITCH (**RLS-SYNCNSW**)† — releases synchronization reference protection switching

RETRIEVE-AUTONOMOUS OUTPUT (**RTRV-AO**)* — retrieves copies of autonomous TL1 messages

RETRIEVE-ALARM (**RTRV-ALM**) — retrieves active alarm conditions

RETRIEVE-ALARM-NETWORK (**RTRV-ALM-NTWK**)§§ — retrieves active alarm/or status conditions for all network elements in the maintenance subnetwork

RETRIEVE-ALARM-ENVIRONMENT (**RTRV-ALM-ENV**)† — retrieves active environmental alarm conditions

RETRIEVE-rr (**RTRV-rr**)† — retrieves current provisioned state of low speed ports and slots

RETRIEVE-ASSIGNMENT-DCC (**RTRV-ASGNMT-DCC**)§§ — retrieves information on the assignment of DCC channels to low-speed slots or slot pairs.

RETRIEVE-ATTRIBUTE ALARM (**RTRV-ATTR-ALM**)§ — retrieves the current values of the incoming signal alarm delay, alarm clear delay interval, and power minor alarm parameters

RETRIEVE-ATTRIBUTE CONTROL (**RTRV-ATTR-CONT**)§ — retrieves provisioned description(s) of the external control outputs

RETRIEVE-ATTRIBUTE ENVIRONMENT (**RTRV-ATTR-ENV**)§ — retrieves provisioned description(s) of the environmental inputs

RETRIEVE-ATTRIBUTE (**RTRV-ATTR-rr**)* — retrieves the notification code associated with incoming signal failure events

RETRIEVE-CIRCUIT-PATH (**RTRV-CKT-rr**)§§ — retrieves a circuit path report showing all of the cross-connections that make up the circuit.

RETRIEVE-CONDITION (**RTRV-COND**)† — retrieves all active alarm and status conditions

RETRIEVE-CROSS-CONNECTION (**RTRV-CRS-rr**)† — retrieves current cross-connections

RETRIEVE-DATE-AND-TIME (**RTRV-DAT**)†† — retrieves the network element's time zone and day light savings time information

RETRIEVE-EQUIPMENT (**RTRV-EQPT**)* — retrieves circuit pack and version information for one or more slots

RETRIEVE-FAR-END-COMMUNICATION (**RTRV-FECOM**)§§ — retrieves the provisioned state of network element's section DCC

RETRIEVE-HEADER (**RTRV-HDR**) — retrieves system ID, time, and date

RETRIEVE-LOG (**RTRV-LOG**)§§ — retrieves a history log, of events, for the Network Elements

RETRIEVE-MAP-NEIGHBOR (**RTRV-MAP-NEIGHBOR**)§§ — retrieves a list of all Network Elements with direct optical connections carrying active DCC channels to that NE

RETRIEVE-MAP-NETWORK (**RTRV-MAP-NETWORK**)‡ — retrieves a list of all network elements in the local SONET subnetwork

RETRIEVE-MAP-RING (**RTRV-MAP-RING**)* — retrieves a list of all network elements in the local ring

RETRIEVE-NETWORK-SECURITY (**RTRV-NE-SECU**)§§ — retrieves network element global security information

RETRIEVE-PATH TRACE (**RTRV-PTHTRC**)‡ — retrieves the provisioned transmit and receive path traces, path trace status, and actual receive path trace

RETRIEVE-PERFORMANCE MONITORING (**RTRV-PM**) — retrieves performance-monitoring (PM) data, including historical PM data

RETRIEVE-PERFORMANCE-MONITORING-START-TIME (**RTRV-PM-STIME**)§ — retrieves the start time for measuring when the day begins

RETRIEVE-PROTECTION-LINE (**RTRV-PROTN-LINE**)§§ — retrieves the "wait-to-restore" line protection switching characteristics

RETRIEVE-PROTECTION-LOW-SPEED (**RTRV-PROTN-LS**)§§ — retrieves the "wait-to-restore" low speed protection switching characteristics

RETRIEVE-PROTECTION-STS1 (**RTRV-PROT-rr**)§§ — retrieves protection state for an STS-1 (NPPA)

RETRIEVE-STATE (**RTRV-STATE**)§§ — retrieves the slot, port, tributary, and line protection switching state for the Network Element

RETRIEVE-SYNCHRONIZATION (**RTRV-SYNCN**)§§ — retrieves the provisioning and operational information on the synchronization attributes

RETRIEVE-SYSTEM (**RTRV-SYS**)† — retrieves network element name and other system level parameters

RETRIEVE-THRESHOLD (**RTRV-TH**)§ — retrieves the current threshold level of one or more monitored parameters

RETRIEVE-TIME-SLOT (**RTRV-TMSLT**)§§ — retrieves the Tributary Map report

RETRIEVE-USER-SECURITY (**RTRV-USER-SECU**)* — retrieves security parameters associated with a login

SET-ATTRIBUTE ALARM (**SET-ATTR-ALM**)§ — sets the current values of the incoming signal alarm delay, alarm clear delay interval, and power minor alarm parameters

SET-ATTRIBUTE CONTROL (**SET-ATTR-CONT**)§ — provisions the description(s) of the external control outputs

SET-ATTRIBUTE ENVIRONMENT (**SET-ATTR-ENV**)§ — provisions the description(s) of the environmental inputs

SET-ATTRIBUTE (**SET-ATTR-rr**)* — sets notification code associated with incoming signal failure events

SET-PERFORMANCE-MONITORING-START-TIME (**SET-PM-STIME**)§§ — sets the start time for the measurement of all 24 hour performance monitoring parameters

SET-SITE ID (**SET-SID**)* — sets the target identifier (TID) of the network element

SET-THRESHOLD (**SET-TH**)§ — sets the threshold level for monitored parameters

SWITCH-TO PROTECTION (**SW-TOPROTN**)* — performs an equipment (circuit pack) protection switch on DS3 and/or STS1E, or TG3 (DS1) circuit packs

SWITCH-TO WORKING (**SW-TOWKG**)* — resets an equipment (circuit pack) protection switch on DS3 and/or STS1E, or TG3 (DS1) circuit packs

TEST-AUTO-TURNUP (**TEST-AUTO**)§§ — tests the system and cabling

TEST-EQUIPMENT (**TEST-EQPT**)§§ — tests the audible and visual office alarms and the system LEDs

TEST-TELEMETRY (**TEST-TLM**)§§ — tests the system's parallel telemetry

UPDATE-SYSTEM (**UPD-SYS**)* — updates the state of all the automatically provisioned parameters

- * Available in Releases 5 and later.
- † Available in Releases 4.1 and later.
- ‡ Available in Releases 6 and later.
- § Available in Releases 7 and later.
- §§ Available in Releases 7.2 and later.



NOTE:

Ensure that there are no active local or remote TL1 login sessions before removing the X.25 cable from the X.25 connector at the local FT-2000 OC-48 Add/Drop-Rings Terminal. If the X.25 cable is removed from the X.25 connector, any active local or remote TL1 login sessions are not dropped. Using the CIT at the local and remote network elements, the Active User Report shows these TL1 login sessions are still active.

The X.25/TL1 interface (X.25) is located on the interconnection panel of the Low Speed Shelf - System Controller. Refer to Volume I, Section 3, "Platform Descriptions," for more information about the physical location of the X.25/TL1 interface.

Refer to 824-102-148, *FT-2000 OC-48 Lightwave System Operations Systems Engineering Guide*, for detailed information about the X.25/TL1 interface and TL1 message/command descriptions. *

Orderwire Interface

The orderwire interface provides a facility for voice communications between terminals for maintenance personnel to perform facility maintenance. In Release 6 and later releases, the orderwire interface provides a facility for voice communications and remote access between terminals and repeaters, and between repeaters. The FT-2000 OC-48 Repeater Bay must be equipped with the Orderwire (OW) circuit pack to access the section orderwire byte. The FT-2000 OC-48 Add/Drop-Rings Terminal provide four 64-kb/s orderwire interfaces for section and line orderwire. The FT-2000 OC-48 Repeater Bay provides four 64-kb/s orderwire interfaces for section orderwire. The line (express) and section (local) orderwire interfaces are V.11/EIA-422. The section orderwire interface will not provide voice communications between FT-2000 OC-48 Add/Drop-Rings Terminals when there is an FT-2000 OC-48 Repeater Bay in the intervening OC-48 high speed line that is not equipped with the Orderwire (OW) circuit pack.

The line (express) and section (local) orderwire interfaces [LINE OW (EXP OW) and SECTION OW (LOC OW)] are located on the interconnection panel of the Enhanced High Speed Shelf (or High Speed Shelf). The section (local) orderwire interfaces [SECTION OW (LOC OW)] are located on the interconnection panel of the Repeater Shelf - System Controller. Refer to Section 3, "Platform Descriptions," for more information about the physical location of the line and section orderwire interfaces.

To use the orderwire interface, the FT-2000 OC-48 Add/Drop-Rings Terminal must be equipped with two OHCTL (TERM) circuit packs. The FT-2000 OC-48 Repeater Bay must be equipped with an Orderwire (OW) circuit pack. The OHCTL

*

Available in Release 5 and later releases.

(TERM) and OW circuit packs insert/extract the orderwire bytes to/from the synchronous optical network (SONET) overhead channel.

Standard equipment can be connected to the orderwire interface. For example, a *DANTEL** orderwire shelf may be used with the FT-2000 OC-48 Lightwave System.

* Registered trademark of DANTEL, Incorporated.

Table of Contents

Overview	7-1
General	7-1
Circuit Pack and Software Compatibility	7-4
Control	7-6
■ SYSCTL (LAA23, LAA23B, and LAA23C)	
Circuit Pack Description	7-6
Purpose of Circuit	7-6
Faceplate LED	7-6
General Description of Operation	7-7
Detailed Description of Operation	7-7
Quick Reference Summary	7-10
■ SYSMEM (LAA25) Circuit Pack Description	7-11
Purpose of Circuit	7-11
Faceplate LED	7-11
General Description of Operation	7-12
Detailed Description of Operation	7-12
Quick Reference Summary	7-16
■ LNCTL (LAA28) Circuit Pack Description	7-17
Purpose of Circuit	7-17
Faceplate LED	7-17
General Description of Operation	7-18
Detailed Description of Operation	7-18
Quick Reference Summary	7-20
■ OHCTL (TERM) (LAA21 and LAA22) Circuit	

Table of Contents

Pack Description	7-21
Purpose of Circuit	7-21
Faceplate LED	7-21
General Description of Operation	7-22
Detailed Description of Operation	7-22
Quick Reference Summary	7-25
■ TOHCTL (LAA26) Circuit Pack Description	7-26
Purpose of Circuit	7-26
Faceplate LED	7-26
General Description of Operation	7-27
Detailed Description of Operation	7-27
Quick Reference Summary	7-31
■ OW (LAA14) Circuit Pack Description	7-32
Purpose of Circuit	7-32
Faceplate	7-32
General Description of Operation	7-32
Detailed Description of Operation	7-33
Quick Reference Summary	7-34
Transmission	7-35
■ DS3 (LAA2/LAA2B) Circuit Pack Description	7-35
Purpose of Circuit	7-35
Faceplate LED	7-35
General Description of Operation	7-36
Detailed Description of Operation	7-36
Quick Reference Summary	7-42
■ STS1E (LAA4) Circuit Pack Description	7-44
Purpose of Circuit	7-44
Faceplate LED	7-44
General Description of Operation	7-45
Detailed Description of Operation	7-45
Quick Reference Summary	7-50
■ OC3 (LAA10/LAA10B) Circuit Pack Description	7-52

Table of Contents

Purpose of Circuit	7-52
Faceplate LED	7-52
General Description of Operation	7-53
Detailed Description of Operation	7-53
Quick Reference Summary	7-60
■ IS3 (LAA5) Circuit Pack Description	7-62
Purpose of Circuit	7-62
Faceplate LED	7-62
General Description of Operation	7-63
Detailed Description of Operation	7-63
Quick Reference Summary	7-69
■ OC-12 (T939A) Circuit Pack Description	7-71
Purpose of Circuit	7-71
Faceplate LED	7-71
General Description of Operation	7-72
Detailed Description of Operation	7-73
Quick Reference Summary	7-78
■ LSSW (LAA12B) Circuit Pack Description	7-80
Purpose of Circuit	7-80
Faceplate LED	7-80
General Description of Operation	7-81
Detailed Description of Operation	7-82
Quick Reference Summary	7-85
■ OC48 RCVR A/D (839B2, 839B4B, 839B5, and 839E5) Circuit Pack Description	7-86
Purpose of Circuit	7-86
Faceplate LED	7-87
General Description of Operation	7-88
Detailed Description of Operation	7-88
Quick Reference Summary	7-95
■ OC48 TRMTR A/D (739B_, 739C_, 739E_, 739G_, 739H_, 739J_, 739P5, 739R_, 739S_) Circuit Pack Description	7-97

Table of Contents

Purpose of Circuit	7-97
Faceplate LED	7-98
General Description of Operation	7-99
Detailed Description of Operation	7-100
Quick Reference Summary	7-106
■ OC48 REGENR (39B1, 39B2, 39C1, 39C2, 39J2, 39R2, and 39S2) Circuit Pack Description	7-108
Purpose of Circuit	7-108
Faceplate LED	7-108
General Description of Operation	7-109
Detailed Description of Operation	7-110
Quick Reference Summary	7-114
Synchronization	7-116
■ TG3 (DS1) (LAA18) Circuit Pack Description	7-116
Purpose of Circuit	7-116
Faceplate LED	7-116
General Description of Operation	7-117
Detailed Description of Operation	7-118
Quick Reference Summary	7-122

Overview

This section provides a detailed functional description of the FT-2000 OC-48 Lightwave System circuit packs.

General

The FT-2000 OC-48 Lightwave System circuit packs are divided into three main categories:

- Control circuit packs
 - System Controller (SYSCTL) LAA23^{*}
 - System Controller (SYSCTL) LAA23B^{*}
 - System Controller (SYSCTL) LAA23C^{*}
 - System Memory - 4 Megabyte (SYSMEM) LAA25
 - Line Controller - 4 Megabyte [LNCTL (4 MEG)] LAA28
 - Overhead Controller - Terminal [OHCTL (TERM)] LAA21
 - Overhead Controller - Terminal [OHCTL (TERM)] LAA22
 - Overhead Controller - Tributary (TOHCTL) LAA26

^{*} LAA23 was previously discontinued and replaced by LAA23B. LAA23B was discontinued April 30, 2001 and replaced by LAA23C. The LAA23C circuit pack is compatible with (non-TARP) Release 7.2.7 and later releases. To obtain LAA23B circuit packs (107712689) through the Lucent reuse program, contact your Lucent Account Executive. However, it is strongly recommended that you upgrade the current release.

- Orderwire (OW) LAA14
- Transmission circuit packs
 - DS3 Interface (DS3) LAA2/LAA2B
 - STS-1 Electrical Interface (STS1E) LAA4
 - IS3 Optical Interface -
Contains LED optics (for intraoffice applications only) LAA5
 - OC3 Optical Interface - 1.3 μm Standard Performance
[OC3 (1.3 STD)] LAA10^{*}/LAA10B
 - OC12 Optical Interface - 1.3 μm Standard Performance
[OC12 (1.3 STD)] T939A
 - Low Speed Switch (LSSW) LAA12[†]/LAA12B
 - OC48 Receiver, Add/Drop [OC48 RCVR (A/D)] 839B2[‡]
 - OC48 Receiver, Add/Drop [OC48 RCVR (A/D)] 839B4B
 - OC48 Receiver, Add/Drop [OC48 RCVR (A/D)] 839B5
 - OC48 Receiver, Add/Drop [OC48 RCVR (A/D)] 839E5
 - OC48 Transmitter, Add/Drop - 1.3 μm Standard Performance [OC48
TRMTR (A/D 1.3 STD)] 739B2[‡]
 - OC48 Transmitter, Add/Drop - 1.3 μm Standard Performance
[OC48 TRMTR (A/D 1.3 STD)] 739B4
 - OC48 Transmitter, Add/Drop - 1.3 μm Standard Performance
[OC48 TRMTR (A/D STS1 1.3 STD)] 739B5
 - OC48 Transmitter, Add/Drop - 1.3 μm High Performance
[OC48 TRMTR (A/D 1.3 HI PERF)] 739C2[‡]
 - OC48 Transmitter, Add/Drop - 1.3 μm High Performance
[OC48 TRMTR (A/D STS1 1.3 HI PERF)] 739C5
 - OC48 Transmitter, Add/Drop - 1.5 μm Wavelength n , 360km
Dispersion Limit [OC48 TRMTR (A/D 1.5 Wn) 360km DISP]
($n=1-16$) 739E[1-16]
 - OC48 Transmitter, Add/Drop - 1.5 μm Wavelength n , 640km
Dispersion Limit [OC48 TRMTR (A/D 1.5 Wn) 640km DISP]
($n=1-16$) 739G[1-8]
 - OC48 Transmitter, Add/Drop - 1.5 μm Wavelength n , 60km
Dispersion Limit [OC48 TRMTR (A/D 1.5 Wn) 60km DISP] ($n=1-16$)
739H[1-16]

* The LAA10 has been discontinued and replaced with LAA10B.

† The LAA12 circuit pack was rated manufacture discontinued (MD) in January 1997. It was replaced with the LAA12B circuit pack.

‡ These transmitter and receivers are DA effective 6/30/97.

- OC48 Transmitter, Add/Drop - 1.5 μm Standard Performance [OC48 TRMTR (A/D 1.5 STD)] 739J2[†]
- OC48 Transmitter, Add/Drop - 1.5 μm LBA Standard Performance [OC48 TRMTR (A/D 1.5 LBA)] 739J3[†]
- OC48 Transmitter, Add/Drop - 1.5 μm LBA Standard Performance [OC48 TRMTR (A/D 1.5 LBA)] 739J4
- OC48 Transmitter, Add/Drop - 1.5 μm Standard Performance [OC48 TRMTR (A/D STS1 1.5 STD)] 739J5
- OC48 Transmitter, Add/Drop - 1.5 μm LBA Standard Performance [OC48 TRMTR (A/D STS1 1.5 LBA)] 739P5
- OC48 Transmitter, Add/Drop - 1.5 μm LBA Wavelength 1 [OC48 TRMTR (A/D 1.5 LBA W1)] 739R2[†]
- OC48 Transmitter, Add/Drop - 1.5 μm LBA Standard Performance [OC48 TRMTR (A/D STS1 1.5 LBA, W1)] 739R5
- OC48 Transmitter, Add/Drop - 1.5 μm LBA Wavelength 2 [OC48 TRMTR (A/D 1.5 LBA W2)] 739S2[†]
- OC48 Transmitter, Add/Drop - 1.5 μm LBA Standard Performance [OC48 TRMTR (A/D STS1 1.5 LBA, W2)] 739S5
- OC48 Regenerator - 1.3 μm Standard Performance [OC48 REGENR (1.3 STD)] 39B1^{*}
- OC48 Regenerator - 1.3 μm Standard Performance [OC48 REGENR (1.3 STD)] 39B2
- OC48 Regenerator - 1.3 μm High Performance [OC48 REGENR (1.3 HI PERF)] 39C1[†]
- OC48 Regenerator - 1.3 μm High Performance [OC48 REGENR (1.3 HI PERF)] 39C2
- OC48 Regenerator - 1.5 μm Standard Performance [OC48 REGENR (1.5 STD)] 39J2
- OC48 Regenerator - 1.5 μm Standard Performance [OC48 REGENR (1.5 LBA W1)] 39R2
- OC48 Regenerator - 1.5 μm Standard Performance [OC48 REGENR (1.5 LBA W2)] 39S2

■ Synchronization circuit pack

- Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] LAA18.

* The 39B1 OC48 REGENR circuit pack has been discontinued and replaced with the 39B2 OC48 REGENR circuit pack.

† The 39C1 OC48 REGENR circuit pack has been discontinued and replaced with the 39C2 circuit pack.

Circuit Pack and Software Compatibility

Refer to Table 7-1 for information about circuit packs and compatible software.

Table 7-1. Circuit Packs and Compatible Software

Circuit Pack		Compatible Software										
Code	Name	R2.1.0- RPTR	R3.0.1- ADR	R4.0.0- ADR	R4.0.0- RPTR	R4.1.0- ADR	R5.0- ADR	R6.0 - ADR	R6.0.0- RPTR	R7.0.0 - ADR	R7.1.0 - ADR	R7.2 - ADR
LAA2,S1:1	DS3		X									
LAA2,S4:4	DS3		X	X		X	X	X		X	X	X
LAA2B	DS3		X	X		X	X	X		X	X	X
LAA4	STS1E			X		X	X	X		X	X	X
LAA5	IS3										X	X
LAA10*	OC3 (1.3 STD)						X	X		X	X	X
LAA10B*	OC3 (1.3 STD)						X	X		X	X	X
T939A	OC12										X	X
LAA12†	LSSW		X	X		X	X	X		X	X	X
LAA12B†	LSSW		X	X		X	X	X		X	X	X
LAA14	OW	X			X				X	X		
LAA17‡	TG3 (DS1)		X	X		X	X	X		X	X	X
LAA18	TG3 (DS1)		X	X		X	X	X		X	X	X
LAA21	OHCTL		X	X		X	X	X		X	X	X
LAA22§	OHCTL		X	X		X	X	X		X	X	X
LAA23¶	SYSCTL	X	X	X	X	X	X	X	X	X	X	X
LAA23B¶*	SYSCTL	X	X	X	X	X	X	X	X	X	X	X
LAA23C¶	SYSCTL											X
LAA25	SYSMEM	X	X	X	X	X	X	X	X	X	X	X
LAA26	TOHCTL							X		X	X	X
LAA28	LNCTL (4 MEG)		X	X		X	X	X		X	X	X
39B1**	OC-48 REGENR (1.3 STD)	X			X				X			
39B2	OC-48 REGENR (1.3 STD)	X			X				X			
39C1††	OC-48 REGENR (1.3 STD)	X			X				X			
39C2	OC-48 REGENR (1.3 STD)	X			X				X			
39J2	OC-48 REGENR (1.5 STD)	X			X				X			
39R2	OC-48 REGENR (1.5 STD)	X			X				X			
39S2	OC-48 REGENR (1.5 STD)	X			X				X			
739B2**	OC48 TRMTR (A/D 1.3 STD)		X	X		X	X	X		X	X	X
739B4	OC48 TRMTR (A/D 1.3 STD)									X	X	X
739B5	OC48 TRMTR									X	X	X
739C2**	OC48 TRMTR (A/D 1.3 HI PERF)		X	X		X	X	X		X	X	X

Table 7-1. Circuit Packs and Compatible Software (Contd)

Circuit Pack		Compatible Software										
Code	Name	R2.1.0-RPTR	R3.0.1-ADR	R4.0.0-ADR	R4.0.0-RPTR	R4.1.0-ADR	R5.0-ADR	R6.0-ADR	R6.0.0-RPTR	R7.0.0-ADR	R7.1.0-ADR	R7.2-ADR
739C5	OC48 TRMTR (A/D STS1 1.3 HI PERF)									X	X	X
739E1-E16	OC48 TRMTR (A/D STS1 1.5 360km DISP)											X
739G1-G8	OC48 TRMTR (A/D STS1 1.5 640km DISP)											X
739H1-H16	OC48 TRMTR (A/D STS1 1.5 60km DISP)											X
739J2 ^{††}	OC48 TRMTR (A/D 1.5 STD)		X	X		X	X	X		X	X	X
739J3 ^{††}	OC48 TRMTR (A/D 1.5 LBA)						X	X		X	X	X
739J4	OC48 TRMTR (A/D 1.5 LBA)									X	X	X
739J5	OC48 TRMTR (A/D STS1 1.5 STD)									X	X	X
739P5	OC48 TRMTR (A/D STS1 1.5 LBA)									X	X	X
739R2 ^{††}	OC48 TRMTR (A/D 1.5 LBA W1)							X		X	X	X
739R5	OC48 TRMTR (A/D STS1 1.5 LBA, W1)									X	X	X
739S2 ^{††}	OC48 TRMTR (A/D 1.5 LBA W2)							X		X	X	X
739S5	OC48 TRMTR (A/D STS1 1.3 LBA, W2)									X	X	X
839B2 ^{††}	OC48 RCVR (A/D)		X	X		X	X	X		X	X	X
839B4B	OC48 RCVR (A/D)									X	X	X
839B5	OC48 RCVR (A/D STS1)									X	X	X
839E5	OC48 RCVR (A/D STS1)											X

* LAA10 has been discontinued and replaced by LAA10B.

† The LAA12 circuit pack was rated manufacture discontinued (MD) in January 1997. It was replaced with the LAA12B circuit pack.

‡ The LAA17 TG3 (DS1) circuit pack has been discontinued and replaced with the LAA18 TG3 (DS1) circuit pack.

§ The LAA22 OHCTL circuit pack is only used in slot OHCTL 1W of the High Speed Shelf of the FT-2000 OC-48 Add/Drop-Rings Terminal.

¶ The LAA23B circuit pack was discontinued April 30, 2001 and replaced by the LAA23C circuit pack. The LAA23C circuit pack is compatible with (non-TARP) Release 7.2.7 and later releases. To obtain LAA23B circuit packs (107712689) through the Lucent reuse program, contact your Lucent Account Executive. However, it is strongly recommended that you upgrade to the current release.

** The 39B1 OC48 REGENR circuit pack has been discontinued and replaced with the 39B2 OC48 REGENR circuit pack.

†† The 39C1 OC48 REGENR circuit pack has been discontinued and replaced with the 39C2 OC48 REGENR circuit pack.

‡‡ These transmitter and receivers are DA effective 6/30/97.

Control

SYSTCL (LAA23, LAA23B, and LAA23C) Circuit Pack Description

Purpose of Circuit

The System Controller (SYSTCL) circuit pack is the main controller in the system. It has control over all FT-2000 OC-48 Lightwave System functions, and together with the System Memory (SYSMEM) circuit pack, provides all user interfaces to the system.

Beginning with Release 7.2.7-ADR, the LAA23C* SYSTCL circuit pack is used in OC-48 point-to-point and bidirectional ring applications.

Faceplate LED

The SYSTCL circuit pack has a red FAULT LED on its faceplate (Figure 7-1). The red FAULT LED is lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the SYSMEM circuit pack to light the FAULT LED on the SYSTCL circuit pack.

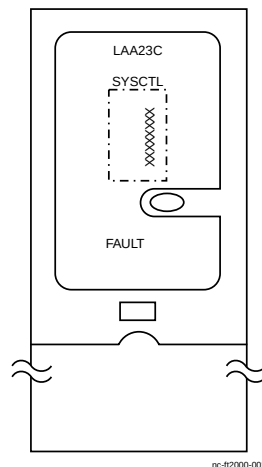


Figure 7-1. SYSTCL (LAA23C) Circuit Pack

* LAA23 was previously discontinued and replaced by LAA23B. LAA23B was discontinued April 30, 2001 and replaced by LAA23C. See Table 7-1 for software compatibility information. The LAA23C circuit pack is compatible with (non-TARP) Release 7.2.7 and later releases. To obtain LAA23B circuit packs (107712689) through the Lucent reuse program, contact your Lucent Account Executive. However, it is strongly recommended that you upgrade the current release.

The sanity check circuitry detects hardware and software faults. When a fault occurs, the microprocessor is reset and the FAULT LED on the circuit pack faceplate is lighted.

General Description of Operation

The SYSCTL circuit pack provides overall administrative control of the FT-2000 OC-48 Lightwave System. It performs system-wide computations and performance monitoring. It also provides the serial user interfaces.

The SYSCTL circuit pack also interfaces with the System Memory (SYSMEM), Line Controller (LNCTL), Overhead Controller - Terminal [OHCTL (TERM)], and Overhead Controller - Tributary (TOHCTL) (Release 6 and later) circuit packs.

Detailed Description of Operation

Control Circuitry

Microprocessor . Figure 7-2 provides an overall block diagram of the SYSCTL circuit pack. The SYSCTL circuit pack is the highest level processor in the three-level control hierarchy of the FT-2000 OC-48 Lightwave System.

Memory

Start-Up Memory . The start-up memory consists of 64 kilobytes of read-only memory. The start-up memory contains the program that allows the microprocessor to copy the executable code and data for the system from the nonvolatile memory of the SYSMEM circuit pack into the operating memory of the SYSCTL circuit pack. This allows the microprocessor to start up operation during a power-up or a reset condition or when initializing new software. The SYSCTL circuit pack also has a unique network services access point (NSAP) code in the read-only memory. This is required for bidirectional ring applications.

Operating Memory . The main operating memory consists of 4 megabytes of dynamic random access memory (DRAM). Direct memory access (DMA) allows the SYSCTL circuit pack to copy the executable code and data for the system from the nonvolatile memory of the SYSMEM circuit pack into the operating memory during a power-up or reset condition, or when initializing new software.

Nonvolatile Memory . The nonvolatile memory consists of 128 bytes of electrically-erasable programmable read-only memory (EEPROM). The nonvolatile memory is used to store information about the history of the SYSCTL circuit pack such as the *Common Language** CLEI code and type/version information.

For more information about memory administration, refer to Volume I, Section 8, "Administration and Provisioning." *

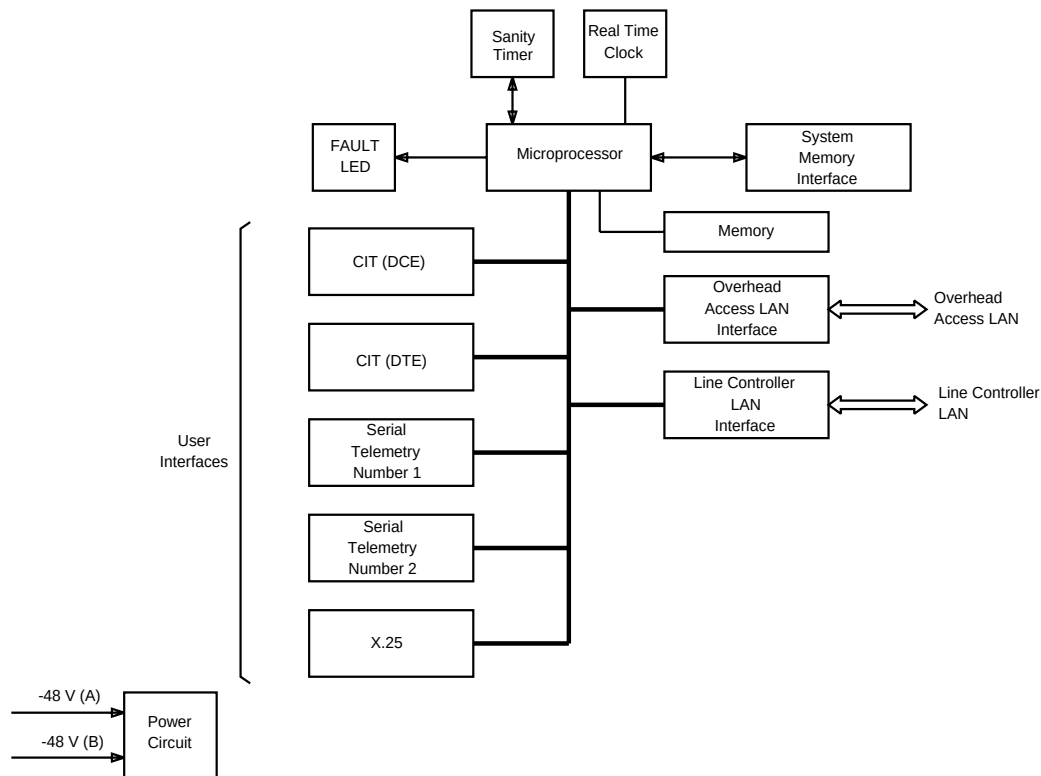


Figure 7-2. SYSCTL Circuit Pack Block Diagram

Interface Circuitry

Line Controller Local Area Network Interface. The line controller local area network (LCLAN) interface allows the SYSCTL circuit pack to communicate with the Line Controller (LNCTL) circuit packs. In the FT-2000 OC-48 Repeater Bay, the SYSCTL circuit pack communicates directly with the board controllers on the individual circuit packs. The LNCTL circuit pack is not required for the FT-2000 OC-48 Repeater Bay.

Overhead Access Local Area Network Interface. The overhead access local area network (OALAN) interface allows the SYSCTL circuit pack to communicate with the Overhead Controller - Terminal [OHCTL (TERM)] and Overhead Controller - Tributary (TOHCTL) (Release 6 and later) circuit pack.

* Common Language is a registered trademark and CLEI, CLLI, CLCI, and CLFI are trademarks of Telcordia Technologies, Inc.

Serial User Interfaces. The SYSCTL circuit pack provides the following serial user interfaces:

CIT (DCE) interface: The CIT (DCE) interface is configured as data circuit equipment (DCE) for direct craft interface terminal (CIT) access. In releases prior to Release 5, the CIT (DCE) interface allows direct access to a PC running FT-2000 CIT-PC software only. In Release 5 and later releases, the CIT (DCE) interface may also be provisioned to allow direct access to a PC running the CPro-2000 software or an ASCII-based terminal. The CIT (DCE) interface supports data rates up to 9600 baud.

- **CIT (DTE) interface:** The CIT (DTE) interface is configured as data terminating equipment (DTE) to allow a permanent connection to a modem. In releases prior to Release 5, the CIT (DTE) interface allows dial-up access to a PC running FT-2000 CIT-PC software only. In Releases 5 and later releases, the CIT (DTE) interface may also be provisioned to allow direct access to a PC running the CPro-2000 software or an ASCII-based terminal. The CIT (DTE) interface supports data rates up to 9600 baud.

- **Serial telemetry interface:** The serial telemetry interface supports telemetry byte-oriented serial (TBOS) protocol. The serial telemetry interface is used to transfer alarm, status, and control and performance-monitoring information to and from a remote maintenance system using a 2400-baud EIA-422 port (FT-2000 OC-48 Add/Drop-Rings Terminals only).

- **X.25 interface:** The X.25 interface supports X.25 protocol and the transaction language 1 (TL1) message language. The X.25 interface is used to report alarm and status conditions and performance-monitoring data to Telcordia's Network Monitoring and Analysis (NMA^{*}) system. The physical connection is an EIA-232-D port that operates at data rates from 1200 to 19,200 bps (FT-2000 OC-48 Add/Drop-Rings Terminals only).

For more information about these interfaces, refer to Volume I, Section 6, "Operations Interfaces."

Power Circuitry

The SYSCTL circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts and –5 volts used on the circuit pack. The power converter also provides a +5 volt source for the FAULT LED on any circuit pack that has a power failure.

The SYSCTL circuit pack monitors the two –48 volt sources to the circuit pack. If the SYSCTL circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or power converter fails, the SYSMEM circuit pack provides +5 volts to light the FAULT LED on the SYSCTL circuit pack.

* NMA is a trademark of Telcordia Technologies, Inc.

Quick Reference Summary

Control Functions

The SYSCTL circuit pack performs the following control and maintenance functions:

- a. Stores the operating copy of the system executable code and data
- b. Performs system-wide maintenance computations and performance monitoring
- c. Automatically resets the system during power up
- d. Isolates faults for the LCLAN and OALAN
- e. Maintains the real-time clock
- f. Performs self audits
- g. Controls the circuit pack faceplate FAULT LED on the SYSCTL and the SYSMEM circuit packs.

Interface Functions

The SYSCTL circuit pack provides the following interface functions:

- a. Communications between the SYSCTL and SYSMEM circuit packs using the SYSCTL/SYSMEM interface
- b. Communications between the SYSCTL and the LNCTL circuit packs using the line controller local area network (LCLAN) (FT-2000 OC-48 Add/Drop-Rings Terminals only)
- c. Communications between the SYSCTL circuit pack and the OHCTL (TERM) circuit pack(s) using the overhead access local area network (OALAN)
- d. Communications between the SYSCTL circuit pack and the TOHCTL circuit pack using the overhead access local area network (OALAN)
- e. CIT (DCE) interface for direct local access to the system operating at 1200, 2400, 4800, or 9600 baud
- f. CIT (DTE) interface for remote dial-up access to the system operating at 1200, 2400, 4800, or 9600 baud
- g. Serial telemetry for advanced provisioning, performance monitoring, administration, and maintenance activities operating at 2400 baud (FT-2000 OC-48 Add/Drop-Rings Terminals only).
- h. X.25 interface for advanced provisioning, performance monitoring, administration, and maintenance activities operating at 1200 to 19,200 baud (FT-2000 OC-48 Add/Drop-Rings Terminals only).

SYSMEM (LAA25) Circuit Pack Description

Purpose of Circuit

The System Memory (SYSMEM) circuit pack provides the nonvolatile memory necessary for storing the executable code and data for the system. The SYSMEM circuit pack also contains the office alarm circuitry and the processor parallel input/output (I/O) circuitry.

Faceplate LED

The SYSMEM circuit pack has a red FAULT LED on its faceplate (Figure 7-3). The red FAULT LED is lighted during a reset condition, when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the System Controller (SYSCTL) circuit pack to light the FAULT LED on the SYSMEM circuit pack.

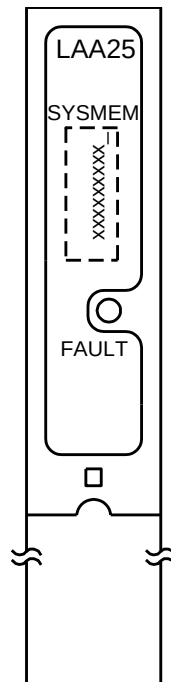


Figure 7-3. SYSMEM (LAA25) Circuit Pack

General Description of Operation

The SYSMEM circuit pack provides the following:

- Nonvolatile program (executable code) and configuration (data) memory
- Office alarms interface
- User panel LEDs and controls
- Parallel telemetry interface (FT-2000 OC-48 Add/Drop-Rings Terminals only)
- User-settable miscellaneous discrete interface.

Detailed Description of Operation

Memory

Nonvolatile Program (Executable Code) Storage. Figure 7-4 shows a functional block diagram of the SYSMEM circuit pack. The nonvolatile program storage is provided by 4 megabytes of flash erasable programmable read-only memory (flash-EPROM). This contains the main system software for all the controllers in the system (for example, System Controller, Line Controllers, and board controllers). Flash-EPROM combines the nonvolatility of EPROM with the in-circuit reprogramming ability of electrically erasable programmable read-only memory (EEPROM). Flash-EPROM allows in-service software upgrades to be performed locally or remotely (via modem connections).

Nonvolatile Configuration (Data) Storage. The nonvolatile configuration storage is provided by 128 kilobytes of EEPROM. This contains all system state and user-provisioned data. This includes all system parameters with their original and current values. EEPROM is byte erasable and writable. This allows configuration information to be easily changed during system operation.

Nonvolatile Memory. The nonvolatile memory consists of 128 bytes of EEPROM. The nonvolatile memory is used to store information about the history of the SYSMEM circuit pack such as the *CLEI* code and type/version information.

For more information about memory administration, refer to Volume I, Section 8, "Administration and Provisioning."

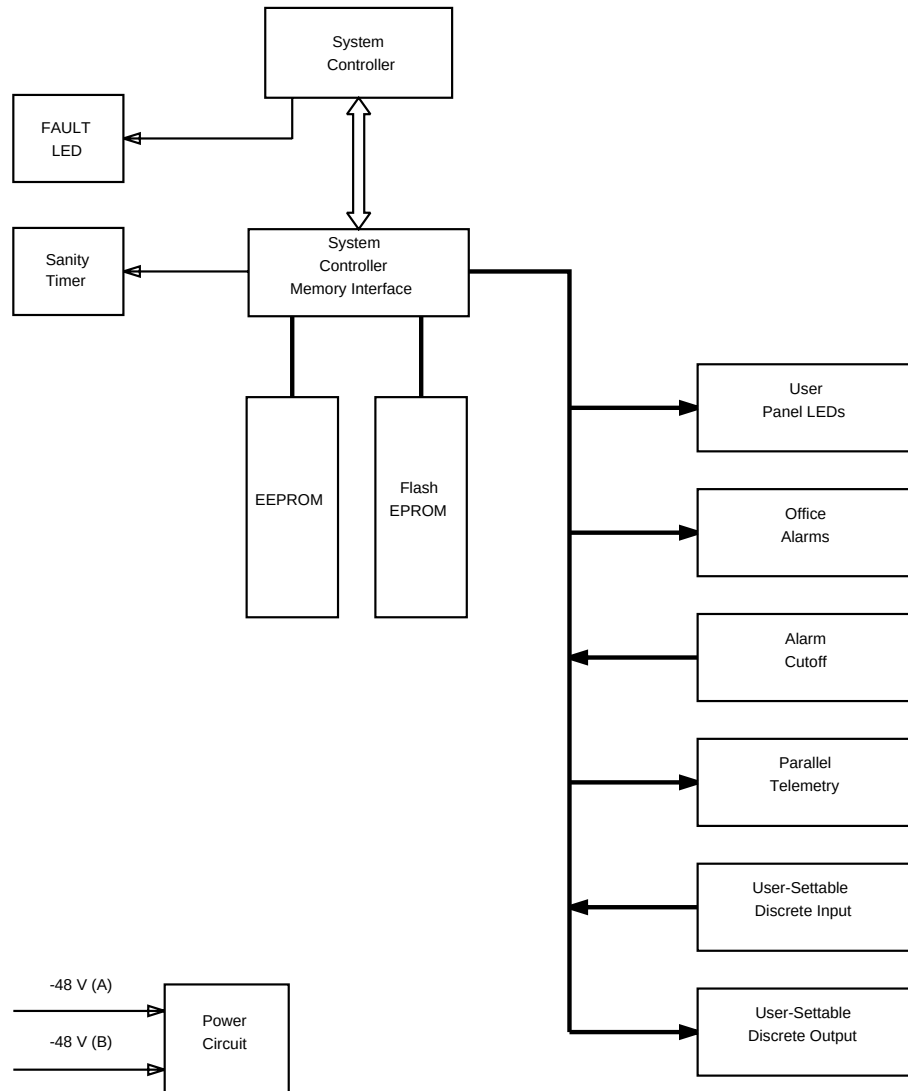


Figure 7-4. SYSMEM Circuit Pack Block Diagram

Office Alarms Interface

The SYSMEM circuit pack provides relays for the FT-2000 OC-48 Add/Drop-Rings Terminal and FT-2000 OC-48 Repeater Bay to interface with a central office alarm grid. The relays are controlled by the microprocessor on the SYSTL circuit pack. Relays are provided for audible and visible alarms for:

- Critical
- Major
- Minor.

The normally closed contacts for the critical and major relays provide the alarm closure if power fails. For more information about the office alarms interface, refer to Volume I, Section 6, "Operations Interfaces."

User Panel LEDs and Controls

The SYSMEM circuit pack provides the outputs to light the following LEDs on the user panel:

- Critical (CR) alarm
- Major (MJ) alarm
- Minor (MN) alarm
- Abnormal (ABN) status
- Near-end activity (NE ACTY)
- Far-end activity (FE ACTY)
- Line locator (LINE LOCTR) display
- Use CIT (USE CIT).

The SYSMEM circuit pack also accepts the alarm cutoff (ACO) input from the user panel. For more information about the user panel, refer to Volume I, Section 6, "Operations Interfaces."

Parallel Telemetry Interfaces

The SYSMEM circuit pack supplies the following alarms from the FT-2000 OC-48 Add/Drop-Rings Terminal as remote information accessible to an operations system:

- Local CR, MJ, and MN alarms
- Remote CR, MJ, and MN alarms

The FT-2000 OC-48 Repeater Bay does not support a parallel telemetry interface. For more information about the parallel telemetry interface, refer to Volume I, Section 6, "Operations Interfaces."

User-Settable Miscellaneous Discrete Interface

The SYSMEM circuit pack accepts 16 user-settable environmental inputs and provides 4 user-settable control outputs at the FT-2000 OC-48 Add/Drop-Rings Terminal. At the FT-2000 OC-48 Repeater Bay, the SYSMEM circuit pack accepts 16 environmental inputs only. The four control outputs will be available in a future release. For more information about the miscellaneous discrete interface, refer to Volume I, Section 6, "Operations Interfaces."

Power Circuitry

The SYSMEM circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. Two modular DC-to-DC power converters produce +5 volts and +12 volts used on the circuit pack. The +5 volt power converter provides a +5 volt source for the FAULT LED on the System Controller (SYSCTL) circuit pack if a power failure occurs on the SYSCTL circuit pack. The +12 volt power converter is used to program the flash memory.

The SYSMEM circuit pack monitors the two –48 volt sources to the circuit pack. If the SYSMEM circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or +5 volt power converter fails, the SYSCTL circuit pack provides +5 volts to light the FAULT LED on the SYSMEM circuit pack.

Quick Reference Summary

Control Functions

The SYSMEM circuit pack performs the following control functions:

- a. Provides program (executable code) memory storage
- b. Provides configuration (data) memory storage.

Interface Functions

The SYSMEM circuit pack provides the following interface functions:

- a. Office alarms
- b. User panel LEDs and controls
- c. Parallel telemetry to remotely report alarm information to an operations system (FT-2000 OC-48 Add/Drop-Rings Terminals only)
- d. User-settable miscellaneous discrete interface that provides environmental inputs (FT-2000 OC-48 Add/Drop-Rings Terminals and FT-2000 OC-48 Repeater Bays) and control outputs (FT-2000 OC-48 Add/Drop-Rings Terminals only).

LNCTL (LAA28) Circuit Pack Description

Purpose of Circuit

The Line Controller (LNCTL) circuit pack is the middle level of the three-level control hierarchy for the FT-2000 OC-48 Lightwave System. It performs real-time intensive line functions, primarily protection switching, and line specific computations. The LNCTL circuit pack is not required for the FT-2000 OC-48 Repeater Bay.

The LAA28 LNCTL (4 MEG) circuit pack is required for bidirectional ring applications and is compatible with Release 1.1.1-TRM or later software.

Faceplate LED

The LNCTL circuit pack has a red FAULT LED on its faceplate (Figure 7-5). The red FAULT LED is lighted during a reset condition, when a circuit pack failure is detected, or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the System Controller (SYSCTL) circuit pack to light the FAULT LED on the LNCTL circuit pack.

The sanity check circuitry detects hardware and software faults. When a fault occurs, the microprocessor is reset and the FAULT LED on the circuit pack faceplate is lighted.

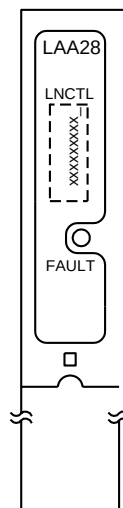


Figure 7-5. LNCTL (LAA28) Circuit Pack

General Description of Operation

The LAA28 LNCTL (4 MEG) circuit pack provides overall administrative control of up to two OC-48 high speed lines for the FT-2000 OC-48 Add/Drop-Rings Terminal. The LNCTL circuit pack provides low and high speed protection switching computations and other line specific computations. It interfaces with the board controllers on each circuit pack associated with an OC-48 high speed line and the System Controller (SYSCTL) circuit pack.

Detailed Description of Operation

Control Circuitry

Processor. Figure 7-6 provides an overall block diagram of the LNCTL circuit pack. The LNCTL circuit pack is the middle level processor in the three-level control hierarchy of the FT-2000 OC-48 Lightwave System.

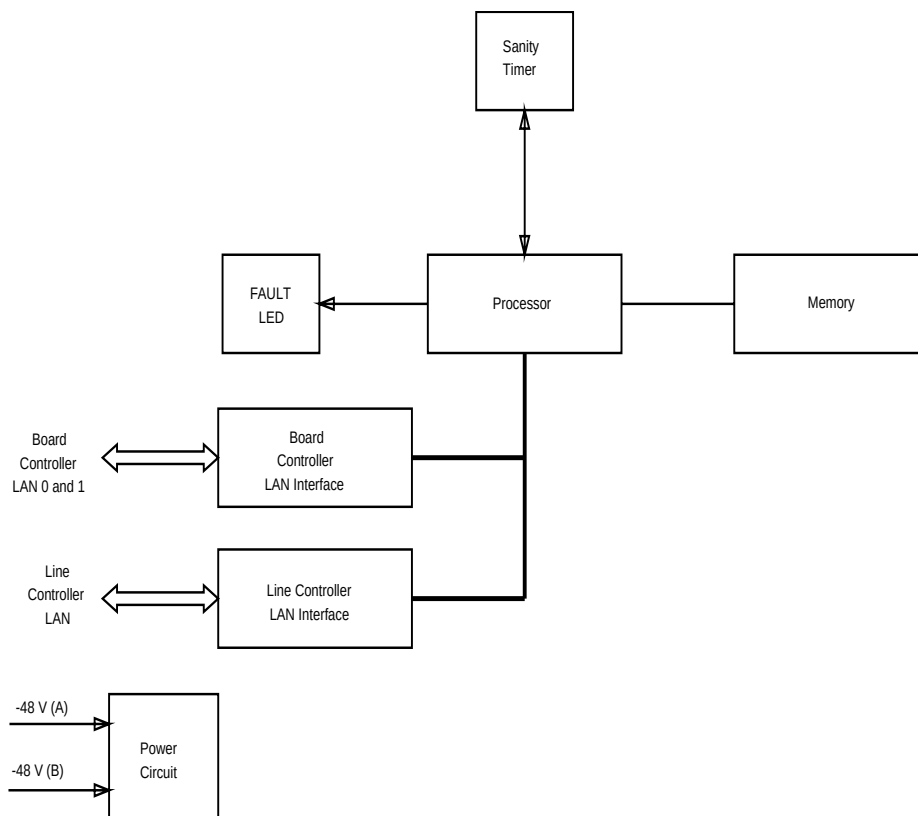


Figure 7-6. LNCTL Circuit Pack Block Diagram

Memory

Start-Up Memory. The start-up memory consists of 64 kilobytes of read-only memory. The start-up memory contains the program that allows the processor to load the executable code and data from the SYSCTL circuit pack into the operating memory. This allows the processor to start up operation during a power-up or a reset condition, or when initializing new software.

Operating Memory. The main operating memory of the LAA28 LNCTL (4 MEG) circuit pack consists of 4 megabytes of DRAM. Direct memory access (DMA) allows the board controller to load the executable code and data received from the SYSCTL circuit pack (via the line controller local area network) into the operating memory during a power-up or reset condition, or when initializing new software.

Nonvolatile Memory. The nonvolatile memory consists of 128 bytes of electrically-erasable programmable read-only memory (EEPROM). The nonvolatile memory is used to store information about the history of the LNCTL circuit pack such as the *CLEI* code and type/version information.

For more information about memory administration, refer to Volume I, Section 8, "Administration and Provisioning."

Interface Circuitry

Line Controller Local Area Network Interface. The line controller local area network (LCLAN) interface allows the LNCTL circuit pack to communicate with the SYSCTL circuit pack.

Board Controller Local Area Network Interface. The board controller local area network (BCLAN) interface allows the LNCTL circuit pack to communicate with the board controllers on each circuit pack associated with a particular OC-48 high speed line.

Power Circuitry

The LNCTL circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack. The power converter also provides two +5 volt sources for the FAULT LEDs on the other circuit packs (associated with the same OC-48 high speed line) if a power failure occurs.

The LNCTL circuit pack monitors the two –48 volt sources to the circuit pack. If the LNCTL circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or power converter fails, the SYSCTL circuit pack provides +5 volts to light the FAULT LED on the LNCTL circuit pack.

Quick Reference Summary

Control Functions

The LNCTL circuit pack performs the following control and maintenance functions:

- a. Performs low and high speed protection switching computations
- b. Performs line specific computations
- c. Controls the circuit pack faceplate FAULT LED
- d. Isolates faults for the BCLANs.

Interface Functions

The LNCTL circuit pack provides the following interface functions:

- a. Communications between the LNCTL and SYSCTL circuit packs using the LCLAN interface
- b. Communications between the LNCTL and the board controller on the circuit packs associated with a particular OC-48 high speed line using the BCLANs.

OHCTL (TERM) (LAA21 and LAA22) Circuit Pack Description

Purpose of Circuit

The Overhead Controller - Terminal [OHCTL (TERM)] circuit pack provides access to the OC-48 SONET overhead channel. The overhead channel contains SONET line and section overhead bytes. The LAA21 OHCTL (TERM) circuit pack is required for orderwire access and single-ended operations (for example, CIT remote login). Two LAA21 OHCTL (TERM) circuit packs are required for the FT-2000 OC-48 Add/Drop-Rings Terminals (equipped with the Enhanced High Speed Shelf).

One LAA21 (East) OHCTL (TERM) and one LAA22 (West) OHCTL (TERM) circuit pack are required for the FT-2000 OC-48 Add/Drop-Rings Terminal (equipped with the High Speed Shelf). The LAA21 OHCTL (TERM) circuit pack is compatible with Releases 2.1.1-TRM, 3.0.1-ADR, and later software. The LAA22 OHCTL (TERM) circuit pack is compatible with Release 3.0.1-ADR and later software only.

Faceplate LED

The OHCTL (TERM) circuit pack has a red FAULT LED on its faceplate (Figure 7-7). The red FAULT LED is lighted during a reset condition, when a circuit pack failure is detected, or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the System Controller (SYSCTL) circuit pack to light the FAULT LED on the OHCTL (TERM) circuit pack.

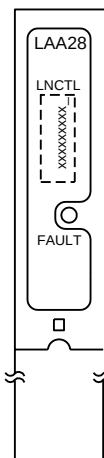


Figure 7-7. OHCTL (TERM) (LAA21 and LAA22) Circuit Pack

The sanity check circuitry detects hardware and software faults. When a fault occurs, the microprocessor is reset and the FAULT LED on the circuit pack faceplate is lighted.

General Description of Operation

The OHCTL (TERM) circuit pack assembles and disassembles the embedded information for the section and line overhead bytes. These overhead bytes include section and line data communications bytes, section and line orderwire bytes, growth bytes, and user bytes. Refer to Appendix A, "A SONET Overview," for definitions of SONET sections, lines, and paths. The OHCTL (TERM) circuit pack also interfaces with the SYSCTL circuit pack.

Detailed Description of Operation

Control Circuitry

Processor. Figure 7-8 provides an overall block diagram of the OHCTL (TERM) circuit pack. The OHCTL (TERM) circuit pack provides protocol processing for the data communications channels and manages the overhead access local area network (OALAN) communications with the SYSCTL circuit pack.

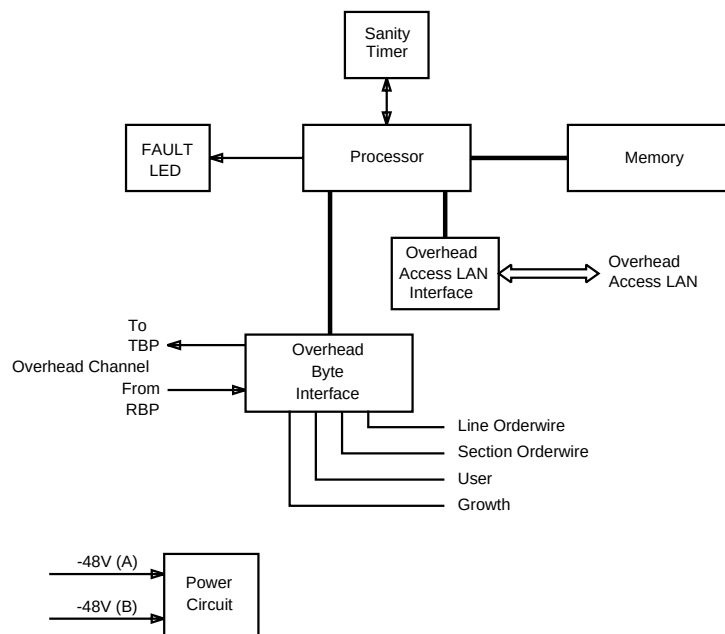


Figure 7-8. OHCTL (TERM) Circuit Pack Block Diagram

Memory

Start-Up Memory. The start-up memory consists of 64 kilobytes of read-only memory. The start-up memory contains the program that allows the processor to load the executable code and data from the SYCTL circuit pack into the operating memory. This allows the processor to start up operation during a power-up or a reset condition, or when initializing new software.

Operating Memory. The main operating memory consists of 4 megabytes of DRAM. Direct memory access (DMA) allows the board controller to load the executable code and data received from the SYCTL circuit pack (via the overhead access local area network) into the operating memory during a power-up or reset condition, or when initializing new software.

Nonvolatile Memory. The nonvolatile memory consists of 128 bytes of electrically-erasable programmable read-only memory (EEPROM). The nonvolatile memory is used to store information about the history of the OHCTL (TERM) circuit pack such as the *CLEI* code and type/version information.

Interface Circuitry

Overhead Access Local Area Network Interface. The overhead access local area network (OALAN) interface allows the OHCTL (TERM) circuit pack to communicate with the SYCTL circuit pack. In Release 6 and later releases, the OALAN allows the OHCTL (TERM) circuit pack to communicate with the Overhead Controller - Tributary (TOHCTL) circuit pack.

Overhead Byte Interface. The overhead byte interface interacts with the overhead channel. The overhead byte interface accepts the data communications channel (DCC) bytes from the System Controller circuit pack (via the OALAN) and the following overhead bytes from the interconnection panel of the Enhanced High Speed Shelf (or High Speed Shelf):

- Line orderwire
- Section orderwire
- User
- Growth.

The overhead byte interface multiplexes this information and sends it via the overhead channel to the transmit byte processor (TBP) on the OC48 Transmitter (OC48 TRMTR) circuit pack for transmission over the OC-48 high speed line. This information provides end-to-end communications and maintenance. In the LAA22 OHCTL (TERM) circuit pack, the protection overhead channel from the overhead byte interface to the TBP on the OC48 TRMTR circuit pack is not used and cannot be switched into service. One LAA22 and one LAA21 OHCTL (TERM) circuit pack are required for FT-2000 OC-48 Add/Drop-Rings Terminals equipped with the High Speed Shelf.

In the receive direction, the receive byte processor (RBP) on the OC48 Receiver (OC48 RCVR) circuit pack transmits the overhead bytes via the overhead channel to the overhead byte interface. The overhead byte interface then demultiplexes the DCC and overhead bytes and makes them available for use.

Table 7-2 shows the DCC and overhead bytes that are provided by the OHCTL (TERM) circuit pack.

Table 7-2. Overhead Bytes

Name	Overhead Byte	Data Rate	Access
Orderwire*	E1	64 kb/s	Interconnection Panel
Orderwire	E2	64 kb/s	Interconnection Panel
Growth†	Z1-Z2	1.536 Mb/s	Interconnection Panel
User†	F1	64 kb/s	Interconnection Panel
DCC	D1-D3	192 kb/s	System Controller
DCC	D4-D12	576 kb/s	System Controller

* Not available at FT-2000 OC-48 Add/Drop-Rings Terminal when there is an FT-2000 OC-48 Repeater Bay in the high speed line.

† Reserved for use in a future release.

Power Circuitry

The OHCTL (TERM) circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack.

The OHCTL (TERM) circuit pack monitors the two –48 volt sources to the circuit pack. If the OHCTL (TERM) circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or power converter fails, the SYSCTL circuit pack provides +5 volts to light the FAULT LED on the OHCTL (TERM) circuit pack.

Quick Reference Summary

The OHCTL (TERM) circuit pack performs the following control and interface functions:

- a. Provides communications between the OHCTL (TERM) and SYSCTL circuit packs using the OALAN interface
- b. Provides communications between the TOHCTL and SYSCTL circuit packs using the OALAN interface
- c. Provides communications between the transmit byte processor on the OC48 TRMTR circuit pack and the receive byte processor on the OC48 RCVR circuit pack via the overhead channel
- d. Combines the section and line data communications channel, section and line orderwire, user bytes, and growth bytes to form the transmit overhead channel
- e. Splits the received overhead channel into the section and line data communications channel, section and line orderwire, user bytes, and growth bytes
- f. Sends/receives the data communications channel information to/from the SYSCTL circuit pack
- g. Sends/receives the section and line orderwire, user bytes, and growth bytes to/from the interconnection panel
- h. Controls the circuit pack faceplate FAULT LED.

TOHCTL (LAA26) Circuit Pack Description

Purpose of Circuit

The Overhead Controller - Tributary (TOHCTL) circuit pack provides access to the OC-3/OC-12 SONET overhead channel. The OC-3/OC-12 overhead channel contains SONET section data communications channel (DCC) bytes (bytes D1-D3).

One LAA26 TOHCTL circuit pack is optional for the FT-2000 OC-48 Add/Drop-Rings Terminal. The LAA26 TOHCTL circuit pack is required for single-ended operations using the OC-3/OC-12 DCC (for example, CIT remote login to a DDM-2000 Multiplexer). The LAA26 TOHCTL is compatible with Release 6.0.0-ADR and later software. Release 7.1 supports only 0x1 OC-12 DCC. Release 7.2 and later releases support 0x1 and 1+1 OC-12 DCC.

Faceplate LED

The TOHCTL circuit pack has a red FAULT LED on its faceplate (Figure 7-9). The red FAULT LED is lighted during a reset condition, when a circuit pack failure is detected, or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the System Controller (SYSCTL) circuit pack to light the FAULT LED on the TOHCTL circuit pack.

The sanity check circuitry detects hardware and software faults. When a fault occurs, the microprocessor is reset and the FAULT LED on the circuit pack faceplate is lighted.

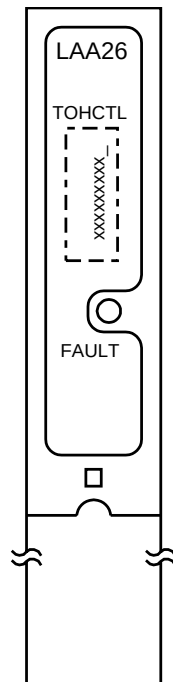


Figure 7-9. TOHCTL (LAA26) Circuit Pack

General Description of Operation

The TOHCTL circuit pack terminates (a) up to eight OC-3, (b) or two OC-12 1+1 (each with an active and standby DCC), (c) or four OC-12 0x1 section overhead channels. The OC-3/OC-12 overhead channel includes section data communications channel bytes D1-D3. Refer to Appendix A, "A SONET Overview," for definitions of SONET sections, lines, and paths. The TOHCTL circuit pack also interfaces with the OHCTL (TERM) and SYSCTL circuit packs.

Detailed Description of Operation

Control Circuitry

Processor. Figure 7-10 provides an overall block diagram of the TOHCTL circuit pack. The processor performs protocol processing for layer 3 (network) of the Open Systems Interconnection (OSI) 7-layer stack for the OC-3/OC-12 section DCC. The processor also manages the overhead access local area network (OALAN) communications with the OHCTL (TERM) and SYSCTL circuit packs.

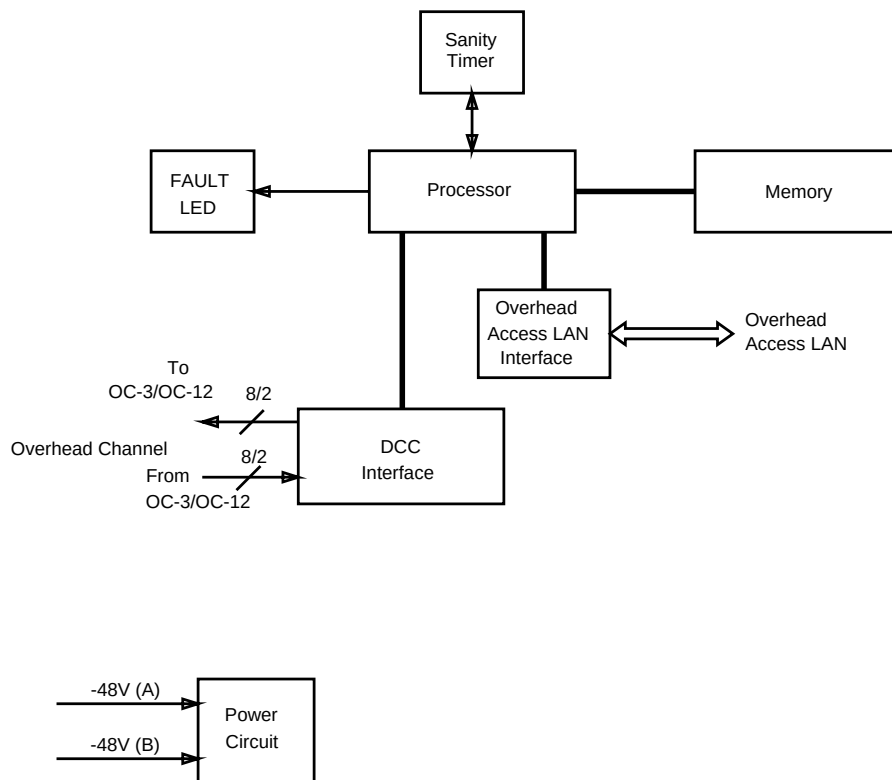


Figure 7-10. TOHCTL Circuit Pack Block Diagram

Memory

Start-Up Memory. The start-up memory consists of 64 kilobytes of read-only memory. The start-up memory contains the program that allows the processor to load the executable code and data from the SYCTL circuit pack into the operating memory. This allows the processor to start up operation during a power-up or a reset condition, or when initializing new software.

Operating Memory. The main operating memory consists of 4 megabytes of DRAM. Direct memory access (DMA) allows the processor to load the executable code and data received from the SYCTL circuit pack (via the overhead access local area network) into the operating memory during a power-up or reset condition, or when initializing new software.

Nonvolatile Memory. The nonvolatile memory consists of 128 bytes of electrically-erasable programmable read-only memory (EEPROM). The nonvolatile memory is used to store information about the history of the TOHCTL circuit pack such as the *CLEI* code and type/version information.

Interface Circuitry

DCC Interface. The DCC interface interacts with the OC-3/OC-12 (1.3 STD) circuit packs on the Low Speed Shelf - System Controller. Up to eight LS INTFC slots (1A through 8B) equipped with OC3/OC-12 (1.3 STD) circuit packs may be assigned to the DCC interface. The DCC interface performs protocol processing for layer 1 (physical) and layer 2 (data link) of the OSI 7-layer stack for the OC-3/OC-12 section DCC. Refer to Volume I, Section 8, "Administration and Provisioning," for more information about OC-3/OC-12 DCC channel assignments.

In the receive direction (from the OC-3/OC-12 line), the assigned OC3/OC-12 (1.3 STD) circuit pack transmits the overhead bytes via the overhead channel to the DCC interface. The DCC interface transmits the information to the processor for layer 3 (network) protocol processing.

In the transmit direction (toward the OC-3/OC-12 line), the DCC interface accepts the OC-3/OC-12 section DCC bytes from the processor and transmits the information via the overhead channel to the assigned OC3/OC-12 (1.3 STD) circuit pack. This information provides end-to-end communications and maintenance.

In addition to support for OC-3 and OC-12 links configured as 0x1 unprotected—the DCC interface also supports 1+1 protection switching for OC-3 and OC-12 links.

Table 7-3 shows the OC-3/OC-12 section DCC bytes that are provided by the TOHCTL circuit pack.

Table 7-3. Overhead Bytes

Name	Overhead Byte	Data Rate	Access
DCC	D1-D3	192 kb/s	System Controller

Overhead Access Local Area Network Interface. The overhead access local area network (OALAN) interface allows the TOHCTL circuit pack to communicate with the OHCTL (TERM) and SYSCTL circuit packs. The OHCTL (TERM) circuit pack performs protocol processing for layers 1 through 3 of the OSI 7-layer stack for the OC-48 section DCC. The SYSCTL circuit pack allows users to access the OC-3/OC-12 section DCC using the CIT, serial telemetry, and message-based operations system (X.25) interfaces for single-ended operations.

Power Circuitry

The TOHCTL circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack.

The TOHCTL circuit pack monitors the two –48 volt sources to the circuit pack. If the TOHCTL circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or power converter fails, the SYSCTL circuit pack provides +5 volts to light the FAULT LED on the TOHCTL circuit pack.

Quick Reference Summary

The TOHCTL circuit pack performs the following control and interface functions:

- a. Terminates up to eight OC-3/two OC-12 section overhead channels.
- b. Provides communications with the OC-3/OC-12 (1.3 STD) circuit packs via the overhead channel and DCC interface
- c. Performs low-level (layers 1 through 3 of the OSI 7-layer stack) protocol processing for the OC-3/OC-12 section DCC
- d. Provides communications with the OHCTL (TERM) and SYSCTL circuit packs using the OALAN interface
- e. Controls the circuit pack faceplate FAULT LED.

OW (LAA14) Circuit Pack Description

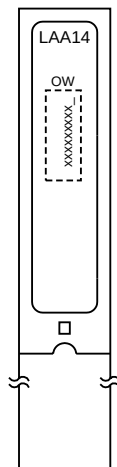
Purpose of Circuit

The Orderwire (OW) circuit pack provides access to the OC-48 SONET section overhead channel. The overhead channel contains the SONET section overhead bytes.

One LAA14 OW circuit pack is optional for the FT-2000 OC-48 Repeater Bay. However, the LAA14 OW circuit pack is required for orderwire access at repeater sites. The LAA14 OW circuit pack is compatible with Release 2.1.0-RPTR and later software.

Faceplate

Figure 7-11 shows the OW circuit pack faceplate. The faceplate of the OW circuit pack does not have a red FAULT LED.



tpa 847833/01

Figure 7-11. OW (LAA14) Circuit Pack

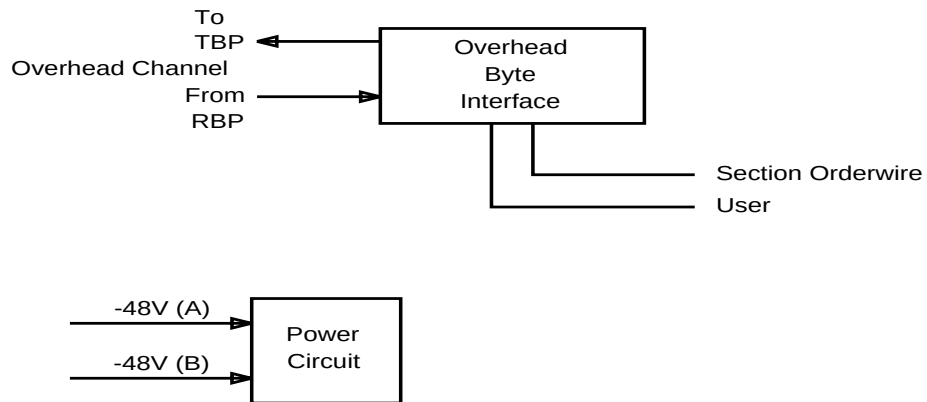
General Description of Operation

The OW circuit pack assembles and disassembles the embedded information for the SONET section overhead bytes. These overhead bytes include section orderwire bytes and user bytes. Refer to Appendix A, "A SONET Overview," for definitions of SONET sections, lines, and paths.

Detailed Description of Operation

Overhead Byte Interface

Figure 7-12 provides an overall block diagram of the OW circuit pack.



tpa 847834/01

Figure 7-12. OW Circuit Pack Block Diagram

The overhead byte interface interacts with the SONET section overhead channel. The overhead byte interface accepts the following overhead bytes from the interconnection panel of the Repeater Shelf - System Controller:

- Section orderwire
- User.

The overhead byte interface multiplexes this information and sends it via the overhead channel to the transmit byte processor (TBP) on the OC48 Regenerator (OC48 REGENR) circuit pack for transmission over the OC-48 high speed line. This information provides end-to-end communications and maintenance.

In the receive direction, the receive byte processor (RBP) on the OC48 Regenerator (OC48 REGENR) circuit pack transmits the overhead bytes via the overhead channel to the overhead byte interface. The overhead byte interface then demultiplexes the overhead bytes and makes them available for use.

Table 7-4 shows the overhead bytes that are provided by the OW circuit pack.

Table 7-4. Section Overhead Bytes

Name	Section Overhead Byte	Data Rate	Access
Orderwire	E1	64 kb/s	Interconnection Panel
User	F1	64 kb/s	Interconnection Panel

Power Circuitry

The OW circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack.

Quick Reference Summary

The OW circuit pack performs the following control and interface functions:

- a. Provides communications between the transmit byte processor on the OC48 REGENR circuit pack and the receive byte processor on the OC48 REGENR circuit pack via the overhead channel
- b. Combines the section orderwire and user bytes to form the transmit overhead channel
- c. Splits the received overhead channel into the section orderwire and user bytes
- d. Sends/receives the section orderwire and user bytes to/from the interconnection panel.

Transmission

DS3 (LAA2/LAA2B) Circuit Pack Description

Purpose of Circuit

The DS3 Interface (DS3) circuit pack provides a low speed interface between three asynchronous DS3 signals and an internal synchronous 155.52 Mb/s signal. The LAA2/LAA2B DS3 (S1:1) circuit pack is compatible with Release 3.0.1-ADR software only. The LAA2/LAA2B DS3 (S4:4) circuit pack is compatible with Release 3.0.1-ADR or later software.

Faceplate LED

The DS3 circuit pack has a red FAULT LED on its faceplate (Figure 7-13). The red FAULT LED is continuously lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the Line Controller (LNCTL) circuit pack to light the FAULT LED on the DS3 circuit pack.

The board controller on the DS3 circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the FAULT LED on the circuit pack is continuously lighted. If an incoming DS3 signal from the DSX-3 panel (or equivalent) fails, the FAULT LED will flash on and off.

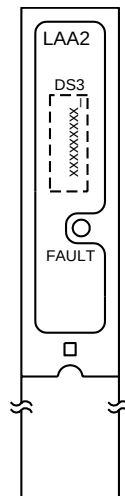


Figure 7-13. DS3 (LAA2/LAA2B) Circuit Pack

General Description of Operation

The DS3 circuit pack terminates three bidirectional DS3 lines complying with standard DSX-3 signal specifications. The three DS3 signals are multiplexed to one 155.52 Mb/s signal and sent to the OC48 Transmitter (OC48 TRMTR) circuit pack. In the receive direction, the DS3 circuit pack accepts one 155.52 Mb/s signal from an OC48 Receiver (OC48 RCVR) circuit pack and demultiplexes it to three DS3 signals. The DS3 circuit pack also interfaces with the Line Controller (LNCTL) circuit pack for that particular OC-48 high speed line.

Detailed Description of Operation

Transmission Circuitry

Transmit Direction. Figure 7-14 provides an overall block diagram of the DS3 circuit pack.

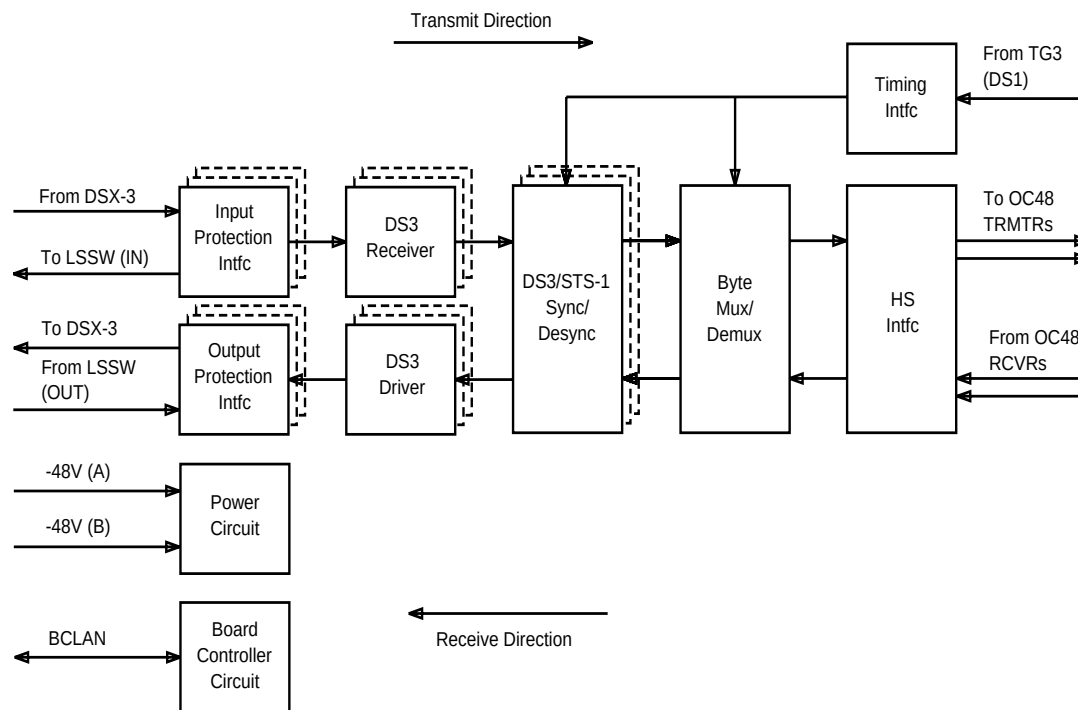


Figure 7-14. DS3 Circuit Pack Block Diagram

In the transmit direction, the incoming DS3 signals can be either formatted or unformatted (clear channel). The DS3 circuit pack accepts up to three 44.736 Mb/s bipolar 3-zero substitution (B3ZS) coded DS3 signals. These DS3 signals are split by the input protection interface. The outputs of the input protection interface are connected to the DS3 receiver and the Low Speed Switch (LSSW IN) circuit pack (for protection). The DS3 receiver recovers timing from the B3ZS-coded DS3 signals and sends the received data and recovered clock to the DS3/STS-1 synchronizer/desynchronizer. The DS3/STS-1 synchronizer/desynchronizer converts each DS3 to an STS-1 format (51.84 Mb/s). The STS-1 path overhead and pointer bytes are also added to the STS-1 signal. The byte multiplexer/demultiplexer then scrambles and multiplexes the three STS-1 signals into one 155.52 Mb/s signal. The high speed interface sends the 155.52 Mb/s signal to the OC48 TRMTR circuit packs.

Receive Direction. In the receive direction, the high speed interface accepts the 155.52 Mb/s signal from the OC48 Receiver (OC48 RCVR) circuit pack and sends it to the byte multiplexer/demultiplexer. The byte multiplexer/demultiplexer recovers timing, descrambles, and demultiplexes the 155.52 Mb/s signal into three STS-1 signals. The three STS-1 signals are then sent to the DS3/STS-1 synchronizer/desynchronizer. Each STS-1 signal goes through STS-1 pointer processing, and the STS-1 path overhead is removed and processed. After each DS3 signal is recovered from the STS-1 payload envelope, the DS3 P-bits may be monitored and corrected through a provisionable violation, monitor, and removal function with optional alarm indication signals (AIS). The DS3/STS-1 synchronizer/desynchronizer sends three B3ZS-coded DS3 signals and a 44.736 MHz clock to the DS3 driver. The DS3 driver provides three standard B3ZS-coded DS3 signals to the output protection interface. The output protection interface selects the signals from the DS3 driver or the Low Speed Switch (LSSW OUT) circuit pack (for protection) and outputs then to the DSX-3 panel (or equivalent).

The four provisionable modes are:

- Violation monitoring and removal (VMR): In this mode, DS3 P-bit (parity) violation monitoring and removal are performed on the outgoing (toward the cross-connect panel) DS3 signals.
- Violation monitoring (VM) without removal: In this mode, DS3 P-bit (parity) violation monitoring is performed on the outgoing DS3 signals, but no corrections are made.
- Clear channel (CC) with AIS insertion: In this mode, outgoing DS3 signals are passed through without any violation monitoring. If an incoming loss of signal is detected, a DS3 AIS signal is inserted.
- CC without AIS insertion: In this mode, outgoing DS3 signals are passed through without any violation monitoring. If an incoming loss of signal is detected, no DS3 AIS signal is inserted.

Control Circuitry

The board controller circuit controls all the circuit pack activities. The board controller circuit interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN). The DS3 circuit pack reports the status of the circuit pack and the incoming DS3 and 155.52 Mb/s signals, as well as the circuit pack inventory information (CLEI code, serial number, etc.). The LNCTL circuit pack uses the status information for fault detection and isolation. The DS3 circuit pack also responds to control signals from the LNCTL circuit pack.

Timing Circuitry

The DS3 circuit pack accepts two 155.52 MHz clock signals from the Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs (primary and secondary). During normal operation, the board controller selects the clock signal that is used. The selected clock signal (155.52 MHz) is connected to the byte multiplexer/demultiplexer that provides a divide-by-3 circuit. The divide-by-3 circuit generates a 51.84 MHz signal and sends it to an 8 KHz synchronization generator that is part of the high speed interface. The 8 KHz synchronization signal and 51.84 MHz clock signals are connected to each DS3/STS-1 synchronizer/desynchronizer.

Protection Circuitry

Low Speed Interface Protection. Optional 1xM ($M \leq 16$) revertive DS3 circuit pack protection is provided. A DS3 protection bus is implemented with on-board relays on the DSX-3 side and with selectors at the 155.52 Mb/s level on the service and protection OC48 Transmitter and OC48 Receiver circuit packs. The Low Speed Switch (LSSW) circuit pack provides power to these relays and controls their operation. If the +5 volt power on the DS3 circuit pack fails, the relays are still operational and controlled by the LSSW and LNCTL circuit packs. If the LNCTL circuit pack is removed, the relays will remain in their current state. Shorting contacts are provided in the DS3 circuit pack backplane connector so that when the circuit pack is removed, the DSX-3 cables short through to the LSSW circuit packs for routing to the protection DS3 circuit pack. When the DS3 circuit pack is reinserted, the relays are in the protection state until the LNCTL circuit pack determines that the circuit pack is good.

High Speed Protection. If an OC-48 signal fails or is degraded, the LNCTL circuit pack broadcasts a switch request to the board controllers on the DS3 circuit packs via the BCLAN. In the transmit direction, the DS3 circuit packs perform head-end bridges. In the FT-2000 OC-48 Add/Drop-Rings Terminal, the 155.52 Mb/s signals are provided to the OC48 TRMTR A/D circuit packs for lines 1E and 1W at all times. In the receive direction, the DS3 circuit packs perform tail-end switches. In the FT-2000 OC-48 Add/Drop-Rings Terminal, selector circuitry on the DS3 circuit packs accepts the 155.52 Mb/s signals from the OC48 RCVR A/D circuit pack opposite of the configured direction.

Dual Ring Interworking Protection. The DS3 circuit pack at the primary node of a dual ring interworking application monitors the DS3 signal(s) coming from the other ring. If an incoming DS3 loss-of-signal condition is detected, the DS3 circuit pack notifies the LNCTL circuit pack. The LNCTL circuit pack broadcasts an automatic switch request to the board controller on the OC48 TRMTR circuit pack via the BCLAN. In the add (toward to OC-48 line) direction only, the OC48 TRMTR circuit pack switches to (selects) the standby 155.52 Mb/s signal (STS-3 tributary) from the secondary node. In Release 7 and later releases, the OC48 TRMTR circuit pack may switch to (select) the standby 51.84 Mb/s signal (STS-1 tributary) from the secondary node. This completes the circuit from the secondary node to the termination node in the ring.

Synchronization Reference Protection. Synchronization reference switching is done by selecting the primary or secondary synchronization reference signal from the TG3 (DS1) circuit packs. The DS3 circuit pack monitors these synchronization reference signals and can autonomously select the primary or secondary. The LNCTL circuit pack can inhibit this autonomous selection and make its own selection.

Fault Detection Circuitry

Monitoring and Testing. The board controller circuit monitors all the activities on the circuit pack. The DS3 circuit pack has an in-service and out-of-service built-in test capability. In-service testing is continuous. If an error occurs, the board controller reports the error to the LNCTL circuit pack using the board controller local area network (BCLAN). An out-of-service test is performed whenever the DS3 circuit pack is inserted in a slot or reset.

Loopbacks. The DS3 circuit pack provides one DS3 loopback for each DS3 signal. The loopback is implemented inside the DS3/STS-1 synchronizer/desynchronizer device. It bridges the desynchronizer output signal (transmitted towards the DSX-3 panel) back into the DS3 synchronizer input. The loopback is implemented in response to CIT commands (via the SYSCTL and LNCTL circuit packs) and controlled by board controller.

Performance Monitoring. The DS3 circuit pack provides performance-monitoring circuitry for the following performance-monitoring parameters:

- DS3 line errored seconds
- DS3 path coding violations
- DS3 path errored seconds
- DS3 path severely errored seconds
- DS3 path severely errored frame/AIS seconds
- DS3 path unavailable seconds
- STS-1 path coding violations
- STS-1 path errored seconds
- STS-1 path severely errored seconds
- STS-1 path unavailable seconds
- Far-end STS-1 path coding violations
- Far-end STS-1 path errored seconds
- Far-end STS-1 path severely errored seconds
- Far-end STS-1 path unavailable seconds.

The DS3 line errored seconds parameter is measured on DS3 signals from the DSX-3 cross-connect panel, and all other performance-monitoring parameters are measured on the 155.52 Mb/s signals from the OC48 RCVR circuit pack.

The DS3 line errored seconds count is based only on B3ZS coding violations and bipolar violations.

In releases prior to 5, the DS3 path coding violations, errored seconds, severely errored seconds, severely errored frame/AIS seconds, and unavailable seconds counts are based on P-bit errors. In Release 5 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal may be provisioned to accumulate DS3 path performance-monitoring data based on P-bit errors or F and M framing bit errors. The raw F and M framing bit error counts are adjusted to approximate the actual error count in the DS3 path payload.

When an incoming line AIS, high speed defect, or internal fault is detected, the DS3 circuit pack inserts a remote defect indicator-path (RDI-P) signal in the 155.52 Mb/s signal transmitted toward the OC48 TRMTR circuit pack.

The DS3 circuit pack also inserts and detects STS-1 path trace and path signal label signals.

Power Circuitry

The DS3 circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack. The LSSW circuit packs supply power to the input and output protection relays on the DS3 circuit packs.

If the DS3 circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or power converter fails, the LNCTL circuit pack provides +5 volts to light the FAULT LED on the DS3 circuit pack.

Quick Reference Summary

Transmit Functions

The DS3 circuit pack performs the following transmit functions:

- a. Receives three B3ZS-coded DS3 signals (44.736 Mb/s) from a DSX-3 panel (or equivalent)
- b. Recovers DS3 clock from each DS3 signal
- c. Synchronizes and maps each DS3 signal to an STS-1 (51.84 Mb/s) synchronous payload envelope (SPE)
- d. Inserts STS-1 path overhead and pointer bytes
- e. Multiplexes three STS-1 signals to a 155.52 Mb/s signal
- f. Provides the 155.52 Mb/s signal to an OC48 TRMTR circuit pack.

Receive Functions

The DS3 circuit pack performs the following receive functions:

- a. Receives a 155.52 Mb/s signal from an OC48 RCVR circuit pack
- b. Demultiplexes the 155.52 Mb/s into three STS-1 signals
- c. Extracts the path overhead and the embedded DS3 signal from each STS-1 signal
- d. Desynchronizes the DS3 signals
- e. Encodes each DS3 signal into a B3ZS format
- f. Provides three DS3 signals to a DSX-3 panel (or equivalent).

Timing and Control Functions

The DS3 circuit pack performs the following timing and control functions:

- a. Selects the primary or secondary 155.52 MHz timing signal from the TG3 (DS1) circuit packs
- b. Provides relays for low speed protection switching
- c. Provides selector circuitry for high speed protection switching
- d. Performs internal fault detection and interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN)
- e. Provides one loopback for each DS3 signal
- f. Stores inventory information (CLEI code, serial number, etc.)
- g. Controls the circuit pack faceplate FAULT LED.

Maintenance Signal Functions

The DS3 circuit pack performs the following maintenance signal functions:

- a. Provides a provisionable violation, monitor, and removal function with optional DS3 AIS insertion toward the DSX-3 panel (or equivalent)
- b. Detects embedded DS3 AIS in the 155.52 Mb/s signal from the OC48 RCVR circuit pack
- c. Detects STS-1 path AIS in the 155.52 Mb/s signal from the OC48 RCVR circuit pack
- d. Detects RDI-P signals in the 155.52 Mb/s signal from the OC48 RCVR circuit pack
- e. Inserts an RDI-P signal in the 155.52 Mb/s signal transmitted toward the OC48 TRMTR circuit pack
- f. Inserts an STS-1 path trace and path signal label signals in the 155.52 Mb/s signal transmitted toward the OC48 TRMTR circuit pack
- g. Detects an STS-1 path trace and path signal label signals in the 155.52 Mb/s signal from the OC48 RCVR circuit pack.

STS1E (LAA4) Circuit Pack Description

Purpose of Circuit

The STS-1 Electrical Interface (STS1E) circuit pack provides a low speed interface between three synchronous EC-1 signals and an internal synchronous 155.52 Mb/s signal. The STS1E circuit pack is compatible with Release 4.0.0-ADR and later software.

Faceplate LED

The STS1E circuit pack has a red FAULT LED on its faceplate (Figure 7-15). The red FAULT LED is continuously lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the Line Controller (LNCTL) circuit pack to light the FAULT LED on the STS1E circuit pack.

The board controller on the STS1E circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the FAULT LED on the circuit pack is continuously lighted. If an incoming EC-1 signal from the STSX-1 panel (or equivalent) fails, the FAULT LED will flash on and off.

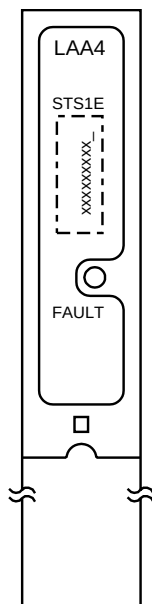


Figure 7-15. STS1E (LAA4) Circuit Pack

General Description of Operation

The STS1E circuit pack terminates three bidirectional EC-1 lines complying with standard STSX-1 signal specifications. The three 51.84 Mb/s EC-1 signals are multiplexed to one 155.52 Mb/s signal and sent to the OC48 Transmitter (OC48 TRMTR) circuit pack. In the receive direction, the STS1E circuit pack accepts one 155.52 Mb/s signal from an OC48 Receiver (OC48 RCVR) circuit pack and demultiplexes it to three 51.84 Mb/s EC-1 signals. The STS1E circuit pack also interfaces with the Line Controller (LNCTL) circuit pack for that particular OC-48 high speed line.

Detailed Description of Operation

Transmission Circuitry

Transmit Direction . Figure 7-16 provides an overall block diagram of the STS1E circuit pack.

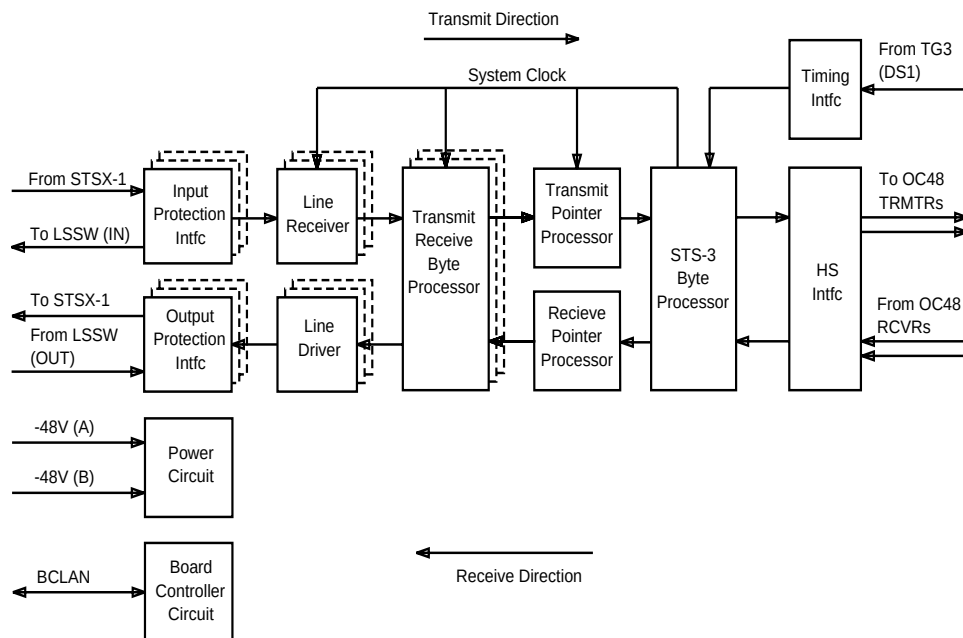


Figure 7-16. STS1E Circuit Pack Block Diagram

In the transmit direction, the STS1E circuit pack accepts up to three 51.84 Mb/s bipolar 3-zero substitution (B3ZS) coded EC-1 signals. These EC-1 signals are split by the input protection interface. The outputs of the input protection interface are connected to the line receiver and the Low Speed Switch (LSSW IN) circuit pack (for protection). The line receiver recovers timing from the B3ZS-coded EC-1 signals and sends the EC-1 signals (as STS-1 signals) with the recovered clock to the transmit/receive byte processor. The transmit/receive byte processor performs B3ZS decoding, framing, descrambling, and overhead byte processing on the received STS-1 signals. The transmit/receive byte processor then sends the clock and STS-1 signals to the transmit pointer processor. The transmit pointer processor performs frequency adjustment and frame alignment on the clock and STS-1 signals using the 51.84 MHz system clock. The system clock is obtained by locally dividing the 155.52 MHz clock signal from the TG3 (DS1) circuit pack by three. The pointer processor sends the clock and STS-1 signals to the byte processor. The byte processor scrambles and multiplexes the three STS-1 signals into one 155 Mb/s signal. The high speed interface sends the 155.52 Mb/s signal to the OC48 TRMTR circuit packs.

Receive Direction. In the receive direction, the high speed interface accepts the 155.52 Mb/s signal from the OC48 Receiver (OC48 RCVR) circuit pack. The high speed interface recovers the 155.52 MHz clock signal, retimes the 155.52 Mb/s signal, and sends it to the byte processor. The byte processor descrambles and demultiplexes the 155.52 Mb/s signal into three 51.84 Mb/s STS-1 signals. The three STS-1 signals are then sent to the receive pointer processor. The receive pointer processor performs pointer processing on the three STS-1 signals and sends a clock and three STS-1 signals that are synchronized to the system clock to the transmit/receive byte processor. The transmit/receive byte processor performs scrambling, B3ZS encoding, and overhead byte processing. The transmit/receive byte processor then sends the clock and STS-1 signals to the line driver. The line driver converts the STS-1 signals to three standard bipolar EC-1 output signals. The line driver sends the three standard B3ZS-coded EC-1 signals to the output protection interface. The output protection interface selects the signals from the line driver or the Low Speed Switch (LSSW OUT) circuit pack (for protection) and outputs them to the STSX-1 panel (or equivalent).

Control Circuitry

The board controller circuit controls all the circuit pack activities. The board controller circuit interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN). The STS1E circuit pack reports the status of the circuit pack and the incoming EC-1 and 155.52 Mb/s signals, as well as the circuit pack inventory information (CLEI code, serial number, etc.). The LNCTL circuit pack uses the status information for fault detection and isolation. The STS1E circuit pack also responds to control signals from the LNCTL circuit pack.

Timing Circuitry

The STS1E circuit pack accepts two 155.52 MHz clock signals from the Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs (primary and secondary). During normal operation, the board controller selects the clock signal that is used. The selected clock signal (155.52 MHz) is connected to the byte processor that provides a divide-by-3 circuit. The divide-by-3 circuit generates a 51.84 MHz signal and sends it to the transmit and receive pointer processors, transmit/receive byte processor, and the line driver circuits.

Protection Circuitry

Low Speed Interface Protection. Optional 1xM ($M \leq 16$) revertive STS1E circuit pack protection is provided. An STS-1 protection bus is implemented with on-board relays on the STSX-1 side and with selectors at the 155.52 Mb/s level on the service and protection OC48 Transmitter and OC48 Receiver circuit packs. The Low Speed Switch (LSSW) circuit pack provides power to these relays and controls their operation. If the +5 volt power on the STS1E circuit pack fails, the relays are still operational and controlled by the LSSW and LNCTL circuit packs. If the LNCTL circuit pack is removed, the relays will remain in their current state. Shorting contacts are provided in the STS1E circuit pack backplane connector so that when the circuit pack is removed, the STSX-1 cables short through to the LSSW circuit packs for routing to the protection STS1E circuit pack. When the STS1E circuit pack is reinserted, the relays are in the protection state until the LNCTL circuit pack determines that the circuit pack is good.

High Speed Protection. If an OC-48 signal fails or is degraded, the LNCTL circuit pack broadcasts a switch request to the board controllers on the STS1E circuit packs via the BCLAN. In the transmit direction, the STS1E circuit packs perform head-end bridges. In the FT-2000 OC-48 Add/Drop-Rings Terminal, the 155.52 Mb/s signals are provided to the OC48 TRMTR A/D circuit packs for lines 1E and 1W. In the receive direction, the STS1E circuit packs perform tail-end switches. In the FT-2000 OC-48 Add/Drop-Rings Terminal, selector circuitry on the STS1E circuit packs accepts the 155.52 Mb/s signals from the OC48 RCVR A/D circuit pack opposite of the configured direction.

Dual Ring Interworking Protection. The STS1E circuit pack at the primary node of a dual ring interworking application monitors the EC-1 signal(s) coming from the other ring. If an incoming EC-1 loss-of-signal condition is detected, the STS1E circuit pack notifies the LNCTL circuit pack. The LNCTL circuit pack broadcasts an automatic switch request to the board controller on the OC48 TRMTR circuit pack via the BCLAN. In the add (toward to OC-48 line) direction only, the OC48 TRMTR circuit pack switches to (selects) the standby 155.52 Mb/s signal (STS-3 tributary) from the secondary node. In Release 7 and later releases, the OC48 TRMTR circuit pack may switch to (select) the standby 51.84 Mb/s signal (STS-1 tributary) from the secondary node. This completes the circuit from the secondary node to the termination node in the ring.

Synchronization Reference Protection. Synchronization reference switching is done by selecting the primary or secondary synchronization reference signal from the TG3 (DS1) circuit packs. The STS1E circuit pack monitors these synchronization reference signals and can autonomously select the primary or secondary. The LNCTL circuit pack can inhibit this autonomous selection and make its own selection.

Fault Detection Circuitry

Monitoring and Testing. The board controller circuit monitors all the activities on the circuit pack. The STS1E circuit pack has an in-service and out-of-service built-in test capability. In-service testing is continuous. If an error occurs, the board controller reports the error to the LNCTL circuit pack using the board controller local area network (BCLAN). An out-of-service test is performed whenever the STS1E circuit pack is inserted in a slot or when a reset is performed.

Loopbacks. The STS1E circuit pack provides one EC-1 loopback for each EC-1 signal. The loopback is implemented inside the transmit/receive byte processor device. It bridges the output signal (transmitted towards the STSX-1 panel) back into the transmit/receive byte processor input. The loopback is implemented in response to CIT commands (via the SYSCTL and LNCTL circuit packs) and controlled by board controller.

Performance Monitoring. The STS1E circuit pack provides performance-monitoring circuitry for the following performance-monitoring parameters:

- EC-1 section severely errored frame seconds
- EC-1 line coding violations
- EC-1 line errored seconds
- EC-1 line severely errored seconds
- EC-1 line unavailable seconds

When an incoming EC-1 signal defect from the STSX-1 cross-connect panel is detected, the STS1E circuit pack inserts STS-1 path AIS in each STS-1 tributary of the 155.52 Mb/s signal transmitted toward the OC-48 TRMTR circuit pack.

When an incoming OC-48 signal defect is detected by the OC48 RCVR circuit pack or an STS-1 loss of pointer is detected by the STS1E circuit pack in the 155.52 Mb/s signal received from the OC48 RCVR circuit pack, the STS1E circuit pack inserts STS-1 path AIS in the EC-1 signal transmitted toward the STSX-1 cross-connect panel.

When an internal fault is detected, the STS1E circuit pack inserts STS-1 path AIS in each STS-1 tributary of the 155.52 Mb/s signal transmitted toward the OC48 TRMTR circuit pack and in the EC-1 signals transmitted toward the STSX-1 cross-connect panel.

Power Circuitry

The STS1E circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack. The LSSW circuit packs supply power to the input and output protection relays on the STS1E circuit packs.

If the STS1E circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or power converter fails, the LNCTL circuit pack provides +5 volts to light the FAULT LED on the STS1E circuit pack.

Quick Reference Summary

Transmit Functions

The STS1E circuit pack performs the following transmit functions:

- a. Receives three B3ZS-coded EC-1 signals (51.84 Mb/s) from an STSX-1 panel (or equivalent)
- b. Recovers an EC-1 clock from each EC-1 signal
- c. Converts each EC-1 signal to an internal STS-1 signal
- d. Inserts pointer bytes
- e. Multiplexes three STS-1 signals to a 155.52 Mb/s signal
- f. Provides the 155.52 Mb/s signal to an OC48 TRMTR circuit pack.

Receive Functions

The STS1E circuit pack performs the following receive functions:

- a. Receives a 155.52 Mb/s signal from an OC48 RCVR circuit pack
- b. Demultiplexes the 155.52 Mb/s into three STS-1 signals
- c. Extracts the pointer bytes
- d. Converts each internal STS-1 signal to an EC-1 signal
- e. Provides three standard B3ZS-encoded EC-1 signals to an STSX-1 panel (or equivalent).

Timing and Control Functions

The STS1E circuit pack performs the following timing and control functions:

- a. Selects the primary or secondary 155.52 MHz timing signal from the TG3 (DS1) circuit packs
- b. Provides relays for low speed protection switching
- c. Provides selector circuitry for high speed protection switching
- d. Performs internal fault detection and interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN)
- e. Provides one loopback for each EC-1 signal
- f. Stores inventory information (CLEI code, serial number, etc.)
- g. Controls the circuit pack faceplate FAULT LED.

Maintenance Signal Functions

The STS1E circuit pack performs the following maintenance signal functions:

- a. Detects incoming (from the cross-connect panel) EC-1 loss of signal (LOS), loss of frame (LOF), loss of pointer (LOP), signal degrade (SD) based on the B2 error rate, and EC-1 line AIS
- b. Inserts STS-1 path AIS in each STS-1 tributary of the 155.52 Mb/s signal transmitted toward the OC48 TRMTR circuit pack when incoming (from the cross-connect panel) EC-1 LOS, LOF, LOP, or EC-1 line AIS is detected
- c. Detects loss of pointer (LOP) in the 155.52 Mb/s signal from the OC48 RCVR circuit pack and inserts STS-1 path AIS in the EC-1 signals transmitted toward the cross-connect panel
- d. Inserts STS-1 path AIS in each STS-1 tributary of the 155.52 Mb/s signal transmitted toward the OC48 TRMTR circuit pack and in the EC-1 signals transmitted toward the cross-connect panel when an internal fault is detected.

OC3 (LAA10/LAA10B) Circuit Pack Description

Purpose of Circuit

The OC3 Optical Interface - 1.3 μ m Standard Performance [OC3 (1.3 STD)] circuit pack provides a bidirectional low speed interface between one synchronous OC-3 optical signal (155.52 Mb/s) and an internal synchronous STS-3/3c signal (155.52 Mb/s). The OC3 circuit pack is compatible with Release 5.0.1-ADR and later software only.



NOTE:

The OC3 (1.3 STD) LAA10B circuit pack is backward-compatible with the current OC3 (1.3 STD) LAA10 circuit pack. The OC3 LAA10B circuit pack provides all the maintenance, provisioning, and performance monitoring features of the OC3 LAA10 circuit pack.



NOTE:

The FT-2000 ADR Terminal does not support mixed LAA10/LAA10B operation in a 1+1 protected configuration. In a 1+1 configuration, all adjacent LS INTFC slots (for example, slots 2A and 2B) **MUST** be equipped with either LAA10 circuit packs or LAA10B circuit packs (but not a mix of the two) in both slots **IF** that slot pair is configured for 1+1 protection.

Faceplate LED

The OC3 (1.3 STD) circuit pack has a red FAULT LED on its faceplate (Figure 7-17). The red FAULT LED is continuously lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the Line Controller (LNCTL) circuit pack to light the FAULT LED on the OC3 (1.3 STD) circuit pack.

The board controller on the OC3 (1.3 STD) circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the FAULT LED on the circuit pack is continuously lighted. If an incoming OC-3 signal from the lightguide cross-connect panel (or equivalent) fails, the FAULT LED will flash on and off.

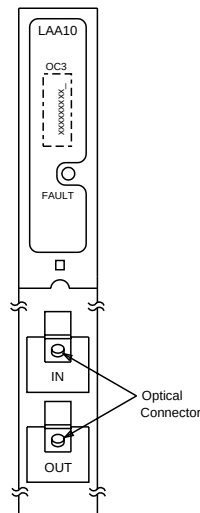


Figure 7-17. OC3 (1.3 STD) (LAA10/LAA10B) Circuit Pack

General Description of Operation

The OC3 (1.3 STD) circuit pack terminates one bidirectional OC-3 line. In the transmit direction (toward the OC-48 line), the OC3 (1.3 STD) circuit pack converts the incoming OC-3 optical signal to an electrical STS-3/3c signal (155.52 Mb/s). The STS-3/3c signal is demultiplexed into three STS-1 signals (51.84 Mb/s) and synchronized to the system clock. The three STS-1 signals are then multiplexed back to one STS-3/3c signal and sent to an OC48 Transmitter (OC48 TRMTR) circuit pack. In the receive direction (toward the OC-3 line), the OC3 (1.3 STD) circuit pack accepts one STS-3/3c signal from an OC48 Receiver (OC48 RCVR) circuit pack and demultiplexes it to three STS-1 signals. The three STS-1 signals are synchronized to the system clock, multiplexed back to one STS-3/3c signal, and converted to an optical OC-3 signal for transmission. The OC3 (1.3 STD) circuit pack also interfaces with the Line Controller (LNCTL) circuit pack for that particular OC-48 high speed line.

Detailed Description of Operation

Transmission Circuitry

Transmit Direction. Figure 7-18 provides an overall block diagram of the OC3 (1.3 STD) LAA10 circuit pack. Figure 7-19 shows an overall block diagram of the OC3 (1.3 STD) LAA10B circuit pack. To take advantage of the additional capabilities of the LAA10B circuit pack, you must be running Release 9.0 or later software.

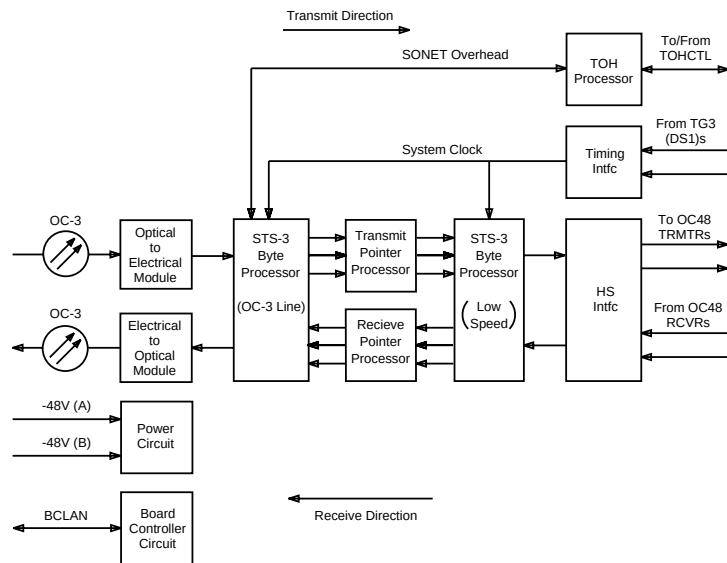


Figure 7-18. OC3 (1.3 STD) LAA10 Circuit Pack Block Diagram

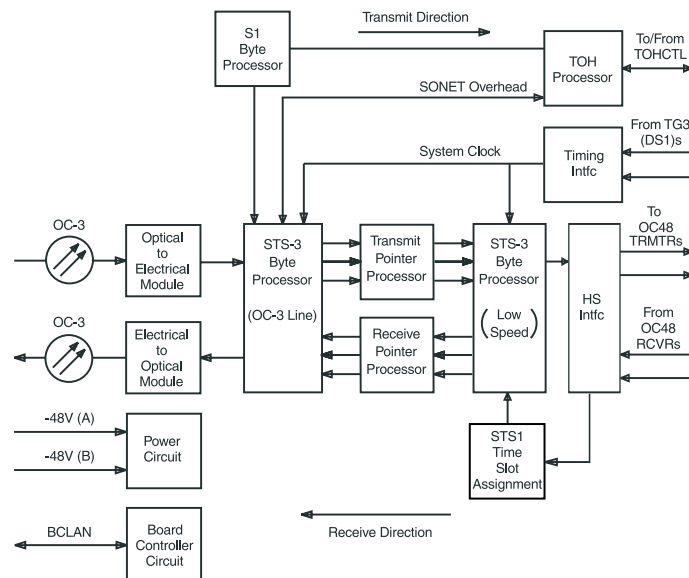


Figure 7-19. OC3 (1.3 STD) LAA10B Circuit Pack Block Diagram

In the transmit direction (toward the OC-48 line), the OC3 (1.3 STD) circuit pack accepts one OC-3 (155.52 Mb/s) non-return-to-zero (NRZ) synchronous optical network (SONET) compatible optical signal. Fiber access to the OC3 (1.3 STD) circuit pack is via a fixed *ST*[®]-type buildout block and a removable *ST*-type, FC/PC-type, or SC-type lightguide buildout on the circuit pack faceplate (labeled IN). Lightguide buildouts are chosen based on the attenuation desired, the type of connector interface, and the type of lightguide jumpers (single-mode or multimode). All factory-equipped OC3 (1.3 STD) circuit packs come with a removable *ST*-type 0-dB lightguide buildout. When installing or removing lightguide buildouts, do not pull the beam (front tab) outward. Pulling the beam (front tab) outward could result in breaking the beam (front tab).

The optical-to-electrical module receives the OC-3 signal, converts it to an electrical STS-3/3c signal (155.52 Mb/s), and sends it to a timing recovery circuit. The timing recovery circuit recovers a 155.52 MHz clock signal from the STS-3/3c signal and retimes the STS-3/3c signal with the recovered clock signal. The STS-3/3c and clock signals are then sent to the OC-3 line STS-3 byte processor. The OC-3 line STS-3 byte processor descrambles and demultiplexes the STS-3/3c signal into three STS-1 signals (51.84 Mb/s) and outputs the three STS-1 signals and a common clock signal to the transmit pointer processor.

The transmit pointer processor performs frequency adjustment and pointer processing on the three received STS-1 signals. This synchronizes the STS-1 signals to the 51.84 MHz system clock. The 51.84 MHz clock signal is obtained by dividing the 155.52 MHz from the Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs by three. The divide-by-three circuit is part of the low speed STS-3 byte processor. The three STS-1 signals are then sent to the low speed STS-3 byte processor.

The low speed STS-3 byte processor scrambles and multiplexes the three STS-1 signals back to one STS-3/3c signal (155.52 Mb/s). The high speed interface distributes the STS-3/3c signal to the OC48 TRMTR circuit packs.

Receive Direction. In the receive direction (toward the OC-3 line), the high speed interface of a LAA10 OC-3 circuit pack accepts an STS-3/3c signal (155.52 Mb/s) from one of the OC48 RCVR circuit packs and sends it to a timing recovery circuit. The timing recovery circuit recovers a 155.52 MHz clock signal from the STS-3/3c signal and retimes the STS-3/3c signal with the recovered clock signal.

In the receive direction (toward the OC-3 line), the high-speed interface of a LAA10B OC-3 circuit pack accepts two STS-3/3c signals (155.52 Mb/s) from both of the OC48 RCVR circuit packs and sends them to two timing recovery circuits. The timing recovery circuits recover a 155.52 MHz clock signal from each STS-3/3c signal and retime the STS-3/3c signals with the recovered clock signals. The two retimed STS-3/3c signals are then demultiplexed into six STS-1 signals. Three of these STS-1 signals are chosen depending on system provisioning and multiplexed into a STS-3/3c signal.

The STS-3/3c signal and clock signals are then sent to the low-speed STS-3 byte processor. The low-speed STS-3 byte processor descrambles and demultiplexes

the STS-3/3c signal into three STS-1 signals (51.84 Mb/s) and outputs the three STS-1 signals and a common clock signal to the receive pointer processor.

The receive pointer processor performs frequency adjustment and pointer processing on the three received STS-1 signals. This synchronizes the STS-1 signals to the 51.84 MHz system clock. The 51.84 MHz clock signal is obtained by dividing the 155.52 MHz from the TG3 (DS1) circuit pack by three. The divide-by-three circuit for the receive side is part of the OC-3 line STS-3 byte processor. The three STS-1 signals are then sent to the OC-3 line STS-3 byte processor.

The OC-3 line STS-3 byte processor scrambles and multiplexes the three STS-1 signals back to one STS-3/3c signal (155.52 Mb/s). The electrical-to-optical module receives the STS-3/3c signal and converts it to an OC-3 optical signal (155.52 Mb/s) for transmission. Fiber access to the OC3 (1.3 STD) circuit pack is via a fixed *ST*-type buildout block and a removable *ST*-type, FC/PC-type, or SC-type lightguide buildout on the circuit pack faceplate (labeled OUT). Lightguide buildouts are chosen based on the attenuation desired, the type of connector interface, and the type of lightguide jumpers (single-mode or multimode). All factory-equipped OC3 (1.3 STD) circuit packs come with a removable *ST*-type 7.1-dB lightguide buildout. When installing or removing lightguide buildouts, do not pull the beam (front tab) outward. Pulling the beam (front tab) outward could result in breaking the beam (front tab).

Control Circuitry

The board controller circuit controls all the circuit pack activities. The board controller circuit interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN). The OC3 (1.3 STD) circuit pack reports the status of the circuit pack and the incoming OC-3 and STS-3/3c signals, as well as the circuit pack inventory information (CLEI code, serial number, etc.). The LNCTL circuit pack uses the status information for fault detection and isolation. The OC3 (1.3 STD) circuit pack also responds to control signals from the LNCTL circuit pack.

Overhead Controller Circuitry

In Release 6 and later releases, the transport overhead hyperchannel (TOH) circuitry provides access to the data communications channel (DCC) of the OC-3 line. Access is only provided to the D1-D3 overhead bytes of the DCC. In the LAA10B OC-3 circuit pack, the transport overhead hyperchannel circuitry also inserts the S1 Byte for synchronization messaging in the receive direction (toward the OC-3 line).

Timing Circuitry

The OC3 (1.3 STD) circuit pack accepts one 155.52 MHz clock signal from each of the two Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs (primary and secondary). During normal operation, the timing interface circuitry selects the clock signal that is used and automatically switches to the other clock signal if it detects a loss of signal. The board controller can also override the automatic clock selection mode and force either clock signal to be selected.

The selected clock signal (155.52 MHz) is connected to the OC-3 line STS-3 byte processor and the low speed STS-3 byte processor. The STS-3 byte processors provide divide-by-three circuits that generate a 51.84 MHz signal. The 51.84 MHz signal from the divide-by-three circuit on the OC-3 line STS-3 byte processor is distributed to the receive circuitry of the OC-3 line STS-3 byte processor and the receive pointer processor. The 51.84 MHz signal from the divide-by-three circuit on the low speed STS-3 byte processor is distributed to the transmit circuitry of the low speed STS-3 byte processor and the transmit pointer processor.

Protection Circuitry

Low Speed Interface Protection . Optional 1+1 nonrevertive OC3 (1.3 STD) circuit pack protection in the transmit (toward the OC-48 line) and/or receive (toward the OC-3 line) direction is provided. For 1+1 nonrevertive protection, two OC3 (1.3 STD) circuit packs must be placed in adjacent "A" and "B" low speed interface slots of the Low Speed Shelf - System Controller (for example, LS INTFC slots 2A and 2B). Both OC3 (1.3 STD) circuit packs transmit the same OC-3 signal toward the lightguide cross-connect panel (or equivalent). Both OC3 (1.3 STD) circuit packs receive the same OC-3 signal from the lightguide cross-connect panel (or equivalent) and transmit STS-3/3c signals to the OC48 TRMTR circuit pack. The OC3 circuit packs continuously monitor the incoming active and standby OC-3 signals. If the active OC-3 signal fails, the LNCTL circuit pack broadcasts a switch request to the board controller on the OC48 TRMTR circuit pack via the BCLAN. In the transmit (toward the OC-48 line) direction only, the OC48 TRMTR circuit pack selects the standby STS-3/3c signal from the standby OC3 (1.3 STD) circuit pack. In Release 6, the OC3 (1.3 STD) circuit pack also supports 1+1 nonrevertive protection of the OC-3 data communications channels.

High Speed Protection. If an OC-48 signal fails or is degraded, the LNCTL circuit pack broadcasts a switch request to the board controllers on the OC3 (1.3 STD) circuit packs via the BCLAN. In the transmit direction, the OC3 (1.3 STD) circuit packs perform head-end bridges. In the FT-2000 OC-48 Add/Drop-Rings Terminal, the STS-3/3c signals (155.52 Mb/s) are provided to the OC48 TRMTR A/D circuit packs for lines 1E and 1W. In the receive direction, the OC3 (1.3 STD) circuit packs perform tail-end switches. In the FT-2000 OC-48 Add/Drop-Rings Terminal, selector circuitry on the OC3 (1.3 STD) circuit packs accepts the STS-3/3c signals from the OC48 RCVR A/D circuit pack opposite of the configured direction.

Dual Ring Interworking Protection. The OC3 (1.3 STD) circuit pack at the primary node of a dual ring interworking application monitors the OC-3 signal coming from the other ring. If an unprotected incoming OC-3 loss-of-signal or loss-of-frame condition is detected, the OC3 (1.3 STD) circuit pack notifies the LNCTL circuit pack. The LNCTL circuit pack broadcasts an automatic switch request to the board controller on the OC48 TRMTR circuit pack via the BCLAN. In the add (toward to OC-48 line) direction only, the OC48 TRMTR circuit pack switches to (selects) the standby 155.52 Mb/s signal (STS-3 tributary) from the secondary node. In Release 7 and later releases, the OC48 TRMTR circuit pack may switch to (select) the standby 51.84 Mb/s signal (STS-1 tributary) from the secondary node. This completes the circuit from the secondary node to the termination node in the ring.

Synchronization Reference Protection. Synchronization reference switching is done by selecting the primary or secondary synchronization reference signal from the TG3 (DS1) circuit packs. The timing interface circuitry of the OC3 (1.3 STD) circuit pack monitors these synchronization reference signals and can autonomously select the primary or secondary. The LNCTL circuit pack can inhibit this autonomous selection and make its own selection.

Fault Detection Circuitry

Monitoring and Testing. The board controller circuit monitors all the activities on the circuit pack. The OC3 (1.3 STD) circuit pack has an in-service and out-of-service built-in test capability. In-service testing is continuous. If an error occurs, the board controller reports the error to the LNCTL circuit pack using the board controller local area network (BCLAN). An out-of-service test is performed whenever the OC3 (1.3 STD) circuit pack is inserted in a slot or when a reset is performed.

Loopbacks . The OC3 (1.3 STD) circuit pack provides OC-3 and STS-3/3c loopbacks for fault isolation and remote testing. The OC-3 loopback loops the input from the OC-3 line back to the output of the OC-3 line via a loop connection in the low speed STS-3 byte processor. This loopback does not loop the OC-3 DCC overhead channel. The STS-3/3c loopback loops the STS-3/3c signal from the OC-48 line back to the STS-3/3c signal transmitted toward the OC-48 line via a loop connection in the OC-3 line STS-3 byte processor. The OC-3 and STS-3/3c loopback paths include the transmit and receive pointer processors.

These loopbacks are implemented in response to craft interface terminal (CIT) commands (via the SYSCTL and LNCTL circuit packs) and controlled by the board controller.

Performance Monitoring. The OC3 (1.3 STD) circuit pack monitors the following performance-monitoring parameters:

- Laser bias current
- Optical transmit power
- Received optical power
- Loss of signal
- Loss of frame
- Bit error rate thresholds

When an incoming OC-3 signal defect from the lightguide cross-connect panel is detected, the OC3 (1.3 STD) circuit pack inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signal (155.52 Mb/s) transmitted toward the OC-48 TRMTR circuit pack.

When an incoming OC-48 signal defect is detected by the OC48 RCVR circuit pack or an STS-1 loss of pointer is detected by the OC3 (1.3 STD) circuit pack in the 155.52 Mb/s signal received from the OC48 RCVR circuit pack, the OC3 (1.3 STD) circuit pack inserts STS-1 path AIS in each STS-1 tributary of the OC-3 signal transmitted toward the lightguide cross-connect panel.

When an internal fault is detected, the OC3 (1.3 STD) circuit pack inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signal transmitted toward the OC48 TRMTR circuit pack and in the OC-3 signal transmitted toward the lightguide cross-connect panel.

Power Circuitry

The OC3 (1.3 STD) circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack.

If the OC3 (1.3 STD) circuit pack detects the failure of the onboard fuse or power converter the red FAULT LED will light. If the fuse or power converter fails, the LNCTL circuit pack provides +5 volts to light the FAULT LED on the OC3 (1.3 STD) circuit pack.

Quick Reference Summary

Transmit Functions

The OC3 (1.3 STD) circuit pack performs the following transmit functions:

- a. Receives one OC-3 (155.52 Mb/s) non-return-to-zero (NRZ) SONET compatible optical signal from a lightguide cross-connect panel (or equivalent)
- b. Converts the optical OC-3 signal to an electrical STS-3/3c signal
- c. Recovers a 155.52 MHz clock signal from STS-3/3c signal and retimes the STS-3/3c signal with the recovered clock
- d. Descrambles and demultiplexes the STS-3/3c signal into three STS-1 signals (51.84 Mb/s)
- e. Performs frequency adjustments and pointer processing on the three STS-1 signals
- f. Scrambles and multiplexes the three STS-1 signals to an STS-3/3c signal (155.52 Mb/s)
- g. Provides the STS-3/3c signal to an OC48 TRMTR circuit pack
- h. Accepts the OC-3 SONET section DCC bytes from the TOHCTL circuit pack (Release 6 and later).

Receive Functions

The OC3 (1.3 STD) circuit pack performs the following receive functions:

- a. Receives an STS-3/3c signal (155.52 Mb/s) from an OC48 RCVR circuit pack
- b. Recovers a 155.52 MHz clock signal from STS-3/3c signal and retimes the STS-3/3c signal with the recovered clock
- c. Descrambles and demultiplexes the STS-3/3c signal into three STS-1 signals (51.84 Mb/s)
- d. Performs frequency adjustments and pointer processing on the three STS-1 signals
- e. Scrambles and multiplexes the three STS-1 signals to an STS-3/3c signal (155.52 Mb/s)
- f. Converts the electrical STS-3/3c signal to an optical OC-3 signal
- g. Provides the OC-3 optical signal to a lightguide cross-connect panel (or equivalent).
- h. Provides the OC-3 SONET section DCC bytes to the TOHCTL circuit pack (Release 6 and later).

Timing and Control Functions

The OC3 (1.3 STD) circuit pack performs the following timing and control functions:

- a. Selects the primary or secondary 155.52 MHz timing signal from the TG3 (DS1) circuit packs
- b. Provides selector circuitry for high speed protection switching
- c. Performs internal fault detection and interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN)
- d. Provides OC-3 and STS-3/3c loopbacks
- e. Stores inventory information (CLEI code, serial number, etc.)
- f. Controls the circuit pack faceplate FAULT LED.

Maintenance Signal Functions

The OC3 (1.3 STD) circuit pack performs the following maintenance signal functions:

- a. Detects incoming (from the lightguide cross-connect panel) OC-3 loss of signal (LOS), loss of frame (LOF), and signal degrade (SD) based on the B2 error rate
- b. Inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signal (155.52 Mb/s) transmitted toward the OC48 TRMTR circuit pack when incoming (from the lightguide cross-connect panel) OC-3 LOS or LOF is detected
- c. Inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signal transmitted toward the OC48 TRMTR circuit pack and in the OC-3 signal transmitted toward the lightguide cross-connect panel when an internal fault is detected.

IS3 (LAA5) Circuit Pack Description

Purpose of Circuit

The IS3 Optical Interface - 1.3 μ m Standard Performance [IS3 (1.3 STD)] circuit pack provides a bidirectional low speed interface between one synchronous IS-3 optical signal (155.52 Mb/s) and an internal synchronous STS-3/3c signal (155.52 Mb/s). The IS3 circuit pack is compatible with Release 7.1.0-ADR and later software only.

Faceplate LED

The IS3 (1.3 STD) circuit pack has a red FAULT LED on its faceplate (Figure 7-20). The red FAULT LED is continuously lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the Line Controller (LNCTL) circuit pack to light the FAULT LED on the IS3 (1.3 STD) circuit pack.

The board controller on the IS3 (1.3 STD) circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the FAULT LED on the circuit pack is continuously lighted. If an incoming IS-3 signal from the lightguide cross-connect panel (or equivalent) fails, the FAULT LED will flash on and off.

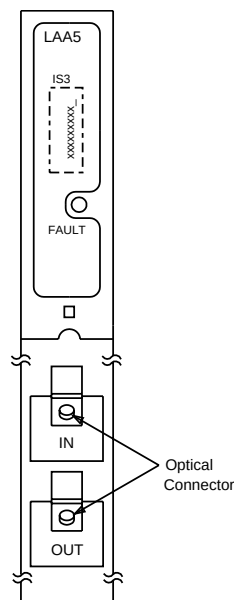


Figure 7-20. IS3 (1.3 STD) (LAA5) Circuit Pack

General Description of Operation

The IS3 (1.3 STD) circuit pack terminates one bidirectional IS-3 line. In the transmit direction (toward the OC-48 line), the IS3 (1.3 STD) circuit pack converts the incoming IS-3 optical signal to an electrical STS-3/3c signal (155.52 Mb/s). The STS-3/3c signal is demultiplexed into three STS-1 signals (51.84 Mb/s) and synchronized to the system clock. The three STS-1 signals are then multiplexed back to one STS-3/3c signal and sent to an OC48 Transmitter (OC48 TRMTR) circuit pack. In the receive direction (toward the IS-3 line), the IS3 (1.3 STD) circuit pack accepts one STS-3/3c signal from an OC48 Receiver (OC48 RCVR) circuit pack and demultiplexes it to three STS-1 signals. The three STS-1 signals are synchronized to the system clock, multiplexed back to one STS-3/3c signal, and converted to an optical IS-3 signal for transmission. The IS3 (1.3 STD) circuit pack also interfaces with the Line Controller (LNCTL) circuit pack for that particular OC-48 high speed line.

Detailed Description of Operation

Transmission Circuitry

Transmit Direction. Figure 7-21 provides an overall block diagram of the IS3 (1.3 STD) circuit pack.

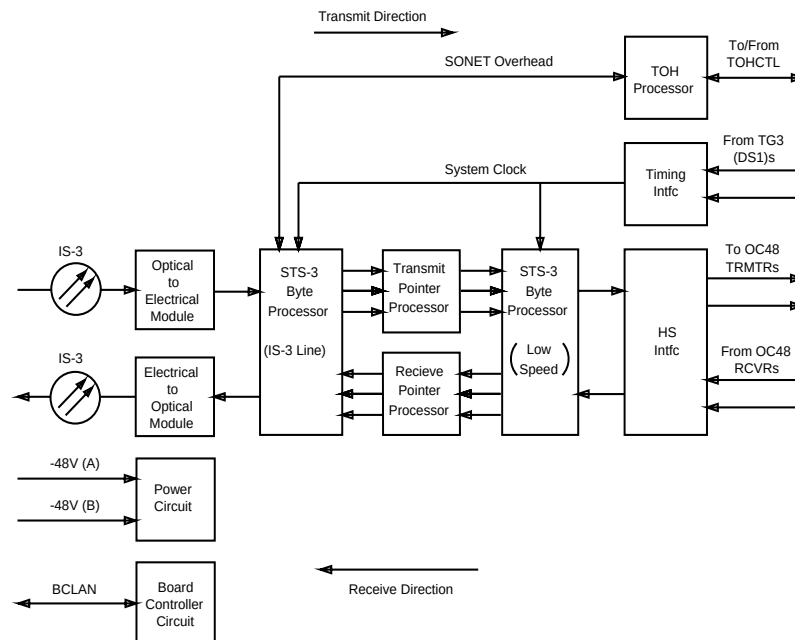


Figure 7-21. IS3 (1.3 STD) Circuit Pack Block Diagram

In the transmit direction (toward the OC-48 line), the IS3 (1.3 STD) circuit pack accepts one IS-3 (155.52 Mb/s) non-return-to-zero (NRZ) synchronous optical network (SONET) compatible optical signal. Fiber access to the IS3 (1.3 STD) circuit pack is via a fixed *ST*[®]-type buildout block and a removable *ST*-type, FC/PC-type, or SC-type lightguide buildout on the circuit pack faceplate (labeled IN). Lightguide buildouts are chosen based on the attenuation desired, the type of connector interface, and the type of lightguide jumpers (single-mode or multimode). All factory-equipped IS3 (1.3 STD) circuit packs come with a removable *ST*-type 0-dB lightguide buildout. When installing or removing lightguide buildouts, do not pull the beam (front tab) outward. Pulling the beam (front tab) outward could result in breaking the beam (front tab).

The optical-to-electrical module receives the IS-3 signal, converts it to an electrical STS-3/3c signal (155.52 Mb/s), and sends it to a timing recovery circuit. The timing recovery circuit recovers a 155.52 MHz clock signal from the STS-3/3c signal and retimes the STS-3/3c signal with the recovered clock signal. The STS-3/3c and clock signals are then sent to the IS-3 line STS-3 byte processor. The IS-3 line STS-3 byte processor descrambles and demultiplexes the STS-3/3c signal into three STS-1 signals (51.84 Mb/s) and outputs the three STS-1 signals and a common clock signal to the transmit pointer processor.

The transmit pointer processor performs frequency adjustment and pointer processing on the three received STS-1 signals. This synchronizes the STS-1 signals to the 51.84 MHz system clock. The 51.84 MHz clock signal is obtained by dividing the 155.52 MHz from the Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs by three. The divide-by-three circuit is part of the low speed STS-3 byte processor. The three STS-1 signals are then sent to the low speed STS-3 byte processor.

The low speed STS-3 byte processor scrambles and multiplexes the three STS-1 signals back to one STS-3/3c signal (155.52 Mb/s). The high speed interface distributes the STS-3/3c signal to the OC48 TRMTR circuit packs.

Receive Direction . In the receive direction (toward the IS-3 line), the high speed interface accepts the STS-3/3c signal (155.52 Mb/s) from an OC48 RCVR circuit pack and sends it to a timing recovery circuit. The timing recovery circuit recovers a 155.52 MHz clock signal from the STS-3/3c signal and retimes the STS-3/3c signal with the recovered clock signal. The STS-3/3c and clock signals are then sent to the low speed STS-3 byte processor. The low speed STS-3 byte processor descrambles and demultiplexes the STS-3/3c signal into three STS-1 signals (51.84 Mb/s) and outputs the three STS-1 signals and a common clock signal to the receive pointer processor.

The receive pointer processor performs frequency adjustment and pointer processing on the three received STS-1 signals. This synchronizes the STS-1 signals to the 51.84 MHz system clock. The 51.84 MHz clock signal is obtained by dividing the 155.52 MHz from the TG3 (DS1) circuit pack by three. The divide-by-three circuit for the receive side is part of the IS-3 line STS-3 byte processor. The three STS-1 signals are then sent to the IS-3 line STS-3 byte processor.

The IS-3 line STS-3 byte processor scrambles and multiplexes the three STS-1 signals back to one STS-3/3c signal (155.52 Mb/s). The electrical-to-optical module receives the STS-3/3c signal and converts it to an IS-3 optical signal (155.52 Mb/s) for transmission. Fiber access to the IS3 (1.3 STD) circuit pack is via a fixed *ST*-type buildout block and a removable *ST*-type, FC/PC-type, or SC-type lightguide buildout on the circuit pack faceplate (labeled OUT). Lightguide buildouts are chosen based on the attenuation desired, the type of connector interface, and the type of lightguide jumpers (single-mode or multimode). All factory-equipped IS3 (1.3 STD) circuit packs come with a removable *ST*-type 7.1-dB lightguide buildout. When installing or removing lightguide buildouts, do not pull the beam (front tab) outward. Pulling the beam (front tab) outward could result in breaking the beam (front tab).

Control Circuitry

The board controller circuit controls all the circuit pack activities. The board controller circuit interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN). The IS3 (1.3 STD) circuit pack reports the status of the circuit pack and the incoming IS-3 and STS-3/3c signals, as well as the circuit pack inventory information (CLEI code, serial number, etc.). The LNCTL circuit pack uses the status information for fault detection and isolation. The IS3 (1.3 STD) circuit pack also responds to control signals from the LNCTL circuit pack.

Overhead Controller Circuitry

The transport overhead hyperchannel (TOH) circuitry provides access to the data communications channel (DCC) of the IS-3 line. Access is only provided to the D1-D3 overhead bytes of the DCC.

Timing Circuitry

The IS3 (1.3 STD) circuit pack accepts one 155.52 MHz clock signal from each of the two Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs (primary and secondary). During normal operation, the timing interface circuitry selects the clock signal that is used and automatically switches to the other clock signal if it detects a loss of signal. The board controller can also override the automatic clock selection mode and force either clock signal to be selected.

The selected clock signal (155.52 MHz) is connected to the IS-3 line STS-3 byte processor and the low speed STS-3 byte processor. The STS-3 byte processors provide divide-by-three circuits that generate a 51.84 MHz signal. The 51.84 MHz signal from the divide-by-three circuit on the IS-3 line STS-3 byte processor is distributed to the receive circuitry of the IS-3 line STS-3 byte processor and the receive pointer processor. The 51.84 MHz signal from the divide-by-three circuit on the low speed STS-3 byte processor is distributed to the transmit circuitry of the low speed STS-3 byte processor and the transmit pointer processor.

Protection Circuitry

Low Speed Interface Protection. Optional 1+1 nonrevertive IS3 (1.3 STD) circuit pack protection in the transmit (toward the OC-48 line) and/or receive (toward the IS-3 line) direction is provided. For 1+1 nonrevertive protection, two IS3 (1.3 STD) circuit packs must be placed in adjacent "A" and "B" low speed interface slots of the Low Speed Shelf - System Controller (for example, LS INTFC slots 2A and 2B). Both IS3 (1.3 STD) circuit packs transmit the same IS-3 signal toward the lightguide cross-connect panel (or equivalent). Both IS3 (1.3 STD) circuit packs receive the same IS-3 signal from the lightguide cross-connect panel (or equivalent) and transmit STS-3/3c signals to the OC48 TRMTR circuit pack. The IS3 circuit packs continuously monitor the incoming active and standby IS-3 signals. If the active IS-3 signal fails, the LNCTL circuit pack broadcasts a switch request to the board controller on the OC48 TRMTR circuit pack via the BCLAN. In the transmit (toward the OC-48 line) direction only, the OC48 TRMTR circuit pack selects the standby STS-3/3c signal from the standby IS3 (1.3 STD) circuit pack. The IS-3 (1.3 STD) circuit pack also supports 1+1 nonrevertive protection of the IS-3 data communications channels.

High Speed Protection . If an OC-48 signal fails or is degraded, the LNCTL circuit pack broadcasts a switch request to the board controllers on the IS3 (1.3 STD) circuit packs via the BCLAN. In the transmit direction, the IS3 (1.3 STD) circuit packs perform head-end bridges. In the FT-2000 OC-48 Add/Drop-Rings Terminal, the STS-3/3c signals (155.52 Mb/s) are provided to the OC48 TRMTR A/D circuit packs for lines 1E and 1W. In the receive direction, the IS3 (1.3 STD) circuit packs perform tail-end switches. In the FT-2000 OC-48 Add/Drop-Rings Terminal, selector circuitry on the IS3 (1.3 STD) circuit packs accepts the STS-3/3c signals from the OC48 RCVR A/D circuit pack opposite of the configured direction.

Synchronization Reference Protection. Synchronization reference switching is done by selecting the primary or secondary synchronization reference signal from the TG3 (DS1) circuit packs. The timing interface circuitry of the IS3 (1.3 STD) circuit pack monitors these synchronization reference signals and can autonomously select the primary or secondary. The LNCTL circuit pack can inhibit this autonomous selection and make its own selection.

Fault Detection Circuitry

Monitoring and Testing. The board controller circuit monitors all the activities on the circuit pack. The IS3 (1.3 STD) circuit pack has an in-service and out-of-service built-in test capability. In-service testing is continuous. If an error occurs, the board controller reports the error to the LNCTL circuit pack using the board controller local area network (BCLAN). An out-of-service test is performed whenever the IS3 (1.3 STD) circuit pack is inserted in a slot or when a reset is performed.

Loopbacks. The IS3 (1.3 STD) circuit pack provides IS-3 and STS-3/3c loopbacks for fault isolation and remote testing. The IS-3 loopback loops the input from the IS-3 line back to the output of the IS-3 line via a loop connection in the low speed STS-3 byte processor. This loopback does not loop the IS-3 DCC overhead channel. The STS-3/3c loopback loops the STS-3/3c signal from the OC-48 line back to the STS-3/3c signal transmitted toward the OC-48 line via a loop connection in the IS-3 line STS-3 byte processor. The IS-3 and STS-3/3c loopback paths include the transmit and receive pointer processors.

These loopbacks are implemented in response to craft interface terminal (CIT) commands (via the SYSCTL and LNCTL circuit packs) and controlled by the board controller.

Performance Monitoring. The IS3 (1.3 STD) circuit pack monitors the following performance-monitoring parameters:

- Optical transmit power
- Loss of signal
- Loss of frame
- Bit error rate thresholds

When an incoming IS-3 signal defect from the lightguide cross-connect panel is detected, the OC3 (1.3 STD) circuit pack inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signal (155.52 Mb/s) transmitted toward the OC-48 TRMTR circuit pack.

When an incoming OC-48 signal defect is detected by the OC48 RCVR circuit pack or an STS-1 loss of pointer is detected by the IS3 (1.3 STD) circuit pack in the 155.52 Mb/s signal received from the OC48 RCVR circuit pack, the IS3 (1.3 STD) circuit pack inserts STS-1 path AIS in each STS-1 tributary of the IS-3 signal transmitted toward the lightguide cross-connect panel.

When an internal fault is detected, the IS3 (1.3 STD) circuit pack inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signal transmitted toward the OC48 TRMTR circuit pack and in the IS-3 signal transmitted toward the lightguide cross-connect panel.

Power Circuitry

The IS3 (1.3 STD) circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack.

If the IS3 (1.3 STD) circuit pack detects the failure of the onboard fuse or power converter the red FAULT LED will light. If the fuse or power converter fails, the LNCTL circuit pack provides +5 volts to light the FAULT LED on the IS3 (1.3 STD) circuit pack.

Quick Reference Summary

Transmit Functions

The IS3 (1.3 STD) circuit pack performs the following transmit functions:

- a. Receives one IS-3 (155.52 Mb/s) non-return-to-zero (NRZ) SONET compatible optical signal from a lightguide cross-connect panel (or equivalent)
- b. Converts the optical IS-3 signal to an electrical STS-3/3c signal
- c. Recovers a 155.52 MHz clock signal from STS-3/3c signal and retimes the STS-3/3c signal with the recovered clock
- d. Descrambles and demultiplexes the STS-3/3c signal into three STS-1 signals (51.84 Mb/s)
- e. Performs frequency adjustments and pointer processing on the three STS-1 signals
- f. Scrambles and multiplexes the three STS-1 signals to an STS-3/3c signal (155.52 Mb/s)
- g. Provides the STS-3/3c signal to an OC48 TRMTR circuit pack
- h. Accepts the IS-3 SONET section DCC bytes from the TOHCTL circuit pack.

Receive Functions

The IS3 (1.3 STD) circuit pack performs the following receive functions:

- a. Receives an STS-3/3c signal (155.52 Mb/s) from an OC48 RCVR circuit pack
- b. Recovers a 155.52 MHz clock signal from STS-3/3c signal and retimes the STS-3/3c signal with the recovered clock
- c. Descrambles and demultiplexes the STS-3/3c signal into three STS-1 signals (51.84 Mb/s)
- d. Performs frequency adjustments and pointer processing on the three STS-1 signals
- e. Scrambles and multiplexes the three STS-1 signals to an STS-3/3c signal (155.52 Mb/s)
- f. Converts the electrical STS-3/3c signal to an optical IS-3 signal
- g. Provides the IS-3 optical signal to a lightguide cross-connect panel (or equivalent).
- h. Provides the IS-3 SONET section DCC bytes to the TOHCTL circuit pack.

Timing and Control Functions

The IS3 (1.3 STD) circuit pack performs the following timing and control functions:

- a. Selects the primary or secondary 155.52 MHz timing signal from the TG3 (DS1) circuit packs
- b. Provides selector circuitry for high speed protection switching
- c. Performs internal fault detection and interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN)
- d. Provides IS-3 and STS-3/3c loopbacks
- e. Stores inventory information (CLEI code, serial number, etc.)
- f. Controls the circuit pack faceplate FAULT LED.

Maintenance Signal Functions

The IS3 (1.3 STD) circuit pack performs the following maintenance signal functions:

- a. Detects incoming (from the lightguide cross-connect panel) IS-3 loss of signal (LOS), loss of frame (LOF), and signal degrade (SD) based on the B2 error rate
- b. Inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signal (155.52 Mb/s) transmitted toward the OC48 TRMTR circuit pack when incoming (from the lightguide cross-connect panel) IS-3 LOS or LOF is detected
- c. Inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signal transmitted toward the OC48 TRMTR circuit pack and in the IS-3 signal transmitted toward the lightguide cross-connect panel when an internal fault is detected.

OC-12 (T939A) Circuit Pack Description

Purpose of Circuit

The OC-12 Optical Interface - 1.3 μ m Standard Performance [OC-12 (1.3 STD)] circuit pack provides a bi-directional low speed interface between one synchronous OC-12 optical signal (622.08 Mb/s) and an internal synchronous STS-12/12c signal (622.08 Mb/s).

Faceplate LED

The OC12 (1.3 STD) circuit pack has a red FAULT LED on its faceplate (Figure 7-22). The red FAULT LED is continuously lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the Line Controller (LNCTL) circuit pack to light the FAULT LED on the OC12 (1.3 STD) circuit pack.

The board controller on the OC12 (1.3 STD) circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the FAULT LED on the circuit pack is continuously lighted. If an incoming OC-12 signal from the lightguide cross-connect panel (or equivalent) fails, the FAULT LED will flash on and off.

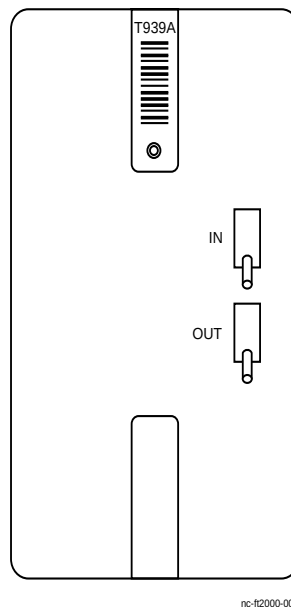


Figure 7-22. OC12 (1.3 STD) (T939A) Circuit Pack

General Description of Operation

The OC12 circuit pack provides a bi-directional interface between an OC-12/12C optical line (622.08 MHz) and electrical STS-3 rate (155.52 MB/s) backplane interface signals. In the FT-2000 OC-48 lightwave system, the OC-12/12C circuit packs are physically located in the OC-48 LS shelf. The circuit pack assembly is four slots wide and has four connectors to pick up and drop STS3 rate signals from each of the slots. The slot location for the OC12 circuit pack is identified by the left most slot, and the only allowable locations for plugging in the OC12 pack are slots 1A, 3A, 5A, and 7A. While the left most slot is used for identifying the circuit pack's location in the backplane connector for powering and the board controller interfaces; i.e., the main connector slots corresponding to the circuit pack locations 1A, 3A, 5A, and 7A are slots 1B, 3B, 5B, and 7B respectively.

In the transmit direction, (OC-12/12C line in to the LS backplane out), the OC12 circuit pack converts the OC-12/12C optical input signal into an electrical STS-12/12C signal which is demultiplexed into four STS-3 rate signals. The four STS-3 rate signals are synchronized to the system clock and distributed to the backplane as data-only signals.

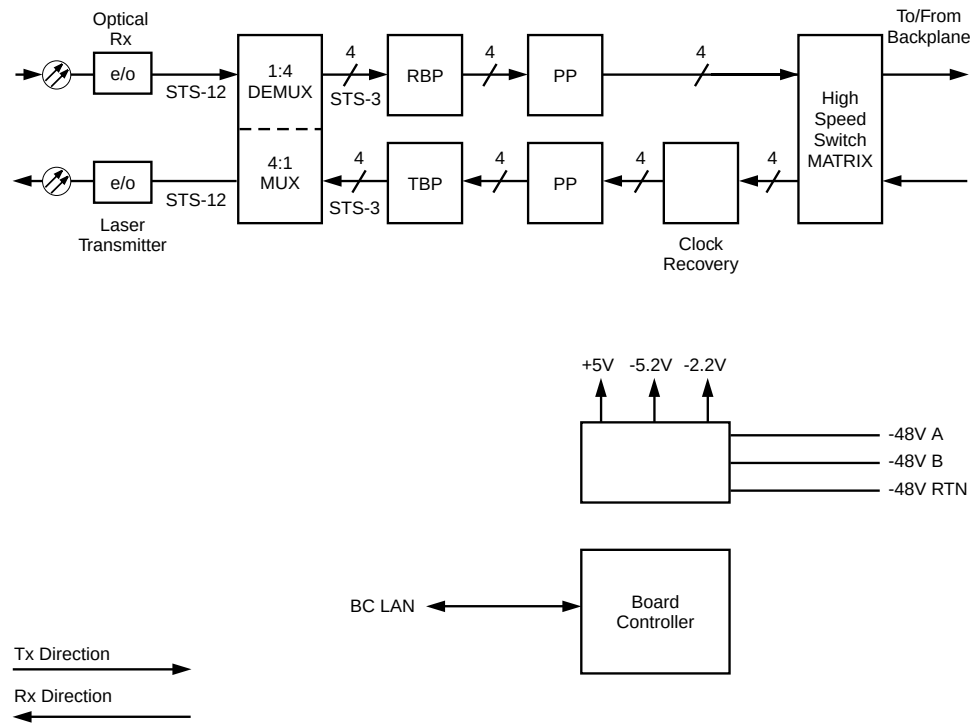
In the receive direction (LS backplane in to OC-12/12C line out), the OC12 circuit pack selects four of the electrical STS-3 rate signals from the service or protection OC-48 receivers in the HS shelf. These four signals are then synchronized to the system clock and multiplexed into an STS-12/12C signal which drives a laser transmitter.

The control and monitoring of the OC12 circuit pack is done by the board controller through GPID leads and the SDSI (SONET device serial interface) leads.

Detailed Description of Operation

Transmission Circuitry

A block diagram of the OC12 circuit pack is shown in Figure 7-23.



nc-ft2000-003

Figure 7-23. OC12 (1.3 STD) Circuit Pack Block Diagram

Transmit Direction. In the transmit direction, the circuit pack accepts one OC-12/12C optical signal. This optical input is connected to an optical receiver module which converts the optical OC-12/12C signal into electrical 522.08 MB/s STS-12/12C data and recovered clock signals. The STS-12/12C clock and data signals are connected to the 1:4 DEMUX section of the STS12 MUX/DEMUX device which de-multiplexes the STS-12/12C signal into four STS-3 bit streams and a common clock. The four bit streams and clock are connected to the STM4RBP (receive byte processor) which re-assembles the four bit streams into four STS-3 rate data signals and processes the received OC-12/12C line overhead bytes. The outputs from the receive byte processor are the four STS-3 rate data signals, a common clock, a common sync signal and the received line hyper channel data and clock signals. In addition to the data inputs, the receive byte processor receives a loss of signal control flag from the receiver module which can be used for automatic line/section AIS and FERF insertion.

Following the receive byte processor, the transmit STS12PP (pointer processor) performs frequency adjustments and pointer processing on the four received STS-3 rate clock and data signals that synchronizes the STS-3 rate data to the 155.52 MHz system clock. The transmit pointer processor also receives an AIS control input from the receive byte processor which can be used for automatic path AIS insertion when there is a loss of frame, loss of clock, or a loss or received STS-3 rate data to the receive byte processor. The outputs of the transmit pointer processor are connected to the HS switch matrix which distributes them to the backplane.

Receive Direction. In the receive direction, four electrical inputs from each of the LS slots are connected to the switch matrix which selects one of the STS-3 rate inputs for each of the four receive input STS-3 rate signals. The four selected STS-3 rate input data signals are connected to four timing recovery circuits which recover a 155.52 MHz clock signal for each of the received data signals and re-times each data signal with its recovered clock.

Following the timing recovery circuits, the receive pointer processor performs frequency adjustment and pointer processing on the four STS-3 rate data signals to synchronize the STS-3 rate data to the 155.52 MHz system clock. The outputs of the receive pointer processor are connected to the STM4TBP (transmit byte processor) which byte processes the four STS-3 rate signals, adds the transmit OC-12/12C line overhead bytes, and generates four STS-3 rate bit streams for bit interleaving into an OC-12/12C signal. The four bit stream outputs are connected to the 4:1 mux section of the STS12 MUX/DEMUX device which bit interleaves and multiplexes the four bit streams into an STS12/12C data signal. Included in the STS12 MUX device is a 622.08 MHz PLL synthesizer which generates an STS-12 clock which is synchronized with the 155.52 MHz system clock. The STS-12/12C data signal is then connected to the laser module.

Control Circuitry

The board controller circuit controls all the circuit pack activities. The board controller circuit interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN). The OC12 (1.3 STD) circuit pack reports the status of the circuit pack and the incoming OC-12 and STS-3/3c signals, as well as the circuit pack inventory information (CLEI code, serial number, etc.). The LNCTL circuit pack uses the status information for fault detection and isolation. The OC12 (1.3 STD) circuit pack also responds to control signals from the LNCTL circuit pack.

Overhead Controller Circuitry

The transport overhead hyperchannel (TOH) circuitry provides access to the data communications channel (DCC) of the OC-12 line. Access is only provided to the D1-D3 overhead bytes of the DCC.

Timing Circuitry

The OC12 (1.3 STD) circuit pack accepts one 155.52 MHz clock signal from each of the two Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs (primary and secondary). During normal operation, the timing interface circuitry selects the clock signal that is used and automatically switches to the other clock signal if it detects a loss of signal. The board controller can also override the automatic clock selection mode and force either clock signal to be selected.

When the network element is in the through-timed mode, the OC-12 Timing Direction parameter determines the high-speed line (1W or 1E) from which the OC-12 derives its timing. The OC-12 Timing Direction parameter is user-provisionable via the CIT, and its default value is 1W. This parameter may be needed when the OC-12 is used to distribute timing to far-end equipment. When the network element is not through-timed, the OC-12 derives its timing from that element system clock.

Protection Circuitry

Low-Speed Interface Protection. Optional 1+1 non-revertive OC12 (1.3 STD) circuit pack protection in the transmit (toward the OC-48 line) and/or receive (toward the OC-12 line) direction is provided. For 1+1 non-revertive protection, two OC12 (1.3 STD) circuit packs must be placed in adjacent low-speed interface slots of the Low Speed Shelf - System Controller (for example, LS INTFC slots 1A-2B and 3A-4B or 5A-6A and 7A-8B). Both OC12 (1.3 STD) circuit packs transmit the same OC-12 signal toward the lightguide cross-connect panel (or equivalent). Both OC12 (1.3 STD) circuit packs receive the same OC-12 signal from the lightguide cross-connect panel (or equivalent) and transmit STS-3/3c signals to the OC48 TRMTR circuit pack(s). The OC12 circuit packs continuously monitor the incoming active and standby OC-12 signals. If the active OC-12 signal fails, the LNCTL circuit pack broadcasts a switch request to the board controller on the OC48 TRMTR circuit pack via the BCLAN. In the transmit (toward the

OC-48 line) direction only, the OC48 TRMTR circuit pack selects the standby STS-3/3c signal(s) from the standby OC12 (1.3 STD) circuit pack.

High-Speed Protection. If an OC-48 signal fails or is degraded, the LNCTL circuit pack broadcasts a switch request to the board controllers on the OC12 (1.3 STD) circuit packs via the BCLAN. In the transmit direction, the OC12 (1.3 STD) circuit packs perform head-end bridges. In the FT-2000 OC-48 Add/Drop-Rings Terminal, four STS-3/3c signals (each at 155.52 Mb/s) are provided to the OC48 TRMTR A/D circuit packs for lines 1E and 1W. In the receive direction, the OC12 (1.3 STD) circuit packs perform tail-end switches. In the FT-2000 OC-48 Add/Drop-Rings Terminal, selector circuitry on the OC12 (1.3 STD) circuit packs accept the STS-3/3c signals from the OC48 RCVR A/D circuit pack opposite of the configured direction.

Dual Ring Interworking Protection. The OC12 (1.3 STD) circuit pack at the primary node of a dual ring interworking application monitors the OC-12 signal coming from the other ring. If an unprotected incoming OC-12 loss-of-signal or loss-of-frame condition is detected, the OC12 (1.3 STD) circuit pack notifies the LNCTL circuit pack. The LNCTL circuit pack broadcasts an automatic switch request to the board controller on the OC48 TRMTR circuit pack via the BCLAN. In the add (toward to OC-48 line) direction only, the OC48 TRMTR circuit pack switches to (selects) the standby 155.52 Mb/s signals (STS-3 tributary) from the secondary node. This completes the circuit from the secondary node to the termination node in the ring.

Synchronization Reference Protection. Synchronization reference switching is done by selecting the primary or secondary synchronization reference signal from the TG3 (DS1) circuit packs. The timing interface circuitry of the OC12 (1.3 STD) circuit pack monitors these synchronization reference signals and can autonomously select the primary or secondary. The LNCTL circuit pack can inhibit this autonomous selection and make its own selection.

When the network element is in the through-timed mode, the OC-12 Timing Direction parameter determines the high-speed line (1W or 1E) from which the OC-12 derives its timing. The OC-12 Timing Direction parameter is user-provisionable via the CIT, and its default value is 1W. This parameter may be needed when the OC-12 is used to distribute timing to far-end equipment. When the network element is not through-timed, the OC-12 derives its timing from that element's system clock.

Fault Detection Circuitry

Monitoring and Testing. The board controller circuit monitors all the activities on the circuit pack. The OC12 (1.3 STD) circuit pack has an in-service and out-of-service built-in test capability. In-service testing is continuous. If an error occurs, the board controller reports the error to the LNCTL circuit pack using the board controller local area network (BCLAN). An out-of-service test is performed whenever the OC12 (1.3 STD) circuit pack is inserted in a slot or when a reset is performed.

Loopbacks. The OC12 (1.3 STD) circuit pack provides facility and equipment loopbacks for fault isolation testing.

- A facility loopback bridges the OC-12/12C line input back to the OC-12/12C line output via a loop connection in the STS12 MUX/DEMUX. The facility loopback includes the receiver and laser transmitter modules.
- An equipment loopback bridges the STS-12/12C transmit data drive to the laser module back to the OC-12/12C receive input via a loop connection in the STS12 MUX/DEMUX. The equipment loopback path includes all the transmission devices on the circuit pack except the optical receiver module and the laser module. Also, the DCC transmit data input from the overhead controller will be looped back to the DCC receive data output to the overhead controller.

These loopbacks are implemented in response to craft interface terminal (CIT) commands (via the SYSCTL and LNCTL circuit packs) and controlled by the board controller.

Performance Monitoring. The OC12 (1.3 STD) circuit pack monitors the following performance-monitoring parameters:

- Laser bias current
- Optical transmit power
- Received optical power
- Loss of signal
- Loss of frame
- Bit error rate thresholds

When an incoming OC-12 signal defect from the lightguide cross-connect panel is detected, the OC12 (1.3 STD) circuit pack inserts STS-1 path AIS in each STS-1 tributary of the four STS-3/3c signals (155.52 Mb/s) transmitted toward the OC-48 TRMTR circuit pack.

When an incoming OC-48 signal defect is detected by the OC48 RCVR circuit pack or an STS-1 loss of pointer is detected by the OC12 (1.3 STD) circuit pack in any of the 155.52 Mb/s signal received from the OC48 RCVR circuit pack, the OC12 (1.3 STD) circuit pack inserts STS-1 path AIS in each STS-1 tributary of the OC-12 signal transmitted toward the lightguide cross-connect panel.

When an internal fault is detected, the OC12 (1.3 STD) circuit pack inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signals transmitted toward the OC48 TRMTR circuit pack and in the OC-12 signal transmitted toward the lightguide cross-connect panel.

Power Circuitry

The OC12 (1.3 STD) circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack.

If the OC12 (1.3 STD) circuit pack detects the failure of the onboard fuse or power converter the red FAULT LED will light. If the fuse or power converter fails, the LNCTL circuit pack provides +5 volts to light the FAULT LED on the OC12 (1.3 STD) circuit pack.

Quick Reference Summary

Transmit Functions

The OC12 (1.3 STD) circuit pack performs the following transmit functions:

- a. Receives one OC-12 (622.08 Mb/s) non-return-to-zero (NRZ) SONET compatible optical signal from a lightguide cross-connect panel (or equivalent)
- b. Converts the optical OC-12 signal to an electrical STS-12/12c signal
- c. Recovers a 622.08 MHz clock signal from STS-12/12c signal and retimes the STS-12/12c signal with the recovered clock
- d. Descrambles and demultiplexes the STS-12/12c signal into four STS-3 signals (155.52 Mb/s)
- e. Scrambles and multiplexes the four STS-3 signals into four STS-3/3c signals (each at 155.52 Mb/s)
- f. Provides four STS-3 signals to an OC48 TRMTR circuit pack (s)
- g. Accepts the OC-12 SONET section DCC bytes from the TOHCTL circuit pack.

Receive Functions

The OC12 (1.3 STD) circuit pack performs the following receive functions:

- a. Receives four STS-3 signals (each at 155.52 Mb/s) from an OC48 RCVR circuit pack(s)
- b. Recovers a clock signal from one of the STS-3 signals and retimes the STS-3/3c signal with the recovered clock
- c. Performs frequency adjustments and pointer processing on the four STS-3 signals
- d. Scrambles and multiplexes the four STS-3 signals to an STS-12/12c signal (622.08 Mb/s)
- e. Converts the electrical STS-12/12c signal to an optical OC-12 signal

- f. Provides the OC-12 optical signal to a lightguide cross-connect panel (or equivalent).
- g. Provides the OC-12 SONET section DCC bytes to the TOHCTL circuit pack.

Timing and Control Functions

The OC12 (1.3 STD) circuit pack performs the following timing and control functions:

- a. Selects the primary or secondary 155.52 MHz timing signal from the TG3 (DS1) circuit packs
- b. Provides selector circuitry for high speed protection switching
- c. Performs internal fault detection and interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN)
- d. Provides facility and equipment loopbacks
- e. Stores inventory information (CLEI code, serial number, etc.)
- f. Controls the circuit pack faceplate FAULT LED.

Maintenance Signal Functions

The OC12 (1.3 STD) circuit pack performs the following maintenance signal functions:

- a. Detects incoming (from the lightguide cross-connect panel) OC-12 loss of signal (LOS), loss of frame (LOF), and signal degrade (SD) based on the B2 error rate
- b. Inserts STS-1 path AIS in each STS-1 tributary of the STS-3/3c signals (each at 155.52 Mb/s) transmitted toward the OC48 TRMTR circuit pack(s) when incoming (from the lightguide cross-connect panel) OC-12 LOS or LOF is detected
- c. Inserts STS-1 path AIS in each STS-1 tributary of the four STS-3 signals (each at 155.52 Mb/s) transmitted toward the OC48 TRMTR circuit pack(s) and in the OC-12 signal transmitted toward the lightguide cross-connect panel when an internal fault is detected.

LSSW (LAA12B) Circuit Pack Description



NOTE:

The LAA12 circuit pack was rated MD as of January 1997. It was replaced with the LAA12B circuit pack.

Purpose of Circuit

The Low Speed Switch (LSSW) circuit pack is a relay switch circuit pack used to build a low speed protection bus in the transmit and receive directions for the DS3 Interface (DS3) and STS-1 Electrical Interface (STS1E) circuit packs.

Faceplate LED

The LSSW circuit pack has a red FAULT LED on its faceplate (Figure 7-24). The red FAULT LED is lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the Line Controller (LNCTL) circuit pack to light the FAULT LED on the LSSW circuit pack.

The board controller on the LSSW circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the FAULT LED on the circuit pack is lighted.

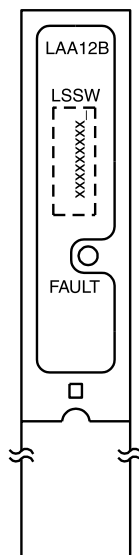


Figure 7-24. LSSW (LAA12B) Circuit Pack

General Description of Operation

The signal flow through the LSSW circuit pack may be in either direction, depending on the slot location. When the LSSW circuit pack is placed in the IN slot on the Low Speed Shelf - System Controller or Low Speed Shelf - Complementary, it provides a low speed protection bus in the transmit direction. When the LSSW circuit pack is placed in the OUT slot on the Low Speed Shelf - System Controller or Low Speed Shelf - Complementary, it provides a low speed protection bus in the receive direction.

Each LSSW circuit pack interfaces with up to 16 service DS3 and STS1E circuit packs, one protection DS3 circuit pack, and one protection STS1E circuit pack.

If a service DS3 or STS1E circuit pack fails, the input protection switching relays on the failed circuit pack route the three low speed signals (DS3 or EC-1) to the LSSW circuit pack in the IN slot. The LSSW circuit pack in the IN slot switches the three low speed signals (DS3 or EC-1) to the protection DS3 or STS1E circuit pack. In the receive direction, the LSSW circuit pack in the OUT slot accepts the three low speed signals (DS3 or EC-1) from the protection circuit pack (DS3 or STS1E) and routes them to the output protection switching relays on the failed circuit pack and on to the DSX-3 or STSX-1 panel (or equivalent).

Shorting contacts are provided in the DS3 or STS1E circuit pack backplane connector so that when the circuit pack is removed, the DSX-3 or STSX-1 cables short through to the LSSW circuit packs for routing to the protection DS3 or STS1E circuit pack.

Detailed Description of Operation

Transmission Circuitry

Transmit Direction. Figure 7-25 provides an overall block diagram of the LSSW circuit pack.

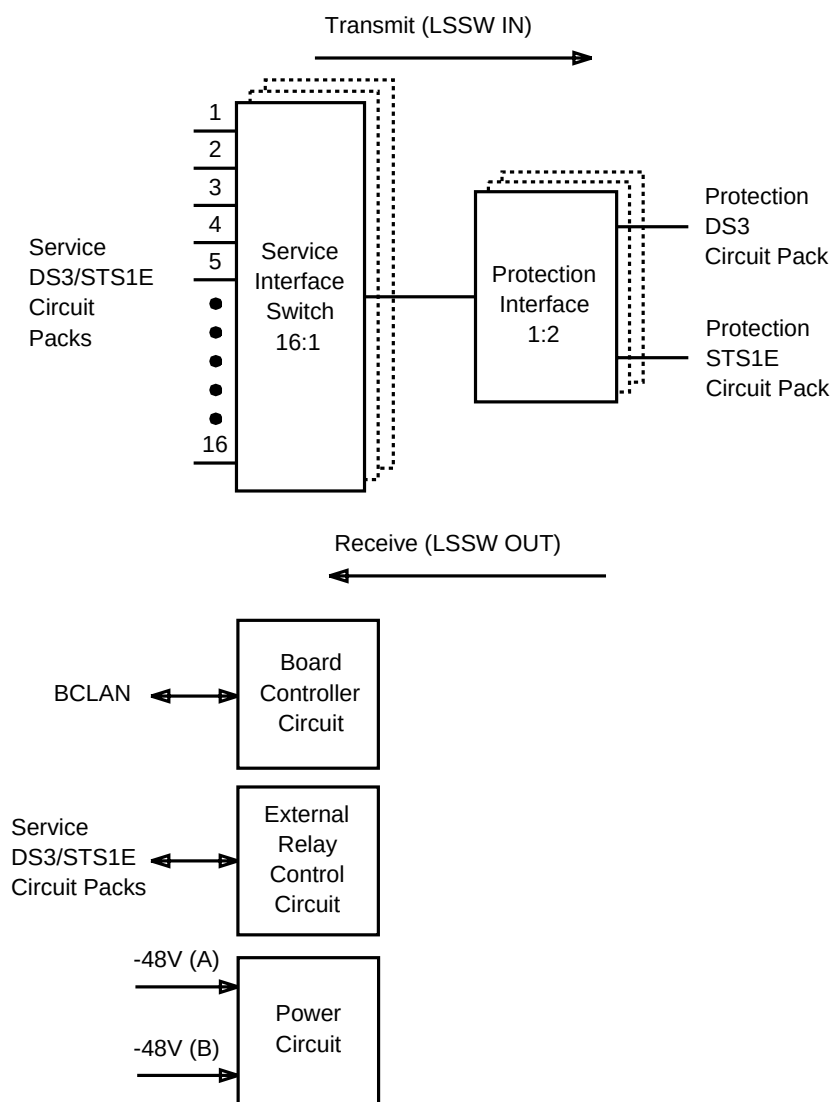


Figure 7-25. LSSW Circuit Pack Block Diagram

In the transmit direction, the LSSW circuit pack in the IN slot interfaces with up to 16 service DS3 and STS1E circuit packs, 1 protection DS3 circuit pack, and 1 protection STS1E circuit pack. If a service DS3 or STS1E circuit pack fails, the input protection switching relays on the failed circuit pack feed each of the three low speed signals (DS3 or EC-1) to the LSSW (IN) circuit pack. Shorting contacts are also provided in the DS3 and STS1E circuit pack backplane connectors so that when the circuit pack is removed, the DSX-3 or STSX-1 cables short through to the LSSW circuit packs for routing to the protection DS3 or STS1E circuit pack. The three low speed signals (DS3 or EC-1) are routed into a service interface switch on the LSSW (IN) circuit pack. Each service interface switch performs a 16:1 selection of the signal needing protection and terminates the other signals. The three low speed signals are routed through the protection interface switches to the protection DS3 or STS1E circuit pack.

Receive Direction. In the receive direction, the LSSW circuit pack in the OUT slot accepts three DS3 or EC-1 signals from the protection DS3 or STS1E circuit pack. Each low speed signal (DS3 or EC-1) is routed through the protection interface switches and fed into a service interface switch. Each service interface switch performs a 1:16 selection to output the low speed signals to the output protection switching relays on the failed DS3 or STS1E circuit pack and on to the DSX-3 or STSX-1 panel (or equivalent).

When a service DS3 or STS1E circuit pack is reinserted in a slot, the protection switching relays remain in the protection state until the LNCTL circuit pack determines that the circuit pack is good.

Control Circuitry

The board controller circuit controls all the circuit pack activities. The board controller circuit interfaces with the Line Controller (LNCTL) circuit pack using the internal board controller local area network (BCLAN). The LSSW circuit pack reports the status of the circuit pack as well as the circuit pack inventory information (CLEI code, serial number, etc.). The status information is used by the LNCTL circuit pack for low speed protection switching. The LSSW circuit packs also respond to control signals from the LNCTL circuit pack; for example, low speed protection switching control.

The LSSW circuit packs also control the protection switching relays on the service DS3 and STS1E circuit packs. The LSSW circuit pack in the IN slot controls the protection switching relays on the DS3 and STS1E circuit packs in slots 1A through 4B. The LSSW circuit pack in the OUT slot controls the protection switching relays on the DS3 and STS1E circuit packs in slots 5A through 8B.

Power Circuitry

The LSSW circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces +5 volts used on the circuit pack.

The LSSW circuit packs also provide power to the protection switching relays on the service DS3 and STS1E circuit packs. The LSSW circuit pack in the IN slot powers the protection switching relays on the DS3 and STS1E circuit packs in slots 1A through 4B. The LSSW circuit pack in the OUT slot powers the protection switching relays on the DS3 and STS1E circuit packs in slots 5A through 8B.

The LSSW circuit pack monitors the two –48 volt sources to the circuit pack and any failures are reported to the board controller. If the LSSW circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or power converter fails, the LNCTL circuit pack provides +5 volts to light the FAULT LED on the LSSW circuit pack.

Quick Reference Summary

Transmit Functions

The LSSW circuit pack performs the following transmit functions:

- a. Receives three DS3 or EC-1 signals from a failed DS3 or STS1E circuit pack
- b. Switches the three DS3 or EC-1 signals to the protection DS3 or STS1E circuit pack.

Receive Functions

The LSSW circuit pack performs the following receive functions:

- a. Receives three DS3 or EC-1 signals from the protection DS3 or STS1E circuit pack
- b. Switches the three DS3 or EC-1 signals to the correct service DS3 or STS1E circuit pack and to the DSX-3 or STSX-1 panel (or equivalent).

Control Functions

The LSSW circuit pack performs the following control functions:

- a. Controls and powers the protection switching relays on the DS3 and STS1E circuit packs
- b. Stores inventory information (CLEI code, serial number, etc.).
- c. Performs internal fault detection and interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN)
- d. Controls the circuit pack faceplate FAULT LED.

OC48 RCVR A/D (839B2, 839B4B, 839B5, and 839E5) Circuit Pack Description

Purpose of Circuit

The OC48 Receiver (OC48 RCVR A/D) circuit pack provides a high speed interface between an incoming OC-48 optical signal (2.5 Gb/s) and sixteen 155.52 Mb/s electrical signals. The 839B2 OC48 RCVR A/D circuit pack is used in bidirectional ring applications and is compatible with Release 3.0.1-ADR and later software. The 839B4B OC48 RCVR A/D circuit pack is a cost-reduced version of the 839B2 OC48 RCVR A/D circuit pack and is compatible with Release 7.0.0-ADR and later software.

The 839B5 OC48 RCVR A/D circuit pack is an enhanced version of the 839B4B OC48 RCVR A/D circuit pack with an STS-1 time slot assignment capability that allows STS-1 cross-connections. The 839B5 OC48 RCVR A/D circuit pack is compatible with Release 7.0.0-ADR and later software.

The 839E5 OC48 RCVR A/D circuit pack provides an STS-1 time slot assignment capability that allows STS-1 cross-connections. This circuit pack is tuned "optimized" for the 1550 nm band and is required to support the DWDM feature (739E[1-16] circuit packs).

Faceplate LED

The OC48 RCVR A/D circuit pack has a red FAULT LED on its faceplate (Figure 7-26).

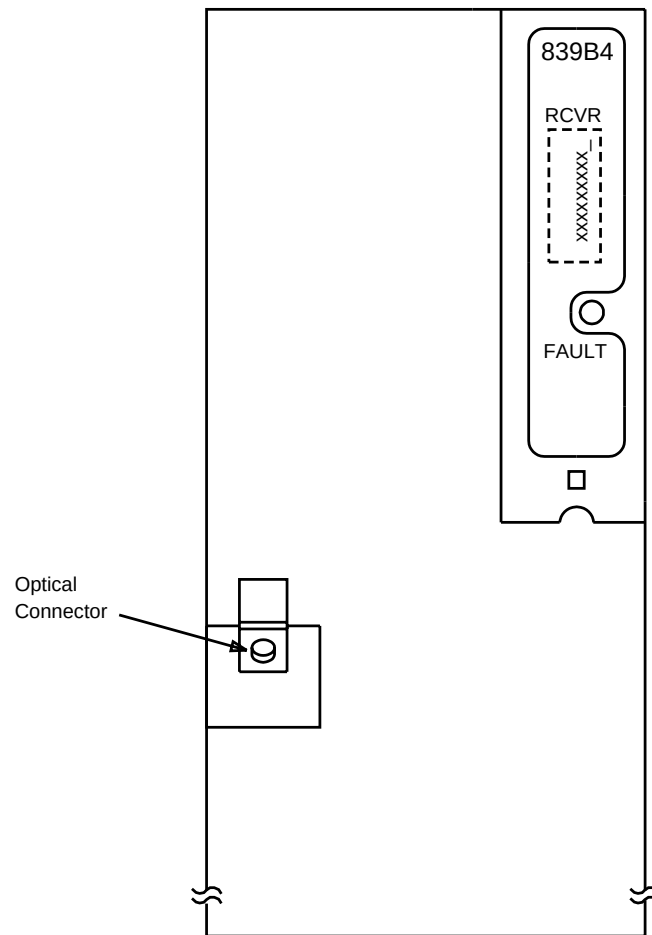


Figure 7-26. OC48 RCVR A/D (839B2, 839B4B, 839B5, and 839E5) Circuit Pack

The red FAULT LED is continuously lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the Line Controller (LNCTL) circuit pack to light the FAULT LED on the OC48 RCVR A/D circuit pack.

The board controller on the OC48 RCVR A/D circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the board controller is

reset and the FAULT LED on the circuit pack is continuously lighted. If an incoming OC-48 signal fails, the FAULT LED will flash on and off.

General Description of Operation

The OC48 RCVR A/D circuit pack accepts the incoming OC-48 signal (2.5 Gb/s) and converts it to an electrical STS-48 signal. The STS-48 signal is descrambled and demultiplexed into sixteen 155.52 Mb/s signals. The SONET transport overhead bytes are also extracted and sent to the Overhead Controller - Terminal [OHCTL (TERM)] circuit pack. The 839B2 and 839B4B OC48 RCVR A/D circuit pack then passes the sixteen 155.52 Mb/s signals to the low speed interface circuit packs or the OC48 TRMTR A/D circuit pack in the opposite direction (bidirectional ring applications).

The 839B5 OC48 RCVR A/D circuit pack demultiplexes the STS-48 electrical signal into sixteen 155.52 Mb/s signals, which are fanned out to 54 STS-3 output ports through two parallel STS-1 time slot assignment (TSA) crosspoint switch devices. The sixteen 155.52 Mb/s signals are then passed to the low speed interface circuit packs or the OC48 TRMTR A/D circuit pack in the opposite direction (bidirectional ring applications).

Detailed Description of Operation

Transmission Circuitry

Figure 7-27 shows an overall block diagram of 839B2 and 839B4B OC48 RCVR A/D circuit packs, and Figure 7-28 shows an overall block diagram of the 839B5 and 839E5 OC48 RCVR A/D circuit pack. The OC48 RCVR A/D circuit pack accepts one 2.5 Gb/s non-return-to-zero (NRZ) SONET compatible optical signal. Fiber access to the OC48 RCVR A/D circuit pack is via a fixed *ST*-type buildout block and a removable *ST*-type, *FC/PC*-type, or *SC*-type lightguide buildout on the circuit pack faceplate. Lightguide buildouts are chosen based on the attenuation desired, and the type of connector interface the single-mode lightguide jumper is equipped with. There are three types of lightguide buildouts available for the OC48 RCVR A/D circuit pack: *ST*-type, *FC/PC*-type, and *SC*-type. All factory-equipped OC48 RCVR A/D circuit packs come with a removable *ST*-type 0-dB lightguide buildout. When installing or removing lightguide buildouts, do not pull the beam (front tab) outward. Pulling the beam (front tab) outward could result in breaking the beam (front tab).

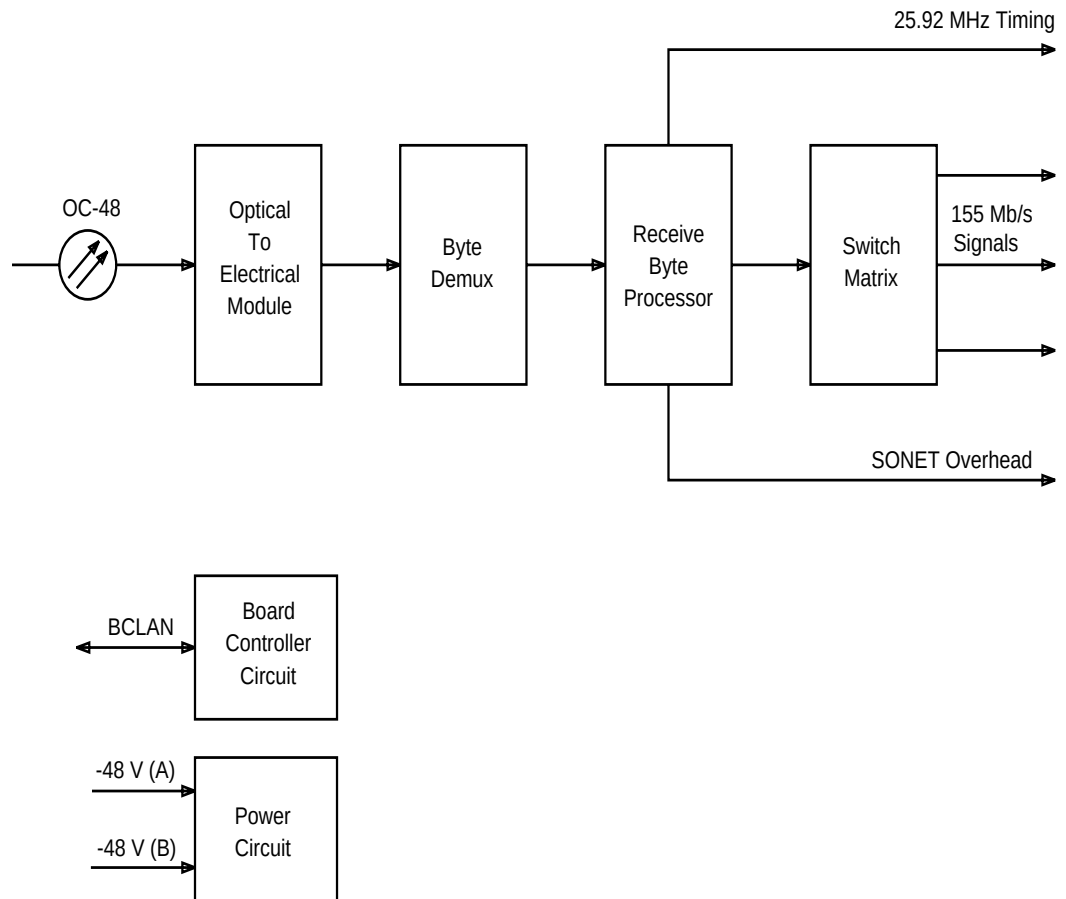


Figure 7-27. OC48 RCVR A/D (839B2 and 839B4B) Circuit Pack Block Diagram

The optical-to-electrical converter receives the OC-48 signal and converts it to an electrical STS-48 signal. The optical-to-electrical converter also detects OC-48 loss of signal (LOS). Timing is also extracted and the data is regenerated. The STS-48 data signal and 2.5 GHz clock signal are then passed to the byte demultiplexer.

The byte demultiplexer receives the STS-48 data signal from the optical-to-electrical converter and demultiplexes the STS-48 data signal into sixteen 155.52 Mb/s data signals. The sixteen 155.52 Mb/s data signals and a 155.52 MHz clock signal are sent to the receive byte processor.

The receive byte processor then frames on the incoming signals. The signals are descrambled and tested for section (B1) and line (B2) parity errors. The error rates are compared to user-settable thresholds. The SONET transport overhead bytes are also extracted and sent to the optional OHCTL (TERM) circuit pack. The receive byte processor also tests overhead parity and inserts overhead alarm indication signals (AIS). The status of the K1 and K2 bytes (used for protection switching) is also monitored. A timing signal (25.92 MHz) is derived from the 2.5 GHz clock signal and sent to the Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs.

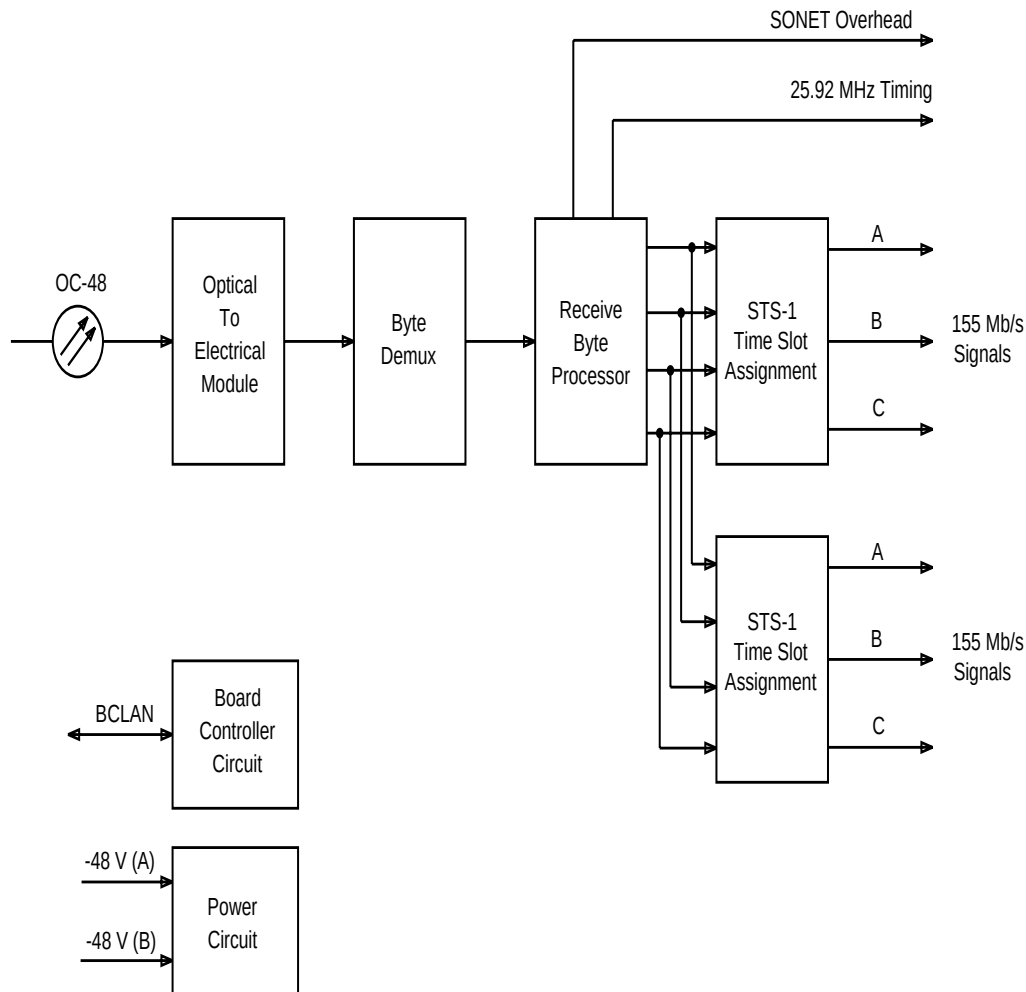


Figure 7-28. OC48 RCVR A/D (839B5 and 839E5) Circuit Pack Block Diagram

The 839B2, 839B4B, and 839E5 OC48 RCVR A/D circuit packs scramble the 155.52 Mb/s signals and pass them through a switching matrix to the low speed interface circuit packs. The switch matrix is used for implementing protection access, high speed protection, and low speed protection.

The receive byte processor (RBP) of the 839B5 OC48 RCVR A/D circuit pack transmits the sixteen 155.52 Mb/s signals to two STS-1 time slot assignment (TSA) devices that together form an equivalent 48x162 STS-1 crosspoint switch. Each STS-1 TSA device has 32 STS-3 inputs and 32 STS-3 outputs. The 16 155.52 Mb/s signal (STS-3) outputs from the receive byte processor are connected to the input of both STS-1 TSA devices (the other 16 inputs per device are unused). Fifty-four of the 64 total STS-3 output channels from the two devices are connected to the backplane.

Within the STS-1 TSA devices, the 16 STS-3 input channels are demultiplexed into 48 STS-1 tributaries. Each of these 48 inputs can be assigned to any of the 162 STS-1 time slots available in the 54 STS-3 output signals from the two devices. The STS-1 inputs can be broadcast to several or all outputs but do not have to be connected to any output. The 162 STS-1 signals are multiplexed back into 54 STS-3 signals after time slot assignment.

The connection matrix of the STS-1 TSA devices is programmed by the Board Controller. The switch fabric provisioning is fully data hitless and 100 percent non-blocking.

The STS-3 inputs to the STS-1 TSA devices are arranged in groups of four, or "quads."

The outgoing STS-3 tributaries are arranged in three groups (A, B, and C), each containing sixteen service STS-3 tributaries and two protection STS-3 tributaries. A low speed protection switch can be performed by switching input signals to output protection tributaries.

The OC48 RCVR A/D circuit pack interfaces with up to three different buses (add/drop, high speed protection, and through) that carry up to eighteen 155.52 Mb/s signals each (sixteen service and two protection). The add/drop bus provides access to the low speed interface circuit packs. The high speed protection bus provides access to the OC48 TRMTR A/D circuit pack in the same direction for high speed line protection switching. The through bus provides access to the OC48 TRMTR A/D circuit pack for the 155.52 Mb/s signals that are passed through in bidirectional ring applications.

Control Circuitry

The board controller circuit controls most circuit pack activities. The board controller circuit interfaces with the Line Controller (LNCTL) circuit pack via the board controller local area network (BCLAN).

The OC48 RCVR A/D circuit pack reports the status of the circuit pack and the incoming OC-48 signal, as well as inventory information (CLEI code, serial number, etc.). The status information is used by the LNCTL circuit pack for fault detection and isolation. Conversely, the OC48 RCVR A/D circuit pack responds to control signals from the LNCTL circuit pack.

The OC48 RCVR A/D circuit pack also accesses the SONET transport overhead bytes and routes them via the transport overhead channel interface to the optional OHCTL (TERM) circuit pack.

Timing Circuitry

The receive byte processor of the OC48 RCVR A/D circuit pack generates a 25.92 MHz reference signal from the 2.5 GHz clock signal and sends it to the TG3 (DS1) circuit packs.

Protection Circuitry

High Speed Protection. In bidirectional ring applications, the FT-2000 OC-48 Add/Drop-Rings Terminal performs revertive bidirectional 2-fiber ring protection switching. The OC48 RCVR A/D circuit pack is protection switched by the LNCTL circuit pack in response to an external command, incoming signal failure, or internal equipment fault.

The switch matrix on the protection OC48 RCVR A/D circuit pack also allows access to the protection OC-48 high speed line (OC-48 point-to-point applications) or the protection 155.52 Mb/s signals (STS-3 tributaries in bidirectional 2-fiber ring applications) for low priority traffic or for restoration purposes.

Low Speed Protection. Low speed protection switching in the receive direction is done by routing the 155.52 Mb/s signal for the failed service electrical low speed interface circuit pack (DS3 or STS1E) to the protection DS3 or STS1E circuit pack. The OC48 RCVR A/D circuit pack also bridges the same 155.52 Mb/s signal (STS-3/3c) to the active and standby OC3 (1.3 STD) circuit packs. Low speed protection switching is performed in response to commands from the LNCTL circuit pack via the BCLAN.

Dual Ring Interworking Protection. The OC48 RCVR A/D circuit pack at the primary node of a dual ring interworking application bridges the 155.52 Mb/s signal (STS-3 tributary) in the drop direction to provide two identical signals to the other ring. One 155.52 Mb/s signal is sent to the local low speed interface circuit pack and the other 155.52 Mb/s signal is sent to the OC48 TRMTR circuit pack in the opposite direction for transmission to the secondary node. In Release 7 and

later releases, the OC48 TRMTR circuit pack may switch to (select) the standby 51.84 Mb/s signal (STS-1 tributary) from the secondary node. This completes the circuit from the secondary node to the termination node in the ring.

Fault Detection Circuitry

Monitoring and Testing. The board controller circuit monitors all the activities on the circuit pack. The OC48 RCVR A/D circuit pack has an in-service and out-of-service built-in test capability. An out-of-service test is performed whenever the OC48 RCVR A/D circuit pack resets or is commanded to reset by the LNCTL circuit pack through the board controller local area network (BCLAN). When the OC48 RCVR A/D is inserted in a slot, an out-of-service test is also performed. In-service testing is continuous. The board controller circuit reports errors when they occur to the LNCTL circuit pack via the BCLAN.

Performance Monitoring. The OC48 RCVR A/D circuit pack provides performance-monitoring circuitry for the following performance-monitoring parameters:

- Received optical power
- Loss of signal
- Loss of frame
- Bit error rate thresholds.

When an incoming line AIS, high speed defect, or internal fault is detected, the OC48 RCVR A/D circuit pack performs the following:

- Generates an STS-1 path AIS (if possible, since clock may not be available) toward the low speed interface circuit pack
- Signals the OC48 TRMTR circuit pack to insert a remote defect indicator-line (RDI-L) signal.

The OC48 RCVR A/D circuit pack passes through any remote defect indicator-path (RDI-P) signals and embedded DS3 AIS to the DS3 circuit packs. The OC48 RCVR A/D circuit pack also passes through any embedded STS-1 path AIS to the low speed interface circuit packs. The OC48 RCVR A/D also detects any incoming RDI-L signals.

Power Circuitry

The OC48 RCVR A/D circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. Modular DC-to-DC power converters produce +5 and –5.2 volts used on the circuit pack.

The OC48 RCVR A/D circuit pack monitors the two –48 volt sources to the circuit pack and any failures are reported to the board controller. If the OC48 RCVR A/D circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or +5 volt power converter fails, the LNCTL circuit pack provides +5 volts to light the FAULT LED on the OC48 RCVR A/D circuit pack.

Quick Reference Summary

Transmission Functions

The OC48 RCVR A/D circuit pack performs the following receive functions:

- a. Receives a SONET standard OC-48 optical signal
- b. Converts the OC-48 signal to an electrical STS-48 signal
- c. Extracts the 2.5 GHz clock signal and regenerates the STS-48 data signal
- d. Demultiplexes the STS-48 signal into sixteen 155.52 Mb/s signals
- e. Extracts the SONET transport overhead bytes and sends them to the OHCTL (TERM) circuit pack
- f. Routes the sixteen 155.52 Mb/s signals to two STS-1 time slot assignment (TSA) devices (839B5 or 839E5 OC48 RCVR A/D circuit packs only)
- g. Routes sixteen 155.52 Mb/s signals to the service DS3, STS1E, and/or OC3 (1.3 STD) circuit packs and one 155.52 Mb/s signal to the protection DS3 or STS1E circuit pack
- h. Passes through up to sixteen 155.52 Mb/s signals to the OC48 TRMTR A/D circuit pack in the opposite direction in bidirectional ring applications.

Timing and Control Functions

The OC48 RCVR A/D circuit pack performs the following timing and control functions:

- a. Extracts a 2.5 GHz clock signal from the incoming OC-48 signal
- b. Generates a 25.92 MHz timing reference signal and provides it to the TG3 (DS1) circuit packs
- c. Tests the incoming 155.52 Mb/s signals for section (B1) and line (B2) parity errors and compares the error rates to preset thresholds
- d. Monitors the K1 and K2 bytes
- e. Routes the 155.52 Mb/s signal for the failed service DS3 or STS1E circuit pack to the protection DS3 or STS1E circuit pack during low speed protection switching
- f. Routes the same 155.52 Mb/s signal to the active and standby OC3 (1.3 STD) circuit packs.
- g. Line switches up to eight 155.52 Mb/s signals to the OC48 TRMTR A/D circuit pack in the same direction in 2-fiber bidirectional ring applications.
- h. Allows access to the protection OC-48 high speed line (OC-48 point-to-point applications) or protection 155.52 Mb/s signals (STS-3 tributaries in 2-fiber bidirectional ring applications) for low priority traffic and for restoration access

- i. Bridges 155.52 Mb/s signals (STS-3 tributaries) at the primary node to provide two identical signals to the other ring in dual ring interworking applications
- j. Performs internal fault detection and interfaces with the LNCTL circuit pack using the board controller local area network (BCLAN)
- k. Controls the circuit pack faceplate FAULT LED
- l. Stores inventory information (CLEI code, serial number, etc.).

Maintenance Signal Functions

The OC48 RCVR A/D circuit pack performs the following maintenance signal functions:

- a. Detects incoming OC-48 loss of signal (LOS) and loss of frame (LOF)
- b. Detects line AIS from the incoming OC-48 signal
- c. Detects RDI-L signals from the incoming OC-48 signal
- d. Inserts an STS-1 path AIS in each STS-1 tributary of the 155.52 Mb/s signal transmitted to the low speed interface circuit pack.
- e. Signals the OC48 TRMTR circuit pack to insert an RDI-L signal in the OC-48 signal
- f. Passes RDI-P signals from the incoming OC-48 signal to the low speed interface circuit packs.
- g. Passes embedded DS3 AIS from the incoming OC-48 signal to the DS3 circuit pack.
- h. Passes embedded STS-1 path AIS from the incoming OC-48 signal to the low-speed interface circuit packs.

OC48 TRMTR A/D (739B_, 739C_, 739E_, 739G_, 739H_, 739J_, 739P5, 739R_, 739S_)

Circuit Pack Description

Purpose of Circuit

The OC48 Transmitter (OC48 TRMTR A/D) circuit pack provides a high speed interface between sixteen 155.52 Mb/s electrical signals and the outgoing OC-48 optical signal (2.5 Gb/s).

The 739B2, 739C2, 739E_, and 739J2 OC48 TRMTR A/D circuit packs are used in bidirectional ring applications and are compatible with Release 3.0.1-ADR and later software. The 739J3 OC48 TRMTR (A/D 1.5 LBA) circuit pack is used in bidirectional ring applications and is compatible with Release 5.0.1-ADR and later software. The 739R2 OC48 TRMTR (A/D 1.5 LBA W1) and 739S2 OC48 TRMTR (A/D 1.5 LBA W2) circuit packs are used in bidirectional ring applications and are compatible with Release 6.0.0-ADR and later software. The 739E_, 739G_, and 739H_ circuit packs are used in DWDM applications and are compatible with Release 7.2 and later software.

The 739B4 OC48 TRMTR (A/D 1.3 STD) circuit pack is a cost-reduced version of the 739B2 OC48 TRMTR (A/D 1.3 STD) circuit pack and is compatible with Release 7.0.0-ADR and later software. The following OC48 TRMTR circuit packs have an STS-1 time slot assignment capability that allows STS-1 cross-connections and are compatible with Release 7.0.0-ADR and later software.

- 739B5 OC48 TRMTR (A/D STS1 1.3 STD)
- 739C5 OC48 TRMTR (A/D STS1 1.3 HI PERF)
- 739J5 OC48 TRMTR (A/D STS1 1.5 STD)
- 739P5 OC48 TRMTR (A/D STS1 1.5 LBA)
- 739R5 OC48 TRMTR (A/D STS1 1.5 LBA, W1)
- 739S5 OC48 TRMTR (A/D STS1 1.5 LBA, W2).

The following OC48 TRMTR circuit packs have an STS-1 time slot assignment capability that allows STS-1 cross-connections and are compatible with Release 7.2.0-ADR and later software:

- 739E_ OC48 TRMTR (A/D STS1 1.5 360 km DISP)
- 739G_ OC48 TRMTR (A/D STS1 1.5 640 km DISP)
- 739H_ OC48 TRMTR (A/D STS1 1.5 60 km DISP).

Faceplate LED

The OC48 TRMTR A/D circuit pack has a red FAULT LED on its faceplate (Figure 7-29).

The red FAULT LED is lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the Line Controller (LNCTL) circuit pack to light the FAULT LED on the OC48 TRMTR A/D circuit pack.

The board controller on the OC48 TRMTR A/D circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the board controller is reset and the FAULT LED on the circuit pack is continuously lighted.

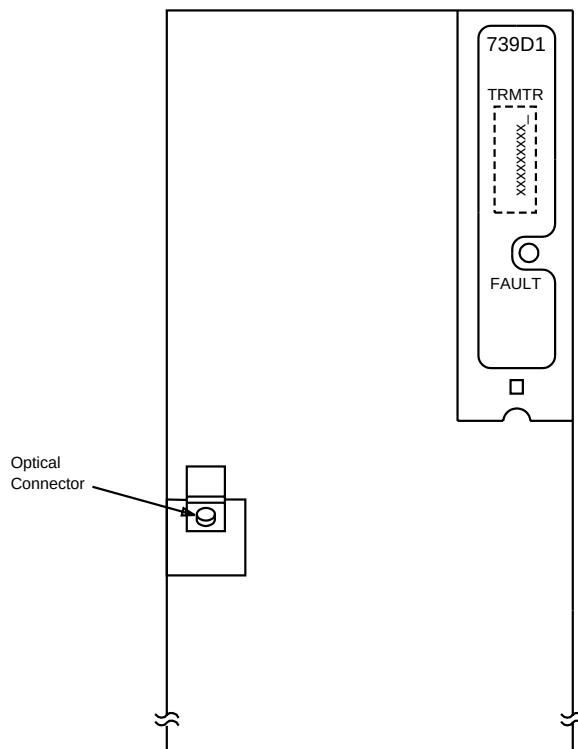


Figure 7-29. OC48 TRMTR A/D (739B2, 739B4, 739B5, 739C2, 739C5, 739E[1-16], 739G[1-8], 739H[1-16], 739J2, 739J3, 739J4, 739J5, 739P5, 739R2, 739R5, 739S2, and 739S5) Circuit Pack

General Description of Operation

The OC48 TRMTR A/D circuit pack interfaces with the outgoing high speed optical line. The OC48 TRMTR A/D circuit pack selects up to sixteen 155.52 Mb/s signals from sixteen DS3, STS1E, and/or OC3 (1.3 STD) circuit packs, one protection DS3 circuit pack, one protection STS1E circuit pack, or the OC48 RCVR A/D circuit pack in the opposite direction (bidirectional ring applications).

The 739B5, 739C5, 739E_, 739G_, 739H_, 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR A/D circuit packs demultiplex the 155.52 Mbs signals to STS-1 signals and select 48 STS-1 signals (out of 96 STS-1 signals) for STS-1 time slot assignment. The 739B5, 739C5, 739E_, 739H_, 739G_, 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR A/D circuit packs then multiplex the 48 STS-1 signals into sixteen 155.52 Mb/s signals.

The sixteen 155.52 Mb/s signals are frame-synchronized and multiplexed into an STS-48 signal (2.488 Gb/s). SONET line and section overhead bytes are also inserted into the STS-48 signal. The electrical STS-48 signal is then converted to an optical OC-48 signal for transmission over a single-mode fiber.

The OC48 TRMTR A/D circuit pack is available in the standard 1.3 μm , high performance 1.3 μm , and standard 1.5 μm options. For more information about loss budgets and transmission distances when operating with or without the FT-Lightwave Booster Amplifier (FT-LBA), refer to Volume I, Section 10, "Technical Specifications."

Detailed Description of Operation

Transmission Circuitry

Figure 7-30 shows an overall block diagram of the 739B2, 739B4, 739C2, 739J2, 739J3, 739J4, 739R2, and 739S2 OC48 TRMTR A/D circuit packs. Figure 7-31 shows an overall block diagram of the 739E[1-16], 739G[1-8], 739H[1-16], 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR A/D circuit packs. The OC48 TRMTR A/D circuit packs interface with up to three different buses (add/drop, high speed protection, and through) that carry up to eighteen 155.52 Mb/s signals each (sixteen service and two protection). The add/drop bus provides access to the 155.52 Mb/s signals from the low speed interface circuit packs. The high speed protection bus provides access to the 155.52 Mb/s signals from the OC48 RCVR A/D circuit pack in the same direction for high speed line protection switching. The through bus provides access to the 155.52 Mb/s signals from the OC48 RCVR A/D circuit pack that are passed through in bidirectional ring applications.

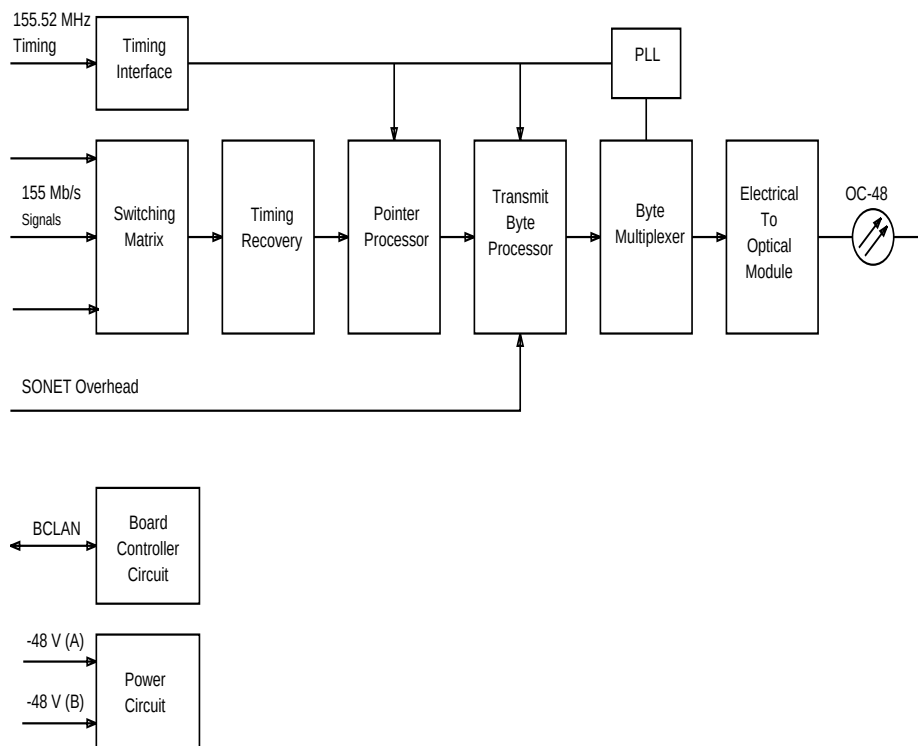


Figure 7-30. OC48 TRMTR A/D (739B2, 739B4, 739C2, 739J2, 739J3, 739J4, 739R2, and 739S2) Circuit Pack Block Diagram

A switching matrix interfaces with up to three 155.52 Mb/s buses (add/drop, high speed protection, and through) that carry up to eighteen 155.52 Mb/s signals each (sixteen service and two protection). The add/drop bus allows access to the low speed interface circuit packs for local traffic to be added. The high speed protection bus allows the through traffic from the OC48 RCVR (A/D) circuit pack to be looped back to the OC48 TRMTR (A/D) circuit pack. The through bus allows through traffic to be routed from OC-48 high speed line 1E to 1W or from OC-48 high speed line 1W to 1E. The switching matrix routes sixteen 155.52 Mb/s signals to the timing recovery circuit. The timing recovery circuit extracts a 155.52 MHz timing signal from the data stream, retimes the data with respect to the output clock, and passes it to the pointer processor. The timing recovery circuit extracts a 155.52 MHz timing signal from the data stream, retimes the data with respect to the output clock, and passes it to the pointer processor.

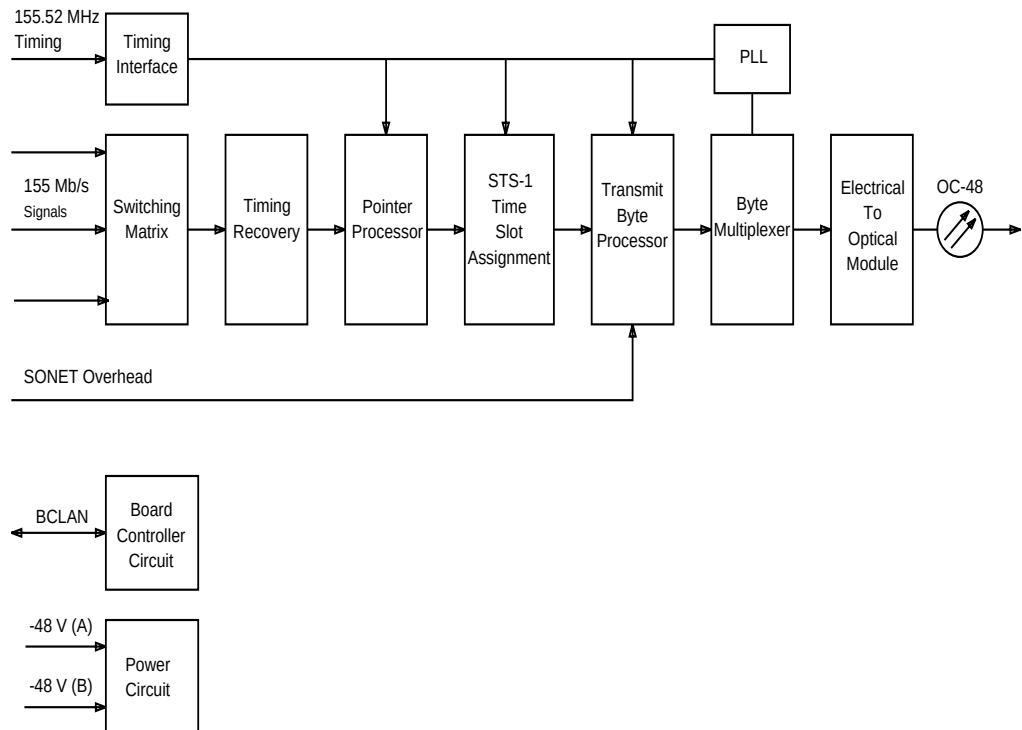


Figure 7-31. OC48 TRMTR A/D Circuit Pack (739B5, 739C5, 739E[1-16], 739G[1-8], 739H[1-16], 739J5, 739P5, 739R5, and 739S5) Block Diagram

The pointer processor performs phase, frequency, and frame realignment. The input data is descrambled and fed into an elastic store that accommodates input signal phase and wander. Frequency alignment is accomplished by using pointer adjustments (byte stuffs). New overhead frames are generated with respect to the local frame synchronization, and new pointer values are inserted into the outgoing overhead. After pointer processing, the 155.52 Mb/s signals are frame-synchronized and phase-aligned to the local system clock. The 739B2, 739B4, 739C2, 739J2, 739J3, 739J4, 739R2, and 739S2 OC48 TRMTR A/D circuit packs then pass the 155.52 Mb/s signals to the transmit byte processor. The 739B5, 739C5, 739E[1-16], 739G[1-8], 739H[1-16], 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR A/D circuit packs pass the 155.52 Mb/s signals to the STS-1 time slot assignment (TSA) device.

The STS-1 TSA device in the 739B5, 739C5, 739E[1-16], 739G[1-8], 739H[1-16], 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR A/D circuit packs demultiplexes the 155.52 Mb/s (STS-3) signals to 96 STS-1 signals. Forty-eight STS-1 signals can be cross-connected to output time slots in the OC-48 signals. The STS-1 cross-connections are based on the connection matrix loaded into the STS-1 TSA device by the board controller. The STS-1 switch fabric provisioning is fully data hitless and 100 percent non-blocking. This allows any input STS-1 signal to be placed in any position in the byte interleaved OC-48 line signal. The STS-1 TSA device multiplexes the 48 STS-1 signals to sixteen STS-3 (155.52 Mb/s) signals and sends them to the transmit byte processor.

The transmit byte processor combines the pointer and payload information from the 155.52 Mb/s signals with internally and externally generated SONET transport overhead to form a parallel scrambled format. The optional Overhead Controller - Terminal [OHCTL (TERM)] circuit pack provides the externally generated SONET transport overhead bytes. The 155.52 Mb/s signals are then passed to the multiplexer circuit.

The multiplexer circuit byte interleaves the 155.52 Mb/s signals into a single 2.488 Gb/s STS-48 serial data stream and passes it to the electrical-to-optical module.

The electrical-to-optical module converts the STS-48 signal to an optical OC-48 signal. A single line distributed feedback laser with driver and control circuits converts the electrical signal to optical pulses for transmission.

Fiber access to the OC48 TRMTR A/D circuit pack is via a fixed *ST*-type buildout block and a removable *ST*-type, *FC/PC*-type, or *SC*-type lightguide buildout on the circuit pack faceplate. Lightguide buildouts are chosen based on the type of connector interface the single-mode lightguide jumper is equipped with. There are three types of lightguide buildouts available for OC48 TRMTR A/D circuit packs: *ST*-type, *FC/PC*-type, and *SC*-type. All factory-equipped OC48 TRMTR A/D circuit packs come with a removable *ST*-type 0-dB lightguide buildout. When installing or removing lightguide buildouts, do not pull the beam (front tab) outward. Pulling the beam (front tab) outward could result in breaking the beam (front tab).

Control Circuitry

The board controller circuit controls all the circuit pack activities. The board controller circuit interfaces with the Line Controller (LNCTL) circuit pack via the board controller local area network (BCLAN).

The OC48 TRMTR A/D circuit pack reports the status of the circuit pack and the outgoing OC-48 signal, as well as inventory information (CLEI code, serial number, etc.). The status information is used by the LNCTL circuit pack for fault detection and isolation. Conversely, the OC48 TRMTR A/D circuit pack responds to control signals from the LNCTL circuit pack.

The OC48 TRMTR A/D circuit pack also accepts the SONET transport overhead bytes from the OHCTL (TERM) circuit pack via the transport overhead channel interface.

Timing Circuitry

The OC48 TRMTR A/D circuit pack accepts two 155.52 MHz timing references from the Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs. A switch device on the OC48 TRMTR A/D circuit pack selects one timing reference. A primary and secondary timing signal is provided for redundancy.

Protection Circuitry

High Speed Protection. In bidirectional ring applications, the FT-2000 OC-48 Add/Drop-Rings Terminal performs revertive bidirectional line protection switching. The OC48 TRMTR A/D circuit pack is protection switched by the LNCTL circuit pack in response to an external command, incoming signal failure, or internal equipment fault.

The switch matrix on the protection OC48 TRMTR A/D circuit pack allows access to the protection OC-48 high speed line (OC-48 point-to-point applications) or the protection 155.52 Mb/s signals (STS-3 tributaries in bidirectional 2-fiber ring applications) for low priority traffic or for restoration purposes.

Low Speed Protection. Low speed protection switching in the transmit direction is done by selecting a 155.52 Mb/s signal from the protection DS3 or STS1E circuit pack to substitute for a 155.52 Mb/s signal from a failed service DS3 or STS1E circuit pack. The OC48 TRMTR A/D circuit pack also accepts the same 155.52 Mb/s signal from a pair of 1+1 protected OC (1.3 STD) circuit packs. The OC48 TRMTR A/D circuit pack selects one 155.52 Mb/s signal as the active line and the other 155.52 Mb/s signal as the standby line. Low speed protection switching is performed in response to commands from the LNCTL circuit pack via the BCLAN.

Dual Ring Interworking Protection. The OC48 TRMTR A/D circuit pack at the primary node of a dual ring interworking application accepts an active 155.52 Mb/s signal (STS-3 tributary) from the local low speed interface circuit pack or an identical standby 155.52 Mb/s signal (STS-3 tributary) from the secondary node. In Release 4.1 and later releases, if an incoming DS3 or EC-1 loss-of-signal

condition is detected at the primary node, the LNCTL circuit pack broadcasts an automatic switch request to the board controller on the OC48 TRMTR A/D circuit pack via the BCLAN. In the add (toward to OC-48 line) direction only, the OC48 TRMTR A/D circuit pack switches to (selects) the standby 155.52 Mb/s signal (STS-3 tributary) from the secondary node. This completes the circuit from the secondary node to the termination node in the ring. In Release 5 and later releases, dual ring interworking protection switching also takes place in response to an unprotected incoming OC-3 loss-of-signal or loss-of-frame condition.

Synchronization Protection. Synchronization reference switching is done by selecting the primary or secondary synchronization reference signal from the TG3 (DS1) circuit packs. The OC48 TRMTR A/D circuit pack monitors these synchronization reference signals and can autonomously select the primary or secondary. The LNCTL circuit pack can inhibit this autonomous selection and make its own selection.

Fault Detection Circuitry

Monitoring and Testing. The board controller circuit monitors all the activities on the circuit pack. The OC48 TRMTR A/D circuit pack has an in-service and out-of-service built-in test capability. An out-of-service test is performed whenever the OC48 TRMTR A/D circuit pack resets or is commanded to reset by the LNCTL circuit pack through the board controller local area network (BCLAN). In-service testing is continuous. The board controller circuit reports errors when they occur to the LNCTL circuit pack via BCLAN. An out-of-service test is also performed whenever the OC48 TRMTR A/D circuit pack is inserted in a slot.

Performance Monitoring. The OC48 TRMTR A/D circuit pack performs the following performance-monitoring functions:

- Laser bias current
- Optical transmit power.

When an incoming high speed defect or line AIS is detected by the OC48 RCVR circuit pack, the OC48 TRMTR A/D circuit pack inserts an RDI signal in the outgoing OC-48 high speed signal.

When an incoming low speed defect is detected from the low speed interface circuit packs, the OC48 TRMTR A/D circuit pack inserts an STS-1 path AIS in each STS-1 tributary of the outgoing OC-48 high speed signal.

When an internal fault is detected in the OC48 TRMTR A/D circuit pack, a line AIS is inserted in the outgoing OC-48 high speed signal.

The OC48 TRMTR A/D circuit pack also passes through any remote defect indicator-path (RDI-P) signals, DS3 AIS, or STS-1 path AIS to the outgoing OC-48 high speed signal.

Power Circuitry

The OC48 TRMTR A/D circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. Modular DC-to-DC power converters produce +5, –5.2, +15 and –2 volts used on the circuit pack.

The OC48 TRMTR A/D circuit pack monitors the two –48 volt sources to the circuit pack and any failures are reported to the board controller. If the OC48 TRMTR A/D circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or +5 volt power converter fails, the LNCTL circuit pack provides +5 volts to light the FAULT LED on the OC48 TRMTR A/D circuit pack.

Quick Reference Summary

Transmit Functions

The OC48 TRMTR A/D circuit pack performs the following transmit functions:

- a. Accepts sixteen 155.52 Mb/s signals from the service DS3 and STS1E circuit packs, one 155.52 Mb/s signal from the protection DS3 circuit pack, and one 155.52 Mb/s signal from the protection STS1E circuit pack
- b. Demultiplexes the sixteen 155.52 Mb/s signals to 48 STS-1 signals, cross-connects the STS-1 signals to time slots in the OC-48 signal, and remultiplexes the 48 STS-1 signals to sixteen 155.52 Mb/s signals (739B5, 739C5, 739E[1-16], 739G[1-8], 739H[1-16], 739J5, 739P5, 739R5, and 739S5 OC48 TRMTR A/D circuit packs only)
- c. Inserts the SONET transport overhead bytes
- d. Bit-interleaves sixteen 155.52 Mb/s signals to produce a SONET standard STS-48 signal
- e. Modulates a laser transmitter to produce a SONET standard OC-48 optical signal.

Timing and Control Functions

The OC48 TRMTR A/D circuit pack performs the following timing and control functions:

- a. Selects the primary or secondary 155.52 MHz synchronization signal from the TG3 (DS1) circuit packs
- b. Provides selector circuitry for low speed protection switching
- c. Provides selector circuitry at the primary node for dual ring interworking protection switching
- d. Accepts up to eight 155.52 Mb/s signals from the OC48 RCVR A/D circuit pack in the opposite direction during line switches in bidirectional ring applications
- e. Allows access to the protection OC-48 high speed line (OC-48 point-to-point applications) or protection 155.52 Mb/s signals (STS-3 tributaries in 2-fiber bidirectional ring applications) for low priority traffic or for restoration purposes
- f. Performs internal fault detection and interfaces with the LNCTL circuit pack using the BCLAN
- g. Controls the circuit pack faceplate FAULT LED
- h. Stores inventory information (CLEI code, serial number, etc.).

Maintenance Signal Functions

The OC48 TRMTR A/D circuit pack performs the following maintenance signal functions:

- a. Inserts an RDI-L signal in the outgoing OC-48 high speed signal
- b. Inserts an STS-1 path AIS in each STS-1 tributary of the outgoing OC-48 high speed signal
- c. Inserts a line AIS in the outgoing OC-48 high speed signal
- d. Passes through any RDI-P signals from the low speed interface circuit packs to the outgoing OC-48 high speed signal
- e. Passes through any embedded DS3 AIS from the DS3 circuit packs to the outgoing OC-48 high speed signal.
- f. Passes through any embedded STS-1 path AIS from the low-speed interface circuit packs to the outgoing OC-48 high-speed signal.

OC48 REGENR (39B1, 39B2, 39C1, 39C2, 39J2, 39R2, and 39S2) Circuit Pack Description

Purpose of Circuit

The OC48 Regenerator (OC48 REGENR) circuit pack regenerates one OC-48 high speed signal. The 39B1 and 39C1 OC48 REGENR circuit packs have been discontinued and replaced with the 39B2 and 39C2 OC48 REGENR circuit packs, respectively. The 39B1, 39B2, 39C1, and 39C2 OC48 REGENR circuit packs are compatible with Release 2.1.0-RPTR and 4.0.0-RPTR software. The 39J2 OC48 REGENR circuit pack is compatible with Release 4.0.0-RPTR software and later. The 39R2 and 39S2 OC48 REGENR circuit packs are compatible with Release 6.0.0-RPTR software and later.

Faceplate LED

The OC48 REGENR circuit pack has a red FAULT LED on its faceplate (Figure 7-32). The red FAULT LED is continuously lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the System Controller (SYSCTL) circuit pack to light the FAULT LED on the OC48 REGENR circuit pack.

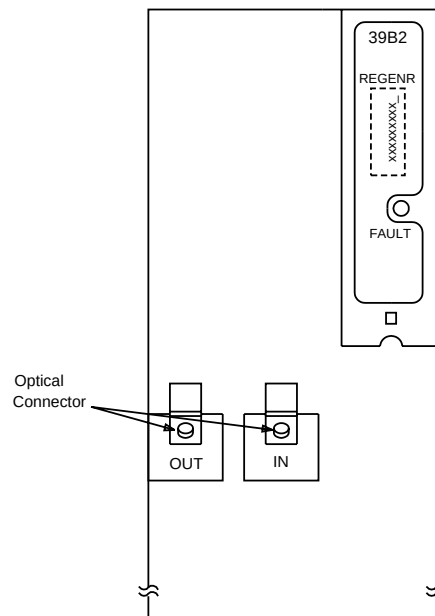


Figure 7-32. OC48 REGENR (39B1, 39B2, 39C1, 39C2, 39J2, 39R2, and 39S2) Circuit Pack

The board controller on the OC48 REGENR circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the board controller is reset and the FAULT LED on the circuit pack is continuously lighted. If an incoming OC-48 signal fails, the FAULT LED will flash on and off.

General Description of Operation

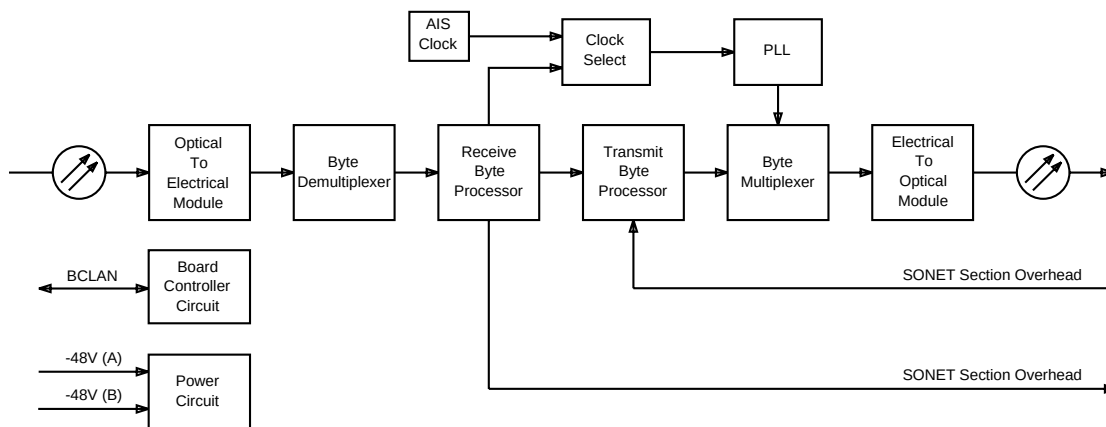
The OC48 REGENR circuit pack accepts the incoming OC-48 signal (2.5 Gb/s) and converts it to an electrical STS-48 signal. The STS-48 signal is descrambled and demultiplexed into sixteen 155.52 Mb/s signals. (In a future release, the SONET section overhead bytes are extracted.) The sixteen 155.52 Mb/s signals are then frame-synchronized and multiplexed back into an STS-48 signal (2.488 Gb/s). The electrical STS-48 signal is then converted to an optical OC-48 signal for transmission over a single-mode fiber.

The OC48 REGENR circuit pack is available in the standard 1.3 μm , high performance 1.3 μm , and standard 1.5 μm options.

Detailed Description of Operation

Figure 7-33 shows an overall block diagram of an OC48 REGENR circuit pack. The OC48 REGENR circuit pack accepts one 2.5 Gb/s non-return-to-zero (NRZ) SONET compatible optical signal.

Transmission Circuitry



tpa 814225/01

Figure 7-33. OC48 REGENR Circuit Pack Block Diagram

Fiber access to the OC48 REGENR circuit pack is via two fixed ST-type buildout blocks and removable ST-type, FC/PC-type, or SC-type lightguide buildouts on the circuit pack faceplate (labeled IN and OUT). Lightguide buildouts for the IN connector are chosen based on the attenuation desired, the type of OC48 REGENR circuit pack, and the type of connector interface the single-mode lightguide jumper is equipped with. Lightguide buildouts for the OUT connector are chosen based on the type of OC48 REGENR circuit pack and the type of connector interface the single-mode lightguide jumper is equipped with. There are two types of lightguide buildouts available for 39B1 and 39C1 OC48 REGENR circuit packs: ST-type and FC/PC-type. There are three types of lightguide buildouts available for 39B2, 39C2, 39J2, 39R2, and 39S2 OC48 REGENR circuit packs: ST-type, FC/PC-type, and SC-type. All factory-equipped OC48 REGENR circuit packs come with a removable ST-type 0-dB lightguide buildout on the IN and OUT connectors. When installing or removing lightguide buildouts, do not pull the beam (front tab) outward. Pulling the beam (front tab) outward could result in breaking the beam (front tab).

The optical-to-electrical module receives the OC-48 signal and converts it to an electrical STS-48 signal. The optical-to-electrical module also detects loss of

signal (LOS). Timing is also extracted and the data is regenerated. The STS-48 data signal and 2.5 GHz clock signal are then passed to the byte demultiplexer.

The byte demultiplexer receives the STS-48 data signal from the optical-to-electrical converter and demultiplexes the STS-48 data signal to sixteen 155.52 Mb/s signals. The sixteen 155.52 Mb/s data signals and a 155.52 MHz clock signal are sent to the receive byte processor in a parallel format.

The receive byte processor frames on the incoming signals and converts them to 16 byte serial 155.52 Mb/s data signals. The SONET section overhead bytes are also extracted and sent to the optional Orderwire (OW) circuit pack. A synchronization reference signal (155.52 MHz) is also generated and sent to the phase-locked loop (PLL) circuit.

The sixteen 155.52 Mb/s signals are then passed to the transmit byte processor. The transmit byte processor combines the pointer and payload information from the 155.52 Mb/s signals to form a parallel format. The 155.52 Mb/s signals are then passed to the byte multiplexer circuit.

The byte multiplexer circuit bit interleaves the 155.52 Mb/s signals into a single 2.488 Gb/s STS-48 serial data stream and passes it to the electrical-to-optical module.

The electrical-to-optical module converts the STS-48 signal to an optical OC-48 signal. A single line distributed feedback laser with driver and control circuits, converts the electrical signal to optical pulses for transmission.

Control Circuitry

The board controller circuit controls all the circuit pack activities. The board controller circuit interfaces with the System Controller (SYSCTL) circuit pack via the board controller local area network (BCLAN).

The OC48 REGENR circuit pack reports the status of the circuit pack and the incoming OC-48 signal, as well as inventory information (CLEI code, serial number, etc.). The status information is used by the SYSCTL circuit pack for fault detection and isolation. Conversely, the OC48 REGENR circuit pack responds to control signals from the SYSCTL circuit pack.

The board controller also interfaces with the receive byte processor and transmit byte processor to access the F1 byte. The F1 byte is used for communication between repeater sites and FT-2000 OC-48 Add/Drop-Rings Terminals (Release 4 and later).

Timing Circuitry

The receive byte processor of the OC48 REGENR circuit pack generates a 155.52 MHz reference signal from the 2.5 GHz clock signal and sends it to the phase-locked loop (PLL) circuit. The PLL circuit uses this 155.52 MHz reference signal or the alarm indication signal (AIS) clock as a timing reference. The PLL circuit provides a 2.5 GHz line rate clock signal to the multiplexer circuitry. If a loss of clock condition is detected, a local 155.52 MHz signal is sent to the PLL circuit.

Fault Detection Circuitry

Monitoring and Test. The board controller circuit monitors all the activities on the circuit pack. The OC48 REGENR circuit pack has an in-service and out-of-service built-in test capability. An out-of-service test is performed whenever the OC48 REGENR circuit pack resets or is commanded to reset by the SYSCTL circuit pack through the BCLAN. In-service testing is continuous. The board controller circuit reports errors when they occur to the SYSCTL circuit pack via the BCLAN. When the OC48 REGENR is inserted in a slot or reset, an out-of-service test is performed.

Performance Monitoring

The OC48 REGENR circuit pack provides performance-monitoring circuitry for the following performance parameters:

- Received optical power
- Errored frames
- Section (B1) errors
- Laser bias current
- Optical transmit power.

When an incoming high speed defect or internal fault is detected, the OC48 REGENR circuit pack inserts a line AIS in the outgoing OC-48 high speed signal.

Power Circuitry

The OC48 REGENR circuit pack receives two sources of –48 volts that are diode OR'd, fused, and filtered. Modular DC-to-DC power converters produce +1.8, +5, +12, +15, –2, and –5.2 volts used on the circuit pack.

The OC48 REGENR circuit pack monitors the two –48 volt sources to the circuit pack and any failures are reported to the board controller. If the OC48 REGENR circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or +5 volt power converter fails, the SYSCTL circuit pack provides +5 volts to light the FAULT LED on the OC48 REGENR circuit pack.

Quick Reference Summary

Receive Functions

The OC48 REGENR circuit pack performs the following receive functions:

- a. Receives a SONET standard OC-48 optical signal
- b. Converts the OC-48 signal to an electrical STS-48 signal
- c. Extracts the 2.5 GHz clock signal and regenerates the STS-48 data signal
- d. Demultiplexes the STS-48 signal into sixteen 155.52 Mb/s signals
- e. Extracts the SONET transport overhead bytes and sends them to the OW circuit pack
- f. Generates a 155.52 MHz timing reference signal and provides it to the PLL circuit.

Transmit Functions

The OC48 REGENR circuit pack performs the following transmit functions:

- a. Bit-interleaves the sixteen 155.52 Mb/s signals to produce a SONET standard STS-48 signal
- b. Modulates a laser transmitter to produce a SONET standard OC-48 optical signal
- c. Inserts the SONET transport overhead bytes in the OC-48 line.

Timing and Control Functions

The OC48 REGENR circuit pack performs the following timing and control functions:

- a. Extracts a 2.5 GHz clock signal from the incoming OC-48 high speed signal
- b. Generates a 155.52 MHz timing reference signal and provides it to the PLL circuit
- c. Performs internal fault detection and interfaces with the LNCTL circuit pack using the BCLAN
- d. Controls the circuit pack faceplate FAULT LED
- e. Stores inventory information (CLEI code, serial number, etc.).

Maintenance Signal Functions

The OC48 REGENR circuit pack performs the following maintenance signal functions:

- a. Inserts line AIS in the outgoing OC-48 high speed signal
- b. Passes RDI-P signals, embedded DS3 AIS, line AIS, and RDI-L signals from the incoming OC-48 high speed signal to the outgoing OC-48 high speed signal.

Synchronization

TG3 (DS1) (LAA18) Circuit Pack Description

Purpose of Circuit

The Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit pack provides the OC48 Transmitter (OC48 TRMTR), DS3 Interface (DS3), and STS1 Electrical Interface (STS1E) circuit packs with their required timing signals. The LAA17 TG3 (DS1) circuit pack has been discontinued and replaced with the LAA18 TG3 (DS1) circuit pack. The LAA18 TG3 (DS1) circuit pack is compatible with Releases 2.1.1-TRM and later software.

Faceplate LED

The TG3 (DS1) circuit pack has a red FAULT LED on its faceplate (Figure 7-34). The red FAULT LED is lighted when a circuit pack failure is detected or when the circuit pack loses power. If the circuit pack loses power, a separate path is provided for power from the Line Controller (LNCTL) circuit pack to light the FAULT LED on the TG3 (DS1) circuit pack.

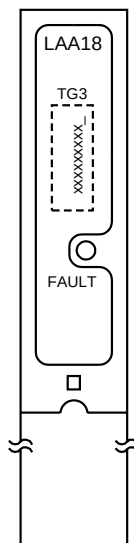


Figure 7-34. TG3 (DS1) (LAA18) Circuit Pack

The board controller on the TG3 (DS1) circuit pack detects hardware and software faults on the circuit pack. When a fault occurs, the board controller is reset and the FAULT LED on the circuit pack is continuously lighted. If an incoming DS1 reference signal fails, the FAULT LED will flash on and off.

General Description of Operation

The TG3 (DS1) circuit pack supports the synchronization features of the FT-2000 OC-48 Lightwave System in the following modes:

- **Externally timed mode:** In the externally timed mode (phase locked mode in releases prior to Release 5), each TG3 (DS1) circuit pack accepts a DS1 reference from an external stratum 3 or better clock. These references synchronize the local terminal, and any others that may loop time to it, with other network equipment operating under the same primary clock source. Within the TG3 (DS1) circuit pack, a highly stable digital phase-locked loop (DPLL) circuit removes any transient impairments on the DS1 reference for improved jitter performance.

The DS1 references are continuously monitored for error-free operation. If the selected reference becomes corrupted, the TG3 (DS1) circuit pack will select the protection reference without causing service degradations. If both DS1 inputs are corrupted, the DPLL circuit holds the on-board oscillator frequency at the last good reference sample while the references are repaired (holdover mode).

The LAA18 TG3 (DS1) circuit pack will also provide an external DS1 synchronization output signal to the office building integrated timing supply (BITS) clock or other SONET network elements in an office. This DS1 output signal is derived directly from the 25.92 MHz line reference signal.

- **Through timed mode:** In the through timed mode (FT-2000 OC-48 Add/Drop-Rings Terminal only), the TG3 (DS1) circuit pack derives timing from the incoming OC-48 high speed signals. The number 1 TG3 (DS1) circuit pack accepts a 25.92 MHz line reference signal from the west OC48 RCVR circuit pack and derives the internal synchronization clock used by the east OC48 TRMTR (for through traffic), DS3 circuit packs terminating add/drop traffic from line 1E, and STS1E, OC3 (1.3 STD), or OC12 (1.3 STD) circuit packs terminating add/drop traffic from line 1W. The number 2 TG3 (DS1) circuit pack accepts a 25.92 MHz line reference signal from the east OC48 RCVR circuit pack and derives the internal synchronization clock used by the west OC48 TRMTR (for through traffic), DS3 circuit packs terminating add/drop traffic from line 1W, and STS1E or OC3 (1.3 STD) circuit packs terminating add/drop traffic from line 1E. If the reference signals are corrupted or lost (for example, because of a cable cut), the DPLL circuit holds the on-board oscillator frequency at the last good reference sample (holdover mode).

The TG3 (DS1) circuit pack also provides an external DS1 synchronization output signal to the office BITS clock or other SONET network elements in an office. The LAA18 TG3 (DS1) circuit pack derives the DS1 output signal directly from the 25.92 MHz line reference signal.

- **Free running mode:** In the free running mode, the TG3 (DS1) circuit pack derives timing from a highly stable crystal oscillator. This oscillator has a long-term accuracy better than ± 4.6 ppm.
- **Line Timed:** In line timed mode (FT-2000 ADR Terminal Release 7.1.0 and later software), the TG3 (DS1) circuit pack derives timing from the incoming OC-48 high-speed signal. The number 1 TG3 (DS1) circuit pack accepts a 25.92 MHz line reference signal from the west OC48 RCVR circuit pack and derives the internal synchronization clock used by the east OC48 TRMTR. The number 2 TG3 (DS1) circuit pack accepts a 25.92 MHz line reference signal from the east OC48 RCVR circuit pack and derives the internal synchronization clock used by the west OC48 TRMTR. As a provisioning option, the user may choose which OC-48 line to derive timing from (e.g., E, W, or Auto). Also, as a provisioning option, the user may choose the backup option (Backup-E, Backup-W, Auto, or none). The type switching is always revertive.

Detailed Description of Operation

Control Circuitry

Figure 7-35 shows an overall block diagram of the LAA18 TG3 (DS1) circuit pack [the LAA18 replaces the LAA17 TG3 (DS1), which was discontinued]. The board controller circuit controls all the circuit pack activities. The board controller circuit interfaces with the LNCTL circuit pack via the BCLAN.

The TG3 (DS1) circuit pack reports the status of the circuit pack and the timing references, as well as inventory information (CLEI code, serial number, etc.). The status information is used by the LNCTL circuit pack for fault detection and isolation. Conversely, the TG3 (DS1) circuit pack responds to control signals from the LNCTL circuit pack.

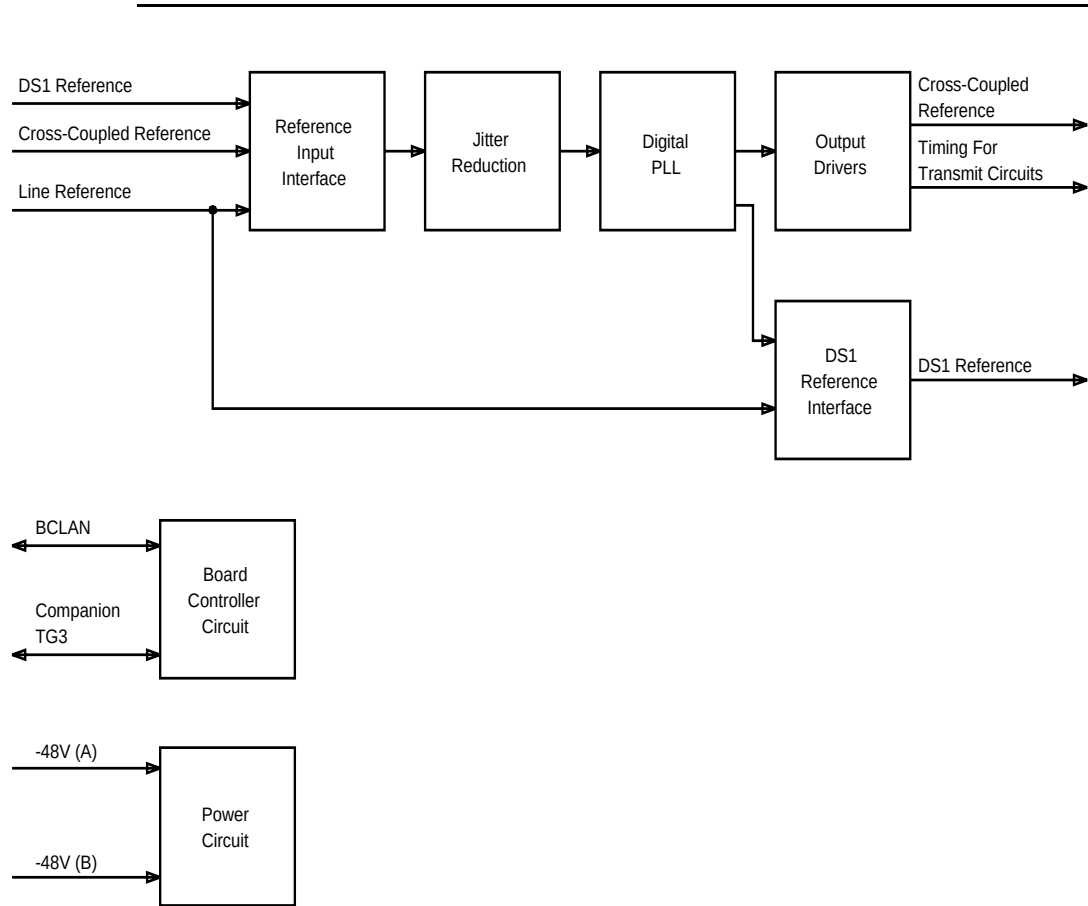


Figure 7-35. TG3 (DS1) (LAA18) Circuit Pack Block Diagram

Timing Circuitry

Reference Input Interface Circuit. The reference input interface circuit provides the required interface to all the reference inputs and selects one of the inputs as the timing reference. The reference input interface circuit interfaces with the following references:

- One external DS1 reference signal (1.544 MHz) from a stratum 3 or better source
- One line reference signal (25.92 MHz) from the OC48 Receiver circuit pack (up to four line reference signals in a future release)
- One cross-coupled reference signal (25.92 MHz) from the companion TG3 (DS1) circuit pack.

Jitter Reduction Circuit. The jitter reduction circuit reduces the jitter on the selected reference input before it enters the digital phase-locked loop circuit.

Digital Phase-Locked Loop Circuit. The digital phase-locked loop (DPLL) circuit consists of a digital counter, a microcontroller, a digital-to-analog converter, and a 51.84 MHz ovenized, voltage-controlled crystal oscillator. The microcontroller and software determine the loop parameters and loop performance, as well as reference monitoring and switching functions. The microcontroller periodically reads phase information contained in the digital counter. This information is used by the software to determine phase error and control the frequency output of the oscillator. The phase-locked loop incorporates a microprocessor as the loop filter. Software in this processor determines the loop bandwidth and performance, as well as monitoring and switching functions.

Output Driver Circuits. The output driver circuits generate and distribute the following types of timing signals:

- A 155.52 MHz signal is distributed to the OC48 Transmitter (OC48 TRMTR) and low speed interface circuit packs.
- A 25.92 MHz cross-coupled reference signal is distributed to the companion TG3 (DS1) circuit pack.

DS1 Reference Interface Circuit. The DS1 reference interface circuit generates and distributes a 1.544 MHz (DS1) synchronization output signal to the office BITS clock or other SONET network elements in an office. In the LAA18 TG3 (DS1) circuit pack (Figure 7-35), the DS1 reference interface circuit derives the DS1 output signal directly from the 25.92 MHz line reference. The DS1 reference interface circuit of the LAA18 TG3 (DS1) circuit pack also accepts a 51.84 MHz signal from the digital phase-locked loop circuit to generate DS1 alarm indication signals (AIS) whenever the line reference is not acceptable.

Protection Circuitry

The TG3 (DS1) circuit packs are 1x1 (revertive) protected. When the active TG3 (DS1) circuit pack board controller determines that its clock output is out of tolerance, it suppresses its timing outputs and signals the LNCTL circuit pack. The LNCTL circuit pack signals the companion TG3 (DS1) circuit pack of the failed condition. The suppressed timing outputs cause the transmission circuit packs to perform a hardware switch to the standby TG3 (DS1) circuit pack.

Timing reference switching is done by selecting the primary or secondary reference signal (phase locked mode or loop timed mode). In the phase locked mode, if the primary DS1 reference signal fails, the secondary DS1 reference signal will be selected. If both DS1 reference signals fail, the TG3 (DS1) circuit pack holds the on-board oscillator frequency at the last good reference sample (holdover mode) while the DS1 reference signals are repaired.

In the loop timed mode, if the primary line reference fails, the secondary line reference is selected. If both line references fail, the TG3 (DS1) circuit pack will attempt to select the primary DS1 reference (if equipped). If the primary DS1

reference is not equipped or fails, the TG3 (DS1) circuit pack will attempt to select the secondary DS1 reference (if equipped). If all four of these references fail, the TG3 (DS1) circuit pack will enter the holdover mode.

In the through timed mode, if both line references fail, the TG3 (DS1) circuit pack will enter the holdover mode.

Fault Detection Circuitry

The TG3 (DS1) circuit pack has an in-service and out-of-service built-in test capability. In-service testing is continuous and errors are reported when they occur to the LNCTL circuit pack via the BCLAN. An out-of-service test is performed whenever the TG3 (DS1) circuit pack is inserted in a slot or reset.

The TG3 (DS1) circuit pack also monitors the incoming DS1 timing reference signals for the following:

- Loss of signal (175 ± 75 consecutive zeros)
- DS1 alarm indication signal (AIS) for at least 125 μ sec
- Bit error rate (BER) greater than 2×10^{-4} for a period ≥ 3 seconds
- Loss of frame for a period ≥ 3 seconds
- Excessive out-of-frame count
- Clock frequency out of specifications.

If one TG3 (DS1) circuit pack detects an incoming DS1 reference failure, it will signal the LNCTL circuit pack. The LNCTL circuit pack signals the companion TG3 (DS1) circuit pack to accept the secondary DS1 reference signal.

Power Circuitry

The TG3 (DS1) circuit pack receives two sources of -48 volts that are diode OR'd, fused, and filtered. A modular DC-to-DC power converter produces $+5$, -15 , and $+15$ volts used on the circuit pack.

The TG3 (DS1) circuit pack monitors the two -48 volt sources to the circuit pack and any failures are reported to the board controller. If the TG3 (DS1) circuit pack detects a failure of the onboard fuse or power converter, the red FAULT LED will light. If the fuse or $+5$ volt power converter fails, the LNCTL circuit provides $+5$ volts to light the FAULT LED on the TG3 (DS1) circuit pack.

Quick Reference Summary

Provisioned Modes

The TG3 (DS1) circuit pack can be provisioned using the craft interface terminal (CIT) for one of the following modes:

a. **Externally timed mode**

In the externally timed mode (phase locked mode in releases prior to Release 5), the TG3 (DS1) circuit pack phase locks to an external stratum 3 (± 4.6 ppm) or better external DS1 reference signal (1.544 MHz). The TG3 (DS1) circuit pack may be provisioned to accept a DS1 reference signal in the superframe or extended superframe format. It can also be provisioned with bipolar 8-zero substitution (B8ZS) or alternate mark inversion (AMI) line coding.

b. **Through timed mode**

In the through timed mode, the TG3 (DS1) circuit pack phase locks to an OC-48 line reference signal (25.92 MHz) provided by the OC48 Receiver (OC48 RCVR) circuit pack (FT-2000 OC-48 Add/Drop-Rings Terminal only).

c. **Free running mode**

In the free running mode, the TG3 (DS1) circuit pack generates timing from an on-board high stability fixed oscillator. The oscillator has a long-term accuracy of ± 4.6 ppm.

d. **Line Timed**

In the line timed mode, the TG3 (DS1) circuit pack phase locks to an OC-48 line reference signal (25.92 MHz) provided by the OC48 Receiver circuit pack (FT-2000 OC-48 ADR Terminal only). The direction selected (E, W, or Auto) is determined via provisioning. Also, the method for obtaining backup, or no backup, is determined at provisioning.

The equalization level of the DS1 reference output signals may also be provisioned. For detailed information about provisioning, refer to Volume I, Section 8, "Administration and Provisioning."

Holdover Mode

The holdover mode can only be entered from the externally timed mode, loop timed mode, or through timed mode. In the holdover mode, the DPLL circuit uses storage techniques to maintain the last known good frequency of the external DS1 reference signal or line reference signal. The holdover mode is entered when the integrity of the external DS1 reference signal or line reference signal is considered unacceptable. In the holdover mode, the on-board oscillator frequency will not degrade below the stratum 3 level.

Reference Input Functions

The incoming DS1 timing references are monitored for:

- a. Loss of signal (175 ± 75 consecutive zeros)
- b. DS1 alarm indication signal (AIS) for at least 125 μ sec
- c. Bit error rate (BER) greater than 2×10^{-4} for a period ≥ 3 seconds
- d. Loss of frame for a period ≥ 3 seconds
- e. Excessive out-of-frame count
- f. Clock frequency out of specifications.

If the microprocessor on one TG3 (DS1) circuit pack detects an incoming DS1 reference failure, it will signal the LNCTL circuit pack. The LNCTL circuit pack will signal the microprocessor on the companion TG3 (DS1) circuit pack to accept the secondary DS1 reference signal.

Clock Output Functions

The TG3 (DS1) circuit pack provides the following output signals:

- a. A 155.52 MHz signal is distributed to the OC48 Transmitter (OC48 TRMTR) and low speed interface circuit packs.
- b. A 25.92 MHz cross-coupled reference signal is distributed to the companion TG3 (DS1) circuit pack.
- c. A 1.544 MHz (DS1) signal is made available as a synchronization reference for the office BITS clock or other central office equipment.

Control Functions

The TG3 (DS1) circuit pack provides the following control functions:

- a. Selects the primary or secondary timing reference signal in the loop timed and phase locked modes
- b. Performs internal fault detection and interfaces with the LNCTL circuit pack using the BCLAN
- c. Controls the circuit pack faceplate FAULT LED
- d. Stores inventory information (CLEI code, serial number, etc.).

Maintenance Functions

The TG3 (DS1) circuit pack provides the following maintenance functions:

- a. Monitors the DS1 timing references
- b. Monitors the internal clock hardware
- c. Detects and inserts DS1 alarm indication signals (AIS).

Table of Contents

Overview	8-1
Administration	8-1
■ Memory Administration	8-1
■ Version Recognition	8-4
■ Security	8-4
General	8-4
Port Security	8-5
User Login Security	8-6
Network Element Login Security	8-7
Password Aging	8-8
■ Software Upgrades	8-9
■ Cross-Connections	8-11
Cross-Connect Rules	8-11
STS-3 Cross-Connections	8-13
Limited STS-1 Cross-Connections	8-24
STS-1 Cross-Connections	8-28
Extra Traffic Capability	8-38
Rolling Cross-Connections	8-40
Cross-Connection Inputs	8-41
Cross-Connection Examples	8-51
OC-3 Data Communications Channel Assignment Inputs	8-84
OC-3 Data Communications Channel Assignment Examples	8-86
OC-12 DCC Assignment Inputs	8-89
OC-12 Data Communications Channel Assignment Examples	8-91

Table of Contents

■ Access Identifiers	8-93
Provisioning	8-98
■ Provisionable Parameters and Original Values	8-98
General	8-98
FT-2000 OC-48 Add/Drop-Rings Terminal	8-99
■ Local Provisioning	8-109
FT-2000 OC-48 Add/Drop-Rings Terminal	8-109
FT-2000 OC-48 Repeater Bay	8-137
■ Pre-provisioning Circuit Packs	8-143
■ Provisioning on Circuit Pack Replacement	8-143
■ Circuit Pack Slot and Low-Speed Port Provisioning States	8-144

Overview

This section describes the administration and provisioning features of the FT-2000 OC-48 Lightwave System. It also presents a list of provisionable parameters with their ranges and original values.

Administration

Memory Administration

The FT-2000 OC-48 Lightwave System memory consists of the following:

- Executable code: This is the collection of "programs" that controls the operation of the system.
- Data: This is a collection of system parameters and their associated values. A parameter is a characteristic of the system that affects its operation. A value is a number, text string, or other menu selection associated with a parameter.

There are two complete copies of executable code in the system under normal conditions. One complete copy of executable code is located in the nonvolatile memory of the System Memory (SYSMEM) circuit pack. The other copy is distributed in the volatile memory of the System Controller (SYSCTL), the Line Controller (LNCTL) circuit packs, the Overhead Controller - Terminal [OHCTL (TERM)] circuit pack, and the individual board controllers on the other circuit packs. The system executes the copy in the board controllers and the SYSCTL, LNCTL, and OHCTL (TERM) circuit packs.

There are three sets of data in the system under normal conditions:

- Two sets of data are located in the nonvolatile memory of the SYSMEM circuit pack.
 - One set contains the system parameters and their original values (value assigned to a parameter at the factory).
 - The second set contains the system parameters and their current values (values currently being used by the system).
- The third set of data is located in the volatile memory of the SYSCTL, LNCTL, and OHCTL (TERM) circuit packs, plus the board controller of the other circuit packs. This set contains the system parameters and their current values.

If the system loses power and then regains power, the executable code and the data (with current values) are copied from the nonvolatile memory in the SYSMEM circuit pack to the volatile memory in the SYSCTL circuit pack. The system then starts running the executable code.

If the SYSCTL circuit pack is replaced, the executable code and data (with current values) are copied from the nonvolatile memory in the SYSMEM circuit pack to the volatile memory in the SYSCTL circuit pack. The system then starts running the executable code.

If the SYSMEM circuit pack is replaced and the executable code in the new SYSMEM circuit pack is different from what is running in the SYSCTL circuit pack, one of the following functions can be performed:

- Load another copy of the currently running version of the executable code from the craft interface terminal (CIT) to the SYSMEM circuit pack.
- Restart the system with the version of executable code in the new SYSMEM circuit pack.

Before Release 4, when a SYSMEM unrecognizable code or SYSMEM/SYSCTL code mismatch condition was active after replacing a SYSMEM circuit pack, all LNCTL and OHCTL circuit pack functions were stopped (no protection switching and condition reporting), and the FAULT LEDs on these circuit packs were lighted. In Release 4 and later releases, the LNTCL and OHCTL circuit pack functions are not stopped when a SYSMEM unrecognizable code or SYSMEM/SYSCTL code mismatch condition is active after replacing a SYSMEM circuit pack.

The FT-2000 OC-48 Lightwave System supports the following CIT memory administration functions:

- **Install-Software:** This copies the executable code and original values from floppy disks in the CIT to the SYSMEM circuit pack. Refer to the Install-Software function in the "Operation" Tab of this manual.
- **Initialize-System:** The **CONFIGURATION-Initialize-System** input replaces most current values in the SYSMEM circuit pack with the original values in the SYSMEM circuit pack and initiates the reset function (see Reset). All cross-connections, loopbacks, and switch states are retained. However, 1+1 protection switch states are not retained. Automatically provisioned parameters are unaffected by this command. The current values that are not replaced with their original values are those set with the following inputs:
 - **CONFIGURATION-Set-Date**
 - **SECURITY-Set-Password**
 - **SECURITY-Set-Login**
 - **SECURITY-Set-Security**
 - **SECURITY-Set-Far End Communications**
 - **SECURITY-Set-Network Element**
- **Reset:** The **FAULT-Reset** input copies the current values from the SYSCTL circuit pack to the SYSMEM circuit pack and the executable code from the SYSMEM circuit pack to the SYSCTL circuit pack. The executable code in the SYSCTL circuit pack is then restarted and the current values in the SYSMEM circuit pack are reformatted to work with the new executable code.
- **Update:** The **CONFIGURATION-Update** input updates the state of all the automatically provisioned parameters based on what circuit packs are present and the condition of the incoming signals. Refer to "Provisionable Parameters and Original Values" in this section for more information about automatically provisioned parameters.

Version Recognition

The FT-2000 OC-48 Lightwave System provides automatic version recognition of all the hardware and software installed. The FT-2000 OC-48 Lightwave System is able to report the type and version of the circuit pack that is installed in each slot. Each circuit pack's *Common Language** CLEI code and serial number are stored on the circuit pack and are accessible by the System Controller circuit pack. The equipment catalog item (ECI) version identification and apparatus code for each circuit pack is also provided. Refer to the **CONFIGURATION-Retrieve-Equipment** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Security

General

The FT-2000 OC-48 Lightwave System provides a security function to protect against unauthorized access to the CIT functions (for example, provisioning). The security function limits access to the FT-2000 OC-48 Lightwave System to legitimate users only. Security is provided using a four tier approach that provides maximum flexibility and control. The four tiers are:

- Port security
- User login security
- Network element login security
- Password aging (available in Release 4.1 and later releases).

* Common Language is a registered trademark and CLEI, CLLI, CLCI, and CLFI are trademarks of Telcordia Technologies, Inc.

Port Security

Port security provides the capability to control user access to the FT-2000 OC-48 Lightwave System on a per port basis. Port security includes the following:

- **Provisionable port status:** Dial-up access using the CIT (DTE) port and remote access using the DCC may be enabled to allow users to log in to that port or disabled to deny any attempt to log in to that port. Local access to the CIT (DCE) port may never be disabled. However, legitimate logins to the CIT (DCE) port are required.
- **Inactivity timers:** A provisionable inactivity timer (0-999 minutes) is provided for the local CIT (DCE) and CIT (DTE) ports in Release 4 and later releases. In releases prior to Release 7, inactivity timers do not apply to TL1 logins using the X.25 interface or data communications channel (DCC). In Release 7 and later releases, a provisionable inactivity timer is also provided for TL1 logins using the X.25 interface or the DCC. Release 3 provides a nonprovisionable 20-minute inactivity timer, and Release 2 provides a nonprovisionable 15-minute inactivity timer. If the user does not interact with the FT-2000 OC-48 Lightwave System within the provisioned time, the user will automatically be disconnected. The inactivity timer is reset by any data received at the port from a user, regardless of whether the data is addressed to a local or remote login session. The inactivity timer is also reset by any output addressed to the local port, regardless of origin (local or remote login session).

The **SECURITY-Set-Security-Port** input is used to enable/disable port security. For more information about provisioning port security, refer to the **SECURITY-Set-Security-Port** input in the "Local Provisioning" part of this section.

The **SECURITY-Retrieve-Security-Port** input is used to report port status. For information about the access port security configuration report, refer to the **SECURITY-Retrieve-Security-Port** input in Volume I, Section 11, "Craft Interface Terminal Usage."

User Login Security

User login security provides the capability to control access to the FT-2000 OC-48 Lightwave System on an individual user basis. User login security includes the following:

- **Login ID and password:** All users must properly identify themselves by providing a valid login ID and password before gaining access to the FT-2000 OC-48 Lightwave System.
- **Four levels of access privileges:** Each user login ID is assigned one of the following access levels to control which system functions may be performed.
 - **Privileged/Expert:** In Release 4.1 and later releases, a privileged login gains access to the "expert" input set by invoking the CIT-PC program in the expert mode. A privileged login in the expert mode has access to all the system functional capabilities. A privileged login in the expert mode is also allowed to execute the auto turnup tests and specify certain inputs that affect all low-speed ports in the system (for example, inputs that have the potential for massive service interruption).
 - **Privileged:** In Release 4.1 and later releases, privileged logins have access to all system functional capabilities except those with potential for massive service interruption. In releases prior to Release 4.1, the privileged logins have access to all the system functional capabilities. Only the privileged logins have access to the security and access functions. These functions include assigning/changing logins/passwords for other users, enabling/disabling ports, setting target identifier (TID) names, and resetting the system. There are only two privileged logins.
 - **General:** The general logins have access to all the system functional capabilities except the security, access, system initialization, and software installation functions. In Release 4.1 and later releases, general logins do not have access to the **Set-Date** input.
 - **Reports Only:** The reports only logins have access to the **Retrieve**, **Set-Password**, **Test-LED**, and **Operate-Alarm Cutoff** inputs. The reports only logins are not allowed to change any of the system provisioning values; however, reports only logins may change their password.

For information about the security level associated with each CIT-PC input, refer to the appropriate Inputs (Commands) Menu table in Volume I, Section 11, "Craft Interface Terminal Usage."

- **Visitor Logins:** In Release 5 and later releases, visitor logins may be provisioned with expiration dates. Visitor logins are automatically deleted on the provisioned expiration date regardless of their use. Visitor logins are general or reports only logins.

Prior to Release 4.1, a privileged user could create up to ten nonprivileged logins (general or reports only) per system. In Release 4.1 and later releases, a privileged user can create up to 98 nonprivileged logins per system. Login IDs and passwords are administered with the **SECURITY-Set-Login-Enter**, **SECURITY-Set-Login-Change**, and **SECURITY-Set-Login-Delete** inputs. For more information about provisioning user login security, refer to the **SECURITY-Set-Login-Enter**, **SECURITY-Set-Login-Change**, and **SECURITY-Set-Login-Delete** inputs in the "Local Provisioning" part of this section.

The **SECURITY-Retrieve-Login** input is used to report login status. For information about the login report, refer to the **SECURITY-Retrieve-Login** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Network Element Login Security

Network element login security provides the capability to control access to the FT-2000 OC-48 Lightwave System on a network element basis. Network element login security is provided by controlling whether or not nonprivileged (general or reports only) users are allowed to access the network element. When logins are allowed, nonprivileged users may log in and access the network element. When logins are not allowed, login attempts are denied. Privileged users are not affected by this security measure and are always allowed to log in.

Network element security is intended to disable nonprivileged logins quickly on a temporary basis. This capability might be used during routine maintenance or upgrade activities. Permanent security should be provided using port security or user login security.

In Release 5 and later releases, login aging prevents inactive, nonprivileged users from accessing the FT-2000 OC-48 Add/Drop-Rings Terminal. If the difference between the last login date and the current date* of a nonprivileged login exceeds the provisioned login lifetime parameter, the login is automatically deleted. An audit runs each day at 11:59 p.m. to check for and delete expired logins. The same audit automatically deletes visitor logins at 11:59 p.m. on the provisioned expiration date. Login aging applies only to nonprivileged logins. A privileged login is never deleted.

* A privileged user may change the current date using the Set-Date input so that the login lifetime is exceeded even though the number of calendar days since the last login attempt is less.

The **SECURITY-Set-Security-Network Element** input is used to enable/disable nonprivileged user logins. For more information about provisioning network element security, refer to the **SECURITY-Set-Security-Network Element** input in the "Local Provisioning" part of this section.

The **SECURITY-Retrieve-Security-Network Element** input is used to report whether logins are allowed or not. For information about the global security report, refer to the **SECURITY-Retrieve-Security-Network Element** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Password Aging

In Release 4.1 and later releases, password aging provides the capability to force users to periodically change their password. If the users do not change their password within the provisioned period of time, their password will expire. The next time the users attempt to login, they will not be allowed to execute any commands until they change their password.

To enable password aging, the password aging interval must be provisioned in the range of 7 to 999 days. To disable password aging, the password aging interval must be provisioned to 0 days.

If password aging is enabled, a user may not change a password unless at least 7 days have passed since the last password change for that login. Any attempt to change a password when less than 7 days have passed shall be denied.

The **SECURITY-Set-Security-Network Element** input is used to enable/disable password aging and provision the password aging interval. For more information about provisioning password aging, refer to the **SECURITY-Set-Security-Network Element** input in the "Local Provisioning" part of this section.

The **SECURITY-Retrieve-Security-Network Element** input is used to report the password aging interval in days. For information about the global security report, refer to the **SECURITY-Retrieve-Security-Network Element** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Software Upgrades

The FT-2000 OC-48 Lightwave System provides an in-service software upgrade capability using a CIT locally or remotely (with a modem or digital data network). The CIT (PC running FT-2000 CIT-PC software) must be used locally to install the initial software into a system. In Release 5 and later releases, remote software upgrades over the OC-48 data communications channel (DCC) are possible. The **CONFIGURATION - Copy - Program** input copies the software contained in the SYSMEM circuit pack of one FT-2000 OC-48 Add/Drop-Rings Terminal (source) to the SYSMEM circuit pack of another FT-2000 OC-48 Add/Drop-Rings Terminal (destination).

The FT-2000 OC-48 Add/Drop-Rings Terminal can copy software to another FT-2000 OC-48 Add/Drop-Rings Terminal in the same ring and through DDM-2000 NEs to a remote FT-2000 OC-48 Add/Drop-Rings Terminal in the same subnetwork via the OC-3 DCC (in Release 6 and later releases) and OC-12 DCC (Release 7.1 and later releases). The FT-2000 OC-48 Add/Drop-Rings Terminal must be equipped with the OC3 (or OC-12) Optical Interface circuit pack and the Overhead Controller - Tributary (TOHCTL) circuit pack to access the DCC. A single TOHCTL circuit pack can access up to eight DCCs. Low-speed interface slots must be assigned to an OC-3/OC-12 DCC. For more information about entering DCC assignments, refer to "OC-3 (or OC-12) DCC Assignment Inputs" in this section.

The FT-2000 OC-48 Lightwave system is **fully** operational during the software upgrade. For example, the FT-2000 OC-48 Lightwave System maintains all transmission, protection switching, performance monitoring, and system reporting functions. For detailed information about installing a particular software release, refer to the applicable software release description.

Release 6.0.0-RTPR	Comcode 107559833, FT-2000 OC-48 Lightwave System Software Release Description, Release 6.0.0-RPTR
Release 7.2.0-ADR	Comcode 108042458, FT-2000 OC-48 Lightwave System Software Release Description, Release 7.2.0-ADR
Release 7.2.8-ADR	Comcode 108361437 (3½" floppy); Comcode 108399973 (DAT tape*), <i>FT-2000 OC-48 Lightwave System</i> , Software Release Description, Release 7.2.8-ADR
Release 7.2.9-ADR	Comcode 108614363 (3½" floppy); Comcode 108686692 (DAT tape*), <i>FT-2000 OC-48 Lightwave System</i> , Software Release Description, Release 7.2.9-ADR
Release 7.2.10-ADR	Comcode 109191635 (3½" floppy); Comcode 109211219 (CD-ROM)

The software release descriptions are shipped with the software and are not orderable from the Lucent Technologies Customer Information Center.

Cross-Connections

Cross-Connect Rules

DS-3/EC-1

- Three DS-3 signals are addressed using port AIDs.
- All STS-1 drop signals (DS-3) must come from the same side.
- The STS-1 add cross-connections can go to just one side or to both sides.

OC-3 Rules

- Three STS-1 signals contained within the OC-3 signal are addressed using port AIDs (i.e., ls-1a-1, ls-1a-2, ls-1a-3).
- All STS-1 drop cross-connections must come from the same side (line).
For example:

1e-1-1 -> 1a-1
1e-3-3 -> 1a-2
1e-8-2 -> 1a-3

- The three consecutive STS-1 tributaries of an OC-3c signal must be cross-connected in parallel. For example,

1a-1 -> 1w-3-1
1a-2 -> 1w-3-2
1a-3 -> 1w-3-3

- An OC-3c signal must be aligned on an STS-3 boundary within the OC-48 signal. For example:

Correct	Incorrect
1a-1 -> 1w-3-1	1a-1 -> 1w-3-2
1a-2 -> 1w-3-2	1a-2 -> 1w-3-3
1a-3 -> 1w-3-3	1a-3 -> 1w-4-1

- An OC-3c cross-connect can be provisioned using either single **CONFIGURATION-Enter-Crossconnection-STS-3** command or three **CONFIGURATION-Enter-Crossconnection-STS-1** commands.



NOTE:

The FT-2000 OC-48 ADR Terminal does support the pre-provisioning of cross-connections. However, in order to prevent the generation of LOP alarms, the OC-3c signal should not be applied until: (a) the circuit pack has been installed; (b) ALL cross-connections for that circuit pack have been provisioned; and (c) all cross-connections in each NE of the signal's path have been provisioned. Then, and only then, should the signal be applied. Before deleting any of the

cross-connections in an OC-3c signal's path, the signal must be removed.

- When deleting a DRI cross-connect for 1+1 protection pair, the cross-connect should be deleted from the 'A' slot. If done from the 'B' slot, the command will be denied.

OC-12/OC-12c Rules

- The STS-1 signals within an OC-12 signal are addressed using port AIDs (i.e., Quad 1 = 1a-1, 1a-2, 1a-3, 1b-1, 1b-2,.....2b-3).
- The OC-3 rules apply to the OC-12 on a per-slot basis. For example, all STS-1 drop cross-connections must come from the same side per slot but can come from different sides across the four slots. The following is permissible (1e-1-1 -> 1a-1, 1w-3-3 -> 1b-1).
- An OC-3c signal can ride within an OC-12 signal as long as it is aligned on an STS-3 boundary. The following example should clarify this rule.

Correct	Incorrect
1b-1 -> 1w-3-1	1a-2 -> 1w-3-1
1b-2 -> 1w-3-2	1a-3 -> 1w-3-2
1b-3 -> 1w-3-3	1b-1 -> 1w-3-3

- An OC-12c signal must be aligned on an STS-12 boundary within the OC-48 signal. For example, STS-3 tributaries 1-4, 5-8, 9-12, 13-16 each form STS-12 boundaries. An OC-12 low-speed quad group (i.e., slots 1A-2B) can be cross-connected within any STS-12 boundary.
- The twelve constituent STS-1 tributaries of an OC-12c signal must be cross-connected in parallel. An example for the STS-3 tributaries is as follows:

1a-1 -> 1w-5-1	2a-1 -> 1w-7-1
1a-2 -> 1w-5-2	2a-2 -> 1w-7-2
1a-3 -> 1w-5-3	2a-3 -> 1w-7-3
1b-1 -> 1w-6-1	2b-1 -> 1w-8-1
1b-2 -> 1w-6-2	2b-2 -> 1w-8-2
1b-3 -> 1w-6-3	2b-3 -> 1w-8-3

- An OC-12c signal can be provisioned using twelve (12) **CONFIGURATION-Enter-Crossconnect-STS-1** commands or four (4) **CONFIGURATION-Enter-Crossconnect-STS-3** commands. There are no STS-12 rate cross-connect commands.

**NOTE:**

The FT-2000 OC-48 ADR Terminal does support the pre-provisioning of cross-connections. However, in order to prevent the generation of LOP alarms, the OC-12c signal should not be applied until: (a) the circuit pack has been installed; (b) ALL cross-connections for that circuit pack have been provisioned; and (c) all cross-connections in each NE of the signal's path have been provisioned. Then, and only then, should the signal be applied. Before deleting any of the cross-connections in an OC-12c signal's path, the signal must be removed.

- An OC-12c signal cannot be "rolled" without a service hit due to the lack of a **CONFIGURATION-Enter-Roll-STs12** command.
- When deleting a DRI cross-connect for 1+1 protection pair, the cross-connect should be deleted from the 'A' quad (1B, 5B). If done from the 'B' quad (3B, 7B), the command will be denied.

STS-3 Cross-Connections

General

The FT-2000 OC-48 Add/Drop-Rings Terminal has no fixed connection, or association, between low-speed interface slots and the 155 Mb/s (STS-3) tributaries of OC-48 high-speed lines 1E and 1W. There is also no fixed connection between the STS-3 tributaries of OC-48 high-speed lines 1E and 1W. (In 2-fiber bidirectional rings, STS-3 tributaries 1 through 8 are service tributaries and STS-3 tributaries 9 through 16 are protection tributaries.) The FT-2000 OC-48 Add/Drop-Rings Terminal has an STS-3 time slot assignment (TSA) capability that supports the following cross-connections:

- 1-way cross-connections (available in Release 4.1 and later releases)
- 2-way cross-connections (Default) (available in Release 3 and later releases)
- 1-way-DRI cross-connections (available in Release 4.1 and later releases)
- Limited STS-1 cross connections (available in Releases 5 and 6 only).

When entering cross-connections, the source node is the node in the ring where the low-speed signals on that STS-3 tributary originate. The destination node is the last node in the same ring where the low-speed signals on that STS-3 tributary terminate.

**NOTE:**

Concatenated signals, such as an OC-3c, must be cross-connected within a single STS-3 tributary and cannot be broken up as STS-1 signals distributed across 3 different STS-3 tributaries. An OC-12c concatenated signal must be cross-connected to four contiguous STS-3 tributaries. Non-concatenated OC-3 and OC-12 signals can be freely cross-connected.

1-Way Cross-Connections

The 1-way cross-connections are the basic building block for all cross-connections. The FT-2000 OC-48 Add/Drop-Rings Terminal supports the following 1-way cross-connections:

- **Add cross-connections:** Add cross-connections are made from any service low-speed interface slot to any outgoing 155 Mb/s (STS-3) tributary of OC-48 high-speed line 1E or 1W. For example, Figure 8-1 shows service low-speed interface slot 2B cross-connected to outgoing service STS-3 tributary 6 of OC-48 high-speed line 1W.

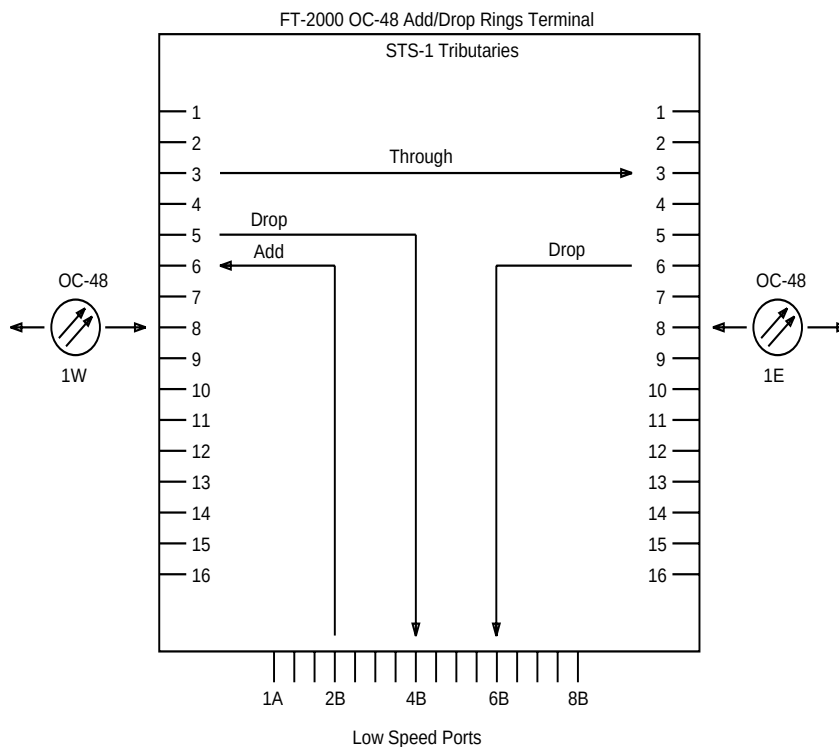


Figure 8-1. STS-3 1-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

In Release 5 and later releases, add cross-connections are made from any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. A single input enters an active add cross-connection from the "A" slot to an outgoing STS-3 tributary of OC-48 high-speed line 1E or 1W and a standby cross-connection from the "B" slot to the same outgoing STS-3 tributary. For example, Figure 8-2 shows service low-speed interface slots 2A and 2B cross-connected to outgoing service

STS-3 tributary 6 of OC-48 high-speed line 1W. The add cross-connection from slot 2A to outgoing service STS-3 tributary 6 is active, and the add cross-connection from slot 2B to the same STS-3 tributary is standby.

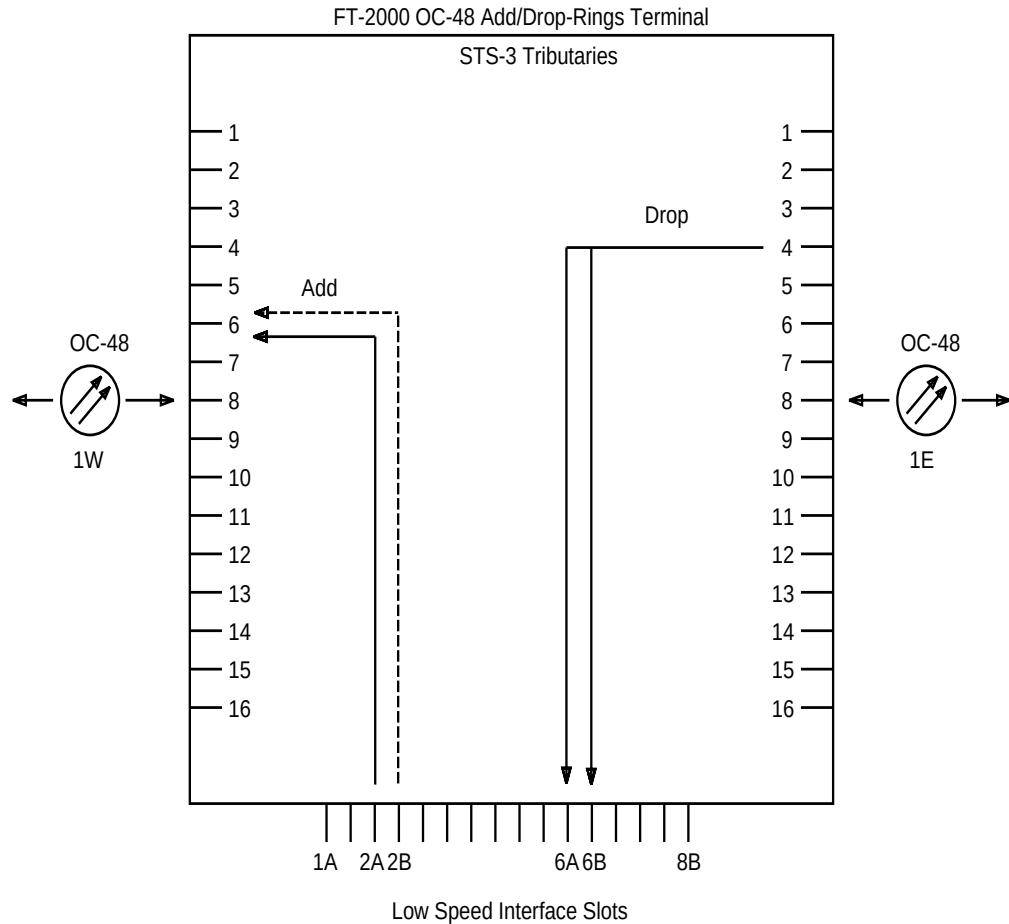


Figure 8-2. STS-3 1-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal (1+1 Protected Slots)

- **Drop cross-connections:** Drop cross-connections are made from any incoming STS-3 tributary of OC-48 high-speed line 1E or 1W to any service low-speed interface slot. For example, Figure 8-1 shows incoming service STS-3 tributary 5 of OC-48 high-speed line 1W cross-connected to service low-speed interface slot 4B.

In Release 5 and later releases, drop cross-connections are made to any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. A single input enters an active drop cross-connection from any incoming STS-3 tributary of OC-48 high-speed line 1E or 1W to

the "A" slot and another active drop cross-connection from the same incoming STS-3 tributary to the "B" slot. For example, Figure 8-2 shows incoming service STS-3 tributary 4 of OC-48 high-speed line 1E cross-connected to service low-speed interface slots 6A and 6B.

- **Through cross-connections:** Through cross-connections are made from any incoming STS-3 tributary of OC-48 high-speed line 1E or 1W to the same-numbered outgoing STS-3 tributary of the other OC-48 high-speed line. For example, Figure 8-1 shows incoming service STS-3 tributary 3 of OC-48 high-speed line 1W cross-connected to outgoing service STS-3 tributary 3 of OC-48 high-speed line 1E.

When an STS-3 tributary of OC-48 high-speed line 1E or 1W is cross-connected to a service low-speed interface slot, another service low-speed interface slot may be cross-connected to the same-numbered STS-3 tributary of the other OC-48 high-speed line. For example, Figure 8-1 shows service low-speed interface slot 2B cross-connected to service STS-3 tributary 6 of OC-48 high-speed line 1W, and service STS-3 tributary 6 of OC-48 high-speed line 1E cross-connected to service low-speed interface slot 6B. This is consistent with the 2-fiber bidirectional, line-switched ring standard.



NOTE:

In terms of functionality, a 1-way add plus a 1-way drop (to the same points) perform all of the same functions as a single 2-way.

The 1-way add, drop, and through cross-connections may be used to provide the following:

- **Bridge cross-connections:** In Release 4.1, bridge cross-connection is limited to a drop cross-connection and a superimposed through cross-connection sharing the same source (drop and continue). The drop cross-connection and through cross-connection share the same incoming STS-3 tributary of OC-48 high-speed line 1E or 1W. For example, Figure 8-3 shows incoming service STS-3 tributary 3 of OC-48 high-speed line 1W cross-connected to outgoing service STS-3 tributary 3 of OC-48 high-speed line 1E. Incoming service STS-3 tributary 3 of OC-48 high-speed line 1W is also cross-connected to service low-speed interface slot 1A.

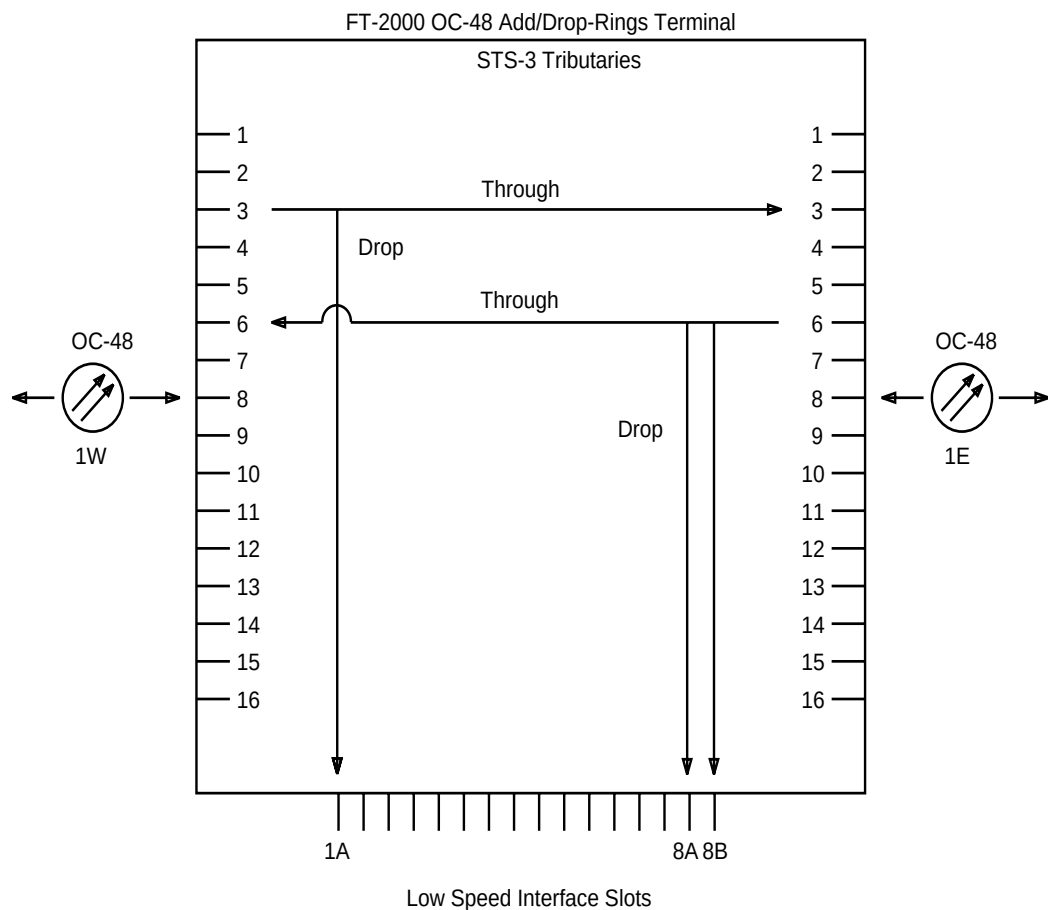


Figure 8-3. STS-3 Bridge Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

In Release 5 and later releases, a bridge cross-connection is any two superimposed cross-connections sharing the same source. The source can be any incoming STS-3 tributary of OC-48 high-speed line 1E or 1W, or any service low-speed interface slot.

In Release 5 and later releases, cross-connections can be made to/from any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. For example, Figure 8-3 shows incoming service STS-3 tributary 6 of OC-48 high-speed line 1E cross-connected to outgoing service STS-3 tributary 6 of OC-48 high-speed line 1W. Incoming service STS-3 tributary 6 of OC-48 high-speed line 1W is also cross-connected to service low-speed interface slots 8A and 8B.

- Multiple bridge cross-connections:** In Release 5 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal supports multiple bridge cross-connections. A multiple bridge cross-connection is three or more superimposed cross-connections sharing the same source. The source can be any incoming STS-3 tributary of OC-48 high-speed line 1E or 1W, or any service low-speed interface slot. For example, Figure 8-4 shows incoming service STS-3 tributary 7 of OC-48 high-speed line 1E cross-connected to service low-speed interface slots 6A, 6B, 7B, and 8A. Figure 8-4 also shows service low-speed interface slot 3A cross-connected to service STS-3 tributaries 1 and 2 of OC-48 high-speed line 1W, and service STS-3 tributary 3 of OC-48 high-speed line 1E.

Multiple bridge cross-connections can also be made to/from any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. For example, Figure 8-4 shows service low-speed interface slots 1A and 1B cross-connected to service STS-3 tributaries 6, 7, and 8 of OC-48 high-speed line 1W.

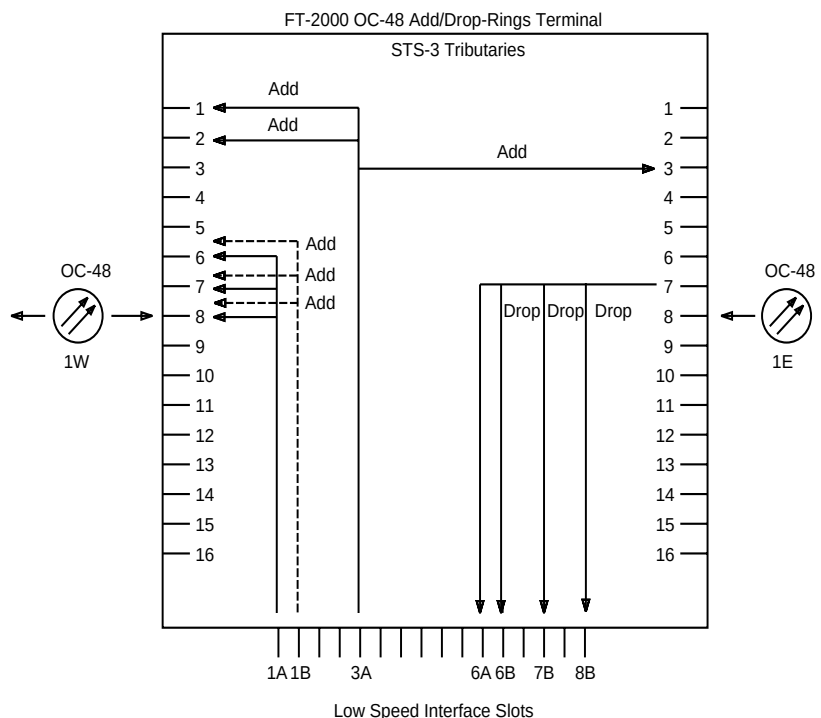


Figure 8-4. Multiple STS-3 Bridge Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

1-Way-DRI Cross-Connections

A 1-way-DRI cross-connection is a 1-way add cross-connection and a superimposed 1-way through cross-connection sharing the same destination. The add cross-connection and through cross-connection share the same outgoing service 155 Mb/s (STS-3) tributary of OC-48 high-speed line 1E or 1W. For example, Figure 8-5 shows incoming service STS-3 tributary 3 of OC-48 high-speed line 1W cross-connected to outgoing service STS-3 tributary 3 of OC-48 high-speed line 1E. Service low-speed interface slot 6B is also cross-connected to outgoing service STS-3 tributary 3 of OC-48 high-speed line 1E.

A single input enters an active add cross-connection and a standby through cross-connection.

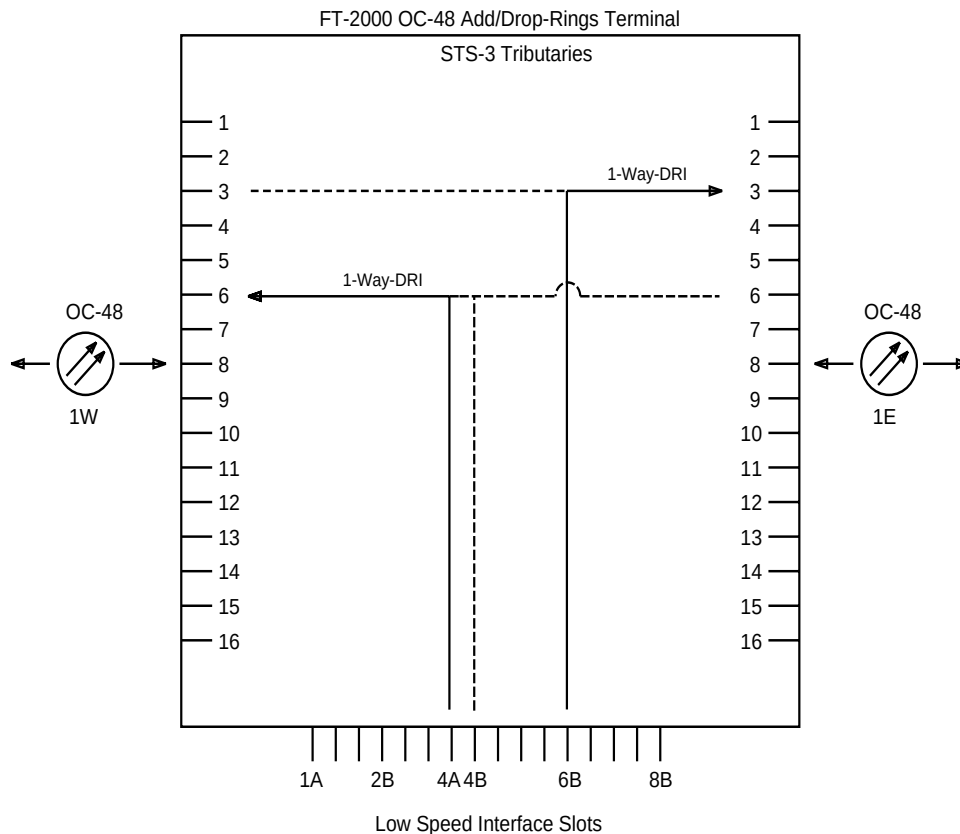


Figure 8-5. STS-3 1-Way-DRI Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

In Release 5 and later releases, 1-way-DRI cross-connections are made from any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. For example, Figure 8-5 shows incoming service STS-3 tributary 6 of OC-48 high-speed line 1E cross-connected to outgoing service STS-3 tributary 6 of OC-48 high-speed line 1W. Service low-speed interface slot pair 4A and 4B is also cross-connected to outgoing service STS-3 tributary 6 of OC-48 high-speed line 1W.

A single input enters an active and standby add cross-connection from the service low-speed interface slot pair to a service STS-3 tributary and a standby through cross-connection for the same STS-3 tributary.

The active add cross-connection carries traffic during normal conditions. In Release 4.1 and later releases, if an incoming DS3 or EC-1 loss-of-signal condition is detected by the low-speed interface circuit pack, the through cross-connection becomes active and the add cross-connection becomes standby. In Release 5 and later releases, an unprotected incoming OC-3 loss-of-signal or loss-of-frame condition also causes the through cross-connection to become active and the add cross-connection to become standby.

For Release 7.0 and prior releases, when the LOS, LOP, LOF, or Line AIS condition clears, traffic does not automatically switch (revert) to the add cross-connection. A reset *must* be performed using the FAULT-Switch-Path-STSS input to switch the traffic back to the add cross-connection.

Release 7.1.0 and later releases provide revertive DRI and automatically switch back to the service fiber.

For more information about the dual ring interworking (DRI) application, refer to Volume I, Section 2, "Applications." For more information about DRI protection switching, refer to Volume I, Section 9, "Maintenance Description."

2-Way Cross-Connections

The FT-2000 OC-48 Add/Drop-Rings Terminal provides a time slot assignment (TSA) capability that supports the following 2-way cross-connections:

- **Add/drop cross-connections:** Add/drop cross-connections are made between any service low-speed interface slot and any 155 Mb/s (STS-3) tributary of OC-48 high-speed line 1E or 1W. For example, Figure 8-6 shows service low-speed interface slot 2B cross-connected to service STS-3 tributary 6 of OC-48 high-speed line 1W. (In 2-fiber bidirectional rings, STS-3 tributaries 1 through 8 are service tributaries and STS-3 tributaries 9 through 16 are protection tributaries.)

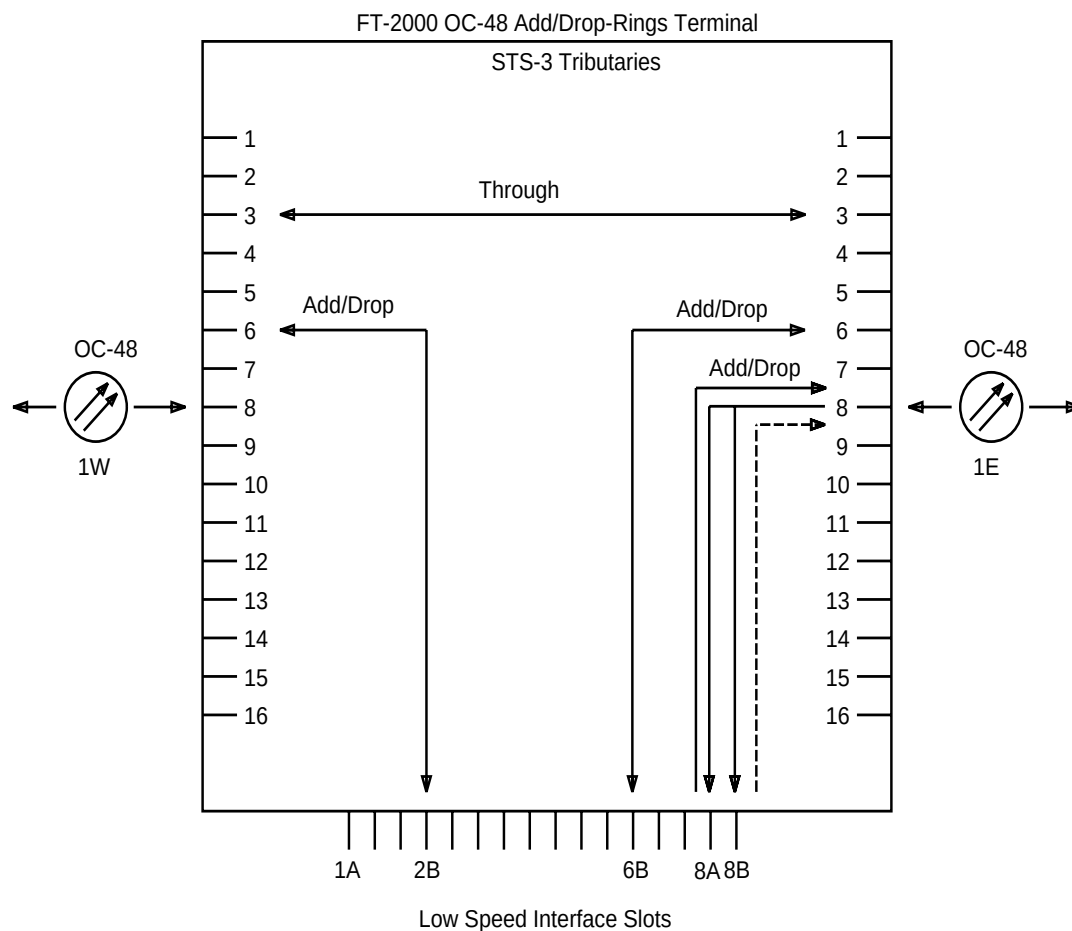


Figure 8-6. STS-3 2-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

In Release 5 and later releases, add/drop cross-connections are made between any service low-speed interface slot and the companion low-speed interface slot (for example, slot 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode and any service STS-3 tributary. A single input enters an active and a standby add cross-connection and two active drop cross-connections. For example, Figure 8-6 shows service low-speed interface slot pair 8A and 8B cross-connected to service STS-3 tributary 8 of OC-48 high-speed line 1E.

- **Through cross-connections:** Through cross-connections are made between any STS-3 tributary on OC-48 high-speed line 1E or 1W and the same-numbered STS-3 tributary of the other OC-48 high-speed line. For example, Figure 8-6 shows service STS-3 tributary 3 of OC-48 high-speed line 1W cross-connected to service STS-3 tributary 3 of OC-48 high-speed line 1E.

When an STS-3 tributary of OC-48 high-speed line 1E or 1W is cross-connected to a service low-speed interface slot, another service low-speed interface slot may be cross-connected to the same-numbered STS-3 tributary of the other OC-48 high-speed line. For example, Figure 8-6 shows service low-speed interface slot 2B cross-connected to service STS-3 tributary 6 of OC-48 high-speed line 1W, and service low-speed interface slot 6B cross-connected to service STS-3 tributary 6 of OC-48 high-speed line 1E. This is consistent with the 2-fiber bidirectional, line-switched ring standard.



NOTE:

In terms of functionality, a 1-way add plus a 1-way drop (to the same points) perform all of the same functions as a single 2-way.

Limited STS-1 Cross-Connections

In Releases 5 and 6 only, the FT-2000 OC-48 Add/Drop-Rings Terminal supports limited STS-1 cross-connections with external loopback cables and an STS-1 squelch map across an STS-3 signal. (Limited STS-1 cross-connections across an STS-3 signal are not supported in Release 7 and later releases.) External loopback cables are required when all three STS-1 tributaries from the same low speed interface circuit pack (DS3 or STS1E) are not being added or dropped. Each FT-2000 OC-48 Add/Drop-Rings Terminal in a ring network also maintains an STS-1 squelch map that allows each STS-1 tributary to have its own source node and destination node.

The source node and destination node information in the STS-1 squelch map enables switching nodes to squelch STS-1 tributaries that are looped back (through cabled) based on the source and destination nodes. Thus, if a node that has through-cabled STS-1 tributaries becomes isolated from the source and destination nodes of the through traffic due to a node failure or multiple line failures, only the add/drop traffic at the isolated node is squelched. The through-cabled STS-1 tributaries are protected using 2-fiber ring protection switching if the source and destination nodes are not isolated from each other. In releases prior to Release 5, through-cabled STS-1 tributaries were not protected against intermediate node failures.

Limited STS-1 cross-connections are established by entering a 1-way or 2-way cross-connection between a service low speed interface slot and a service STS-3 tributary of OC-48 high speed line 1E (or 1W) and another cross-connection between a second service low speed interface slot and the STS-3 tributary of OC-48 high speed line 1W (or 1E). The two service low speed interface slots must be equipped with the same type of low speed interface circuit pack (DS3 or STS1E). The STS-1 tributaries (DS3 or EC-1 signals) to be dropped and/or added are connected to a DSX-3 panel, STSX-1 panel, DACS-III 2000 Cross-Connect System, or DACS-IV 2000 Cross-Connect System. The through-cabled STS-1 tributaries are looped back from the output(s) of the circuit pack in the first slot to the input(s) of the circuit pack in the second slot.

Figure 8-7 shows three STS-1 tributaries (DS3 signals) cabled through an FT-2000 OC-48 Add/Drop-Rings Terminal with DS3 circuit packs.

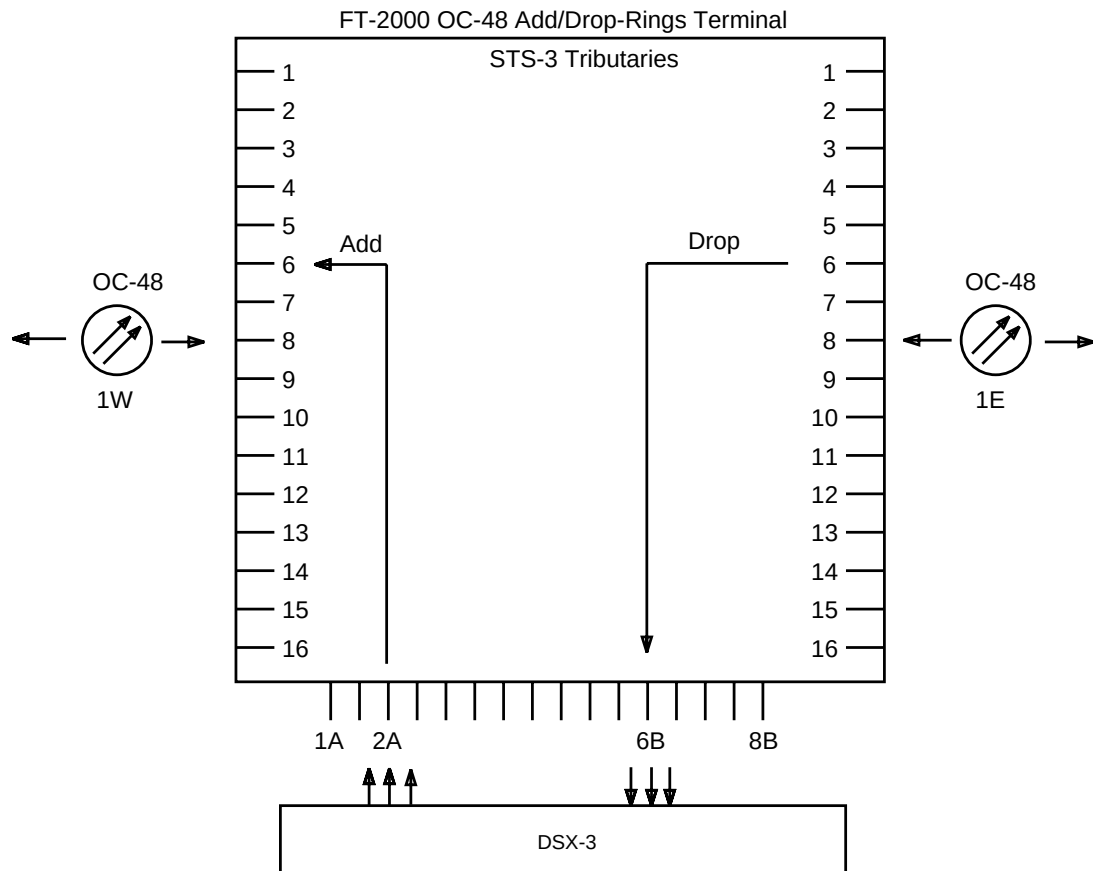


Figure 8-7. Limited STS-1 Cross-Connections Across and STS-3 Tributary

A 1-way drop and a 1-way add cross-connection are entered at the FT-2000 OC-48 Add/Drop-Rings Terminal in Figure 8-7. A 1-way drop cross-connection is made from incoming service STS-3 tributary 6 of OC-48 high speed line 1E to service low speed interface slot 6B. A 1-way add cross-connection is made from service low speed interface slot 2A to outgoing service STS-3 tributary 6 of OC-48 high speed line 1W. The individual STS-1 tributaries (DS3 signals) from the DS3 circuit pack in low speed interface slot 8B are looped back (through cabled) at the DSX-3 cross-connect panel (or equivalent) to the DS3 circuit pack in low speed interface slot 2A.

The transmit side of the DS3 circuit pack in low speed interface slot 6B and the receive side of the DS3 circuit pack in low speed interface slot 2A are available for one of the following:

- They can be cross-connected to an STS-3 tributary in the opposite direction to complete a 2-way application.
- They can be cross-connected to another STS-3 tributary for another 1-way through-cabled STS-1 tributary application.
- They can be used independently for various types of 1-way applications.
- They can be unused.

When the STS-1 tributaries associated with a single STS-3 tributary are split up, the STS-3 tributary source and destination node provisioning is no longer correct for the STS-1 tributaries. The `CONFIGURATION-Enter-Squelch Map-STS1` input must be used to override the STS-3 squelch map provisioning for those STS-1 tributaries. This STS-1 squelch map provisioning must be done at each node where the affected STS-1 tributary(s) is terminated, looped back using external cabling, or passed through using STS-3 through cross-connections.

STS-3 Cross-Connection Restrictions

STS-3 cross-connections cannot be made from one low speed interface slot to another low speed interface slot or between two differently numbered STS-3 tributaries.

Limited STS-1 cross-connections have the following restrictions:

- Any STS-3 tributary that is part of a 1-way-DRI or drop and continue (bridge) cross-connection at an FT-2000 OC-48 Add/Drop-Rings Terminal cannot have through-cabled STS-1 tributaries.
- If the `CONFIGURATION-Enter-Roll-STS3` input is used to move a cross-connection from slot to slot for a cross-connection that has through-cabled STS-1 tributaries, the through-cabled STS-1 tributaries will be disconnected until the external cables are moved at the cross-connect panel.
- When DS3 circuit packs are used, the through-cabled STS-1 tributaries have their path overhead terminated and are desynchronized and synchronized (the DS3 is cabled through, not the STS-1). This affects the interpretation of performance-monitoring data (for example, B3 errors will not be propagated through to the final destination).

- The VMR Mode parameter (DS3 signals only) for the through-connected STS-1 tributaries should be provisioned as follows:

Terminating nodes	VMR	VM	CC w/ AIS	CC w/o AIS
Through-connected nodes	CC w/ AIS	CC w/ AIS	CC w/ AIS	CC w/o AIS

- The incoming and outgoing Path Traces (DS3 signals only) should be the same for through-cabled STS-1 tributaries; otherwise, a protected node failure would create a path trace mismatch.
- All through-cabled connections must have the same time slot assignments on both sides of the FT-2000 OC-48 Add/Drop-Rings Terminal (for example, no rearrangement of the STS-1s within the STS-3 or rearrangement of STS-3s within the STS-48 is allowed for through connections).

STS-1 Cross-Connections

General

In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal has no fixed connection, or association, between low-speed ports and the STS-1 tributaries of OC-48 high-speed lines 1E and 1W. There is also no fixed connection between the STS-1 tributaries of OC-48 high-speed lines 1E and 1W. The FT-2000 OC-48 Add/Drop-Rings Terminal has an STS-1 time slot assignment (TSA) capability that supports the following STS-1 cross-connections across an OC-48 signal:

- 1-way cross-connections
- 2-way cross-connections (Default)
- 1-way-DRI cross-connections

When entering cross-connections, the source node is the node in the ring where the low-speed signals on that STS-1 tributary originate. The destination node is the last node in the same ring where the low-speed signals on that STS-1 tributary terminate.



NOTE:

Concatenated signals, such as an OC-3c, must be cross-connected within a single STS-3 tributary and cannot be broken up as STS-1 signals distributed across 3 different STS-3 tributaries. An OC-12c concatenated signal must be cross-connected to four contiguous STS-3 tributaries. Non-concatenated OC-3 and OC-12 signals can be freely cross-connected.

1-Way Cross-Connections

1-way cross-connections are the basic building block for all STS-1 cross-connections. The FT-2000 OC-48 Add/Drop-Rings Terminal supports the following STS-1 1-way cross-connections:

- **Add cross-connections:** Add cross-connections are made from any service low-speed port to any outgoing STS-1 tributary of OC-48 high-speed line 1E or 1W. For example, Figure 8-8 shows service low-speed port 1 of slot 2B (2B-1) cross-connected to outgoing service STS-1 tributary 1 of STS-3 tributary 6 (6-1) on OC-48 high-speed line 1W.

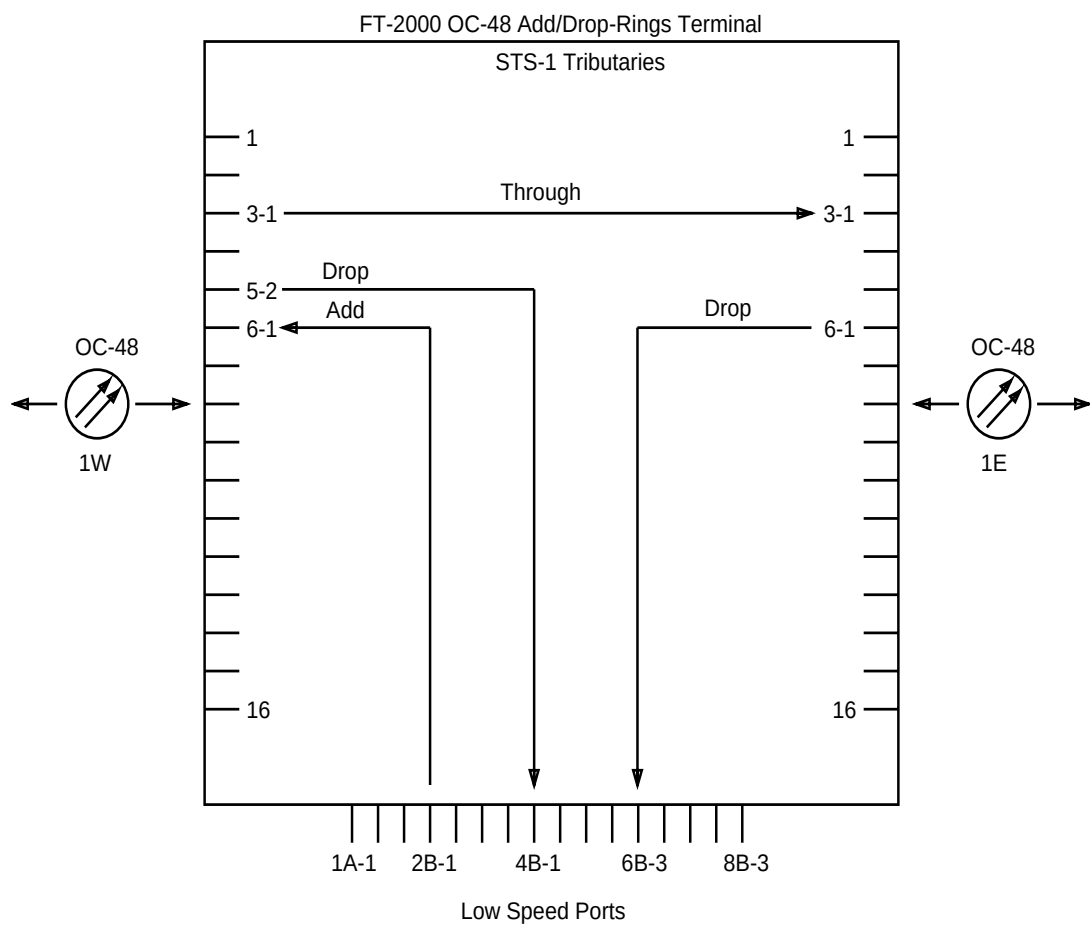


Figure 8-8. STS-1 1-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

Add cross-connections are also made from any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. (Any low-speed port associated with the slot pair may be specified when entering the cross-connection.) A single input enters an active add cross-connection from the "A" slot to the outgoing STS-3 tributary of OC-48 high-speed line 1E or 1W and a standby cross-connection from the "B" slot to the same outgoing STS-3 tributary. For example, Figure 8-9 shows service low-speed interface slots 2A and 2B cross-connected to outgoing service STS-3 tributary 6 of OC-48 high-speed line 1W. The add cross-connection from slot 2A to outgoing service STS-3 tributary 6 is active, and the add cross-connection from slot 2B to the same STS-3 tributary is standby.

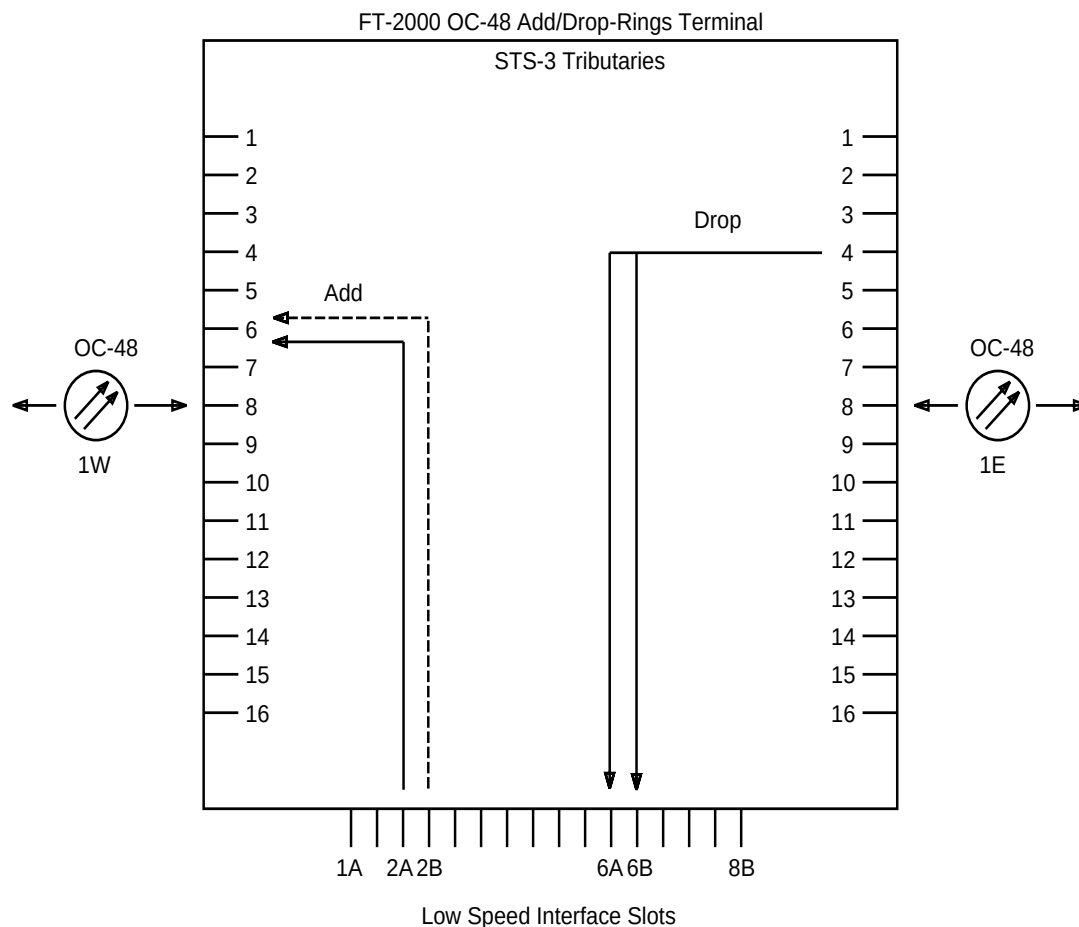


Figure 8-9. 1-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal (1+1 Protected Slots)

- **Drop cross-connections:** Drop cross-connections are made from any incoming STS-1 tributary of OC-48 high-speed line 1E or 1W to any service low-speed port. For example, Figure 8-8 shows incoming service STS-1 tributary 2 of STS-3 tributary 5 (5-2) on OC-48 high-speed line 1W cross-connected to service low-speed port 1 of slot 4B (4B-1).

Drop cross-connections are also made to any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. (Any low-speed port associated with the slot pair may be specified when entering the cross-connection.) A single input enters an active drop cross-connection from any incoming STS-3 tributary of OC-48 high-speed line 1E or 1W to the "A" slot and another active drop cross-connection from the same incoming STS-3 tributary to the "B" slot. For example, Figure 8-9 shows incoming service STS-3 tributary 4 of OC-48 high-speed line 1E cross-connected to service low-speed interface slots 6A and 6B.

- **Through cross-connections:** Through cross-connections are made from any incoming STS-1 tributary of OC-48 high-speed line 1E or 1W to the same-numbered outgoing STS-1 tributary of the other OC-48 high-speed line. For example, Figure 8-8 shows incoming service STS-1 tributary 1 of STS-3 tributary 3 (3-1) on OC-48 high-speed line 1W cross-connected to outgoing service STS-1 tributary 1 of STS-3 tributary (3-1) on OC-48 high-speed line 1E.

When an STS-1 tributary of OC-48 high-speed line 1E or 1W is cross-connected to a service low-speed port, another service low-speed port may be cross-connected to the same-numbered STS-1 tributary of the other OC-48 high-speed line. For example, Figure 8-8 shows service low-speed port 1 of slot 2B cross-connected to service STS-1 tributary 1 of STS-3 tributary 6 (6-1) on OC-48 high-speed line 1W, and service STS-1 tributary 1 of STS-3 tributary 6 (6-1) on OC-48 high-speed line 1E cross-connected to service low-speed port 3 of slot 6B. This is consistent with the 2-fiber bidirectional, line-switched ring standard.

The 1-way add, drop, and through cross-connections may be used to provide the following:

- **Bridge cross-connections:** A bridge cross-connection is two cross-connections sharing the same source. The source can be any incoming STS-1 tributary of OC-48 high-speed line 1E or 1W, or any service low-speed port. For example, Figure 8-10 shows incoming service STS-1 tributary 1 of STS-3 tributary 3 (3-1) on OC-48 high-speed line 1W cross-connected to outgoing service STS-1 tributary 1 of STS-3 tributary 3 (3-1) on OC-48 high-speed line 1E. Incoming service STS-1 tributary 1 of STS-3 tributary 3 (3-1) on OC-48 high-speed line 1W is also cross-connected to service low-speed port 2 of slot 1A (1A-2).

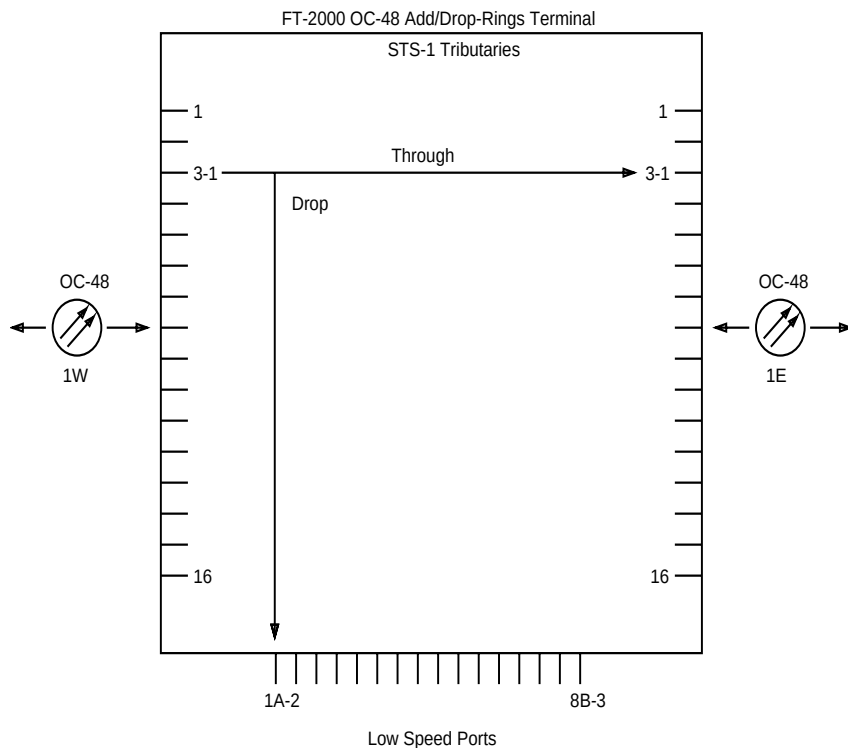


Figure 8-10. STS-1 Bridge Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

Cross-connections can also be made to/from any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. (Any low-speed port associated with the slot pair may be specified when entering the cross-connection.)

- Multiple bridge cross-connections:** A multiple bridge cross-connection is three or more superimposed cross-connections sharing the same source. The source can be any incoming STS-1 tributary of OC-48 high-speed line 1E or 1W, or any service low-speed port. For example, Figure 8-11 shows incoming service STS-1 tributary 1 of STS-3 tributary 7 (7-1) on OC-48 high-speed line 1E cross-connected to service low-speed port 1 of slot 6A (6A-1), port 1 of slot 7B (7B-1), and port 2 of slot 8B (8B-2). Figure 8-11 also shows service low-speed port 1 of slot 3A (3A-1) cross-connected to service STS-1 tributary 3 of STS-3 tributary 1 (1-3) and STS-1 tributary 1 of STS-3 tributary 2 (2-1) on OC-48 high-speed line 1W, and service STS-1 tributary 1 of STS-3 tributary 3 (3-1) on OC-48 high-speed line 1E.

Multiple bridge cross-connections can also be made to/from any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. (Any low-speed port associated with the slot pair may be specified when entering the cross-connection.)

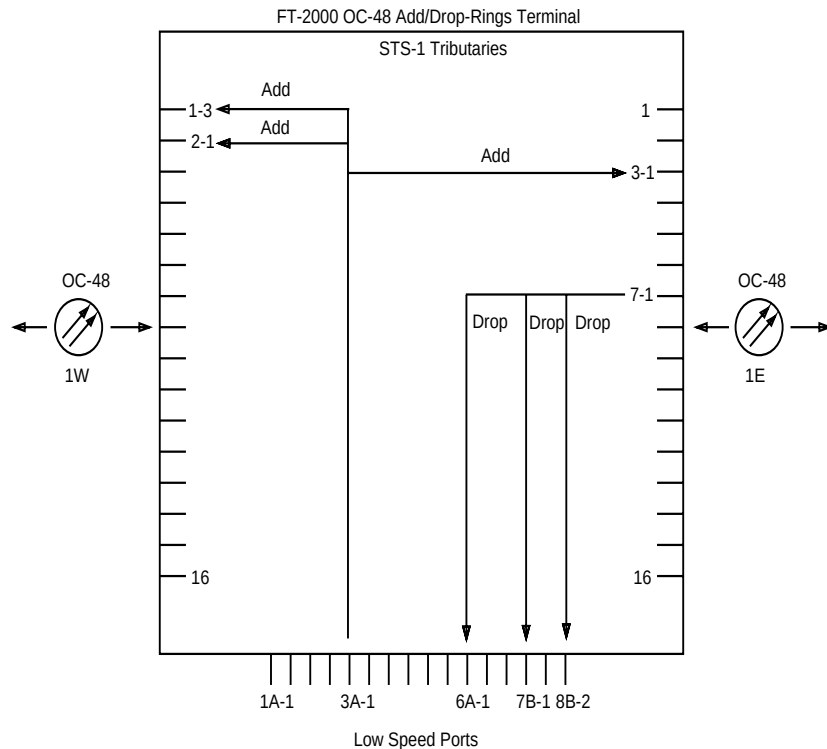


Figure 8-11. Multiple STS-1 Bridge Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

1-Way-DRI Cross-Connections

A 1-way-DRI cross-connection is a 1-way add cross-connection and a superimposed 1-way through cross-connection sharing the same destination. The add cross-connection and through cross-connection share the same outgoing service STS-1 tributary of OC-48 high-speed line 1E or 1W. For example, Figure 8-12 shows incoming service STS-1 tributary 2 of STS-3 tributary 3 (3-2) on OC-48 high-speed line 1W cross-connected to outgoing service STS-1 tributary 2 of STS-3 tributary 3 (3-2) on OC-48 high-speed line 1E. Service low-speed port 2 of slot 6B (6B-2) is also cross-connected to outgoing service STS-1 tributary 2 of STS-3 tributary 3 (3-2) on OC-48 high-speed line 1E.

A single input enters an active add cross-connection and a standby through cross-connection.

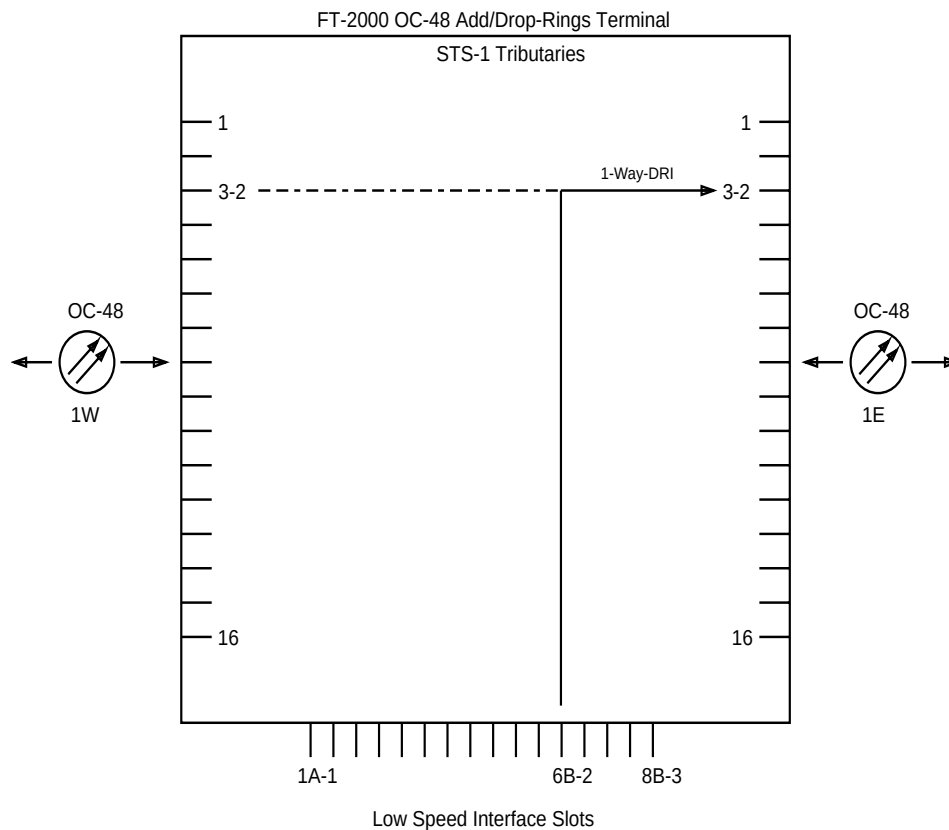


Figure 8-12. STS-1 1-Way-DRI Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

The 1-way-DRI cross-connections are also made from any service low-speed interface slot and the companion low-speed interface slot (for example, 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode. (Any low-speed port associated with the slot pair may be specified when entering the cross-connection.)

A single input enters an active and standby add cross-connection from the service low-speed interface slot pair to a service STS-3 tributary and a standby through cross-connection for the same STS-3 tributary.

The active add cross-connection carries traffic during normal conditions. If an incoming DS3 or EC-1 loss-of-signal condition is detected by the low-speed interface circuit pack, the through cross-connection becomes active and the add cross-connection becomes standby. An unprotected incoming OC-3 loss-of-signal or loss-of-frame condition also causes the through cross-connection to become active and the add cross-connection to become standby.

For Release 7.0 and prior releases, after the LOS, LOP, LOF or Line AIS condition clears, traffic does not automatically switch (revert) to the add cross-connection. A reset must be performed using the FAULT-Switch-Path-STs1 input to switch the traffic back to the add cross-connection.

Release 7.1.0 and later releases provide revertive DRI and do automatically switch back to the service fiber.

For more information about the dual ring interworking (DRI) application, refer to Volume I, Section 2, "Applications." For more information about DRI protection switching, refer to Volume I, Section 9, "Maintenance Description."

2-Way Cross-Connections

The FT-2000 OC-48 Add/Drop-Rings Terminal provides an STS-1 time slot assignment (TSA) capability that supports the following 2-way cross-connections:

- **Add/drop cross-connections:** Add/drop cross-connections are made between any service low-speed port and any STS-1 tributary of OC-48 high-speed line 1E or 1W. For example, Figure 8-13 shows service low-speed port 2 of slot 2B (2B-2) cross-connected to service STS-1 tributary 2 of STS-3 tributary 6 (6-2) on OC-48 high-speed line 1W.

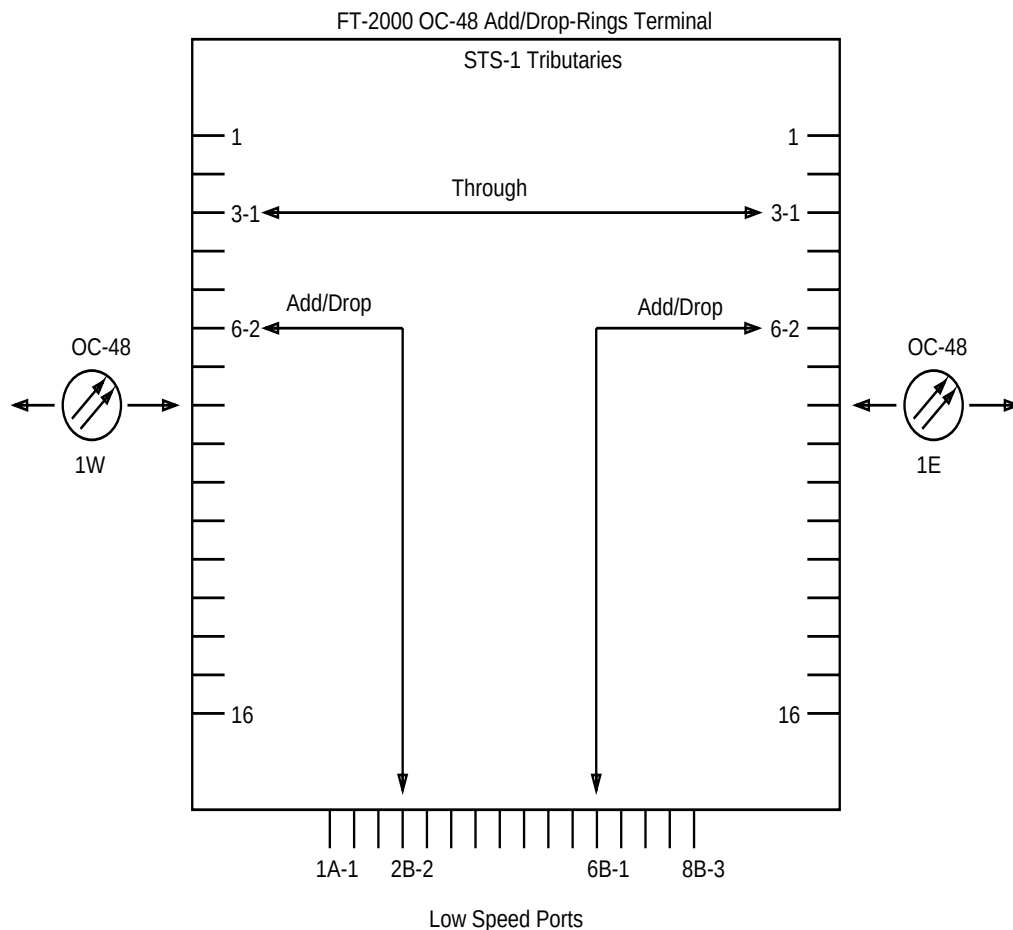


Figure 8-13. STS-1 2-Way Cross-Connections at an FT-2000 OC-48 Add/Drop-Rings Terminal

Add/drop cross-connections are made between any service low-speed interface slot and the companion low-speed interface slot (for example, slot 2A and 2B) when the slot pair is provisioned for the OC-3 1+1 protected mode and any service STS-3 tributary. (Any low-speed port associated with

the slot pair may be specified when entering the cross-connection.) A single input enters an active and a standby add cross-connection and two active drop cross-connections.

- **Through cross-connections:** Through cross-connections are made between any STS-1 tributary on OC-48 high-speed line 1E or 1W and the same-numbered STS-1 tributary of the other OC-48 high-speed line. For example, Figure 8-13 shows service STS-1 tributary 1 of STS-3 tributary (3-1) on OC-48 high-speed line 1W cross-connected to service STS-1 tributary 1 of STS-3 tributary 3 (3-1) on OC-48 high-speed line 1E.

When an STS-1 tributary of OC-48 high-speed line 1E or 1W is cross-connected to a service low-speed port, another service low-speed port may be cross-connected to the same-numbered STS-1 tributary of the other OC-48 high-speed line. For example, Figure 8-13 shows service low-speed port 2 of slot 2B (2B-2) cross-connected to service STS-1 tributary 2 of STS-3 tributary 6 (6-2) on OC-48 high-speed line 1W, and service low-speed port 1 of slot 6B (6B-1) cross-connected to service STS-1 tributary 2 of STS-3 tributary 6 (6-2) on OC-48 high-speed line 1E. This is consistent with the 2-fiber bidirectional, line-switched ring standard.

STS-1 Cross-Connection Restrictions

STS-1 cross-connections have the following restrictions:

- The FT-2000 OC-48 Add/Drop-Rings Terminal must be equipped with Release 7.0.0-ADR or later software.
- The FT-2000 OC-48 Add/Drop-Rings Terminal must be equipped with the OC48 Receivers, Add/Drop [OC48 RCVR (A/D)] 839B5 or 839E5 and the following compatible OC48 Transmitter circuit packs:
 - OC48 Transmitter, Add/Drop - 1.3 μ m Standard Performance [OC48 TRMTR (A/D STS1 1.3 STD)] 739B5
 - OC48 Transmitter, Add/Drop - 1.3 μ m High Performance [OC48 TRMTR (A/D STS1 1.3 HI PERF)] 739C5
 - OC48 Transmitter, Add/Drop - 1.5 μ m Standard Performance [OC48 TRMTR (A/D STS1 1.5 STD)] 739E_
 - OC48 Transmitter, Add/Drop - 1.5 μ m High Performance [OC48 TRMTR (A/D STS1 1.5 HI PERF)] 739G_
 - OC48 Transmitter, Add/Drop - 1.5 μ m High Performance [OC48 TRMTR (A/D STS1 1.5 STD)] 739H_
 - OC48 Transmitter, Add/Drop - 1.5 μ m Standard Performance [OC48 TRMTR (A/D STS1 1.5 STD)] 739J5
 - OC48 Transmitter, Add/Drop - 1.5 μ m LBA Standard Performance [OC48 TRMTR (A/D STS1 1.5 LBA)] 739P5
 - OC48 Transmitter, Add/Drop - 1.5 μ m LBA Standard Performance [OC48 TRMTR (A/D STS1 1.5 LBA, W1)] 739R5
 - OC48 Transmitter, Add/Drop - 1.5 μ m LBA Standard Performance [OC48 TRMTR (A/D STS1 1.5 LBA, W2)] 739S5
- STS-1 cross-connections cannot be made from one low-speed port to another low-speed port or between two differently numbered STS-1 tributaries.
- The three STS-1 signals that are demultiplexed on a low-speed interface circuit pack must come from the same OC-48 high-speed line.

Extra Traffic Capability

The FT-2000 OC-48 Add/Drop-Rings Terminal also has the capability to provide *extra traffic*. Extra traffic allows access to the protection STS-3 and STS-1 (Release 7 and later) tributaries of OC-48 high speed line 1E or 1W for low priority traffic or for restoration purposes.

In 2-fiber bidirectional rings, STS-3 tributaries 9 through 16 are protection tributaries. 1-way, 1-way-DRI, and 2-way cross-connections are possible between any service low-speed interface slot and any protection STS-3 tributary of OC-48

high-speed line 1E or 1W. In Release 7 and later releases 1-way, 1-way-DRI, and 2-way cross-connections are possible between any service low speed port and any protection STS-1 tributary of OC-48 high speed line 1E and 1W. Cross-connections are also possible between any protection STS-3 or STS-1 (Release 7 and later) tributary of OC-48 high-speed line 1E or 1W and the same-numbered protection STS-3 or STS-1 tributary of the other OC-48 high-speed line.

For example, Figure 8-14 shows service low-speed interface slot 4B cross-connected to protection STS-3 tributary 10 of OC-48 high-speed line 1E (2-way) and protection STS-3 tributary 14 of OC-48 high-speed line 1W cross-connected to protection STS-3 tributary 14 of OC-48 high-speed line 1E (2-way).

FT-2000 ADR Release 7.0 and Earlier Releases

Extra traffic is preempted if a 2-fiber ring protection switch occurs. In 2-fiber ring protection switching, the protection STS-3 tributaries of OC-48 high speed line 1E protect the service STS-3 tributaries of OC-48 high speed line 1W, and the protection STS-3 tributaries of OC-48 high speed line 1W protect the service STS-3 tributaries of OC-48 high speed line 1E. For more information about 2-fiber ring protection switching, refer to Volume I, Section 9, "Maintenance Description."

FT-2000 ADR Release 7.1 and later Releases

With R7.1 those tributaries on the protection fiber that are used to carry extra traffic are designated as either "Preemptible Protection Access" or "Non-Preemptible Protection Access (NPPA)."

The extra traffic carried by non-preemptible tributaries is not dropped (preempted) by any ring loopback protection switch. Since protection switching in a ring is always bidirectional there will always be two unprotected tributaries for each tributary of NPPA traffic carried. One on the service fiber and the other on the protection fiber.

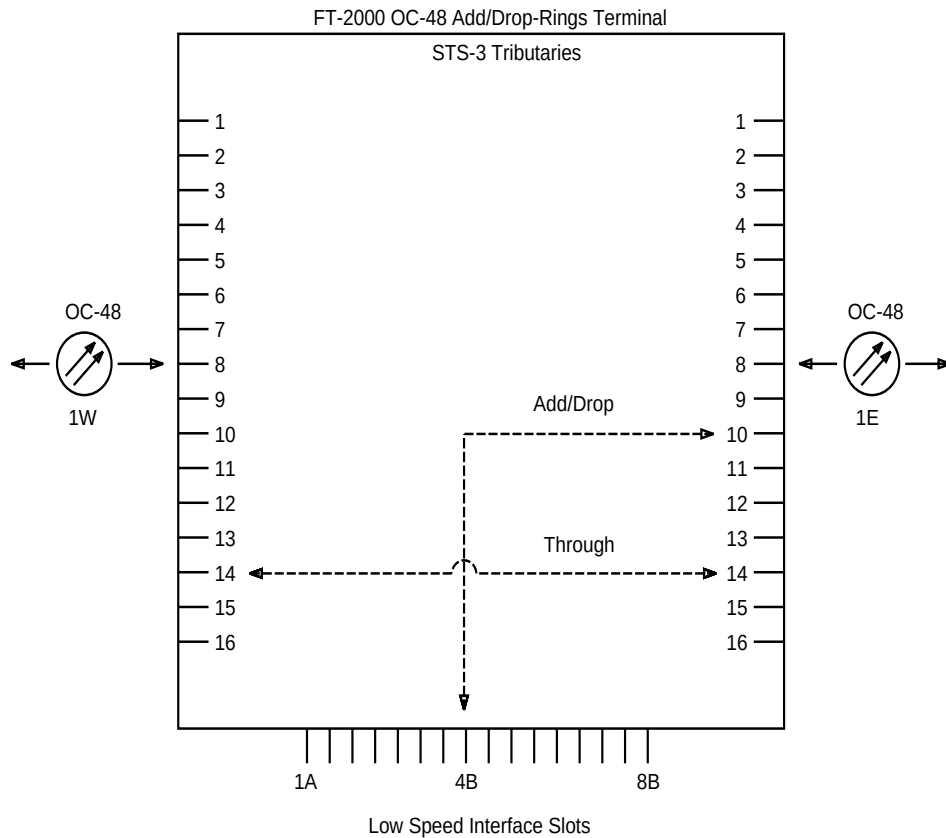


Figure 8-14. Extra Traffic Capability

Rolling Cross-Connections

In Release 4.1 and later releases, the FT-2000 OC-48 Add/Drop Rings Terminal can roll traffic from an existing source port, slot, or tributary to a new source for 1-way and 1-way-DRI cross-connections. In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal can roll traffic from an existing source port, slot, or tributary to a new source for 1-way and 1-way-DRI cross-connections. The existing 1-way or 1-way-DRI cross-connection and the new 1-way or 1-way-DRI cross-connection must have a common destination.

The existing cross-connection is deleted and a new cross-connection is entered as part in a 1-step process. This 1-step process takes less than 60 milliseconds. Both 1-way and 1-way-DRI cross-connections can be rolled from an existing source to a new source, using the **CONFIGURATION-Enter-Roll-STS3** or the **CONFIGURATION-Enter-Roll-STS1** input.

Cross-Connection Inputs

Cross-connections must be specified and administered using the following inputs*:

- **CONFIGURATION-Enter-Crossconnection-STS1:** This input enters a 1-way, 1-way-DRI, 2-way cross-connection.

- A *1-way* cross-connection is entered by specifying a 1-way source and destination. The source may be a low-speed port, a low-speed port in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, or an STS-1 tributary. If the source is a low-speed port, the destination must be an STS-1 tributary. If the source is an STS-1 tributary, the destination must be a low-speed port, a low-speed port in a slot pair provisioned for the OC-3 1+1 protected mode, or the same-numbered STS-1 tributary on the OC-48 high-speed line in the opposite direction. If the source is an STS-1 tributary, the source node TID must be specified. If the destination is an STS-1 tributary, the destination node TID must be specified.

An input requesting a 1-way cross-connection will be denied if the specified destination is already cross-connected to a different source, or the destination is already involved in a 1-way-DRI cross-connection. An input requesting a 1-way cross-connection will not be denied if the specified source is already cross-connected to a different destination. (The bridging capability allows two or more cross-connections to share the same source.)

- A *1-way-DRI* cross-connection is entered by specifying a 1-way source and destination. The source is always a low-speed port or a low-speed port in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, and the destination is always an STS-1 tributary. The DRI secondary node TID and the destination node TID in the ring must also be specified.

An input requesting a 1-way-DRI cross-connection will be denied if the specified destination (always an STS-1 tributary) is already involved in a 1-way, 2-way, or 1-way-DRI cross-connection. An input requesting a 1-way-DRI cross-connection will not be denied if the specified source is already cross-connected to a different destination. (The bridging capability allows two or more cross-connections to share the same source.)

* The FT-2000 OC-48 Lightwave System is menu-driven and prompts the user to enter the desired input.

- A 2-way cross-connection is entered by first specifying a 1-way source and destination. The source may be a low-speed port, a low-speed port in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, or an STS-1 tributary. If the source is a low-speed port, the destination must be an STS-1 tributary. If the source is an STS-1 tributary, the destination must be a low-speed port, a low-speed port in a slot pair provisioned for the OC-3 1+1 protected mode, or the same-numbered STS-1 tributary on the OC-48 high-speed line in the opposite direction. If the source is an STS-1 tributary, the source node TID must be specified. If the destination is an STS-1 tributary, the destination node TID must be specified.

After the user specifies the first 1-way source and destination, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically enters the corresponding 1-way source and destination in the opposite direction. This creates a 2-way cross-connection.

An input requesting a 2-way cross-connection will be denied if the specified source is already cross-connected to a different destination, or if the specified destination is already cross-connected to a different source. This input will also be denied if the specified destination is already involved in a 1-way-DRI cross-connection.

The source node TID and the destination node TID of an STS-1 tributary are required to support squelching during a 2-fiber ring protection switch. If an STS-1 tributary is specified as the source, the source node TID must be specified. The source node is the node in the ring where the low-speed signals on that STS-1 tributary originate. If an STS-1 tributary is specified as the destination, the destination node TID must be specified. The destination node is the node in the same ring where the low-speed signals on that STS-1 tributary terminate. If an STS-1 tributary is bridged at one or more nodes in a ring, the last node where the low-speed signals are terminated must be specified as the destination node. For more information about 2-fiber ring protection switching and squelching, refer to Volume I, Section 9, "Maintenance Description."

The **CONFIGURATION-Enter-Crossconnection-STS1** input can also be used to change the values of the source and destination node parameters (node TIDs) associated with an existing cross-connection. This is accomplished by reentering the same cross-connection with the new TIDs for the source and/or destination nodes.

- **CONFIGURATION-Enter-Crossconnection-STS3:** This input enters a 1-way, 1-way-DRI, 2-way cross-connection.

- A *1-way* cross-connection is entered by specifying a 1-way source and destination. The source may be a low-speed interface slot, a low-speed interface slot in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, or an STS-3 tributary. If the source is a low-speed interface slot, the destination must be an STS-3 tributary. If the source is an STS-3 tributary, the destination must be a low-speed interface slot, a low-speed interface slot in a slot pair provisioned for the OC-3 1+1 protected mode, or the same-numbered STS-3 tributary on the OC-48 high-speed line in the opposite direction. If the source is an STS-3 tributary, the source node TID must be specified. If the destination is an STS-3 tributary, the destination node TID must be specified.

An input requesting a 1-way cross-connection will be denied if the specified destination is already cross-connected to a different source, or the destination is already involved in a 1-way-DRI cross-connection. An input requesting a 1-way cross-connection will not be denied if the specified source is already cross-connected to a different destination. (The bridging capability allows two or more cross-connections to share the same source.)

- A *1-way-DRI* cross-connection is entered by specifying a 1-way source and destination. The source is always a low-speed interface slot or a low-speed interface slot in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, and the destination is always an STS-3 tributary. The DRI secondary node TID and the destination node TID in the ring must also be specified.

An input requesting a *1-way-DRI* cross-connection will be denied if the specified destination (always an STS-3 tributary) is already involved in a 1-way, 2-way, or 1-way-DRI cross-connection. An input requesting a 1-way-DRI cross-connection will not be denied if the specified source is already cross-connected to a different destination. (The bridging capability allows two or more cross-connections to share the same source.)

- A 2-way cross-connection is entered by first specifying a 1-way source and destination. The source may be a low-speed interface slot, a low-speed interface slot in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, or an STS-3 tributary. If the source is a low-speed interface slot, the destination must be an STS-3 tributary. If the source is an STS-3 tributary, the destination must be a low-speed interface slot, a low-speed interface slot in a slot pair provisioned for the OC-3 1+1 protected mode, or the same-numbered STS-3 tributary on the OC-48 high-speed line in the opposite direction. If the source is an STS-3 tributary, the source node TID must be specified. If the destination is an STS-3 tributary, the destination node TID must be specified.

After the user specifies the first 1-way source and destination, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically enters the corresponding 1-way source and destination in the opposite direction. This creates a 2-way cross-connection.

An input requesting a 2-way cross-connection will be denied if the specified source is already cross-connected to a different destination, or if the specified destination is already cross-connected to a different source. This input will also be denied if the specified destination is already involved in a 1-way-DRI cross-connection.

The source node TID and the destination node TID of an STS-3 tributary are required to support squelching during a 2-fiber ring protection switch. If an STS-3 tributary is specified as the source, the source node TID must be specified. The source node is the node in the ring where the low-speed signals on that STS-3 tributary originate. If an STS-3 tributary is specified as the destination, the destination node TID must be specified. The destination node is the node in the same ring where the low-speed signals on that STS-3 tributary terminate. If an STS-3 tributary is bridged at one or more nodes in a ring, the last node where the low-speed signals are terminated must be specified as the destination node. For more information about 2-fiber ring protection switching and squelching, refer to Volume I, Section 9, "Maintenance Description."

The **CONFIGURATION-Enter-Crossconnection-STS3** input can also be used to change the values of the source and destination node parameters (node IDs or TIDs) associated with an existing cross-connection. This is accomplished by reentering the same cross-connection with the new node IDs or TIDs for the source and/or destination nodes.

- **CONFIGURATION-Delete-Crossconnection-STS1:** This input deletes a 1-way, 1-way-DRI, 2-way cross-connection.

- A *1-way* cross-connection is deleted by specifying the source and destination. The source is a low-speed port, a low-speed port in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, or an STS-1 tributary. If the source is a low-speed port, the destination must be an STS-1 tributary. If the source is an STS-1 tributary, the destination must be a low-speed port, a low-speed port in a slot pair provisioned for the OC-3 1+1 protected mode, or the same-numbered STS-1 tributary on the OC-48 high-speed line in the opposite direction.
- A *1-way-DRI* cross-connection is deleted by specifying the source and destination. The source is always a low-speed port or a low-speed port in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, and the destination is always an STS-1 tributary.
- A *2-way* cross-connection is deleted by first specifying the source and destination. The source is a low-speed port, a low-speed port in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, or STS-1 tributary. If the source is a low-speed port, the destination must be an STS-1 tributary. If the source is an STS-1 tributary, the destination must be a low-speed port, a low-speed port in a slot pair provisioned for the OC-3 1+1 protected mode, or the same-numbered STS-1 tributary on the OC-48 high-speed line in the opposite direction.

After the user specifies the first 1-way source and destination, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically deletes the corresponding 1-way source and destination in the opposite direction. This deletes the 2-way cross-connection.

This input is never denied, even if the specified cross-connection is not in effect.

This input does not require the node ID or TID of the source and destination nodes to be specified.

- **CONFIGURATION-Delete-Crossconnection-STS3:** This input deletes a 1-way, 1-way-DRI, 2-way cross-connection.

- A *1-way* cross-connection is deleted by specifying the source and destination. The source is a low-speed interface slot, a low-speed interface slot in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, or an STS-3 tributary. If the source is a low-speed interface slot, the destination must be an STS-3 tributary. If the source is an STS-3 tributary, the destination must be a low-speed interface slot, a low-speed interface slot in a slot pair provisioned for the OC-3 1+1 protected mode, or the same-numbered STS-3 tributary on the OC-48 high-speed line in the opposite direction.
- A *1-way-DRI* cross-connection is deleted by specifying the source and destination. The source is always a low-speed interface slot or a low-speed interface slot in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, and the destination is always an STS-3 tributary.
- A *2-way* cross-connection is deleted by first specifying the source and destination. The source is a low-speed interface slot, a low-speed interface slot in a slot pair (for example, slot 2A and 2B) provisioned for the OC-3 1+1 protected mode, or STS-3 tributary. If the source is a low-speed interface slot, the destination must be an STS-3 tributary. If the source is an STS-3 tributary, the destination must be a low-speed interface slot, a low-speed interface slot in a slot pair provisioned for the OC-3 1+1 protected mode, or the same-numbered STS-3 tributary on the OC-48 high-speed line in the opposite direction.

After the user specifies the first 1-way source and destination, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically deletes the corresponding 1-way source and destination in the opposite direction. This deletes the 2-way cross-connection.

This input is never denied, even if the specified cross-connection is not in effect.

This input does not require the node ID or TID of the source and destination nodes to be specified.

- **CONFIGURATION-Enter-Roll-STS1:** This input performs a **CONFIGURATION-Delete-Crossconnection-STS1** input and a **CONFIGURATION-Enter-Crossconnection-STS1** input in a 1-step process. This capability allows traffic to be rolled from one source to another source as part of normal user operations. The 1-step process rolls traffic from an existing source to a new source for 1-way and 1-way-DRI cross-connections. The existing cross-connection and the new cross-connection must have the same destination. Traffic is rolled from the existing source to the new source by specifying the destination and source of the existing 1-way or 1-way-DRI cross-connection to be deleted, the type of new cross-connection (1-way or 1-way-DRI), and the new source of the 1-way or 1-way-DRI cross-connection to be entered. The node TID of the source node in the ring must be specified. The source node is the node in the ring where the low-speed signals on that STS-1 tributary originate.

This input is denied if the existing cross-connection, as specified, is not in effect or if the new cross-connection does not meet the requirements for a 1-way or 1-way-DRI cross-connection.

- **CONFIGURATION-Enter-Roll-STS3:** This input performs a **CONFIGURATION-Delete-Crossconnection-STS3** input and a **CONFIGURATION-Enter-Crossconnection-STS3** input in a 1-step process. This capability allows traffic to be rolled from one source to another source as part of normal user operations. The 1-step process rolls traffic from an existing source to a new source for 1-way and 1-way-DRI cross-connections. The existing cross-connection and the new cross-connection must have the same destination. Traffic is rolled from the existing source to the new source by specifying the destination and source of the existing 1-way or 1-way-DRI cross-connection to be deleted, the type of new cross-connection (1-way or 1-way-DRI), and the new source of the 1-way or 1-way-DRI cross-connection to be entered. The node TID of the source node in the ring must be specified. The source node is the node in the ring where the low-speed signals on that STS-3 tributary originate.

This input is denied if the existing cross-connection, as specified, is not in effect or if the new cross-connection does not meet the requirements for a 1-way or 1-way-DRI cross-connection.

**NOTE:**

Release 7.1 and later releases do not permit the user to configure the Squelch Map. However, a Squelch Map Report is automatically displayed should there be a cross-connect configuration error.

- **CONFIGURATION-Enter-Squelch-Map-STS1:** For Release 7.0 and earlier releases, this input creates entry(s) in the STS-1 squelch map for the addressed STS-1 tributary(s) and identifies the node TIDs of the source and/or destination node associated with them. When the STS-1 tributaries associated with a single STS-3 tributary are split up, the STS-3 tributary source and destination node TID provisioning is no longer correct for the STS-1 tributaries. This input overrides the provisioned STS-3 source and destination node TIDs for those STS-1 tributaries. This STS-1 squelch map provisioning must be done at each node where the affected STS-1 tributary(s) are terminated, looped back using external cabling, or passed through using STS-3 through cross-connections.

Each FT-2000 OC-48 Add/Drop-Rings Terminal in a 2-fiber ring maintains a squelch map that allows each STS-1 tributary to have its own source and destination nodes. This enables switching nodes to squelch STS-1 tributaries that are looped back (through cabled) at the cross-connect panel based on the source and destination nodes. Thus, if a node that has through-cabled STS-1 tributaries becomes isolated from the source and destination nodes of the through traffic due to a node failure or multiple line failures, only the add/drop traffic at the isolated node will be squelched. The through STS-1 tributaries will be protected using 2-fiber ring protection switching.

An entry in the STS-1 squelch map for a 2-way cross-connection is created by specifying the line, STS-3 tributary, STS-1 tributary, and source node TID. An entry in the STS-1 squelch map for a 1-way cross-connection is created by specifying the line, STS-3 tributary, STS-1 tributary, and direction. If the direction is from the OC-48 line (drop), the source node TID must be specified. If the direction is toward the OC-48 line (add), the destination node TID must be specified.

- **CONFIGURATION-Delete-Squelch Map-STS1:** For Release 7.0 and earlier releases, this input deletes entries in the STS-1 squelch map for the addressed STS-1 tributary(s). The absence of an entry in the STS-1 squelch map means that the provisioned STS-3 source and destination node TIDs associated with the STS-3 tributary are in effect again.

- **CONFIGURATION-Retrieve-Crossconnection-STS1:** This input generates a report showing all the existing STS-1 cross-connections in the FT-2000 OC-48 Add/Drop-Rings Terminal. The **FAULT-Retrieve-History** input also generates a report that can be used to find out how a cross-connection was entered. For more information about the STS-1 Cross-Connection Map Report and History Report, refer to Volume I, Section 11, "Craft Interface Terminal Usage."
- **CONFIGURATION-Retrieve-Crossconnection-STS3:** This input generates a report showing all the existing STS-3 cross-connections in the FT-2000 OC-48 Add/Drop-Rings Terminal. The **FAULT-Retrieve-History** input also generates a report that can be used to find out how a cross-connection was entered. For more information about the STS-3 Cross-Connection Map Report and History Report, refer to Volume I, Section 11, "Craft Interface Terminal Usage."



NOTE:

For software releases 7.0 and 7.1, the command **CONFIGURATION-Retrieve-Crossconnection-STS3** works properly--provided that the command aid field is left "null". An example of a specified aid, which will not work, is (STS3-1E-1). For software release 7.2 and later, only the STS1 option works, e.g., **CONFIGURATION-Retrieve-Crossconnection-STS1**.

- **CONFIGURATION-Retrieve-Squelch Map-STS1:** For Release 7.0 and earlier releases, this input generates a report showing the STS-1 level squelch map of the FT-2000 OC-48 Add/Drop-Rings Terminal. If there is an entry(s) in the STS-1 squelch map report, the **CONFIGURATION-Retrieve-Crossconnection-STS3** generates a report that will also include a warning that additional squelch map information is provided in the **CONFIGURATION-Retrieve-Squelch Map-STS1** report. For more information about the STS-1 Squelch Map Report, refer to Volume I, Section 11, "Craft Interface Terminal Usage."
- **CONFIGURATION-Retrieve-Squelch Map-STS3:** For Releases 7.0 and earlier releases, this input generates a report showing the STS-3 level squelch map of the FT-2000 OC-48 Add/Drop-Rings Terminal. For more information about the STS-3 Squelch Map Report, refer to Volume I, Section 11, "Craft Interface Terminal Usage."

- **FAULT-Switch-Path-STS1:** This input controls the state of an existing 1-way-DRI cross-connection. The state of the existing 1-way-DRI cross-connection is switched by specifying the destination STS-1 tributary and the protection switch priority (reset or manual).

An input requesting a DRI protection switch will be denied if the specified destination STS-1 tributary is not involved in a 1-way-DRI cross-connection or if the input is entered with a manual priority and the DRI protection switch is in the automatic switch state. For more information about dual ring interworking protection switching, refer to Volume I, Section 9, "Maintenance Description."

- **FAULT-Switch-Path-STS3:** This input controls the state of an existing 1-way-DRI cross-connection. The state of the existing 1-way-DRI cross-connection is switched by specifying the destination STS-3 tributary and the protection switch priority (reset or manual).

An input requesting a DRI protection switch will be denied if the specified destination STS-3 tributary is not involved in a 1-way-DRI cross-connection or if the input is entered with a manual priority and the DRI protection switch is in the automatic switch state. For more information about dual ring interworking protection switching, refer to Volume I, Section 9, "Maintenance Description."

Cross-Connection Examples

Entering 1-Way and 2-Way STS-1 Cross-Connections

Figure 8-15 shows two STS-1 circuits that have been established on a 2-fiber bidirectional ring with 1-way and 2-way STS-1 cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals. The CONFIGURATION-Enter - Crossconnection - STS1 input is used to enter 1-way and 2-way cross-connections.

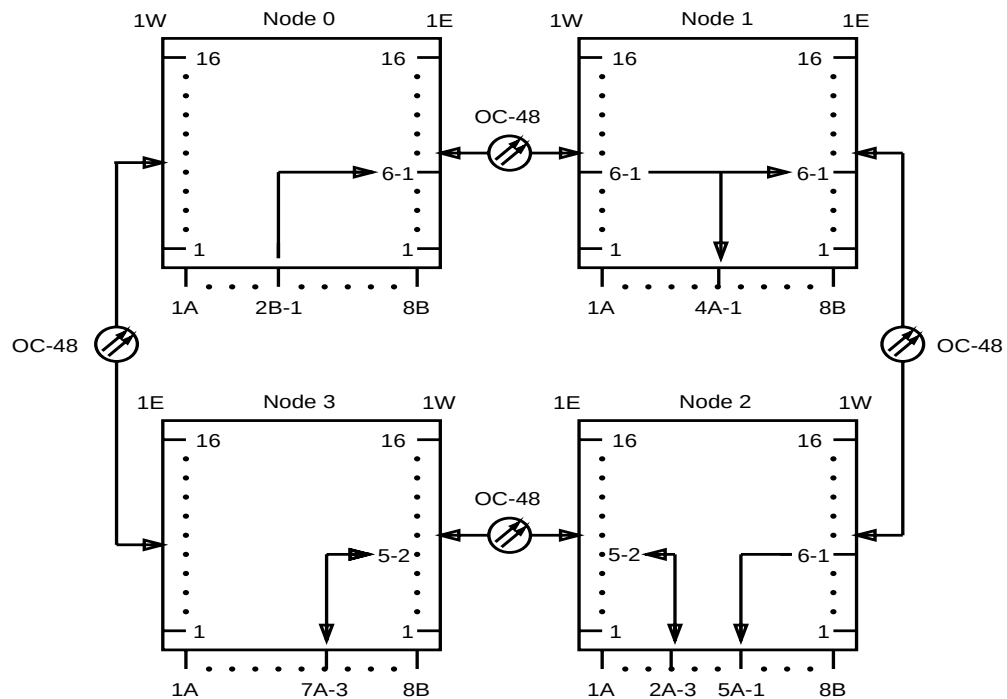


Figure 8-15. Example of 1-Way and 2-Way STS-1 Cross-Connections in a 2-Fiber Bidirectional Ring

- At node 0, a 1-way add cross-connection is entered by specifying:
 - Source low-speed interface slot* (slot 2B)
 - Source STS-1 port (1)
 - Destination line (line 1E)
 - Destination STS-3 tributary (tributary 6)
 - Destination STS-1 tributary (tributary 1)
 - Destination node TID^{†‡} (node 2 TID).
- At node 1, a 1-way through cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source STS-1 tributary (tributary 1)
 - Source node TID[†] (node 0 TID)
 - Destination (tributary)
 - Destination node TID[†] (node 2 TID).

At node 1, a bridge is also established when a 1-way drop cross-connection is entered. The 1-way drop cross-connection is entered by specifying:

- Source (tributary)
- Source line (line 1W)
- Source STS-3 tributary (tributary 6)
- Source STS-1 tributary (tributary 1)
- Source node TID[†] (node 0 TID)
- Destination (STS1 port)
- Destination low-speed interface slot* (slot 4A)
- Destination low-speed port (1).

* If the specified slot is part of a slot pair that has been provisioned for the OC-3 1+1 (Release 5 and later releases) or OC-12 1+1 (Release 7.1 and later releases) protected mode, specifying one slot also includes the companion slot.

† In Release 4.1 and later releases, entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the **Retrieve-Map-Ring** command to update the point-and-shoot list.

‡ If an STS-1 tributary is bridged at one or more nodes in a ring, the last node where the low-speed signals are terminated is the destination node.

- At node 2, a 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source STS-1 tributary (tributary 1)
 - Source node TID† (node 0 TID)
 - Destination (STS-1 port)
 - Destination low-speed interface slot* (slot 5A)
 - Destination low-speed port (1).
- At node 2, a 2-way add/drop cross-connection is entered by specifying:
 - Source (STS-1 port)
 - Source low-speed interface slot* (slot 2A)
 - Source low-speed port (3)
 - Destination line (line 1E)
 - Destination STS-3 tributary (tributary 5)
 - Destination STS-1 tributary (tributary 2)
 - Destination node TID† (node 3 TID).
- The same 2-way add/drop cross-connection may also be entered by specifying:
 - Source (tributary)
 - Source line (line 1E)
 - Source STS-3 tributary (tributary 5)
 - Source STS-1 tributary (tributary 2)
 - Source node TID† (node 3 TID)
 - Destination (STS-1 port)
 - Destination low-speed interface slot* (slot 2A)
 - Destination low-speed port (3).

- At node 3, a 2-way add/drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 5)
 - Source STS-1 tributary (tributary 2)
 - Source node TID[†] (node 2 TID)
 - Destination (STS-1 port)
 - Destination low-speed interface slot* (slot 7A).
 - Destination low-speed port (3).
- The same 2-way add/drop cross-connection may also be entered by specifying:
 - Source (STS-1 port)
 - Source low-speed interface slot* (slot 7A)
 - Source low-speed port (3)
 - Destination line (line 1W)
 - Destination STS-3 tributary (tributary 5)
 - Destination STS-1 tributary (tributary 2)
 - Destination node TID[†] (node 2 TID).



NOTE:

While cross-connections are entered, the FT-2000 OC-48 Add/Drop-Rings Terminal will report several trouble conditions (for example, circuit provisioning error and path integrity failure). However, these trouble conditions should be ignored until all the cross-connections for a circuit have been entered. If the trouble conditions remain after all the cross-connections have been entered, refer to the "Operation and Maintenance (TOP)" section (Volume II).

For more information about entering cross-connections at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the CIT Tutorial in Section 11, "Craft Interface Terminal Usage," and the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II). Figure 8-16 shows a worksheet that may

* In the specified slot is part of a slot pair that has been provisioned for the OC-3 1+1 (Release 5 and later releases) or OC-12 1+1 (Release 7.1 and later releases) protected mode, specifying one slot also includes the companion slot.

† In Release 4.1 and later releases, entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the **Retrieve-Map-Ring** command to update the point-and-shoot list.

be used to record STS-1 cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals in a 2-fiber bidirectional ring.

	W	E		W	E		W	E		W	E
1	1 2 3	1 2 3	1	1 2 3	1 2 3	1	1 2 3	1 2 3	1	1 2 3	1 2 3
2	1 2 3	1 2 3	2	1 2 3	1 2 3	2	1 2 3	1 2 3	2	1 2 3	1 2 3
3	1 2 3	1 2 3	3	1 2 3	1 2 3	3	1 2 3	1 2 3	3	1 2 3	1 2 3
4	1 2 3	1 2 3	4	1 2 3	1 2 3	4	1 2 3	1 2 3	4	1 2 3	1 2 3
5	1 2 3	1 2 3	5	1 2 3	1 2 3	5	1 2 3	1 2 3	5	1 2 3	1 2 3
6	1 2 3	1 2 3	6	1 2 3	1 2 3	6	1 2 3	1 2 3	6	1 2 3	1 2 3
7	1 2 3	1 2 3	7	1 2 3	1 2 3	7	1 2 3	1 2 3	7	1 2 3	1 2 3
8	1 2 3	1 2 3	8	1 2 3	1 2 3	8	1 2 3	1 2 3	8	1 2 3	1 2 3
9	1 2 3	1 2 3	9	1 2 3	1 2 3	9	1 2 3	1 2 3	9	1 2 3	1 2 3
10	1 2 3	1 2 3	10	1 2 3	1 2 3	10	1 2 3	1 2 3	10	1 2 3	1 2 3
11	1 2 3	1 2 3	11	1 2 3	1 2 3	11	1 2 3	1 2 3	11	1 2 3	1 2 3
12	1 2 3	1 2 3	12	1 2 3	1 2 3	12	1 2 3	1 2 3	12	1 2 3	1 2 3
13	1 2 3	1 2 3	13	1 2 3	1 2 3	13	1 2 3	1 2 3	13	1 2 3	1 2 3
14	1 2 3	1 2 3	14	1 2 3	1 2 3	14	1 2 3	1 2 3	14	1 2 3	1 2 3
15	1 2 3	1 2 3	15	1 2 3	1 2 3	15	1 2 3	1 2 3	15	1 2 3	1 2 3
16	1 2 3	1 2 3	16	1 2 3	1 2 3	16	1 2 3	1 2 3	16	1 2 3	1 2 3

TID _____ TID _____ TID _____ TID _____

Figure 8-16. STS-1 Cross-Connection Worksheet

Entering 1-Way and 2-Way STS-3 Cross-Connections

Figure 8-17 shows two circuits that have been established on a 2-fiber bidirectional ring with 1-way and 2-way cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals. The **CONFIGURATION-Enter-Crossconnection-STs3** input is used to enter 1-way and 2-way cross-connections.

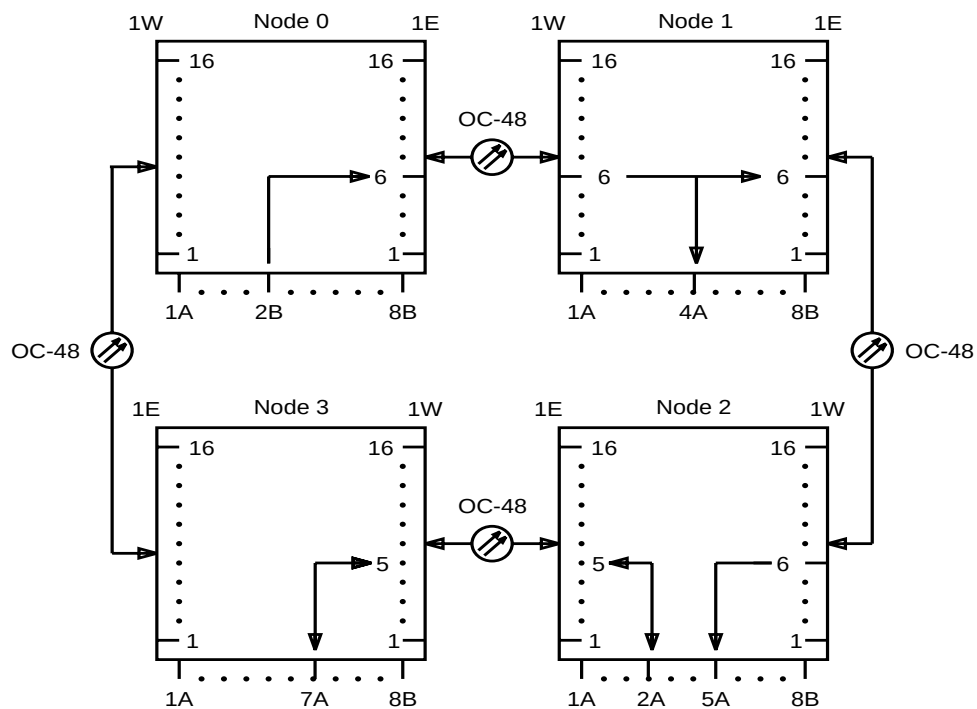


Figure 8-17. Example of 1-Way and 2-Way Cross-Connections in a 2-Fiber Bidirectional Ring

- At node 0, a 1-way add cross-connection is entered by specifying:
 - Source (slot)
 - Source low-speed interface slot* (slot 2B)
 - Destination line (line 1E)
 - Destination STS-3 tributary (tributary 6)
 - Destination node TID^{†‡} (node 2 TID).
- At node 1, a 1-way through cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source node TID[†] (node 0 TID)
 - Destination (tributary)
 - Destination node TID[†] (node 2 TID).
- At node 1, a bridge is also established when a 1-way drop cross-connection is entered. The 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source node TID[†] (node 0 TID)
 - Destination (slot)
 - Destination low-speed interface slot* (slot 4A).

* In the specified slot is part of a slot pair that has been provisioned for the OC-3 1+1 (Release 5 and later releases) or OC-12 1+1 (Release 7.1 and later releases) protected mode, specifying one slot also includes the companion slot.

† In Release 4.1 and later releases, entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the **Retrieve-Map-Ring** command to update the point-and-shoot list.

‡ If an STS-3 tributary is bridged at one or more nodes in a ring, the last node where the low-speed signals are terminated is the destination node.

In Releases 3 and 4.0, the node ID is entered.

- At node 2, a 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source node TID[†] (node 0 TID)
 - Destination (slot)
 - Destination low-speed interface slot* (slot 5A).
- At node 2, a 2-way add/drop cross-connection is entered by specifying:
 - Source (slot)
 - Source low-speed interface slot* (slot 2A)
 - Destination line (line 1E)
 - Destination STS-3 tributary (tributary 5)
 - Destination node TID^{†‡} (node 3 TID).
- The same 2-way add/drop cross-connection may also be entered by specifying:
 - Source (tributary)
 - Source line (line 1E)
 - Source STS-3 tributary (tributary 5)
 - Source node TID^{†‡} (node 3 TID)
 - Destination (slot)
 - Destination low-speed interface slot* (slot 2A).

* In the specified slot is part of a slot pair that has been provisioned for the OC-3 1+1 (Release 5 and later releases) or OC-12 1+1 (Release 7.1 and later releases) protected mode, specifying one slot also includes the companion slot.

† In Release 4.1 and later releases, entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the **Retrieve-Map-Ring** command to update the point-and-shoot list.

‡ If an STS-3 tributary is bridged at one or more nodes in a ring, the last node where the low-speed signals are terminated is the destination node.

In Releases 3 and 4.0, the node ID is entered.

- At node 3, a 2-way add/drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 5)
 - Source node TID^{†‡} (node 2 TID)
 - Destination (slot)
 - Destination low-speed interface slot* (slot 7A).
- The same 2-way add/drop cross-connection may also be entered by specifying:
 - Source (slot)
 - Source low-speed interface slot* (slot 7A)
 - Destination line (line 1W)
 - Destination STS-3 tributary (tributary 5)
 - Destination node TID^{†‡} (node 2 TID).

**NOTE:**

While cross-connections are entered, the FT-2000 OC-48 Add/Drop-Rings Terminal will report several trouble conditions (for example, circuit provisioning error and path integrity failure). However, these trouble conditions should be ignored until all the cross-connections for a circuit have been entered. If the trouble conditions remain after all the cross-connections have been entered, refer to the "Operation and Maintenance (TOP)" section (Volume II).

For more information about entering cross-connections at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the CIT Tutorial in Section 11, "Craft Interface Terminal Usage," and the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II).

Figure 8-18 shows a worksheet that may be used to record STS-3 cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals in a 2-fiber bidirectional ring.

-
- * In the specified slot is part of a slot pair that has been provisioned for the OC-3 1+1 (Release 5 and later releases) or OC-12 1+1 (Release 7.1 and later releases) protected mode, specifying one slot also includes the companion slot.
 - † In Release 4.1 and later releases, entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the **Retrieve-Map-Ring** command to update the point-and-shoot list.
 - ‡ If an STS-3 tributary is bridged at one or more nodes in a ring, the last node where the low-speed signals are terminated is the destination node.

In Releases 3 and 4.0, the node ID is entered.

	W	E	W	E	W	E	W	E	
1	1 2 3	1 2 3	1 2 3	1 2 3	1 2 3	1 2 3	1 2 3	1 2 3	1
2	1 2 3	1 2 3	2 2 3	1 2 3	2 2 3	1 2 3	2 2 3	1 2 3	2
3	1 2 3	1 2 3	3 2 3	1 2 3	3 2 3	1 2 3	3 2 3	1 2 3	3
4	1 2 3	1 2 3	4 2 3	1 2 3	4 2 3	1 2 3	4 2 3	1 2 3	4
5	1 2 3	1 2 3	5 2 3	1 2 3	5 2 3	1 2 3	5 2 3	1 2 3	5
6	1 2 3	1 2 3	6 2 3	1 2 3	6 2 3	1 2 3	6 2 3	1 2 3	6
7	1 2 3	1 2 3	7 2 3	1 2 3	7 2 3	1 2 3	7 2 3	1 2 3	7
8	1 2 3	1 2 3	8 2 3	1 2 3	8 2 3	1 2 3	8 2 3	1 2 3	8
9	1 2 3	1 2 3	9 2 3	1 2 3	9 2 3	1 2 3	9 2 3	1 2 3	9
10	1 2 3	1 2 3	10 2 3	1 2 3	10 2 3	1 2 3	10 2 3	1 2 3	10
11	1 2 3	1 2 3	11 2 3	1 2 3	11 2 3	1 2 3	11 2 3	1 2 3	11
12	1 2 3	1 2 3	12 2 3	1 2 3	12 2 3	1 2 3	12 2 3	1 2 3	12
13	1 2 3	1 2 3	13 2 3	1 2 3	13 2 3	1 2 3	13 2 3	1 2 3	13
14	1 2 3	1 2 3	14 2 3	1 2 3	14 2 3	1 2 3	14 2 3	1 2 3	14
15	1 2 3	1 2 3	15 2 3	1 2 3	15 2 3	1 2 3	15 2 3	1 2 3	15
16	1 2 3	1 2 3	16 2 3	1 2 3	16 2 3	1 2 3	16 2 3	1 2 3	16

TID _____ TID _____ TID _____ TID _____

Figure 8-18. STS-3 Cross-Connection Worksheet

Entering 1-Way-DRI STS-1 Cross-Connections in Dual Ring Interworking Applications

Figure 8-19 shows a 1-way circuit that has been established on a 2-fiber bidirectional ring that is part of a dual ring interworking application. 1-way and 1-way-DRI cross-connections are used at the primary and secondary nodes. The **CONFIGURATION-Enter-Crossconnection-STs1** input is used to enter 1-way and 1-way-DRI cross-connections. Refer to "Entering 1-Way and 2-Way STS-1 Cross-Connections" in this section for examples showing bridge and 2-way cross-connections.

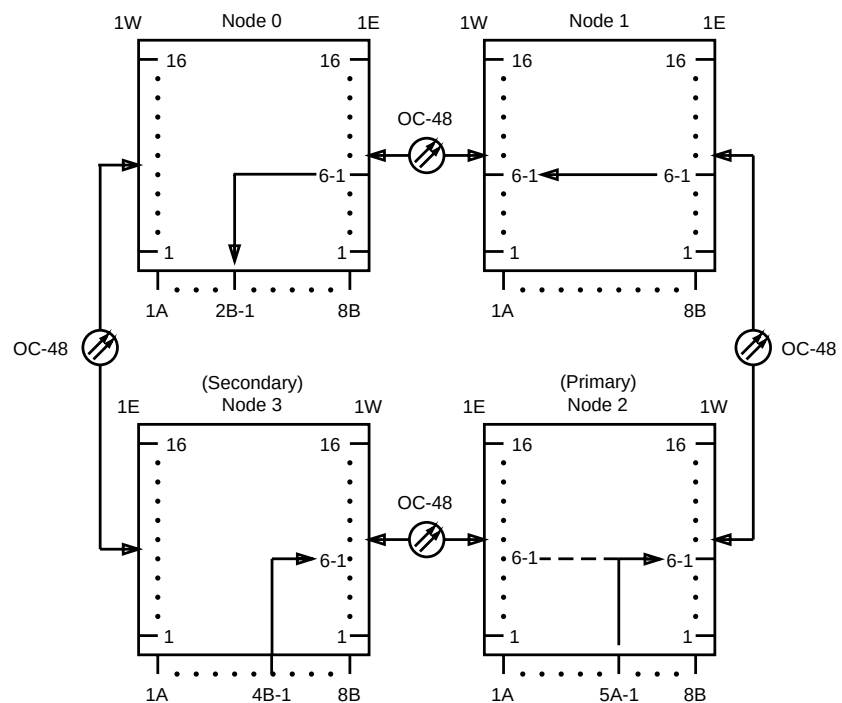


Figure 8-19. Example of STS-1 1-Way-DRI Cross-Connections in a 2-Fiber Bidirectional Ring

- At node 2 (primary node), a 1-way-DRI cross-connection is entered by specifying:
 - Source low-speed interface slot* (slot 5A)
 - Source low-speed port (1)
 - Destination line (line 1W)
 - Destination STS-3 tributary (tributary 6)
 - Destination STS-1 tributary (tributary 1)
 - Destination node TID (node 0 TID)
 - Secondary node TID (node 3 TID).
- At node 3 (secondary node), a 1-way add cross-connection is entered by specifying:
 - Source (STS-1 port)
 - Source low-speed interface slot* (slot 4B)
 - Source low-speed port (1)
 - Destination line (line 1W)
 - Destination STS-3 tributary (tributary 6)
 - Destination STS-1 tributary (tributary 1)
 - Destination node TID^{†‡} (node 0 TID).
- At node 1, a 1-way through cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1E)
 - Source STS-3 tributary (tributary 6)
 - Source STS-1 tributary (tributary 1)
 - Source node TID (node 3 TID)
 - Destination (tributary)
 - Destination node TID^{†‡} (node 0 TID).

* If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 protected mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 (Release 7.1 and later releases) protected mode.

† Entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the **Retrieve-Map-Ring** command to update the point-and-shoot list.

‡ If an STS-1 tributary is bridged at one or more nodes in a ring, the last node where the low-speed signals are terminated must be specified as the destination node.

- At node 0, a 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1E)
 - Source STS-3 tributary (tributary 6)
 - Source STS-1 tributary (tributary 1)
 - Source node TID* (node 3 TID)
 - Destination (STS-1 port)
 - Destination low-speed interface slot† (slot 2B)
 - Destination low-speed port (1).

**NOTE:**

While cross-connections are entered, the FT-2000 OC-48 Add/Drop-Rings Terminal will report several trouble conditions (for example, circuit provisioning error and path integrity failure). However, these trouble conditions should be ignored until all the cross-connections for a circuit have been entered. If the trouble conditions remain after all the cross-connections have been entered, refer to the "Operation and Maintenance (TOP)" section (Volume II).

Figure 8-16 shows a worksheet that may be used to record STS-1 cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals in a 2-fiber bidirectional ring.

For more information about entering cross-connections at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the CIT Tutorial in Volume I, Section 11, "Craft Interface Terminal Usage," and the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II). For more information about DRI protection switching, refer to Section 9, "Maintenance Description."

* Entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the **Retrieve-Map-Ring** command to update the point-and-shoot list.

† If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 protected mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 (Release 7.1 and later releases) protected mode.

Entering 1-Way-DRI STS-3 Cross-Connections in Dual Ring Interworking Applications

Figure 8-20 shows a 1-way circuit that has been established on a 2-fiber bidirectional ring that is part of a dual ring interworking application. 1-way and 1-way-DRI cross-connections are used at the primary and secondary nodes. The **CONFIGURATION-Enter-Crossconnection-STs3** input is used to enter 1-way and 1-way-DRI cross-connections. Refer to "Entering 1-Way and 2-Way Cross-Connections" in this section for examples showing bridge and 2-way cross-connections.

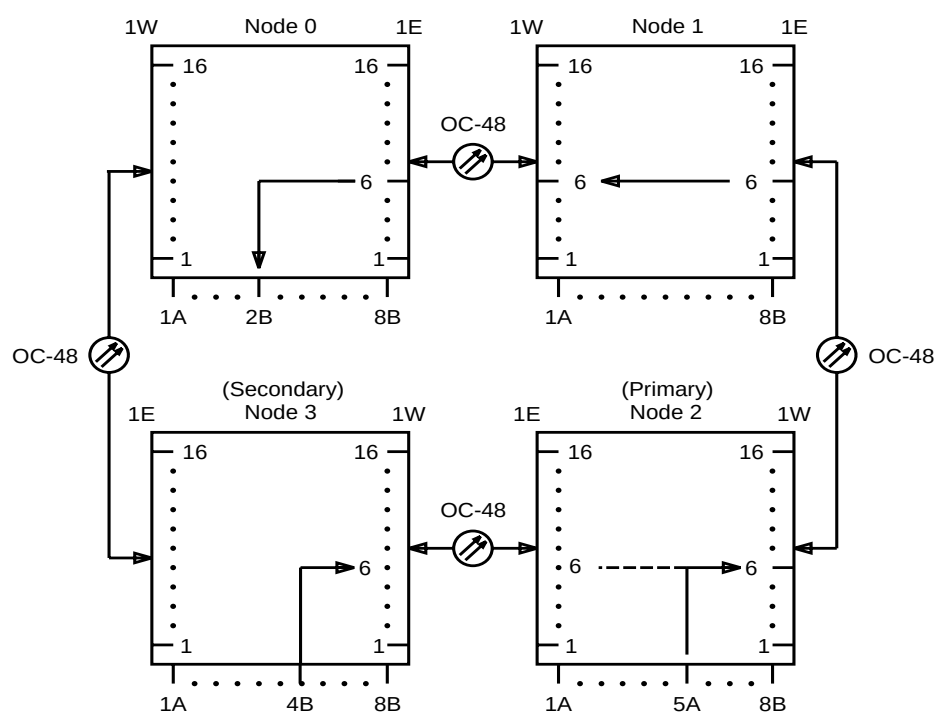


Figure 8-20. Example of STS-3 1-Way-DRI Cross-Connections in a 2-Fiber Bidirectional Ring

- At node 2 (primary node), a 1-way-DRI cross-connection is entered by specifying:
 - Source low-speed interface slot* (slot 5A)
 - Destination line (line 1W)
 - Destination STS-3 tributary (tributary 6)
 - Destination node TID^{†‡} (node 0 TID).
 - Secondary node TID[†] (node 3 TID).
- At node 3 (secondary node), a 1-way add cross-connection is entered by specifying:
 - Source (slot)
 - Source low-speed interface slot* (slot 4B)
 - Destination line (line 1W)
 - Destination STS-3 tributary (tributary 6)
 - Destination node TID^{†‡} (node 0 TID).
- At node 1, a 1-way through cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1E)
 - Source STS-3 tributary (tributary 6)
 - Source node TID[†] (node 3 TID)
 - Destination (tributary)
 - Destination node TID^{†‡} (node 0 TID).
- At node 0, a 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1E)
 - Source STS-3 tributary (tributary 6)
 - Source node TID[†] (node 3 TID)
 - Destination (slot)
 - Destination low-speed interface slot* (slot 2B).

* If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 protected mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 protected mode.

† Entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the **Retrieve-Map-Ring** command to update the point-and-shoot list.

‡ If an STS-3 tributary is bridged at one or more nodes in a ring, the last node where the low-speed signals are terminated must be specified as the destination node.

 **NOTE:**
While cross-connections are entered, the FT-2000 OC-48 Add/Drop-Rings Terminal will report several trouble conditions (for example, circuit provisioning error and path integrity failure). However, these trouble conditions should be ignored until all the cross-connections for a circuit have been entered. If the trouble conditions remain after all the cross-connections have been entered, refer to the "Operation and Maintenance (TOP)" section (Volume II).

 **NOTE:**
When a DRI circuit is provisioned in Release 5, the circuit provisioning audits check the active add cross-connection only for provisioning errors. To check the standby through cross-connection, a manual switch and then a reset *must* be performed using the FAULT-Switch-Path-STS3 input. This switches the through cross-connection to the active state and then resets the cross-connection to the original standby state. For more information about circuit provisioning audits, refer to Volume I, Section 9, "Maintenance Description."

Figure 8-18 shows a worksheet that may be used to record STS-3 cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals in a 2-fiber bidirectional ring.

For more information about entering cross-connections at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the CIT Tutorial in Volume I, Section 11, "Craft Interface Terminal Usage," and the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II). For more information about DRI protection switching, refer to Volume I, Section 9, "Maintenance Description."

Entering 1-Way-DRI Cross-Connections in Video Applications

Figure 8-21 shows a 1-way circuit that has been established on a 2-fiber bidirectional ring that is part of a 1-way video application. The 2-fiber bidirectional ring has two nodes with 1-way-DRI cross-connections (node 0 and node 2) and each node has a 1-way drop cross-connection from line 1W. Nodes 1, 3, and 4 also have a 1-way through cross-connection.

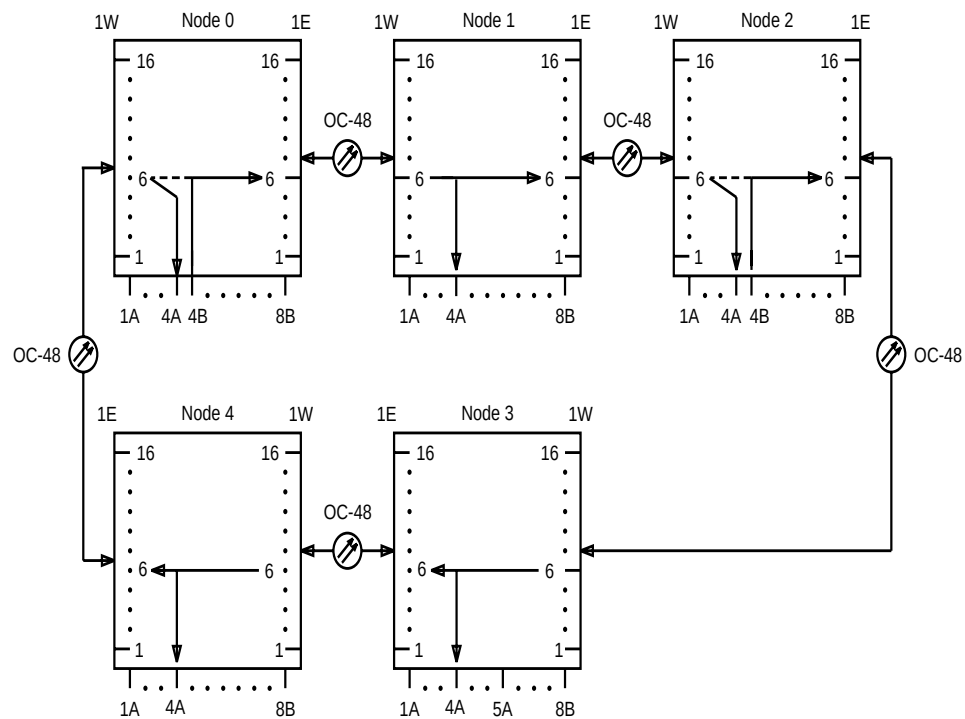


Figure 8-21. Example of 1-Way and 1-Way-DRI Cross-Connections in Video Applications

The **CONFIGURATION-Enter-Crossconnection-STS3** input is used to enter 1-way and 1-way-DRI cross-connections at the nodes. (Refer to "Entering 1-Way and 2-Way Cross-Connections" in this section for examples showing bridge and 2-way cross-connections.)

The following rules should be observed when specifying the source node TID, secondary node TID, and destination node TID parameters.

- The value of the source node TID parameter (1-way cross-connections) and the value of the secondary node TID parameter (1-way-DRI cross-connections) must be the *same* at all nodes. The value must be the TID of a node where there is a 1-way-DRI cross-connection.
- The value of the destination node TID parameter depends on exactly what cross-connections are present. To determine the value of the destination node TID at the local node, assign the following:
 - **X** is the TID of the local node.
 - **Y** is the TID of the next downstream node with a 1-way-DRI cross-connection. X and Y will never be the same node.
 - **Z** is the TID of the last node with a 1-way drop cross-connection when following the circuit from node X to node Y. Z can be node X or Y, some other node between X and Y, or null if there are no 1-way drop cross-connections between X and Y.

If Z is X or null, use the TID of node Y as the value of the destination node TID parameter. Otherwise, use the TID of node Z as the value of the destination node TID parameter.

The following cross-connections are required to establish the circuit shown in Figure 8-21:

- At node 0, a 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source node TID* (node 0 TID)
 - Destination (slot)
 - Destination low-speed interface slot (slot 4A).
- A 1-way-DRI cross-connection is entered by specifying:
 - Source low-speed interface slot (slot 4B)
 - Destination line (line 1E)
 - Destination STS-3 tributary (tributary 6)
 - Destination node TID* (node 2 TID)
 - Secondary node TID* (node 0 TID).

*

In Release 4.1 and later releases, entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the Retrieve-Map-Ring command to update the point-and-shoot list.

- At node 1, a 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source node TID* (node 0 TID)
 - Destination (slot)
 - Destination low-speed interface slot (slot 4A).
- A 1-way through cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1E)
 - Source STS-3 tributary (tributary 6)
 - Source node TID* (node 0 TID)
 - Destination (tributary)
 - Destination node TID* (node 2 TID).
- At node 2, a 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source node TID* (node 0 TID)
 - Destination (slot)
 - Destination low-speed interface slot (slot 4A).
- A 1-way-DRI cross-connection is entered by specifying:
 - Source low-speed interface slot (slot 4B)
 - Destination line (line 1E)
 - Destination STS-3 tributary (tributary 6)
 - Destination node TID* (node 0 TID).
 - Secondary node TID* (node 0 TID).

* In Release 4.1 and later releases, entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the Retrieve-Map-Ring command to update the point-and-shoot list.

- At node 3, a 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source node TID* (node 0 TID)
 - Destination (slot)
 - Destination low-speed interface slot (slot 4A).
- A 1-way through cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1E)
 - Source STS-3 tributary (tributary 6)
 - Source node TID* (node 0 TID)
 - Destination (tributary)
 - Destination node TID* (node 0 TID).
- At node 4, a 1-way drop cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source node TID* (node 0 TID)
 - Destination (slot)
 - Destination low-speed interface slot (slot 4A).
- A 1-way through cross-connection is entered by specifying:
 - Source (tributary)
 - Source line (line 1E)
 - Source STS-3 tributary (tributary 6)
 - Source node TID* (node 0 TID)
 - Destination (tributary)
 - Destination node TID* (node 0 TID).

* In Release 4.1 and later releases, entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the OC-48 ring. Enter the Retrieve-Map-Ring command to update the point-and-shoot list.



NOTE:

While cross-connections are entered, the FT-2000 OC-48 Add/Drop-Rings Terminal will report several trouble conditions (for example, circuit provisioning error and path integrity failure). However, these trouble conditions should be ignored until all the cross-connections for a circuit have been entered. If the trouble conditions remain after all the cross-connections have been entered, refer to the "Operation and Maintenance (TOP)" section (Volume II).



NOTE:

When a DRI circuit is provisioned in Release 5, the circuit provisioning audits check the active add cross-connection only for provisioning errors. To check the standby through cross-connection, a manual switch and then a reset *must* be performed using the FAULT-Switch-Path-ST3 input. This switches the through cross-connection to the active state and then resets the cross-connection to the original standby state. For more information about circuit provisioning audits, refer to Volume I, Section 9, "Maintenance Description."

Figure 8-18 shows a worksheet that may be used to record STS-3 cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals in a 2-fiber bidirectional ring.

For more information about entering cross-connections at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the CIT Tutorial in Volume I, Section 11, "Craft Interface Terminal Usage," and the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II). For more information about DRI protection switching, refer to Volume I, Section 9, "Maintenance Description."

Rolling STS-1 Cross-Connections

Figure 8-22 shows a 1-way circuit that has been established on a 2-fiber bidirectional ring with 1-way STS-1 cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals. The

CONFIGURATION-Roll-Crossconnection-STS1 input is used to roll traffic from an existing source to a new source.

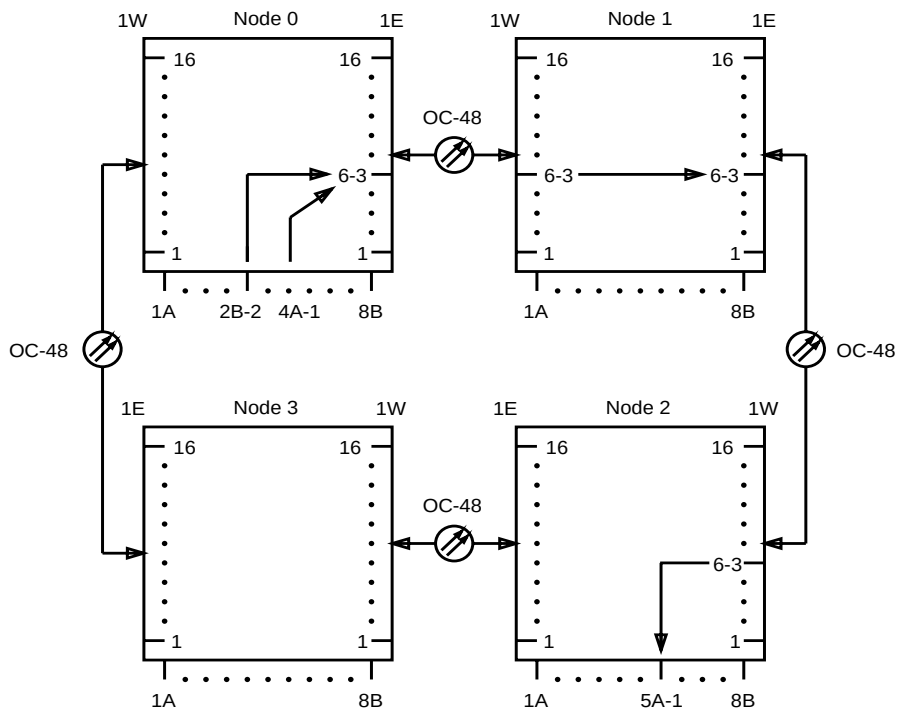


Figure 8-22. Example of Rolling STS-1 Cross-Connections in a 2-Fiber Bidirectional Ring

- At node 0, traffic is rolled from low-speed interface slot 2B, port 2, to low-speed interface slot 4A, port 1, by specifying:
 - Source (STS-1 port)
 - Source low-speed interface slot* (slot 2B)
 - Source low-speed port (2)
 - Destination line (line 1E)
 - Destination STS-3 tributary (tributary 6)
 - Destination STS-1 tributary (tributary 3)
 - New cross-connection type (1-way)
 - New source (STS-1 port)
 - New source low-speed interface slot* (slot 4A)
 - New source low-speed port (1).

* If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 (Release 5 or later releases) protection mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 (Release 7.1 or later releases) protected mode.

Rolling STS-3 Cross-Connections

Figure 8-23 shows a 1-way circuit that has been established on a 2-fiber bidirectional ring with 1-way STS-3 cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals. The

CONFIGURATION-Roll-Crossconnection-STS3 input is used to roll traffic from an existing source to a new source.

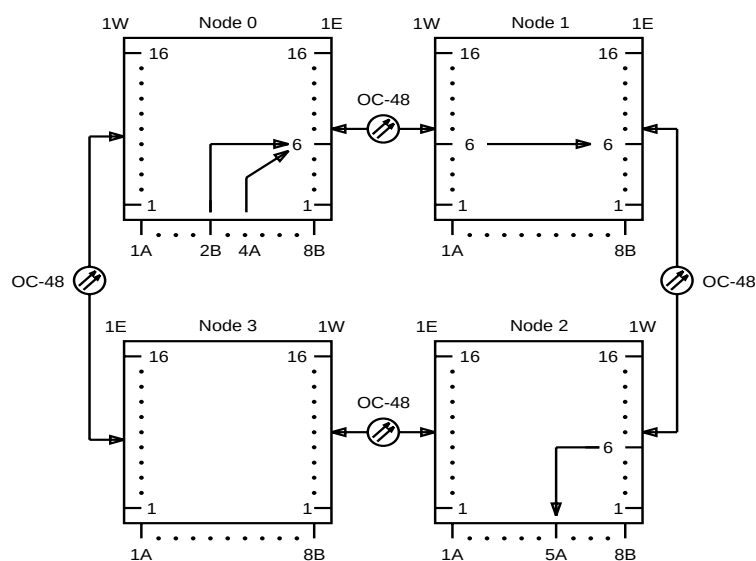


Figure 8-23. Example of Rolling STS-3 Cross-Connections in a 2-Fiber Bidirectional Ring

At node 0, traffic is rolled from low-speed interface slot 2B to low-speed interface slot 4A by specifying:

- Source (slot)
- Source low-speed interface slot* (slot 2B)
- Destination line (line 1E)
- Destination STS-3 tributary (tributary 6)
- New cross-connection type (1-way)
- New source (slot 4A).

* If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 (Release 5 or later releases) protection mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 (Release 7.1 or later releases) protected mode.

Deleting STS-1 Cross-Connections

The cross-connections shown in Figure 8-24 may be deleted using the **CONFIGURATION-Delete-Crossconnection-STS1** input.

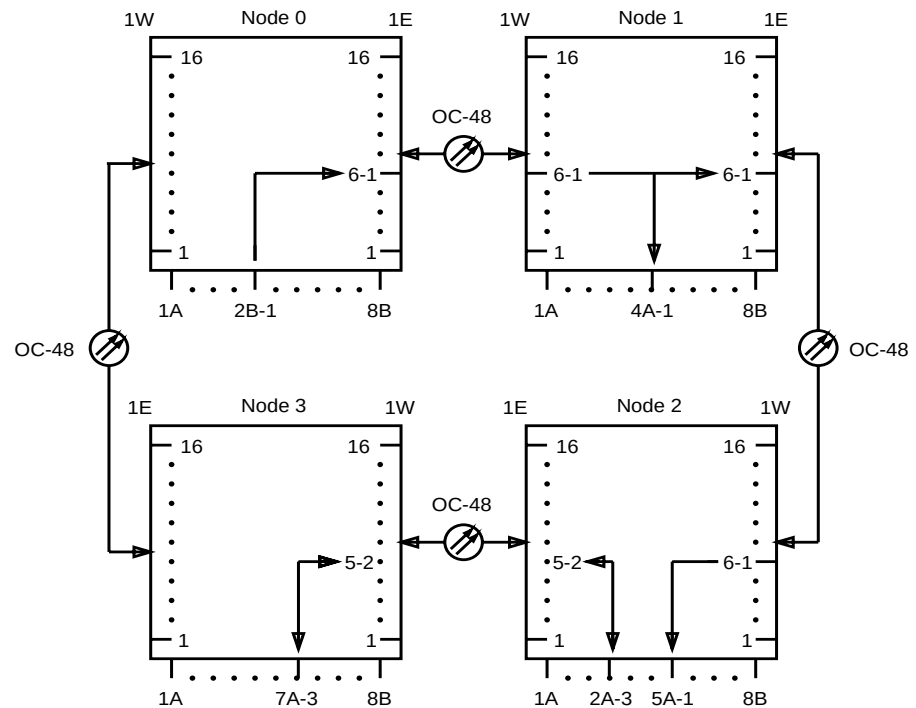


Figure 8-24. Example of STS-1 Cross-Connections in a 2-Fiber Bidirectional Ring

- At node 0, the 1-way add cross-connection is deleted by specifying:
 - Source (STS-1 port)
 - Source low-speed interface slot* (slot 2B)
 - Source low-speed port (1)
 - Destination line (line 1E)
 - Destination STS-3 tributary (tributary 6)
 - Destination STS-1 tributary (tributary 1).

* If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 (Release 5 or later releases) protection mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 (Release 7.1or later releases) protected mode.

- At node 1, the 1-way through cross-connection is deleted by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source STS-1 tributary (tributary 1)
 - Destination (tributary).

The 1-way drop cross-connection is deleted by specifying:

- Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source STS-1 tributary (tributary 1)
 - Destination (STS-1 Port)
 - Destination low-speed interface slot* (slot 4A)
 - Destination low-speed port (1).
- At node 2, the 1-way drop cross-connection is deleted by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 6)
 - Source STS-1 tributary (tributary 1)
 - Destination (STS-1 port)
 - Destination low-speed interface slot* (slot 5A)
 - Destination low-speed port (1).
- The 2-way add/drop cross-connection is deleted by specifying:
 - Source (STS-1 port)
 - Source low-speed interface slot* (slot 2A)
 - Source low-speed port (3)
 - Destination line (line 1E)
 - Destination STS-3 tributary (tributary 5)
 - Destination STS-1 tributary (tributary 2).

* If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 (Release 5 or later releases) protection mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 (Release 7.1 or later releases) protected mode.

- At node 3, the 2-way add/drop cross-connection is deleted by specifying:
 - Source (tributary)
 - Source line (line 1W)
 - Source STS-3 tributary (tributary 5)
 - Source STS-1 tributary (tributary 2)
 - Destination (STS-1 port)
 - Destination low-speed interface slot* (slot 7A)
 - Destination low-speed port (3).



NOTE:

While cross-connections are deleted, the FT-2000 OC-48 Add/Drop-Rings Terminal will report several trouble conditions (for example, circuit provisioning error and path integrity failure). However, these trouble conditions should be ignored until all the cross-connections for a circuit have been deleted. If the trouble conditions remain after all the cross-connections have been deleted, refer to the "Operation and Maintenance (TOP)" section (Volume II).

For more information about deleting cross-connections at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II).

*

If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 (Release 5 or later releases) protection mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 (Release 7.1 or later releases) protected mode.

Deleting STS-3 Cross-Connections

The cross-connections shown in Figure 8-25 may be deleted using the **CONFIGURATION-Delete-Crossconnection-STS3** input.

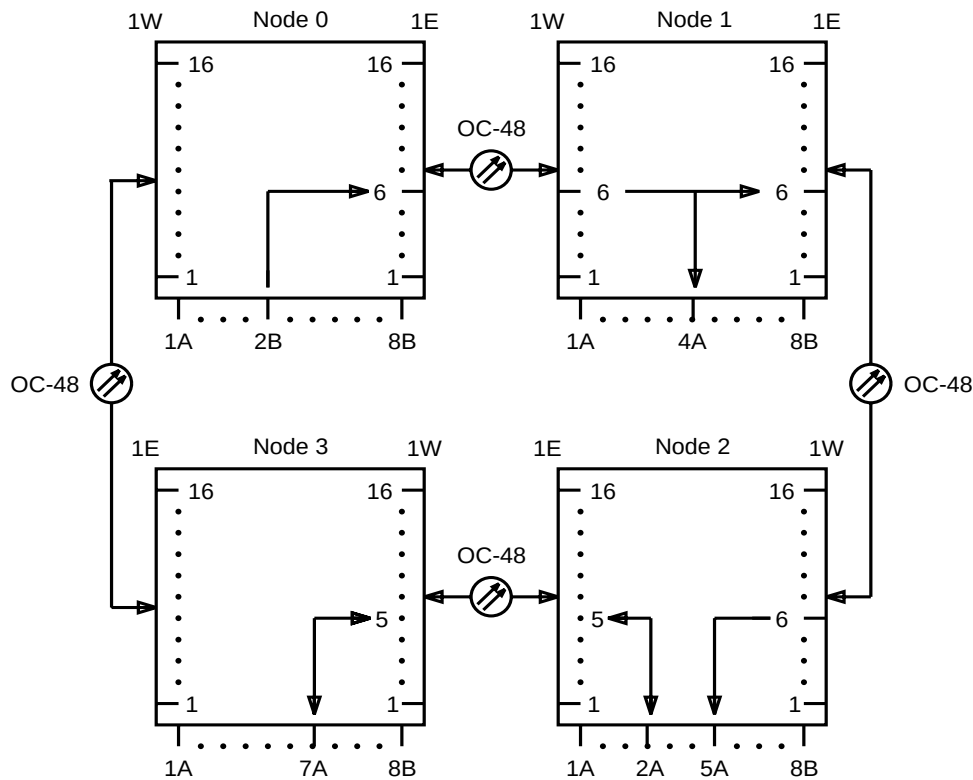


Figure 8-25. Example of STS-3 Cross-Connections in a 2-Fiber Bidirectional Ring

- At node 0, the 1-way add cross-connection is deleted by specifying:
 - Source (slot)
 - Source low-speed interface slot* (slot 2B)
 - Destination line (line 1E)

* If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 (Release 5 or later releases) protection mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 (Release 7.1 or later releases) protected mode.

- Destination STS-3 tributary (tributary 6).

- At node 1, the 1-way through cross-connection is deleted by specifying:

- Source (tributary)
- Source line (line 1W)
- Source STS-3 tributary (tributary 6)
- Destination (tributary).

The 1-way drop cross-connection is deleted by specifying:

- Source (tributary)
- Source line (line 1W)
- Source STS-3 tributary (tributary 6)
- Destination (slot)
- Destination low-speed interface slot^{*} (slot 4A).

- At node 2, the 1-way drop cross-connection is deleted by specifying:

- Source (tributary)
- Source line (line 1W)
- Source STS-3 tributary (tributary 6)
- Destination (slot)
- Destination low-speed interface slot^{*} (slot 5A).

- The 2-way add/drop cross-connection is deleted by specifying:

- Source (slot)
- Source low-speed interface slot^{*} (slot 2A)
- Destination line (line 1E)
- Destination STS-3 tributary (tributary 5).

- At node 3, the 2-way add/drop cross-connection is deleted by specifying:

- Source (tributary)
- Source line (line 1W)
- Source STS-3 tributary (tributary 5)
- Destination (slot)
- Destination low-speed interface slot[†] (slot 7A).

* If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 protected mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 protected mode.



NOTE:

While cross-connections are deleted, the FT-2000 OC-48 Add/Drop-Rings Terminal will report several trouble conditions (for example, circuit provisioning error and path integrity failure). However, these trouble conditions should be ignored until all the cross-connections for a circuit have been deleted. If the trouble conditions remain after all the cross-connections have been deleted, refer to the "Operation and Maintenance (TOP)" section (Volume II).

For more information about deleting cross-connections at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II).

†

If the specified slot is part of a slot pair (for example, slot 1A and 1B) that has been provisioned for the OC-3 1+1 protected mode, specifying one slot also includes the companion slot. The same type of logic applies to OC-12 1+1 protected mode.

Entering Circuit Information in the STS-1 Squelch Map (Releases 7.0 and Earlier Releases)

Figure 8-26 shows DS3 circuits that have been established on a 2-fiber bidirectional ring with 2-way STS-3 cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals. Figure 8-17 also shows a DS3 circuit that has been established with limited STS-1 cross-connections using external loopback cables at a DSX-3 cross-connect panel (node 1). The

CONFIGURATION-Enter-Crossconnection-STs3 input is used to enter 2-way cross-connections. For more information about entering 1-way and 2-way cross-connections, refer to "Entering 1-Way and 2-Way Cross-Connections" in this section.

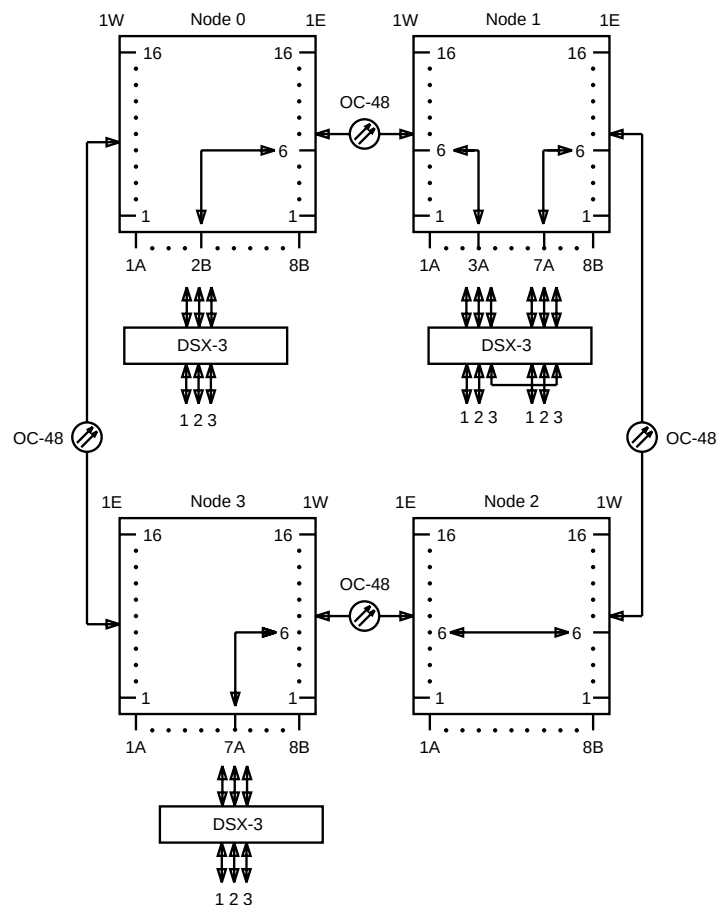


Figure 8-26. Example of Limited STS-1 Cross-Connections in a 2-Fiber Bidirectional Ring

When the STS-1 tributaries associated with a single STS-3 tributary are split up, the STS-3 tributary source and destination node provisioning is no longer correct for the STS-1 tributaries. The CONFIGURATION-Enter-Squelch Map-STs1 input must be used to override the STS-3 squelch map provisioning for those STS-1 tributaries. This STS-1 squelch map provisioning must be done at each node where the affected STS-1 tributary(s) are terminated, looped back using external cabling, or passed through using STS-3 through cross-connections.

- At node 0, STS-1 squelch map information is entered by specifying:
 - Type (2-way)
 - Line (line 1E)
 - STS-3 tributary (tributary 6)
 - STS-1 tributary (tributary 3)
 - Source node TID* (node 3 TID).
- At node 1, STS-1 squelch map information is entered by specifying:
 - Type (2-way)
 - Line (line 1W)
 - STS-3 tributary (tributary 6)
 - STS-1 tributary (tributary 3)
 - Source node TID* (node 0 TID).
- At node 1, the following is also entered:
 - Type (2-way)
 - Line (line 1E)
 - STS-3 tributary (tributary 6)
 - STS-1 tributary (tributary 3)
 - Source node TID* (node 3 TID).
- At node 2, STS-1 squelch map information is entered by specifying:
 - Type (2-way)
 - Line (line 1W)
 - STS-3 tributary (tributary 6)
 - STS-1 tributary (tributary 3)
 - Source node TID* (node 0 TID).

* Entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the ring network. Enter the **Retrieve-Map-Ring** input to update the point-and-shoot list.

- At node 2, the following is also entered:
 - Type (2-way)
 - Line (line 1E)
 - STS-3 tributary (tributary 6)
 - STS-1 tributary (tributary 3)
 - Source node TID* (node 3 TID).
- At node 3, STS-1 squelch map information is entered by specifying:
 - Type (2-way)
 - Line (line 1W)
 - STS-3 tributary (tributary 6)
 - STS-1 tributary (tributary 3)
 - Source node TID* (node 0 TID).



NOTE:

While cross-connections are entered, the FT-2000 OC-48 Add/Drop-Rings Terminal will report several trouble conditions (for example, circuit provisioning error and path integrity failure. However, these trouble conditions should be ignored until all the cross-connections for a circuit have been entered. If the trouble conditions remain after all the cross-connections have been entered, refer to the "Operation and Maintenance (TOP)" section (Volume II).

For more information about entering limited STS-1 cross-connections at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II).

*

Entering a question mark (?) in response to the TID prompt will display a point-and-shoot list of available addresses in the ring network. Enter the **Retrieve-Map-Ring** input to update the point-and-shoot list.

OC-3 Data Communications Channel Assignment Inputs

The OC-3 DCC assignments must be specified and administered using the following inputs*:

- **CONFIGURATION-Enter-Assignment-DCC:** This input assigns an OC-3 DCC to a particular low-speed interface slot or pair of low-speed interface slots. An OC-3 DCC assignment is entered by first specifying a low-speed interface slot and a DCC. The low-speed interface slot may be a single slot in the OC-3 0x1 operating mode, or a low-speed interface slot in a slot pair (for example, slot 2A and 2B) operating in the OC-3 1+1 protected mode. (When entering an assignment to a 1+1 protected slot pair, an active assignment is made to the active slot of the slot pair and a standby assignment is made to the standby slot of the slot pair.) The user must specify a single DCC (channel 1-8).

After the low-speed interface slot and DCC are specified, the user must specify the orientation (*user* or *network*). If the *user* orientation is specified at one end of the OC-3 DCC link, the *network* orientation must be specified at the other end of the OC-3 DCC link.

An input requesting an OC-3 DCC assignment will be denied if one of the following conditions is present:

- The specified slot is not equipped with an OC3 (1.3 STD) circuit pack.
- The TOHCTL slot is in the AUTO state.
- The TOHCTL circuit pack has been removed.
- The TOHCTL circuit pack has failed.

An input requesting an OC-3 DCC assignment may be denied if an automatic turnup test is running. During that part of the test cycle when the DCC connections between the OC3 (1.3 STD) and TOHCTL circuit packs are being verified, an input requesting an OC-3 DCC assignment will be denied.

The **CONFIGURATION-Enter-Assignment-DCC** input can also be used to change the value (*user* or *network*) of the orientation parameter associated with an existing OC-3 DCC assignment. This is accomplished by reentering the same OC-3 DCC assignment with the new value for the orientation parameter.

* The FT-2000 OC-48 Lightwave System is menu-driven and prompts the user to enter the desired input.



NOTE:

When the operating mode of a slot pair changes from 1+1 to 0x1 in the "from OC-48" and/or the "from OC-3" directions, any DCC assignment to the slot pair changes to an assignment to the "A" slot and there will be no assignment to the "B" slot.



NOTE:

When the operating mode of a slot pair changes from 0x1 to 1+1 in the "from OC-48" and the "from OC-3" direction, any DCC assignment to the "A" slot changes to an assignment to the slot pair and any assignment to the "B" slot is deleted.

- **CONFIGURATION-Delete-Assignment-DCC:** This input deletes an OC-3 DCC assignment to a low-speed interface slot or pair of low-speed interface slots. Deleting an OC-3 DCC assignment disables DCC communications on the supporting OC-3 line. An OC-3 DCC assignment is deleted by specifying a low-speed interface slot and a DCC. The low-speed interface slot may be a single slot in the OC-3 0x1 operating mode, or a low-speed interface slot in a slot pair (for example, slot 2A and 2B) operating in the OC-3 1+1 protected mode. The user must specify a single DCC (channel 1-8).

An input deleting an OC-3 DCC assignment will be denied if one of the following conditions is present:

- The TOHCTL slot is in the AUTO state.
- The TOHCTL circuit pack has been removed.
- The TOHCTL circuit pack has failed.

An input deleting an OC-3 DCC assignment may be denied if an automatic turnup test is running. During that part of the test cycle when the DCC connections between the OC3 (1.3 STD) and TOHCTL circuit packs are being verified, an input deleting an OC-3 DCC assignment will be denied.

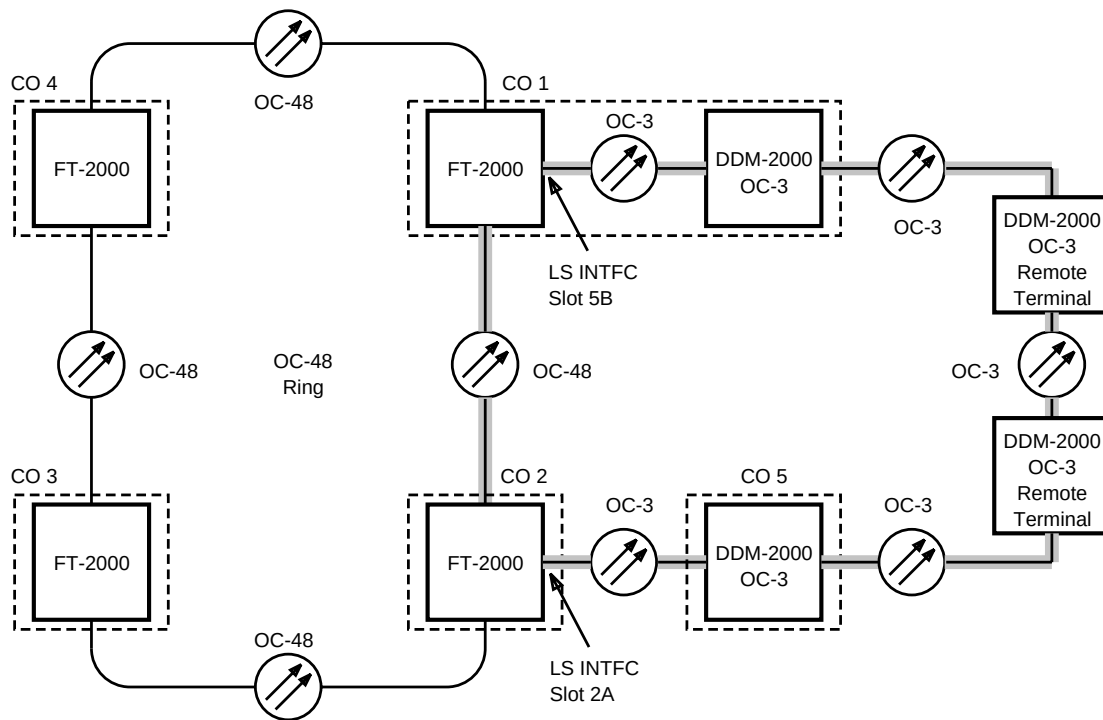
If the DCC assignment specified by the input does not exist, the input will be completed and no DCC assignment is deleted.

- **CONFIGURATION-Retrieve-Assignment-DCC:** This input generates a report showing all the OC-3 DCCs assigned to low-speed interface slots or low-speed interface slot pairs in the FT-2000 OC-48 Add/Drop-Rings Terminal. For more information about the DCC Assignment Report, refer to Volume I, Section 11, "Craft Interface Terminal Usage."

OC-3 Data Communications Channel Assignment Examples

Entering OC-3 DCC Assignments

Figure 8-27 shows a DDM-2000 OC-3 ring transport application with OC-3 DCC connectivity. The **CONFIGURATION-Enter-Assignment-DCC** input is used to enter the OC-3 DCC assignments.



The grey shading indicates the extent of the OC-3 ring and data communications channel.

Figure 8-27. Example of OC-3 DCC Connectivity

- At CO 1, an OC-3 DCC assignment is entered by specifying:
 - Low-speed interface slot (slot 5B)
 - DCC (channel 3^{*})
 - Orientation (network[†]).
- At CO 2, an OC-3 DCC assignment is entered by specifying:
 - Low-speed interface slot (slot 2A)
 - DCC (channel 6^{*})
 - Orientation (network[†]).

For more information about entering OC-3 DCC assignments in an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the CIT Tutorial in Volume I, Section 11, "Craft Interface Terminal Usage," and the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II).

* The DCC assignment may be specified as channel 1-8 at each FT-2000 OC-48 Add/Drop-Rings Terminal.

† If the orientation is specified "network" at the FT-2000 OC-48 Add/Drop-Rings Terminal, the DDM-2000 OC-12 Multiplexer must be specified "user."

Deleting OC-3 DCC Assignments

The OC-3 DCC assignments Figure 8-27 may be deleted using the **CONFIGURATION-Delete-Assignment-DCC** input.

- At CO 1, an OC-3 DCC assignment is deleted by specifying:
 - Low-speed interface slot (slot 5B)
 - DCC (channel 3).
- At CO 2, an OC-3 DCC assignment is deleted by specifying:
 - Low-speed interface slot (slot 2A)
 - DCC (channel 6).

For more information about deleting OC-3 DCC assignments at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II).

OC-12 DCC Assignment Inputs

The OC-12 DCC assignments must be specified and administered using the following inputs*:

- **CONFIGURATION-Enter-Assignment-DCC:** This input assigns a DCC channel to a particular low-speed interface "command" slot. Or, (whenever 1+1 protection is used) to a pair of low-speed interface "command" slots. The service command slot must be specified first and then the protection command slot will be assigned automatically.



NOTE:

For Release 7.1, the DCC channel will only be assigned for 0x1 protected links. The command slots are 1B, 3B, 5B and 7B.

The user must specify a single DCC (channel 1-8).

After the low-speed interface command slot and DCC channel (channel 1-8) are specified, the user must specify the orientation (*user* or *network*). If the *user* orientation is specified at one end of the OC-12 DCC link, the *network* orientation must be specified at the other end of the OC-12 DCC link.

An input requesting an OC-12 DCC assignment will be denied if one of the following conditions is present:

- The specified slot is not equipped with an OC12 (1.3 STD) circuit pack.
- The TOHCTL slot is in the AUTO state.
- The TOHCTL circuit pack has been removed.
- The TOHCTL circuit pack has failed.

An input requesting an OC-12 DCC assignment may be denied if an automatic turnup test is running. During that part of the test cycle when the DCC connections between the OC-12 (1.3 STD) and TOHCTL circuit packs are being verified, an input requesting an OC-12 DCC assignment will be denied.

The **CONFIGURATION-Enter-Assignment-DCC** input can also be used to change the value (*user* or *network*) of the orientation parameter associated with an existing OC-12 DCC assignment. This is accomplished by reentering the same OC-12 DCC assignment with the new value for the orientation parameter.

* The FT-2000 OC-48 Lightwave System is menu-driven and prompts the user to enter the desired input.

- **CONFIGURATION-Delete-Assignment-DCC:** This input deletes an OC-12 DCC assignment to a low-speed interface quad slot or pair of low-speed interface quad slots. Deleting an OC-12 DCC assignment disables DCC communications on the supporting OC-12 line. An OC-12 DCC assignment is deleted by specifying a low-speed interface command slot and a DCC channel.

An input deleting an OC-12 DCC assignment will be denied if one of the following conditions is present:

- The TOHCTL slot is in the AUTO state.
- The TOHCTL circuit pack has been removed.
- The TOHCTL circuit pack has failed.

An input deleting an OC-12 DCC assignment may be denied if an automatic turnup test is running. During that part of the test cycle when the DCC connections between the OC-12 (1.3 STD) and TOHCTL circuit packs are being verified, an input deleting an OC-12 DCC assignment will be denied.

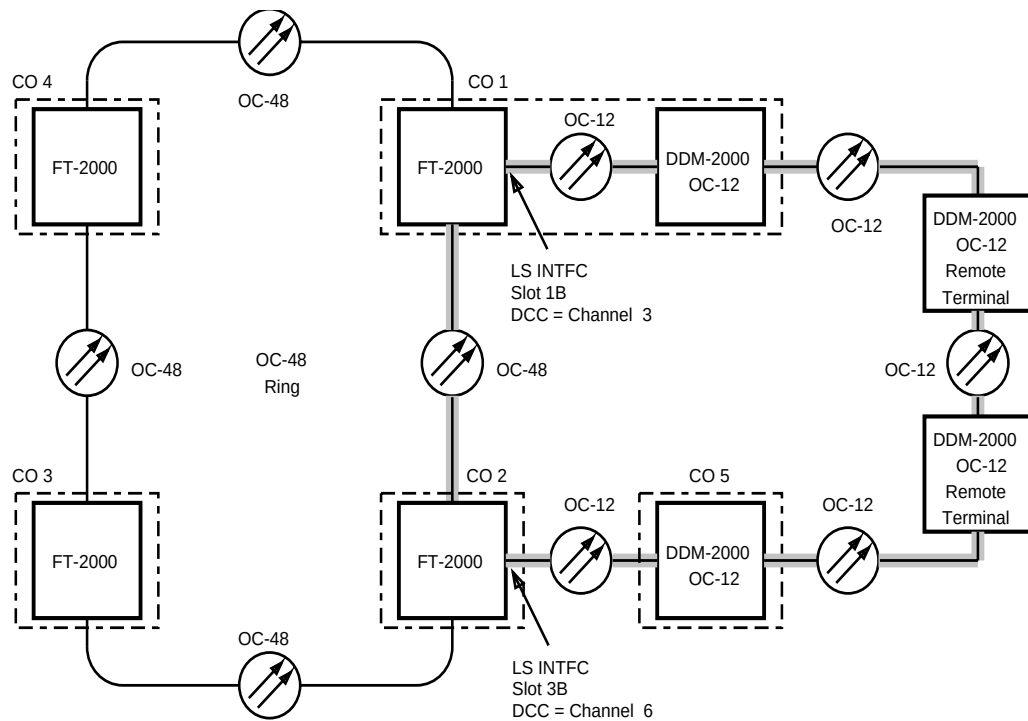
If the DCC assignment specified by the input does not exist, the input will be completed and no DCC assignment is deleted.

- **CONFIGURATION-Retrieve-Assignment-DCC:** This input generates a report showing all the assigned (both to OC-3 and to OC-12). For more information about the DCC Assignment Report, refer to Volume I, Section 11, "Craft Interface Terminal Usage."

OC-12 Data Communications Channel Assignment Examples

Entering OC-12 Data Communications Channel Assignments

Figure 8-28 shows a DDM-2000 OC-12 ring transport application with OC-12 DCC connectivity. The **CONFIGURATION-Enter-Assignment-DCC** input is used to enter the OC-12 DCC assignments.



The grey shading indicates the extent of the OC-12 ring and data communications channel.

Figure 8-28. Example of OC-12 DCC Connectivity

- At CO 1, an OC-12 DCC assignment is entered by specifying:
 - Low-speed interface slot (slot 1B)
 - DCC (channel 3^{*})
 - Orientation (network[†]).
- At CO 2, an OC-12 DCC assignment is entered by specifying:
 - Low-speed interface slot (slot 3B)
 - DCC (channel 6^{*})
 - Orientation (network[†]).

For operations interworking between the FT-2000 OC-48 Add/Drop-Rings Terminal and the DDM-2000 OC-12 Multiplexer, the routing domain and area fields of the network service access point (NSAP) code at the DDM-2000 OC-12 Multiplexer must be 0 (default value). The routing domain and area fields of the FT-2000 OC-48 Add/Drop-Rings Terminal have a fixed value (0) and cannot be user provisioned. For any operations interworking application to work, the network elements participating in the application must have the same routing domain and area fields.

For more information about entering OC-12 DCC assignments in an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the CIT Tutorial in Volume I, Section 11, "Craft Interface Terminal Usage," and the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II).

* The DCC assignment may be specified as channel 1-8 at each FT-2000 OC-48 Add/Drop-Rings Terminal.

† If the orientation parameter is specified "network" at the FT-2000 OC-48 Add/Drop-Rings Terminal, the orientation parameter at the DDM-2000 OC-3 Multiplexer must be specified "user."

Deleting OC-12 Data Communications Channel Assignments

The OC-12 DCC assignments shown in Figure 8-28 may be deleted using the **CONFIGURATION-Delete-Assignment-DCC** input.

- At CO 1, an OC-12 DCC assignment is deleted by specifying:
 - low-speed interface slot (slot 1B)
 - DCC (channel 3).
- At CO 2, an OC-12 DCC assignment is deleted by specifying:
 - low-speed interface slot (slot 3B)
 - DCC (channel 6).

For more information about deleting OC-12 DCC assignments at an FT-2000 OC-48 Add/Drop-Rings Terminal, refer to the Operation Task Index in the "Operation and Maintenance (TOP)" section (Volume II).

Access Identifiers

The access identifiers (AIDs) are used to access the functionality of the FT-2000 OC-48 Lightwave System. The FT-2000 OC-48 Lightwave System also uses AIDs to identify, address, and report on entities within the system. Entities are circuit pack slots, ports, signals, transmission lines, operations interfaces, data communications channels (DCC), and alarm interfaces.

The following tables provide the AIDs used by the FT-2000 OC-48 Lightwave System:

FT-2000 OC-48 Add/Drop-Rings Terminal Input Values	Table 8-1
FT-2000 OC-48 Add/Drop-Rings Terminal Output Values	Table 8-2
FT-2000 OC-48 Repeater Bay Input Values	Table 8-3
FT-2000 OC-48 Repeater Bay Output Values	Table 8-4

Table 8-1. FT-2000 OC-48 Add/Drop-Rings Terminal Input Values

Prompt	Values
Line	All,1E,1W
Single Line	1E, 1W
Type (Slot)	All,TRMTR,RCVR,LS INTFC,TG,LSSW SYSCTL,SYSTEM,LNCTL,OHCTL TOHCTL
Type (Operations Interface)	All,CIT-DCE,CIT-DTE,CIT-DCC, TL1-DCE*,TL1-DTE*,TL1-DCC*,TL1-X25 †
Slot (LS INTFC)	All,1A-8B,P1,P2
Single Slot (LS INTFC)	1A-8B,P1,P2
Service Slot (LS INTFC)	All,1A-8B
Single Service Slot (LS INTFC)	1A-8B
Single Protection Slot (LS INTFC)	P1,P2
Slot (LNCTL)	All, 1W
Slot (TG)	All,1,2
Slot (LSSW)	All,OUT,IN
Port (STS-1)‡	All,1-3
Single Port (STS-1)‡	1-3
Port (DS3)	All,1-3
Port (EC1)	All,1-3
Port (TIMING IN)	All,PRIMARY,SECONDARY
Port (COM)*	All,DCE,DTE,DCC,X25
Quad (LS INTFC)	All,1B,3B,5B,7B
Single Quad (LS INTFC)	1B,3B,5B,7B
Tributary (STS-1)	All,1-3
Single Tributary (STS-1)	1-3
Tributary (STS-3)	All,1-16
Single Tributary (STS-3)	1-16
DCC (OC-48)§	All,DCC-1E,DCC-1W
DCC (OC-3)§	All,1-8
DCC (OC-12)§	All,1-8
Single Channel (DCC)§	1-8
Point (Control)	All,1-4
Point (Environment)	All,1-16
PM Register	All,Day,QH

* Available in Release 5 and later releases.

† Available in Release 4.1 and later releases.

‡ Available in Release 7 and later releases.

§ Available in Release 6 and later releases.

Table 8-2. FT-2000 OC-48 Add/Drop-Rings Terminal Output Values

Entity	Values
TID	1 to 20 alphanumeric characters
NID*	0-15
Filename	1 to 8 alphanumeric characters
User-Login	1 to 10 alphanumeric characters
provisionedcontrlpt	1 to 26 alphanumeric characters
provisionedenvironmentalpt	1 to 26 alphanumeric characters
System	system
Line	line-{all,1e,1w}
Slot (TRMTR)	trmtr-{all,1e,1w}
Slot (RCVR)	rcvr-{all,1e,1w}
Slot (LS INTFC)	ls-{all,1a-8b,p1,p2}
Slot (TG)	tg-{all,1,2}
Slot (LSSW)	lssw-{all,out,in}
Slot (SYSCTL)	sysctl
Slot (SYSMEM)	sysmem
Slot (LNCTL)	lnctl-{1w}
Slot (OHCTL)	ohctl-{all,1e,1w}
Slot (TOHCTL) ^{†§}	tohctl
Port (DS3)	ls-{all,1a-8b}-{all,1-3}
Port (EC1) [‡]	ls-{all,1a-8b}-{all,1-3}
Port (OC3)	ls-{all,1a-8b}
Port (IS3)	ls-{all,1a-8b}
Port (OC12)	ls-{all,1b,3b,5b,7b}
Port (STS-1) [¶]	ls-{all,1a-8b}-{all,1-3}
Port (TIMING OUT)	DS1-out-{all,1,2}
Port (TIMING IN)	DS1-in-{all,pri,secy}, OC48-in-{1e,1w}
Port (COM) [§]	{dce,dte,dcc,x25}
Quad (LS INTFC)	ls-{all,1b,3b,5b,7b}
Operations Interface (User Panel)	user panel
Operations Interface (CIT)	cit-{dce,dte,dcc} tl1-{dce,dte,dcc,x25}‡
Operations Interface (UID)	<User-Login>
Operations Interface (DCC-OC48)	dcc48-{all,1e,1w}
Operations Interface (DCC-OC3)	dcc3-{all,1a-8b}
Operations Interface (DCC-OC12)	dcc12-{all,1b,3b,5b,7b}
Shelf	shlf-{1e,1w}-hs shlf-ls
Operations Interface (MISC DSCRT 1)	misc dscrt1
Operations Interface (MISC DSCRT 2)	misc dscrt2

See footnotes at end of table.

Table 8-2. FT-2000 OC-48 Add/Drop-Rings Terminal Output Values (Contd)

Entity	Values
Operations Interface (OFFICE ALARMS)	office alms
Operations Interface (PAR TLM)	par tlm
Operations Interface (SER TLM 1)	ser tlm 1
Operations Interface (SER TLM2)	ser tlm 2
Operations Interface (X.25) [¶]	x25
Tributary (STS-1)	sts1-{all,1e,1w}-{all,1-16}-{all,1-3}
Tributary (STS-3)	sts3-{all,1e,1w}-{all,1-16}
Channel (DCC)	dcc chan-{all,1-8}
Miscellaneous Discrete (Control Point)	cont-{all,1-4}
Miscellaneous Discrete (Environmental Point)	env-{all,1-16}
Site Address [†]	site-{1e,1w}-{0-31}
PM Register	reg-{all,day,QH}
tid	1 to 20 alphanumeric characters, . + - + % #
filename	1 to 8 alphanumeric characters
user-login	1 to 10 alphanumeric characters
provisionedcontrolpt	1 to 26 alphanumeric characters
provisionedenvironmentalpt	1 to 26 alphanumeric characters

* Available in Release 7 and later releases.

† Available in releases prior to Release 5.

‡ Available in Release 4 and later releases.

§ Available in Release 5 and later releases.

¶ Available in releases prior to Release 5.

Table 8-3. FT-2000 OC-48 Repeater Bay Input Values

Prompt	Values
Line	All, 1E, 1W, PE, PW
Type (Slot)	All, SYSCTL, SYSMEM, REGENR, ROHCTL
Type (Operations Interface)	All, CIT-DCE, CIT-DTE
Slot (REGENR)	All, 1EW, 1WE, PEW, PWE
Slot (ROHCTL)	All, 1 P
Point (Environment)	All, 1-16

Table 8-4. FT-2000 OC-48 Repeater Bay Output Values

Entity	Values
System	system
Line	line-{all, 1e, 1w, pe, pw}
Slot (REGENR)	regenr-{all, 1ew, 1we, pew, pwe}
Slot (SYSCTL)	sysctl
Slot (SYSMEM)	sysmem
Slot (ROHCTL)	rohctl-{1,p}
Operations Interface (User Panel)	user panel
Operations Interface (CIT)	cit-{dce, dte}
Miscellaneous Discrete (Environment Point)	env-{all, 1-16}
Site ID	site-{1,p}-{1-30}
Shelf	shlf-{1,p}-{hs}

Provisioning

Provisionable Parameters and Original Values

General

Installation provisioning is minimized with provisionable parameters and original values. Each CIT provisionable parameter is assigned an original value. The provisionable parameters and original values are copied from floppy disks in the CIT to the SYSMEM circuit pack.

The SYSMEM circuit pack contains one copy of the provisionable parameters with their original values and another copy of the provisionable parameters with their current values. The SYSCTL and LNCTL circuit packs, plus the board controllers on the other circuit packs contain one copy of the provisionable parameters with their current values.

The original values cannot be changed. The current values can be changed through local provisioning.



NOTE:

The FT-2000 OC-48 Lightwave System has no switch-settable parameters.

FT-2000 OC-48 Add/Drop-Rings Terminal

Table 8-5 shows the provisionable parameters of the FT-2000 OC-48 Add/Drop-Rings Terminal. This includes the parameters that can be provisioned using the CIT, plus the parameters that are automatically provisioned or have fixed values (cannot be changed). The original value for each provisionable parameter in Table 8-5 is shown in brackets []. Table 8-5 also shows the applicable CIT-PC and TL1 commands.

Table 8-6 shows the provisionable threshold crossing alert (TCA) parameters of Add/Drop-Rings Terminal. This includes the threshold range and original values for the quarter-hour and day bins of each performance-monitoring parameter. The original value for each provisionable parameter in Table 8-6 is shown in brackets [].

Table 8-5. Provisionable Parameters (FT-2000 OC-48 Add/Drop-Rings Terminal)

Parameter	Options [Original Value]	Method	CIT-PC Input [TL1 Command]	CIT-PC Report [TL1 Report]
System Address				
- Target Identifier (TID)	[LT-FT-2000]≤20 characters	CIT	SECURITY-Set-Ne twork-Element [ENT-SYS]	SECURITY-Retrie ve- Network-Element [RTRV-SYS]
- Node Identifier (NID)\$\$\$	0-15 [0]			
- Directory Service NE	Yes, [No]			
- X.25 Packet Size ¶¶¶	128, [256]			
- Alarm Group¶¶¶	0-[255]			
- Alarm Gateway Network Element (AGNE)¶¶¶	Yes, [No]			
- TL1 Gateway Network Element (GNE)###	Yes, [No]			
- Site Address	site-line-site number	Automatic	None	
- Network Services Access Point (Releases 3 and 4 only)	≤ 40 Characters [No Original Value]	Automatic	None	CONFIGURATION-Re trieve-Map-Ring [RTRV-MAP]
Synchronization				
- Provisioned Mode	[Externally Timed], Through Timed, Free Running, Line Timed, (Phase Locked prior to Rel. 5)	CIT	CONFIGURATION- Set- Synchronization	CONFIGURATION- Retrieve- Synchronization
- Pri. DS1 In Format	[SF], ESF			
- Pri. DS1 In/Out Format*	[SF], ESF			
- Sec. DS1 In Format	[SF], ESF			
- Sec. DS1 In/Out Format*	[SF], ESF			
- Pri. DS1 In Line Code	[AMI], B8ZS			
- Sec. DS1 In Line Code	[AMI], B8ZS			
- DS1 Out 1 Equalization	[20], 40, 60, 80, 100 Percent			
- DS1 Out 2 Equalization	[20], 40, 60, 80, 100 Percent			

Table 8-5. Provisionable Parameters (FT-2000 OC-48 Add/Drop-Rings Terminal) (Contd)

Parameter	Options [Original Value]	Method	CIT-PC Input [TL1 Command]	CIT-PC Report [TL1 Report]
Line Protection Switching - Wait to Restore - Ring Switching-Provisioned	0-12 Minutes [5] Enabled, [Disabled]	CIT	CONFIGURATION-Set-Protection-Line	CONFIGURATION-Retrieve-Protection-Line
Protection Access	[Preemptible], Temporary, Permanent	CIT	CONFIGURATION-Enter-Protection-STS1 or STS3	CONFIGURATION-Retrieve-Protection-STS1
Operations - Operation Ports - Operation Port Baud Rates CIT (DCE) TBOS CIT (DTE) X.25‡ - Remote Access Far End Terminal Remote Activity Reporting Remote Office Alarms - Network Element Security Logins Allowed Password Aging* Login Aging*** - Port Security Type (Prior to Release 5) Type (Release 5 and Later) Port Access Port Protocol*** CIT (DCE) CIT (DTE)	DCE, DTE, X.25 1200, 2400, 4800, 9600 [No Original Value] 2400 1200, 2400, 4800, 9600 [No Original Value] 1200, 2400, 4800, 9600, 19200 [No Original Value] [LTXX], Other Enable, [Disable] Enable, [Disable] [Yes], No [0], 7-999 Days [0], 7-999 Days CIT-DCE, CIT-DTE, CIT-DCC, [All] DCE, DTE, DCC, X.25, [All] [No Original Value] [Enable], Disable [CIT], TL1 [CIT], TL1	Choice † Autobaud Fixed Automatic CIT CIT	None None <	

See footnotes at end of table.

Table 8-5. Provisionable Parameters (FT-2000 OC-48 Add/Drop-Rings Terminal) (Contd)

Parameter	Options [Original Value]	Method	CIT-PC Input [TL1 Command]	CIT-PC Report [TL1 Report]
Operations (Contd)				
- Port Security (Contd)				
Baud Rates				
CIT (DCE) (TL1 Protocol)***	1200,2400,4800,[9600]			
CIT (DTE) (TL1 Protocol)***	1200,2400,4800,[9600]			
X.25 Channel Type***	Switched, Permanent [No Original Value]			
OS Type***				
X.25 PVC #1	[maint.], memry-admin cmd-resp,other			
X.25 PVC #2	maint., memry-admin [cmd-resp],other	CIT	SECURITY-Set- Security Port	SECURITY-Retrie e- Security Port
X.25 SVC #1-#6	maint., memry-admin [cmd-resp],other,none			
SVC Calling Address***				
X.25 SVC #1-#6	<i>15 or Less Numeric Characters</i>			
Inactivity Timeout†††	0-999 Minutes [15]			
Logins 1-100**	[ATT01 or ATT††] 1 to 10 Alphanumeric Characters [luc01 or luc02****] 1 to 10	CIT	SECURITY- Set-Login [ENT-USER-SECU]	SECURITY-Retrie v-Login [RTRV-USER-SECU]
Password	[FT-2000††] 6 to 10 Characters, (at least 1 numeric character and 1 symbolic character)	CIT	SECURITY-Set- Password [ED-PID]	None
User Privilege	Reports Only, [General]	CIT	SECURITY-Set-Lo gin [ENT-USER-SECU]	SECURITY-Retrie v-Login [RTRV-USER-SECU]
Visitor Login***	Yes, [No]	CIT	SECURITY-Set-Lo gin [ENT-USER-SECU]	SECURITY-Retrie v-Login [RTRV-USER-SECU]
- Incoming Signal Alarm Delay	0-120 Seconds [2]			
- Alarm Clear Delay	0-120 Seconds [10]	CIT	CONFIGURATION- Set- Attribute-Alarm [SET-ATTR-ALM]	CONFIGURATION- Retrieve- Attribute-Alarm [RTRV-ATTR-ALM]
- Tributary Reporting****	NO, NR, NA, MN, MJ, CR			
- User-Settable Miscellaneous				
Discretes				
Description of Control	["Control n"] ≤26 Characters	CIT	CONFIGURATION- Set- Attribute-Contr ol [SET-ATTR-CONT]	CONFIGURATION- Retrieve- Attribute-Contro l [RTRV-ATTR-CONT]
Outputs 1-4				

See footnotes at end of table.

Table 8-5. Provisionable Parameters (FT-2000 OC-48 Add/Drop-Rings Terminal) (Contd)

Parameter	Options [Original Value]	Method	CIT-PC Input [TL1 Command]	CIT-PC Report [TL1 Report]
Operations (Contd)				
- User-Settable Miscellaneous (Contd)				
Description of Environmental Inputs 1-16	["Environment <i>n</i> "] ≤26 Characters	CIT	CONFIGURATION- Set-Attribute	CONFIGURATION- Retrieve-Attribu te
Environmental Input	CR, MJ, [MN], NA, NR		Environment	Environment
Alarm Level			[SET-ATTR-ENV]	[RTRV-ATTR-ENV]
- Date	YY-MM-DD [70-01-01]			
- Hours	0-23 [0]	CIT	CONFIGURATION- Set-Date	CONFIGURATION- Retrieve-Date
- Minutes	0-59 [0]		[ED-DAT]	[RTRV-HDR]
- Seconds	0-59 [0]			
- Standard Time Zone	[blank] ≤3 Letter Characters			
- Start Daylight Savings Date	MM-DD [01-01]			
- Stop Daylight Savings Date	MM-DD [01-01]			
- Daylight Savings Time Zone	[blank] ≤3 Letter Characters			
Line Identification				
- Line Name	1E, 1W	Automatic	None	CONFIGURATION- Retrieve-OC48
Line Protection Switching		CIT	CONFIGURATION-S et-OC48	CONFIGURATION-Re trieve-OC48
- Signal Degrade Threshold	10 ⁻⁵ , [10 ⁻⁶], 10 ⁻⁷ , 10 ⁻⁸ , 10 ⁻⁹			
Slot State Provisioning				
- All Slots	EQ, [AUTO]	Automatic	None	FAULT- Retrieve-State
Low-Speed Protection Switching				
- Protection Switching Priority (Service)	[Low], High	CIT	CONFIGURATION- Set-Low Speed	CONFIGURATION- Retrieve- Low Speed
- Configuration	[0xN], 1xN	Automatic	None	[RTRV-rr] FAULT- Retrieve-State
- Wait to Restore*	0-12 Minutes [5]	CIT	CONFIGURATION-S et-Protection-L ow Speed	CONFIGURATION-Re trieve- Protection-Low Speed
Performance Monitoring				
- Start Time***	0-23 [0]	CIT	PERFORMANCE- Set-PM-Stime	PERFORMANCE- Retrieve-PM-Stim e
- DS3 PM Mode***	[P-bit], F&M-bit	CIT	PERFORMANCE- Set-PM Mode-T3	PERFORMANCE- Retrieve-PM Mode-T3

See Footnotes at end of table

Table 8-5. Provisionable Parameters (FT-2000 OC-48 Add/Drop-Rings Terminal) (Contd)

Parameter	Options [Original Value]	Method	CIT-PC Input [TL1 Command]	CIT-PC Report [TL1 Report]
DS3 Termination - Violation Monitor Removal Mode - LBO (Electrical)	[VMR], VM, CC w/AIS, and CC w/o AIS In, Out	CIT Termination Cable	CONFIGURATION-Set-T3 [ENT-rr] None (Manually Installed)	CONFIGURATION-Retrieve-T3 [RTRV-rr] None
EC-1 Termination¶ - LBO (Electrical)	In, Out	Termination Cable	None (Manually Installed)	None
OC-48 Termination - LBO (Optical)	0,5,10,15,or 20 dB	Lightguide Jumper	None (Manually Installed)	None (Stamping on LBO)
OC-3 Termination*** - LBO (Optical)	0 dB, 7.1 dB	Lightguide Jumper	None (Manually Installed)	None (Stamping on LBO)
OC-3 Protection Mode*** (From OC3) (From OC48)	[0x1], 1+1	CIT	CONFIGURATION-Set-OC3 [ENT-rr]	CONFIGURATION-Retrieve-OC3 [RTRV-rr]
OC-12 Termination**** - LBO (Optical)	0 dB, 7.1 dB	Lightguide Jumper	None (Manually Installed)	None (Stamping on LBO)
OC-12 Protection Mode**** (From OC12) (From OC48)	[0x1], 1+1 [0x1], 1+1	CIT	CONFIGURATION-Set-OC12 [ENT-rr]	CONFIGURATION-Retrieve-OC12 [RTRV-rr]
Alarm Levels - DS3 Alarm Level (Incoming Signal)	[CR], MJ, MN, NA, NR	CIT	CONFIGURATION-Set-T3 [ENT-rr]	CONFIGURATION-Retrieve-T3 [RTRV-rr]
- EC-1 Alarm Level¶ (Incoming Signal)	[CR], MJ, MN, NA, NR	CIT	CONFIGURATION-Set-EC1 [SET-ATTR-rr]	CONFIGURATION-Retrieve-EC1 [RTRV-ATTR-rr]
- OC-3 Alarm Level*** (Incoming Signal)	[CR], MJ, MN, NA, NR, NO	CIT	CONFIGURATION-Set-OC3 [SET-ATTR-rr]	CONFIGURATION-Retrieve-OC3 [RTRV-ATTR-rr]
- OC-12 Alarm Level**** (Incoming Signal)	[CR], MJ, MN, NA, NR, NO	CIT	CONFIGURATION-Set-OC12 [SET-ATTR-rr]	CONFIGURATION-Retrieve-OC12 [RTRV-ATTR-rr]
Performance Monitoring - DS3 Signal Degrade Threshold	10 ⁻³ , [10 ⁻⁶]	CIT	CONFIGURATION-Set-T3 [SET-ATTR-rr]	CONFIGURATION-Retrieve-T3 [RTRV-ATTR-rr]

Table 8-5. Provisionable Parameters (FT-2000 OC-48 Add/Drop-Rings Terminal) (Contd)

Parameter	Options [Original Value]	Method	CIT-PC Input [TL1 Command]	CIT-PC Report [TL1 Report]
Performance Monitoring (Cont'd)				
- OC-12 Signal Degrade**** (Incoming Signal) Output AU4 Mapping	10 ⁻⁵ , [10 ⁻⁶], 10 ⁻⁷ , 10 ⁻⁸ , 10 ⁻⁹ [SONET], SDH	CIT	CONFIGURATION-Set-OC12 [SET-ATTR-rr]	CONFIGURATION-Retrieve-OC12 [RTRV-ATTR-rr]
- EC-1 Signal Degrade¶¶ (Incoming Signal)	10 ⁻³ , [10 ⁻⁶]	CIT	CONFIGURATION-Set-EC1 [SET-ATTR-rr]	CONFIGURATION-Retrieve-EC1 [RTRV-ATTR-rr]
- OC-3 Signal Degrade*** (Incoming Signal) Output AU4 Mapping***	10 ⁻⁵ , [10 ⁻⁶], 10 ⁻⁷ , 10 ⁻⁸ , 10 ⁻⁹ [SONET], SDH	CIT	CONFIGURATION-Set-OC3 [SET-ATTR-rr]	CONFIGURATION-Retrieve-OC3 [RTRV-ATTR-rr]
OC-3 DCC Assignment¶¶¶¶ OC-12 DCC Assignment**** - DCC Channel - Orientation	 1,2,3,4,5,6,7,8 [No Original Value] user, [network]	CIT	CONFIGURATION-Enter-Assignment-DCC	CONFIGURATION-Retrieve-Assignment-DCC
DS3 Port State Provisioning¶¶¶ - Port - Port	 [AUTO], NMON [AUTO], IS	CIT Automatic	CONFIGURATION-Set-State-T3 [ENT-rr] None	CONFIGURATION-Retrieve-T3\$\$ [RTRV-rr] CONFIGURATION-Retrieve-T3\$\$ [RTRV-rr]
EC1 Port State Provisioning¶¶¶ - Port - Port	 [AUTO], NMON [AUTO], IS	CIT Automatic	CONFIGURATION-Set-State-EC1 [ENT-rr] None	CONFIGURATION-Retrieve-EC1\$\$ [RTRV-rr] CONFIGURATION-Retrieve-EC1\$\$ [RTRV-rr]
OC3 Port State Provisioning¶¶¶ - Port - Port	 [AUTO], NMON [AUTO], IS	CIT Automatic	CONFIGURATION-Set-State-OC3 [ENT-rr] None	CONFIGURATION-Retrieve-OC3 [RTRV-rr]\$\$ CONFIGURATION-Retrieve-OC3 [RTRV-rr]\$\$
OC12 Port State Provisioning*** - Port - Port	 [AUTO], NMON [AUTO], IS	CIT Automatic	CONFIGURATION-Set-State-OC12 [ENT-rr] None	CONFIGURATION-Retrieve-OC12 [RTRV-rr]\$\$ CONFIGURATION-Retrieve-OC12 [RTRV-rr]\$\$

Table 8-5. Provisionable Parameters (FT-2000 OC-48 Add/Drop-Rings Terminal) (Contd)

Parameter	Options [Original Value]	Method	CIT-PC Input [TL1 Command]	CIT-PC Report [TL1 Report]
Path Information - Xmit Path Trace¶ - Rcv Path Trace¶	≤64 characters (Releases 4 and 5) ≤62 characters (Release 6 and later) [No original value]	CIT	CONFIGURATION - Set - Path - Trace [ENT - STS1]	CONFIGURATION - Retrieve - Path - Trace [RTRV - PTHTRC]

* Available in Release 4.1 and later releases.

† Select the desired port by installing the appropriate cabling.

‡ The baud rate is determined by the clock to the X.25 interface.

§ Available in Release 3 only.

¶ Available in Release 4 and later releases.

** Prior to Release 4.1, there is a maximum of 12 logins per system. In Release 4.1 and later releases, there is a maximum of 100 logins per system.

†† At startup, there are exactly two privileged logins and no others.

‡‡ FT-2000 is the original value of the password for ATT01 and ATT02. There is no original password value for any other logins.

§§ The **FAULT-Retrieve-State** report may also be used.

¶¶ In Release 4.1 and later releases, the low speed port state can be provisioned separately in the add and drop directions.

*** Available in Release 5 and later releases.

††† In Release 4 and 4.1, the timeout parameter was provisioned using the FT-2000 CIT-PC setup menu.

‡‡‡ CIT provisionable in Release 4.1 only.

§§§ CIT provisionable in releases prior to Release 6 only. Auto provisioned in Release 6 and later releases.

¶¶¶ Available in Release 6 and later releases.

**** Available in Release 7.1.0 and later releases.

Table 8-6. Provisionable Threshold Crossing Alert (TCA) Parameters

Parameter	Threshold Range [Original Value]		CIT-PC Input [TL1 Command]	CIT-PC Report [TL1 Report]
	Current Quarter Hour	Current Day		
OC-48/OC-12/OC-3 Optics - TCA Autonomous Report	[Enable]/Disable	[Enable]/Disable	PERFORMANCE- Set-PM Threshold-Optics [SET-TH]	PERFORMANCE- Retrieve- PM Threshold-Optics [RTRV-TH]
STS-48/STS-12/STS-3 Section - TCA Autonomous Report - Section Coding Violations - Section Errored Seconds - Section Severely Errored Seconds - Section Severely Errored Frame Seconds	[Enable]/Disable -7,-8,-9,-10,-11 1-900 [25] 1-63 [10] 1-63 [5]	[Enable]/Disable -7,-8,-9,-10,-11,12 1-65535[250] 1-4095 [40] 1-4095 [10]	PERFORMANCE- Set-PM Threshold- Section [SET-TH]	PERFORMANCE- Retrieve- PM Threshold- Section [RTRV-TH]
OC-48/OC-12/OC-3 Line - TCA Autonomous Report - Line Coding Violations - Line Errored Seconds - Line Severely Errored Seconds - Line Unavailable Seconds	[Enable]/Disable -7,-8,-9,-10,-11 1-900 [25] 1-63 [10] 1-63 [10]	[Enable]/Disable -7,-8,-9,-10,-11,12 1-65535[250] 1-4095 [40] 1-4095 [10]	PERFORMANCE- Set-PM Threshold- Line [SET-TH]	PERFORMANCE- Retrieve- PM Threshold- Line [RTRV-TH]
STS-1 Path - TCA Autonomous Report - Path/FEBC Coding Violations - Path/FEBC Errored Seconds - Path/FEBC Severely Errored Seconds - Path/FEBC Unavailable Seconds	[Enable]/Disable -7,-8,-9,-10 1-900 [25] 1-63 [10] 1-63 [10]	[Enable]/Disable -7,-8,-9,-10,-11,12 1-65535[250] 1-4095 [40] 1-4095 [10]	PERFORMANCE- Set-PM Threshold- STS1 [SET-TH]	PERFORMANCE- Retrieve- PM Threshold- STS1 [RTRV-TH]
EC-1 Section and Line - TCA Autonomous Report - Section Severely Errored Frame Seconds - Line Coding Violations - Line Errored Seconds - Line Severely Errored Seconds - Line Unavailable Seconds	[Enable]/Disable 1-63 [5] -7,-8,-9,-10 1-900 [25] 1-63 [10] 1-63 [10]	[Enable]/Disable 1-4095 [10] -7,-8,-9,-10,-11,12 1-65535 [250] 1-4095 [40] 1-4095 [10]	PERFORMANCE- Set-PM Threshold-EC1 [SET-TH]	PERFORMANCE- Retrieve- PM Threshold-EC1 [RTRV-TH]
DS3 Line and Path - TCA Autonomous Report - Line Errored Seconds - Path Coding Violations - Path Errored Seconds - Path Severely Errored Seconds - Path Severely Errored Frame/AIS Seconds - Path Unavailable Seconds	[Enable]/Disable 1-900 [25] -7,-8,-9,-10 1-900 [25] 1-63 [10] 1-900 [5] 1-63 [10]	[Enable]/Disable 1-65535[250] -7,-8,-9,-10,-11,12 1-65535[250] 1-4095 [40] 1-65535 [10] 1-4095 [10]	PERFORMANCE- Set-PM Threshold- T3 [SET-TH]	PERFORMANCE- Retrieve- PM Threshold- T3 [RTRV-TH]

FT-2000 OC-48 Repeater Bay

Table 8-7 shows the provisionable parameters of the FT-2000 OC-48 Repeater Bay. This includes the parameters that can be provisioned using the CIT, plus the parameters that are automatically provisioned or have fixed values (cannot be changed). The original value for each provisionable parameter in Table 8-7 is shown in brackets [].

Table 8-7. Provisionable Parameters (FT-2000 OC-48 Repeater Bay)

Parameter	Options [Original Value]	Method	Input (Command)	Report
System Address				
- Target Identifier (TID)	[LT-FT-2000] ≤20 characters	CIT	SECURITY-Set- Network Element	SECURITY- Retrieve-Network
- Site Address	site-line-site number	Automatic	None	Element
Operations				
- Operation Ports	CIT (DCE), CIT (DTE)	Choice*	None	SECURITY- Retrieve- Security Port
- Operation Port Baud Rates				
CIT (DCE)	1200, 2400, 4800, 9600 [No Original Value]	Autobaud	None	None
CIT (DTE)	1200, 2400, 4800, 9600 [No Original Value]			
Network Element Security				
Logins Allowed	[Yes], No	CIT	SECURITY-Set Security- Network Element	SECURITY-Retrieve Security- Network Element
- Port Security				
Type	CIT-DCE, CIT-DTE, CIT-DCC, [All]	CIT	SECURITY-Set-	SECURITY-Retrieve-
Port Access	[Enable], Disable		Security Port	Security Port
Timeout†	0-999 Minutes [15]	CIT	CIT-PC Setup Menu	None
- Login Security				
Logins 1-12	[LUC01 or LUC02‡] 1 to 10 Alphanumeric Characters	CIT	SECURITY- Set-Login	SECURITY- Retrieve-Login
Password	[FT-2000§]6 to 10 Characters, (at least 1 numeric character and 1 symbolic character)	CIT	SECURITY-Set- Password	None
User Privilege	Reports Only, [General]	CIT	SECURITY- Set-Login	SECURITY- Retrieve-Login
- User-Settable Miscellaneous				
Discretes				
Description of	["Environment "] ≤26 Characters	CIT	CONFIGURATION- Set-Attribute Environment	CONFIGURATION- Retrieve-Attribute Environment
Environmental Inputs 1-16				
Environmental Input	CR, MJ, [MN], NA, NR			
Alarm Level				
- Date	YY-MM-DD [94-06-30]	CIT		
- Hours	0-23 [0]			
- Minutes	0-59 [0]	CIT	CONFIGURATION-	CONFIGURATION-

Table 8-7. Provisionable Parameters (FT-2000 OC-48 Repeater Bay)

Parameter	Options [Original Value]	Method	Input (Command)	Report
- Seconds - Standard Time Zone - Start Daylight Savings Date - Stop Daylight Savings Date - Daylight Savings Time Zone	0-59 [0] <i>[blank] ≤3 Letter Characters</i> MM-DD [01-01] MM-DD [01-01] <i>[blank] ≤3 Letter Characters</i>		Set-Date	Retrieve-Date
Line Identification - Line Name	PE, PW, 1E, 1W	Automatic	None	CONFIGURATION- Retrieve-OC48
OC-48 Termination LBO (Optical)	0, 5, 10, 15, or 20 dB	Lightguide Jumper	None (Manually Installed)	None (LBO Ring Color or Stamping on LBO)
Slot State Provisioning - All Slots	EQ, [AUTO]	Automatic	None	FAULT- Retrieve-State

* Select the desired port by installing the appropriate cabling.

† Available in Release 4 and later releases.

‡ At startup there are exactly two privileged logins and no others.

§ FT-2000 is the original value of the password for ATT01 and ATT02.
There is no original password value for any other logins.

Local Provisioning

FT-2000 OC-48 Add/Drop-Rings Terminal

The following CIT-PC inputs (listed in alphabetical order) and corresponding TL1 commands are used to provision the FT-2000 OC-48 Add/Drop-Rings Terminal. (Refer to Volume I, Section 6, "Operations Interfaces" or Volume I, Section 10, "Technical Specifications" for information about minimum CIT requirements.)

- **CONFIGURATION-Set-Attribute-Alarm** (TL1 = **SET-ATTR-ALM**): This input provisions the incoming Signal Alarm Delay and the Alarm Clear Delay intervals.
 - The Incoming Signal Alarm Delay parameter sets the time (0 to 120 seconds) the network element will wait, after it finds an incoming signal condition, before reporting the event using all autonomous indicators. This parameter applies to defects in the OC-48 line signals, DS3 signals, EC-1 signals, OC-3 signals, OC-12 signals, and DS1 synchronization references.
 - The Alarm Clear Delay parameter sets the time (0 to 120 seconds) the network element will wait, after a condition clears, before reporting the clear event using all indicators. This parameter applies to all conditions.

The TBOS points are not affected by Incoming Signal Alarm Delay or Alarm Clear Delay parameters.

- **CONFIGURATION-Set-Attribute-Control** (TL1 = **SET-ATTR-CONT**): This input sets the Descriptions for the miscellaneous discrete control output points.
 - The Point parameter selects the miscellaneous discrete control output point to provision.
 - The Description parameter is the text string associated with the selected miscellaneous discrete control output point. It is a character string of up to 26 characters, where the characters are letters, digits, or spaces. It is used to identify the point in the **Retrieve-History** and **Retrieve-Alarm (and Status)** reports.
- **CONFIGURATION-Set-Attribute-Environment** (TL1 = **SET-ATTR-ENV**): This input sets the Descriptions and Alarm Levels for the miscellaneous discrete environmental input points.
 - The Point parameter selects the miscellaneous discrete environmental input point to provision.
 - The Alarm Level parameter provisions the severity of the condition associated with the closing of the selected miscellaneous discrete environmental input point. The choices are:

- "CR": critical
 - "MJ": major
 - "MN": minor
 - "NA": status not alarmed
 - "NR": status not alarmed and not reported to the OS.
- The Description parameter is the text string associated with the selected miscellaneous discrete environmental input point. It is a character string of up to 26 characters, where the characters are letters, digits, or spaces. It is used to identify the point in the **Retrieve-History** and **Retrieve-Alarm (and Status)** reports. In Release 5, the number 1, 2, 3, and 4 inputs are reserved for the following alarms from an FT-Lightwave Booster Amplifier.

FT-LBA Miscellaneous Discrete	FT-2000 Environmental Input	Suggested Text
Fuse/Power Alarm	1	FT-LBA fuse/power failure
Fan Alarm	2	FT-LBA fan failure
1EW (Service) Failure	3	FT-LBA 1EW failure
1WE (Service) Failure	4	FT-LBA 1WE failure

- **CONFIGURATION-Set-Date** (TL1 = **ED-DAT**): This input sets the network element clock date and time and provisions automatic daylight savings time processing.
- The Date parameter sets the current date in the network element clock. It is entered in the form YY-MM-DD where:
- YY is the year (00-99)
 - MM is the month (1-12)
 - DD is the day (1-31).
- The Hours parameter sets the hour (0 to 23) in the network element clock. A value of "zero" may be entered as "-0", "0", or "+0". These entries are treated as the same value.
- The Minutes parameter sets the minute (0 to 59) in the network element clock. A value of "zero" may be entered as "-0", "0", or "+0". These entries are treated as the same value.
- The Seconds parameter sets the second (0 to 59) in the network element clock. A value of "zero" may be entered as "-0", "0", or "+0". These entries are treated as the same value.
- The Standard Time Zone parameter is the 3-letter string (for example, EST, MST) that is indicated as part of the date and time stamp in command responses when Standard Time is in effect.

- The Start Daylight Savings Date parameter sets the date when the network element moves the current time forward by 1 hour. It should be entered in the form MM-DD where:

- MM is the month (1-12)
- DD is the Day (1-31).

If this value is different from the Stop Daylight Savings Date parameter value, daylight savings time is enabled and the time change will occur at 02:00:00 on the chosen date. If the two values are the same, daylight savings time processing is disabled. The value 2-29 is not allowed.

- The Stop Daylight Savings Date parameter sets the date when the network element moves the current time backward by 1 hour. It should be entered in the form MM-DD where:

- MM is the month (1-12)
- DD is the Day (1-31).

If this value is different from the Start Daylight Savings Date parameter value, daylight savings time is enabled and the time change will occur at 02:00:00 on the chosen date. If the two values are the same, daylight savings time processing is disabled. The value 2-29 is not allowed.

- The Daylight Savings Time Zone parameter is the 3-letter character string (for example, EST, MST) that is printed as part of the date and time stamp in command responses when Daylight Savings Time is in effect.

- **CONFIGURATION-Set-EC1** (TL1 = **ATTR-rr**): This input sets the Alarm Level and Signal Degrade Threshold of the EC-1 interface ports for signals incoming from cross-connect panel. These parameters can be provisioned when the selected low-speed interface slot(s) is unequipped or equipped with any low-speed interface circuit pack. Thus, it is permissible to pre-provision low-speed interface slots for EC-1 interfaces.

- The Alarm Level* parameter sets the severity of an incoming (from the cross-connect panel) signal failure condition. The choices are:
 - "CR": critical
 - "MJ": major
 - "MN": minor
 - "NA": status not alarmed

* If the EC-1 port is part of a 1-way-DRI cross-connection, the alarm level should be provisioned minor (MN).

- "NR": status not alarmed and not reported to the OS.
- The Signal Degrade Threshold is the base 10 logarithm of the EC-1 line parity (B2) error rate threshold. The allowed values are "-3" or "-6". A signal degrade condition is reported for EC-1 signals received from the cross-connect panel with error rates greater than the threshold.
- **CONFIGURATION-Set -Low Speed** (TL1 = **ENT-rr**): This input sets the automatic Protection Switching Priority for each selected low-speed slot.
 - The Protection Switching Priority parameter sets the protection switching priority of the selected slot(s). The priority is either "Low" or "High." For example, an Auto Switch request from a High priority slot will preempt an Auto Switch of a Low priority slot.
- **CONFIGURATION-Set -NSAP** (TL1 = **SET-NSAP**): In Release 7.1.0 and later releases, this sets (provisions) certain fields on the Network Services Access Point (NSAP) address of a network element. The NSAP consists of the following fields:
 - **L3IDP**: (Level 3 Initial Domain Part)
value always = 0x270348 which indicates that U.S. ANSI is the registration authority responsible for NSAP allocation and assignment.
 - **L3DFI**: (Level 3 Domain Specific Part)
value always = 0x80 to indicate that a format alignment with GOSIP Version 2 is always used.
 - **L3ORG**: (Level 3 Organization Identifier (or Operator ID))
value is user-settable, value = 0x_____
 - **L3RES**: (Level 3 Reserved) this field is reserved for future use
 - **L3ROU**: (Level 3 Routing)
value is user-settable and consists of the two sub-fields "Routing Domain" and "Area", value = 0x_____
 - **L3SYS**: (Level 3 System Identifier)
consists of a unique number that is assigned to each NE. It is programmed into each SYSCTL circuit pack during manufacture
 - **L3SEL**: (Level 3 Selector)
used to differentiate multiple NSAP addresses associated with the same end system, format of the value = 0x0

- **CONFIGURATION-SET-OC-12** (TL1 = **ENT-rr**): This input sets the Protection Mode, Alarm Level, Signal Degrade Threshold, Output AU4 Mapping, and Sync Messaging of the OC-12 line. This input sets the provisioned protection mode parameters successfully even if the quad slot associated with addressed quad pair is not equipped with an OC-12 circuit pack type or is in the AUTO state.

- The Protection Mode (from OC12) parameter sets the protection mode of a low-speed interface slot to "1+1" or "0x1" in the add direction (from OC-12 to OC-48). The "1+1" protection mode is provided by using a pair of adjacent quad low-speed interfaces (for example, command slots 1B and 3B, or command slots 5B and 7B). Each "1+1" protected OC12 quad pair establishes an active cross-connection between one quad of the quad pair and an STS-12/STS-12c tributary, and a standby cross-connection between the other quad and the same tributary.

To implement the OC12 operating protection mode, the following condition must exist:

- Both quads in the quad pair must be provisioned "1+1"
- Both quads in the quad pair must be EQ
- A cross-connection must exist between either quad in the quad pair and an STS-12/STS-12c tributary.

- The Protection Mode (from OC48) parameter sets the protection mode of a low-speed interface slot to "1+1" or "0x1" in the drop direction (from OC-48 to OC-12). The "1+1" protection mode is provided by using a pair of adjacent quad low-speed interfaces. For a "1+1" protected OC12 circuit pack pair, the STS-12/STS-12c tributary feeds the low-speed signal to both quads.

To implement the OC12 operating protection mode, the following conditions must exist:

- Both quads in the quad pair must be provisioned "1+1"
- Both quads in the quad pair must be EQ
- A cross-connection must exist between either quad in the quad pair and an STS-12/STS-12c tributary.



NOTE:

When two independent 1-way 1+1 circuits share the same OC12 (1.3 STD) quad pair, provision the 1+1 system in the drop direction as two 0x1 systems that are bridged. This prevents K-byte signals (used for protection switching) and certain maintenance signals that are based on 2-way service from being returned to the far end terminal.

When a quad pair transitions from 0x1 unprotected operation to 1+1 protected operation, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically selects add cross-connection from the "1B/5B

command slot" to the STS-12 tributary as active and the add cross-connection from the "3B/7B command slot" to the same STS-12 tributary as standby. If the add cross-connection from the "3B/5B command slot" to the STS-12 tributary was previously active and operating unprotected (0x1), a brief service hit (<60 msec) will be observed.

When a quad pair transitions from 1+1 protected operation to 0x1 unprotected operation, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically selects the add/drop cross-connections between the "1B/5B command slot" and the STS-12 tributary as active. If the add cross-connection from the "3B/7B command slot" to the STS-12 tributary was previously active, a brief service hit (<60 msec) will be observed.

**CAUTION:**

When transitioning an OC-12 line from 1+1 protected operation to 0x1 unprotected operation, always start with the quad pair at the tail-end of the OC-12 line. When a quad pair transitions from 1+1 operation to 0x1 operation, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically selects the add/drop cross-connections between the "1B/5B command slot" and the STS-12/STS-12c tributary as active. If the drop cross-connection from the STS-12/STS-12c tributary to the "3B/7B command slot" was previously active, the OC12 (1.3 STD) in the "3B/7B command slot" transmits path AIS toward the OC-12 line. The receiving terminal accepts the path AIS as a good signal, and no trouble condition is reported.

- The Alarm Level parameter sets the severity of an incoming (from the lightguide cross-connect panel) signal failure condition. The choices are:
 - "CR": critical
 - "MJ": major
 - "MN": minor
 - "NA": status not alarmed
 - "NR": status not alarmed and not reported to the OS.
 - "NO": no failure conditions reported.
(Not applicable to slots in the 1+1 Protection Mode.)
- The Signal Degrade Threshold is the base 10 logarithm of the OC-12 line parity (B2) error rate threshold. The allowed values are "-5," "-6," "-7," "-8," or "-9". A signal degrade condition is reported for OC-12 signals received from the lightguide cross-connect panel with error rates greater than the threshold.

- The Output AU4 Mapping parameter sets the AU4 mapping bit to SONET or SDH. The OC12 circuit pack can automatically support transport of:
 - One STS-12c signal
 - Twelve STS-1 signals, or STM-1 signals carrying AU-4 mapped signals. For STM-1 AU-4 mapped payloads, the OC12 circuit pack can be provisioned to output the appropriate values in the pointer bytes so that direct interface to SDH equipment is possible over an STM-1 optical interconnection. The high-speed transport of the payload is SONET, but this does not present any operational difficulties. "SONET" indicates the transport of an OC-12 signal. "SDH" indicates the transport of an AU4 signal.
 - Four STS-3 or STS-3c signals
 - A combination of STS-1 and STS-3/STS-3c signals that add up to the equivalent of 622.08 MB/s.
- **CONFIGURATION-Set - OC3** (TL1 = **ENT - rr**): This input sets the Protection Mode, Alarm Level, Signal Degrade Threshold and Output AU4 Mapping of the OC-3 line. This input sets the provisioned protection mode parameters successfully even if the slot associated with the addressed slot pair is not equipped with an OC3 circuit pack or is in the AUTO state.
 - The Protection Mode (from OC3) parameter sets the protection mode of a low-speed interface slot to "1+1" or "0x1" in the add direction (from OC-3 to OC-48). The "1+1" protection mode is provided by using a pair of adjacent "A" and "B" low-speed interface slots (for example, slot 2A and 2B). Each "1+1" protected OC3 circuit pack pair establishes an active cross-connection between one slot (or port) of the slot pair and an STS-3 (or STS-1) tributary, and a standby cross-connection between the other slot (or port) and the same tributary.

To implement the OC3 operating protection mode, the following condition must exist:

 - Both slots in the slot pair must be provisioned "1+1"
 - Both slots in the slot pair must be EQ
 - A cross-connection must exist between either slot (or port) in the slot pair and an STS-3 (or STS-1) tributary.
 - The Protection Mode (from OC48) parameter sets the protection mode of a low-speed interface slot to "1+1" or "0x1" in the drop direction (from OC-48 to OC-3). The "1+1" protection mode is provided by using a pair of adjacent "A" and "B" low-speed interface slots. For a "1+1" protected OC3 circuit pack pair, the STS-3 tributary feeds the low-speed signal to both slots.

To implement the OC3 operating protection mode, the following conditions must exist:

- Both slots in the slot pair must be provisioned "1+1"
- Both slots in the slot pair must be EQ
- A cross-connection must exist between either slot (or port) in the slot pair and an STS-3 (or STS-1) tributary.



NOTE:

When two independent 1-way 1+1 circuits share the same OC3 (1.3 STD) circuit pack pair, provision the 1+1 system in the drop direction as two 0x1 systems that are bridged. This prevents K-byte signals (used for protection switching) and certain maintenance signals that are based on 2-way service from being returned to the far end terminal.

When a slot pair transitions from 0x1 unprotected operation to 1+1 protected operation, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically selects the add cross-connection from the "A" slot to the STS-3 tributary as active and the add cross-connection from the "B" slot to the same STS-3 tributary as standby. If the add cross-connection from the "B" slot to the STS-3 tributary was previously active and operating unprotected (0x1), a brief service hit (<60 msec) will be observed. When a slot pair transitions from 0x1 operation to 1+1 operation, any DCC assignment to the A slot changes to a DCC assignment to the slot pair. Any DCC assignment to the B slot will be deleted.

When a slot pair transitions from 1+1 protected operation to 0x1 unprotected operation, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically selects the add/drop cross-connections between the "A" slot and the STS-3 tributary as active. If the add cross-connection from the "B" slot to the STS-3 tributary was previously active, a brief service hit (<60 msec) will be observed. When a slot pair transitions from 1+1 operation to 0x1 operation, any DCC assignment to the slot pair changes to a DCC assignment to the A slot only. There will be no DCC assignment to the B slot.



CAUTION:

When transitioning an OC-3 line from 1+1 protected operation to 0x1 unprotected operation, always start with the slot pair at the tail end of the OC-3 line. When a slot pair transitions from 1+1 operation to 0x1 operation, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically selects the add/drop cross-connections between the "A" slot and the STS-3 tributary as active. If the drop cross-connection from the STS-3 tributary to the "B" slot was previously active, the OC3 (1.3 STD) circuit pack in the "B" slot transmits path AIS toward the OC-3 line. The receiving terminal

accepts the path AIS as a good signal, and no trouble condition is reported.

- The Alarm Level parameter sets the severity of an incoming (from the lightguide cross-connect panel) signal failure condition. The choices are:
 - "CR": critical
 - "MJ": major
 - "MN": minor
 - "NA": status not alarmed
 - "NR": status not alarmed and not reported to the OS.
 - "NO": no failure conditions reported.

(Not applicable to slots in the 1+1 Protection Mode.)

- The Signal Degrade Threshold is the base 10 logarithm of the OC-3 line parity (B2) error rate threshold. The allowed values are "-5," "-6," "-7," "-8," or "-9". A signal degrade condition is reported for OC-3 signals received from the lightguide cross-connect panel with error rates greater than the threshold.
- The Output AU4 Mapping parameter sets the AU4 mapping bit to SONET or SDH. The OC3 circuit pack can automatically support transport of STS-3, STS-3c, or STM-1 signals carrying AU-4 mapped signals. For STM-1 AU-4 mapped payloads, the OC3 circuit pack can be provisioned to output the appropriate values in the pointer bytes so that direct interface to SDH equipment is possible over an STM-1 optical interconnection. The high-speed transport of the payload is SONET, but this does not present any operational difficulties. "SONET" indicates the transport of an OC-3 signal. "SDH" indicates the transport of an AU4 signal.
- The Timing Direction parameter determines which high speed line (1W, 1E) the LAA10B circuit pack receives its timing from when the network element's synchronization mode is through-timed.



NOTE:

The OC3 timing direction of a 1+1 protected slot pair should be set to the same OC-48 line to avoid potential downstream synchronization problems.

- **CONFIGURATION-Set-OC48** (TL1= **ENT-rr**): This input sets the Signal Degrade Threshold, F Byte Processing and Sync Messaging of high-speed lines.

- The Line parameter selects the high-speed line(s).
- The Signal Degrade Threshold parameter sets the line parity (B2) error rate at which a signal degrade condition is declared. It is an integer in the range "-9" to "-5." These values are the base 10 logarithm of the error rate threshold (for example, "-7" means an error rate of 10^{-7}).
- The Sync Messaging parameter determines whether synchronization messaging is enabled or disabled on the K2 byte. For sync messaging on the K2 byte, the setting should be "Enabled." For no sync messaging, the setting should be "Disabled." There is no option available for sync messaging on the S1 byte at the OC48 level.

**NOTE:**

The synchronization messaging status of the OC48 TRMTR and OC48 RCVR circuit packs on the same OC-48 high-speed line should both have the same setting (i.e., either "enabled" or "disabled") to avoid potential synchronization problems. For example, if the OC48 TRMTR circuit pack does not have sync messaging enabled but the OC48 RCVR circuit pack does, a DUS (do not use for synchronization) message may appear when executing the **CONFIGURATION-Retrieve-Synchronization** command.

- **CONFIGURATION-Set-Path-Trace**: This input sets the transmitted and expected received path traces. This input also enables or disables DRI switching on the STS Path Payload Defect Indicator (PDI-P) in the STS SPE.

The Receive Path Trace parameter is the character string that the **Retrieve-Path-Trace** input will compare with the actual receive path trace for the selected DS3 low-speed port(s). The string must be less than or equal to 62 characters long and may use spaces, double quotes, or any of the following characters:

```
#$%&'()*+,-./0123456789;<>@ABCDEFGHIJKLMNO  
PQRSTUVWXYZ[\]^_`abcdefghijklmnopqrstuvwxyz{|}~
```

- The Transmit Path Trace parameter is the character string that will be transmitted toward the OC-48 line in the path trace of the selected DS3 low-speed port(s). The string must be less than or equal to 62 characters long and may use spaces, double-quotes, or any of the following characters:

#\$%&'()*+,-./0123456789;<>@ABCDEDFHIJKLMNO
PQRSTUVWXYZ[\]^_`abcdefghijklmnopqrstuvwxyz{ }~

The provisioned value of the Transmit Path Trace parameter **MUST** be different from the provisioned value of the Receive Path Trace parameter, at the same node. Also, the provisioned value of the Transmit Path parameter at the near end node must be **EXACTLY** the same as the provisioned value of the Receive Path Trace parameter at the far end node.

To set the path trace to all nulls, enter any number of spaces (as long as no non-space characters are entered). The spaces will be translated and stored as null characters.

- The PDI-P Switching parameter determines whether PDI-P switching is enabled or disabled.

- **CONFIGURATION-Set-Protection-Line:** This input sets the Wait to Restore interval for line protection switching.

- The Wait To Restore parameter is the time ("0" to "12" minutes) that the network element waits after a condition clears before breaking automatic high-speed protection switches.

- **CONFIGURATION-Set-Protection-Low Speed:** This input sets the Wait to Restore interval for low-speed protection switching.

- The Wait To Restore parameter is the time ("0" to "12" minutes) that the network element waits after a condition clears before breaking automatic low-speed protection switches.

- **CONFIGURATION-Set-State-EC1** (TL1 = **ENT-rr**): This input sets the Primary State of EC-1 ports to "AUTO" or "NMON." This parameter can be provisioned when the selected low-speed interface slot(s) is unequipped or equipped with any type low-speed interface circuit pack. Thus, it is permissible to preprovision low-speed interface slots for EC-1 interfaces. The EC-1 port state can be provisioned separately in the add and drop directions.

- The Direction parameter selects the direction of the selected low-speed port(s) to provision (From STSX1, From OC48).

- The Primary State parameter provisions the state of the selected port(s) (and selected direction) to either:

- "NMON": The EC-1 signals for the selected direction of this port are not monitored for errors. No conditions are reported and PM quantities will be "-" for this port. The port will not make a transition to the IS (in service) state if a good signal appears.

- "AUTO": Alarms will not be raised until after a valid signal appears on the selected direction of this port. After the signal appears, it is continuously monitored, and the port transitions to the IS state.
- **CONFIGURATION-Set-State-OC3** (TL1 = **ENT-rr**): This input sets the Primary State of OC-3 ports to "AUTO" or "NMON." This parameter can be provisioned when the selected low-speed interface slot(s) is unequipped or equipped with any type low-speed interface circuit pack. Thus, it is permissible to preprovision low-speed interface slots for OC-3 interfaces. The OC-3 port state can be provisioned separately in the add and drop directions.
 - The Direction parameter selects the direction of the selected low-speed port(s) to provision (From STS-3, From OC48).
 - The Primary State parameter provisions the state of the selected port(s) to either:
 - "NMON": The OC-3 signals for the selected direction of this port are not monitored for errors. No conditions are reported and PM quantities will be "-" for this port. The port will not make a transition to the IS (in service) state if a good signal appears.
 - "AUTO": Alarms will not be raised until after a valid signal appears on the selected direction of this port. After the signal appears, it is continuously monitored, and the port transitions to the IS state.
- **CONFIGURATION-Set-State-OC12** (TL1 = **ENT-rr**): This input sets the Primary State of OC12 ports to "AUTO" or "NMON." This parameter can be provisioned when the selected low-speed interface slot(s) is unequipped or equipped with any type low-speed interface circuit pack. Thus, it is permissible to preprovision low-speed interface slots for OC12 interfaces. The OC-12 port state can be provisioned separately in the add and drop directions.
 - The Direction parameter selects the direction of the selected low-speed port(s) to provision (From STS-12, From OC48).
 - The Primary State parameter provisions the state of the selected port(s) (and selected direction) to either:
 - "NMON": The OC-12 signals for the selected direction of this port are not monitored for errors. No conditions are reported and PM quantities will be "-" for this port. The port will not make a transition to the IS (in service) state if a good signal appears.

- "AUTO": Alarms will not be raised until after a valid signal appears on the selected direction of this port. After the signal appears, it is continuously monitored, and the port transitions to the IS state.
- **CONFIGURATION-Set-State-T3** (TL1 = **ENT-rr**): This input sets the Primary State of DS3 input ports to "AUTO" or "NMON." This parameter can be provisioned when the selected low-speed interface slot(s) is unequipped or equipped with any type low-speed interface circuit pack. Thus, it is permissible to preprovision low-speed interface slots for DS3 interfaces. The DS3 port state can be provisioned separately in the add and drop directions.
 - The Direction parameter selects the direction of the selected low-speed port(s) to provision (From DSX, From OC48).
 - The Primary State parameter provisions the state of the selected port(s) (and selected direction) to either:
 - "NMON": The DS3 signals for the selected direction of this port are not monitored for errors. No conditions are reported and PM quantities will be "-" for this port. The port will not make a transition to the IS (in service) state if a good signal appears.
 - "AUTO": Alarms will not be raised until after a valid signal appears on the selected direction of this port. After the signal appears, it is continuously monitored, and the port transitions to the IS state.
- **CONFIGURATION-Set-Synchronization**: This input sets the network element synchronization mode and synchronization operation parameters.
 - The Provisioned Mode specifies the primary synchronization mode of the network element. The options are:
 - "Free Running": The network element operates asynchronously, with the timing of the transmitted high-speed signals derived from the local timing generator.
 - "Through Timed": The network element synchronizes the transmitted high-speed signals to the high-speed signals received from the opposite direction.
 - "Line Timed": The network element synchronizes the transmitted high-speed and low-speed signals to the selected incoming high-speed signal.
 - "Externally Timed": The network element synchronizes the transmitted high-speed and low-speed signals to an external DS1 reference signal.
 - The Primary DS1 In Format parameter specifies whether the primary DS1 reference signal uses superframe (SF) or extended superframe (ESF) format.

- The Primary DS1 In/Out Format parameter specifies whether the TIMING IN PRI and TIMING OUT 1 DS1 reference signals use superframe (SF) or extended superframe (ESF) format.
- The Secondary DS1 In Format parameter specifies whether the secondary DS1 reference signal uses superframe (SF) or extended superframe (ESF) format.
- The Secondary DS1 In/Out Format parameter specifies whether the TIMING IN SEC and TIMING OUT 2 DS1 reference signals use superframe (SF) or extended superframe (ESF) format.
- The Primary DS1 In Line Code parameter specifies whether the primary DS1 reference signal uses alternate mark inversion (AMI) or bipolar eight zero substitution (B8ZS) line coding.
- The Secondary DS1 In Line Code parameter specifies whether the secondary DS1 reference signal uses alternate mark inversion (AMI) or bipolar eight zero substitution (B8ZS) line coding.
- The Synchronization Source parameter specifies that timing is derived from the East or West high-speed lines or that timing is automatically chosen depending upon which line provides the best timing source.
- The DS1 Out 1 Equalization parameter provisions the DS1 Out 1 to drive "20, 40, 60, 80, or 100" percent of its maximum rated cable length. The cable lengths are based on YR28365 cable. Provision the equalization to the smallest value greater than the actual cable length based on the following:

Optimum Equalization Setting (Percent)	Cable Length to DSX-1 Panel (Feet)
20	0 - 133
40	133 - 267
60	267 - 400
80	400 - 533
100	533 - 655

- The DS1 Out 2 Equalization parameter provisions the DS1 Out 2 to drive "20, 40, 60, 80, or 100" percent of its maximum rated cable length. The cable lengths are based on YR28365 cable. Provision the equalization to the smallest value greater than the actual cable length based on the following:

Optimum Equalization Setting (Percent)	Cable Length to DSX-1 Panel (Feet)
20	0 - 133
40	133 - 267
60	267 - 400
80	400 - 533
100	533 - 655

- **CONFIGURATION-Set - T3** (TL1 = **ENT-rr**): This input sets the VMR mode, Alarm Level, and Signal Degrade Threshold of DS3 interface ports. These parameters can be provisioned when the selected low-speed interface slot(s) is unequipped or equipped with any type low-speed interface circuit pack. Thus, it is permissible to preprovision low-speed interface slots for DS3 interfaces.

- The Violation Monitor Removal parameter sets the mode of the Violation Monitor and Removal (VMR) function of the addressed DS3 ports. The choices are:
 - "VMR": DS3 P-bit parity errors in the output DS3 (towards the cross-connect panel) are monitored and removed.
 - "VM": DS3 P-bit parity error in the output DS3 (towards the cross-connect panel) are monitored and not removed.
 - "CC w/AIS": The incoming DS3 signal (from the OC-48 line) is transmitted to the cross-connect panel without DS3 format violation monitoring (clear channel). DS3 AIS is inserted toward the OC-48 line, if there are excessive bipolar violations or there is loss of signal from the cross-connect panel. DS3 AIS is passed through in both directions.
 - "CC w/o AIS": Same as CC w/AIS, except DS3 AIS is not inserted.
- The Alarm Level* parameter sets the severity of an incoming (from the cross-connect panel) signal failure condition. The choices are:
 - "CR": critical
 - "MJ": major
 - "MN": minor

* If the DS3 port is part of a 1-way-DRI cross-connection, the alarm level should be provisioned minor (MN).

- "NA": status not alarmed

- "NR": status not alarmed and not reported to the OS.

— The Signal Degrade Threshold is the base 10 logarithm of the DS3 Bipolar Violation error rate threshold. The allowed values are "-3" or "-6". A signal degrade condition is reported for DS3 signals coming in from the cross-connect panel with error rates greater than the threshold.

- **PERFORMANCE-Set-PM Start-Time** (TL1= **SET-PM-STIME**): This input sets the start time for the measurement of all 24 hour performance-monitoring parameters. The start time is set relative to the current clock and time zone. When the command is executed, all parameters in the current Day bin are marked corrupt with a "?".

— The Start Hour parameter specifies the start hour for all 24 hour performance-monitoring parameters. The performance-monitoring parameters will reset and begin daily at the provisioned time.

- **PERFORMANCE-Set-PM Mode-T3** (TL1= **ENT-rr**): This input specifies the algorithm used for determining and accumulating DS3 path performance-monitoring data. The user may select F and M bit adjusted processing or P-bit processing.

— The DS3 PM Mode parameter specifies the DS3 performance-monitoring algorithm to be chosen. The user may select F and M-bit adjusted processing or P-bit processing. The raw F and M framing bit error counts are adjusted to approximate the actual error count in the DS3 path payload. When the command is executed, all parameters in the current day and quarter hour bins for DS3 path performance-monitoring data are marked corrupt with a "?".

- **PERFORMANCE-Set-PM Threshold-EC1** (TL1 = **SET-TH**): This input provisions values for the threshold crossing alert (TCA) parameters associated with EC-1 ports. Note that these parameters only apply to EC-1 signals received from the cross-connect panel:

— The TCA Autonomous Report parameter specifies whether autonomous reporting of threshold crossing alerts (TCAs) via TBOS and TL1 is "Enable" or "Disable" for the selected performance-monitoring bin and EC-1 port(s).

— The Section Severely Errored Frame Seconds parameter specifies the number of EC-1 severely errored frame seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and EC-1 port(s). This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin, and an integer from 1 to 4095 (inclusive) for the day bin.

— The Line Coding Violations parameter specifies the number of EC-1 line coding violations that will cause a threshold crossing alert (TCA) for the selected performance-monitoring bin and EC-1 port(s). This

parameter is the base 10 logarithm of the desired error rate. The values are integers from -10 to -7 (inclusive) for the quarter-hour bin, and integers from -12 to -7 (inclusive) for the day bin.

- The Line Errored Seconds parameter specifies the number of EC-1 line errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and EC-1 port(s). An EC-1 line errored second is recorded for each second during which there are one or more B2 coding violations, or an EC-1 line AIS defect is detected. This parameter is an integer from 1 to 900 (inclusive) for the quarter-hour bin and an integer from 1 to 65535 (inclusive) for the day bin.
- The Line Severely Errored Seconds parameter specifies the number of EC-1 line severely errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and EC-1 port(s). An EC-1 line severely errored second is recorded for each second during which there are nine or more B2 coding violations or an EC-1 line AIS defect is detected. For intervals with 10 or more contiguous severely errored seconds, the severely errored seconds will be recorded as unavailable seconds, not as severely errored seconds. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.
- The Line Unavailable Seconds parameter specifies the number of EC-1 line unavailable seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and EC-1 port(s). An EC-1 line unavailable second is recorded after 10 consecutive line severely errored seconds. These 10 seconds are counted, and each subsequent second is counted until there are 10 consecutive seconds without a line severely errored second. The 10 consecutive seconds without a line severely errored second are not counted as unavailable seconds. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.

- **PERFORMANCE-Set-PM Threshold-Line** (TL1 = **SET-TH**): This input provisions values for the threshold crossing alert parameters applied to the B2 line data collected for each OC-48, OC-12 and OC-3 line.
 - The TCA Autonomous Report parameter specifies whether autonomous reporting of threshold crossing alerts (TCAs) via TBOS and TL1 is "Enable" or "Disable" for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s).
 - The Line Coding Violations parameter specifies the number of line composite B2 coding violations that will cause a threshold crossing alert for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s). This parameter is the base 10 logarithm of the desired error rate. The values are integers from -11 to -7 (inclusive) for the quarter-hour bin and integers from -12 to -7 (inclusive) for the day bin.
 - The Line Errored Seconds parameter specifies the number of line errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s). A line errored second is recorded for each second during which there are one or more composite B2 coding violations, or a line AIS defect is detected. This parameter is an integer from 1 to 900 (inclusive) for the quarter-hour bin and an integer from 1 to 65,535 (inclusive) for the day bin.
 - The Line Severely Errored Seconds parameter specifies the number of line severely errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s). A line severely errored second is recorded for each second during which there are 494 or more composite B2 coding violations, or a line AIS defect is detected. For intervals with 10 or more contiguous severely errored seconds, the severely errored seconds will be recorded as unavailable seconds, not as severely errored seconds. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.
 - The Line Unavailable Seconds parameter specifies the number of line unavailable seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s). A line unavailable second is recorded after 10 consecutive line severely errored seconds. These 10 seconds are counted, and each subsequent second is counted until there are 10 consecutive seconds without a line severely errored second. The 10 consecutive seconds without a line severely errored second are not counted as unavailable seconds. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.
- **PERFORMANCE-Set-PM Threshold-Optics** (TL1 = **SET-TH**): This input provisions values for the threshold crossing alert parameters applied to the optical health data collected for each OC-48, OC-12 or OC-3 line.

- The TCA Autonomous Report parameter specifies whether autonomous reporting of threshold crossing alerts (TCAs) via TBOS and TL1 is "Enable" or "Disable" for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s).
- **PERFORMANCE-Set-PM Threshold-Section** (TL1 = **SET-TH**): This input provisions values for the threshold crossing alert parameters applied to the B1 section data collected for each STS-48, STS-12 or STS-3 section.
 - The TCA Autonomous Report parameter specifies whether autonomous reporting of threshold crossing alerts (TCAs) via TBOS and TL1 is "Enable" or "Disable" for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s).
 - The Section Coding Violations parameter specifies the number of section coding violations that will cause a threshold crossing alert for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s). This parameter is the base 10 logarithm of the desired error rate. The values are integers from -11 to -7 (inclusive) for the quarter-hour bin and integers from -12 to -7 (inclusive) for the day bin.
 - The Section Errored Seconds parameter specifies the number of section errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s). A section errored second is recorded for each second during which there are one or more B1 coding violations, one or more loss-of-signal defects, or one or more loss-of-frame defects. This parameter is an integer from 1 to 900 (inclusive) for the quarter-hour bin and an integer from 1 to 65,535 (inclusive) for the day bin.
 - The Section Severely Errored Seconds parameter specifies the number of section severely errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s). A section severely errored second is recorded for each second during which there are 249 or more B1 coding violations, one or more loss of signal defects, or one or more loss-of-frame defects. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.
 - The Section Severely Errored Frame Seconds parameter specifies the number of section severely errored frame seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and OC-48, OC-12 or OC-3 line(s). A section severely errored frame second is the number of seconds during which an out-of-frame event occurred. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.

- **PERFORMANCE-Set-PM Threshold-STS1** (TL1 = **SET-TH**): This input provisions values for the threshold crossing alert parameters associated with STS-1 tributaries. Note that these parameters only apply to STS-1 signals received from the OC-48 line. These parameters are applied to both the B3 (path parity) and far end block error (FEBE) data collection for each STS-1 tributary.
 - The TCA Autonomous Report parameter specifies whether autonomous reporting of threshold crossing alerts (TCAs) via TBOS and TL1 is "Enable" or "Disable" for the selected performance-monitoring bin and STS-1 tributary(s).
 - The Path/FEBE Coding Violations parameter specifies the number of STS-1 path/FEBE coding violations that will cause a threshold crossing alert for the selected performance-monitoring bin and STS-1 tributary(s). This parameter is the base 10 logarithm of the desired error rate. The values are integers from -10 to -7 (inclusive) for the quarter-hour bin and integers from -12 to -7 (inclusive) for the day bin.
 - The Path/FEBE Errored Seconds parameter specifies the number of STS-1 path/FEBE errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and STS-1 tributary(s). An STS-1 path errored second is recorded for each second during which there are one or more B3 coding violations, one or more STS-1 path AIS defects, or one or more STS-1 loss-of-pointer defects. A FEBE errored second is recorded for each second during which there are one or more FEBE coding violations, or one or more remote defect indicator-path (RDI-P) defects. This parameter is an integer from 1 to 900 (inclusive) for the quarter-hour bin and an integer from 1 to 65,535 (inclusive) for the day bin.
 - The Path/FEBE Severely Errored Seconds parameter specifies the number of STS-1 path/FEBE severely errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and STS-1 tributary(s). An STS-1 path severely errored second is recorded for each second during which there are nine or more B3 coding violations, one or more STS-1 path AIS defects, or one or more STS-1 path loss-of-pointer defects. A FEBE severely errored second is recorded for each second during which there are nine or more FEBE coding violations or one or more RDI-P defects. For intervals with 10 or more contiguous severely errored seconds, the severely errored seconds will be recorded as unavailable seconds, not as severely errored seconds. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.
 - The Path/FEBE Unavailable Seconds parameter specifies the number of STS-1 path/FEBE unavailable seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and STS-1 tributary(s). An STS-1 path/FEBE unavailable second is

recorded after 10 consecutive STS-1 path/FEBE severely errored seconds. These 10 seconds are counted, and each subsequent second is counted until there are 10 consecutive seconds without an STS-1 path/FEBE severely errored second. The ten consecutive seconds without an STS-1 path/FEBE severely errored second are not counted as path/FEBE unavailable seconds. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.

- **PERFORMANCE-Set-PM Threshold-T3** (TL1 = **SET-TH**): This input provisions values for the threshold crossing alert parameters applied to the DS3 line coding violations (DS3 B3ZS coding violations or bipolar violations) and to the DS3 path coding violations data collected for each DS3 port. DS3 path coding violations are based on P-bit errors. The DS3 path coding violations are based on P-bits errors or F and M framing bit errors (provisionable). The F and M framing bit error counts are adjusted to approximate the actual error count in the DS3 path payload.
 - The TCA Autonomous Report parameter specifies whether autonomous reporting of threshold crossing alerts (TCAs) via TBOS and TL1 is "Enable" or "Disable" for the selected performance-monitoring bin and DS3 port(s).
 - The Line Errored Seconds parameter specifies the number of DS3 line errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and DS3 port(s). A DS3 line errored second is recorded for each second during which there are one or more B3ZS coding violations (bipolar violations), or a DS3 loss-of-signal defect in the DS3 signal input from the cross-connect panel. This parameter is an integer from 1 to 900 (inclusive) for the quarter-hour bin and an integer from 1 to 65,535 (inclusive) for the day bin.
 - The Path Coding Violations parameter specifies the number of DS3 path coding violations that will cause a threshold crossing alert for the selected performance-monitoring bin and DS3 port(s). This parameter is the base 10 logarithm of the desired error rate. The values are integers from -10 to -7 (inclusive) for the quarter-hour bin and integers from -12 to -7 (inclusive) for the day bin.
 - The Path Errored Seconds parameter specifies the number of DS3 path errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and DS3 port(s). A DS3 path errored second is recorded for each second during which there are one or more DS3 coding violations, one or more DS3 path AIS Defects, or one or more DS3 path loss-of-frame defects detected in the DS3 signal received from the OC-48 line. This parameter is an integer from 1 to 900 (inclusive) for the quarter-hour bin and an integer from 1 to 65,535 (inclusive) for the day bin.
 - The Path Severely Errored Seconds parameter specifies the number of DS3 path severely errored seconds that will cause a threshold crossing alert for the selected performance-monitoring bin

and DS3 port(s). A DS3 path severely errored second is recorded for each second during which there are 44 or more DS3 coding violations, one or more DS3 path AIS defects, or one or more DS3 path loss-of-frame defects in the DS3 signal received from the OC-48 line. For intervals with 10 or more contiguous severely errored seconds, the severely errored seconds will be recorded as unavailable seconds, not as severely errored seconds. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.

- The Path Severely Errored Frame Seconds/AIS Seconds parameter specifies the number of DS3 path severely errored frame seconds/AIS seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and DS3 port(s). A DS3 path severely errored second/AIS second is recorded for each second during which there is an out-of-frame (OOF) or an AIS defect detected in the DS3 signal received from the OC-48 line. This parameter is an integer from 1 to 900 (inclusive) for the quarter-hour bin and an integer from 1 to 65,535 (inclusive) for the day bin.
 - The Path Unavailable Seconds parameter specifies the number of DS3 path unavailable seconds that will cause a threshold crossing alert for the selected performance-monitoring bin and DS3 port(s). A DS3 path unavailable second is recorded after 10 consecutive DS3 path severely errored seconds. These 10 seconds are counted, and each subsequent second is counted until there are 10 consecutive seconds without a DS3 path severely errored second. The 10 consecutive seconds without a DS3 path severely errored second are not counted as unavailable seconds. This parameter is an integer from 1 to 63 (inclusive) for the quarter-hour bin and an integer from 1 to 4095 (inclusive) for the day bin.
- **SECURITY-Set-Login-Change** (TL1 = **ED-USER-SECU**): This input edits the user ID, password, user privileges, and status of any nonprivileged login, and changes the other privileged login's login ID and password. This input is also used to re-enable a nonprivileged login ID that has expired due to login aging.
 - The Login parameter is the login ID of the login to be changed. (The **Retrieve-Login** input will list the login IDs.)
 - The New Login parameter replaces the login ID for the selected login. It must be a string of from 1 to 10 alphanumeric characters, in which the difference between upper and lower case is significant, and it must be different from all other login IDs in this network element. If ENTER is pressed, the system will default to the current login ID.
 - The New Password parameter becomes the new password for the selected login ID. A password consists of a string of alphabetic, numeric, or symbolic characters conforming to the following rules:
 - String must contain from 6 to 10 characters

- At least 2 of the characters must be nonalphabetic
- At least 1 of the characters must be symbolic (The \, @, #, ?, \$, or space not allowed) (the comma, :, ;, or _ not allowed)
- The difference between upper and lower case is significant.

If ENTER is pressed, the system will default to the current password.

- The Re-enter Password parameter confirms the New Password parameter. Reenter the exact characters that were entered for the New Password parameter.
- The User Privilege parameter sets the user's security level for nonprivileged users to one of the following:
 - "Reports Only": User will be able to execute only the **Retrieve**, **Set-Password**, **Operate-Alarm Cutoff**, and **Test-LED** inputs
 - "General": User will be able to execute all commands, except **Initialize-System**, **Set-Far End Communications**, **Set-Login**, **Set-Network Element**, and **Set-Security**.

The user privilege parameter for a privileged user can only be set to Privileged.

- The Visitor parameter specifies whether the login will be changed to a visitor login (Yes or No). A visitor login is a temporary login.
- The Status parameter enables or disables the login. The status parameter for a privileged user can only be set to Enabled.
- **SECURITY-Set-Login-Delete** (TL1 = **DLT-USER-SECU**): This input deletes a nonprivileged login. If the login is active at the time this command is executed, the user is immediately logged off the system.
 - The Login parameter selects the login that is to be deleted. (The **Retrieve-Login** input will list login IDs.)

- **SECURITY-Set-Login-Enter** (TL1 = **ENT-USER-SECU**): This input enters a new login, including password and user privileges. A privileged user can create up to 98 nonprivileged logins. There are 2 privileged user IDs allowed per network element.
 - The New Login parameter is the login ID for the login being created. It must be a string of from 1 to 10 alphanumeric characters, in which the difference between upper and lower case is significant, and it must be different from all other login IDs in this network element.
 - The New Password parameter is the password for the login being created. A password consists of a string of alphabetic, numeric, or symbolic characters conforming to the following rules:
 - String must contain from 6 to 10 characters
 - At least 2 of the characters must be nonalphabetic
 - At least 1 of the characters must be symbolic (the \, @, #, ?, \$, or space not allowed)
 - The difference between upper and lower case is significant.
 - The Re-enter Password parameter confirms the New Password parameter. Reenter the exact characters that were entered for the New Password parameter.
 - The User Privilege parameter sets the user's security level to one of the following:
 - "Reports Only": User will be able to execute only the **Retrieve**, **Set-Password**, **Operate-Alarm Cutoff**, and **Test-LED** inputs
 - "General": User will be able to execute all commands, except **Initialize-System**, **Set-Far End Communications**, **Set-Login**, **Set-Network Element**, and **Set-Security**.
 - The Visitor parameter specifies if the new login will be a visitor login (Yes or No). A visitor login is a temporary login.

- **SECURITY-Set-Network Element** (TL1 = **ENT-SYS**): This input sets the TID (Target IDentifier), NID (Node IDentifier), Directory Server, X.25 packet size, alarm group, alarm gateway network element (AGNE) and TL1 gateway parameters.



CAUTION:

*In Release 7.1 and later generics, a caution should be exercised so as to NOT execute a second **'Set-Network Element'** command until any preceding **'Set-Network Element'** command has completed execution. This precaution is necessary so as to prevent generating the "ECMD_SYS_ERROR" should one of the controller boards be in an initialization state. Refer to Table B in DLP-527 for a listing of the initialization times (in minutes) for each controller.*

- The TID (Target IDentifier) parameter is the name used to identify the local network element within a SONET network. It is used on all CIT and TL1 reports, and is the destination address for all remote logins. It is a character string of up to 20 characters, where the characters are letters, digits, or hyphens (-). Lowercase letters will be translated into uppercase before they are stored in the network element. Each TID in the network must be unique.

Whenever the value of the TID parameter is changed, the user should enter the **Retrieve-Map-Ring** input or log off and log back in again to ensure that the new value of the TID parameter is included in the ring map.



NOTE:

If a user logs in to the system while a reset in progress or ring startup in progress condition is active, wait until the condition clears and enter the **Retrieve-Map-Ring** input or log off and log back in again to ensure that the list of TIDs in the ring map is complete.



NOTE:

Changing the value of the local TID will result in all *incoming* remote TL1 sessions being disconnected. This means the local network element will terminate all login sessions that were requested from TL1 gateway network elements.

- The NID (Node IDentifier) parameter (CIT provisionable in Releases 4 and 5 only) is the number (integer from 0 to 15) of the network element within the ring. Each network element in the ring must be numbered uniquely.
- The Directory Server parameter specifies ("Yes" or "No") whether the network element is the directory services network element (DS-NE). There must be one and only one DS-NE in the ring. The

DS-NE is the single reference point in a ring network. The DS-NE stores the IDs of the other network elements in the ring and updates the other network elements after configuration changes. In Release 6, one FT-2000 OC-48 Add/Drop-Rings Terminal can act as a DS-NE for a SONET subnetwork of up to 24 network elements. In Release 7, one FT-2000 OC-48 Add/Drop-Rings Terminal can act as a DS-NE for a SONET subnetwork of up to 32 network elements.



CAUTION:

Changing the value of the directory server parameter may affect the operation of other network elements in the ring.

- The X.25 Packet Size parameter (Release 6 and later) specifies the X.25 packet size to be used for a TL1 session to the local network element.



CAUTION:

Changing the value of the X.25 packet size parameter will cause the X.25 link to be reset and all active TL1 logins will be automatically dropped. All established SVCs and PVCs on the affected X.25 link will have to be reestablished.



CAUTION:

Changing the value of the X.25 packet size parameter and the directory server parameter will cause the X.25 link to be reset and all active TL1 logins will be automatically dropped. All established SVCs and PVCs on the affected X.25 link will have to be reestablished. Also, the operation of other network elements in the ring may be affected.

- The Alarm Group parameter (Release 6 and later) specifies the alarm group to which the local network element belongs. An alarm group is a set of up to 32 network elements that share remote alarm information. When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 of the DDM-2000 OC-3 Multiplexer, the recommended maximum alarm group size is 24 network elements. When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 9 of the DDM-2000 OC-3 Multiplexer, the recommended maximum alarm group size is 24 network elements. When Release 7 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 or Release 9 of the DDM-2000 OC-3 Multiplexer, the recommended maximum alarm group size is 32 network elements.

The Alarm Group parameter may have a value of 0 to 255. A value of 0 indicates that the local network element does not exchange remote status information with other network elements. All other

values (1 to 255) indicate that the local network element exchanges remote status information with all other network elements that have the same value for the alarm group parameter.

- The Alarm Gateway Network Element (AGNE) parameter (Release 6 and later) specifies whether the local element will serve the other network elements in the alarm group as the AGNE. More than one AGNE may be provisioned for a SONET subnetwork.
- When Release 6 of the FT-2000 OC-48 ADR Terminal interworks with Release 7.2 of the DDM-2000 OC-3 Multiplexer in subnetworks with less than 16 network elements, the same network element may serve as the AGNE and the TL1 gateway network element.



CAUTION:

In subnetworks with more than 16 NEs, the NE serving as the AGNE CANNOT serve as the TL1 gateway NE, nor can it serve as the DS-NE. The AGNE, TL1 gateway NE, as well as the DS-NE functions must be distributed (i.e., at three separate NEs in the subnetwork).

- The TL1 Gateway parameter (CIT provisionable in Release 4.1 only) specifies ("Yes" or "No") whether the NE is a TL1 gateway network element (GNE). There can be one or more TL1 GNEs in the ring. The TL1 GNE serves as a single interface to the local X.25 message-based operations system for all the NEs in the ring. In Release 6 and later releases, the FT-2000 OC-48 ADR Terminal can also act as a TL1 GNE for other network elements in the SONET subnetwork (for example, a DDM-2000 Multiplexer) via the OC-3 DCC.

When Release 6 of the FT-2000 OC-48 ADR Terminal interworks with the DDM-2000 OC-3 Multiplexer, the recommended maximum subnetwork size is 24 NEs. When Release 7 of the FT-2000 OC-48 ADR Terminal interworks with the DDM-2000 OC-3 Multiplexer, the recommended maximum subnetwork size is 32 NEs.



CAUTION:

In subnetworks with more than 16 NEs, the NE serving as the TL1 gateway NE CANNOT serve as the AGNE. Neither can it serve as the DS-NE. The AGNE, TL1 gateway NE, as well as the DS-NE functions must be distributed (i.e., at three separate NEs in the subnetwork).

- **SECURITY-Set -Password (TL1 = ED-PID):** This input changes your password.
 - The Old Password parameter must match the user's current password.

- The New Password parameter replaces the old password. The New Password must be different from the old password. A password consists of a string of alphabetic, numeric, or symbolic characters conforming to the following rules:
 - String must contain from 6 to 10 characters
 - At least 2 of the characters must be nonalphabetic
 - At least 1 of the characters must be symbolic (the \, @, #, ?, \$, or space not allowed)
 - The difference between upper and lower case is significant.
- The Re-enter Password parameter confirms the New Password parameter. Reenter the exact characters that were entered for the New Password parameter.
- **SECURITY-Set-Security-Network Element:** This input sets the global security parameter for the system.
 - The Logins Allowed parameter specifies whether nonprivileged users are allowed to log in to the system. When Logins Allowed is set to "No," all nonprivileged users will be immediately logged off.
 - The Password Aging parameter specifies the password aging interval. The password aging interval must be zero (0) or a number between 7 and 999 days. If zero (0) is specified, the password aging mechanism is disabled.
 - The Login Aging parameter specifies the login aging interval. The login aging interval must be zero (0) or a number between 7 and 999 days. If zero (0) is specified, the login aging mechanism is disabled.
- **SECURITY-Set-Security-Port:** This input enables or disables user access via the CIT-DCC and CIT-DTE. This input enables a privileged user to condition a CIT/TL1 port.
 - The Type parameter chooses the Operations Interface(s) to enable or disable. The choices are:
 - "DCE": access via the user panel
 - "DTE": dial-up access
 - "DCC": remote login
 - "X.25": for TL1 only.
 - The Channel Type parameter selects the switched type or the permanent type virtual circuit(s) of X.25 for setting the parameters.
 - The Table Entry Qualifier parameter selects the circuit(s) of the X.25 port for setting the parameters. (PVC #1, PVC #2, SVC #1-#6)
 - The OS Type parameter selects the autonomous message set to be reported on the selected X.25 virtual circuit. Values are from the following:

- "maint": all autonomous messages but REPT DBCHG
- "memry-admin": all REPT DBCHG autonomous messages
- "test": no autonomous messages
- "cmd-resp": no autonomous messages
- "other": all autonomous messages
- "none": if no calling address is specified for SVC
- The Calling Address parameter specifies a 15-digit dial string for the X.25 switched virtual circuits (SVC #1-#6).
- The Port Access parameter enables or disables access to the selected port(s).
- The Inactivity Timeout parameter sets the period of minutes during which no activity results in automatic logout from a port. This parameter must be between zero (0) and 999 minutes. If zero (0) is specified, the inactivity timeout mechanism is disabled. The Inactivity Timeout parameter also applies to any port provisioned for TL1 input/output.
- The Port (Protocol) Type parameter sets the communications protocol used at the CIT (DCE) or CIT (DTE) port. If "CIT" is selected, the F-interface protocol is used to communicate with a PC running the FT-2000 CIT-PC software. A PC running CPro-2000 software or an ASCII-based (dumb) terminal cannot communicate with a port provisioned as "CIT." If "TL1" is selected, TL1 protocol is used to communicate with a PC running CPro-2000 software or an ASCII-based (dumb) terminal. A PC running the FT-2000 CIT-PC software cannot communicate with a port provisioned as "TL1."



NOTE:

When logging in to a CIT (DCE) or CIT (DTE) port that is provisioned as "TL1," enter **<Ctrl-Q><cr>** before entering the initial **ACT-USER** TL1 command. The **<Ctrl-Q>** ensures that the port is not in the X/off state and the **<cr>** clears the input buffer.

- The Baud Rate parameter selects the baud rate used for the CIT (DCE) or CIT (DTE) port that is configured for TL1 input/output. The values are "1200, 2400, 4800, or 9600."

For more information about the CIT-PC inputs, refer to Volume I, Section 11, "Craft Interface Terminal Usage." For more information about the TL1 commands, refer to 824-102-148, *2000 Family of Products Operations Systems Engineering Guide*.

FT-2000 OC-48 Repeater Bay

The following CIT inputs (listed in alphabetical order) are used to provision the FT-2000 OC-48 Repeater Bay. (Refer to Volume I, Section 6, "Operations

Interfaces" or Volume I, Section 10, "Technical Specifications" for information about minimum CIT requirements.)

- **CONFIGURATION-Set-Attribute-Environment:** This input reports the Alarm Level and Description parameters for each selected miscellaneous discrete environmental input point. The Description parameter is used to identify the point in the **Retrieve-History** and **Retrieve-Alarm (and Status)** reports. The Alarm Level parameter provisions the severity of the condition associated with the closing of the selected miscellaneous discrete environmental input point. For repeater applications the only choice is "MN" (minor).
 - The Point parameter selects the miscellaneous discrete environmental input point(s) to include in the report.
 - The Alarm Level parameter provisions the severity of the condition associated with the closing of the selected miscellaneous discrete environmental input point. For repeater applications, the only choice is "MN" (minor).
 - The Description parameter provisions the text string associated with the selected miscellaneous discrete environmental input point. It is a character string of up to 26 characters, where the characters are letters, digits, or spaces. It is used to identify the environmental point in the **Retrieve-Alarm (and Status)** and **Retrieve-History** reports.
- **CONFIGURATION-Set-Date:** This input sets the network element clock date and time and provisions automatic daylight savings time processing.
 - The Date parameter sets the current date in the network element clock. It is entered in the form YY-MM-DD where:
 - YY is the year (00-99)
 - MM is the month (1-12)
 - DD is the day (1-31)
 - The Hours parameter sets the hour (0 to 23) in the network element clock.
 - The Minutes parameter sets the minute (0 to 59) in the network element clock.
 - The Seconds parameter sets the second (0 to 59) in the network element clock.
 - The Standard Time Zone parameter is the 3-letter character string (for example, EST, MST) that is indicated as part of the date and time stamp in command responses when Standard Time is in effect.
 - The Start Daylight Savings Date parameter sets the date when the network element moves the current time forward by 1 hour. It should be entered in the form MM-DD where:
 - MM is the month (1-12)

- DD is the Day (1-31)

If this value is different from the Stop Daylight Savings Date parameter value, then daylight savings time is enabled and the time change will occur at 02:00:00 on the chosen date. If the two values are the same, daylight savings time processing is disabled.

- The Stop Daylight Savings Date parameter sets the date when the network element moves the current time backward by 1 hour. It should be entered in the form MM-DD where:

- MM is the month (1-12)

- DD is the Day (1-31)

If this value is different from the Start Daylight Savings Date parameter value, then daylight savings time is enabled and the time change will occur at 02:00:00 on the chosen date. If the two values are the same, daylight savings time processing is disabled.

- The Daylight Savings Time Zone parameter is the 3-letter character string (for example, EDT, MDT) that is printed as part of the date and time stamp in command responses when Daylight Savings Time is in effect.

- **SECURITY-Set-Login-Change:** This input edits the user ID, password, user privileges and status of any nonprivileged login, and changes the other privileged login's login ID and password.

- The Login parameter is the login ID of the login to be changed. (The **Retrieve-Login** input will list login IDs.)

- The New Login parameter replaces the login ID for the selected login. It must be a string of 6 to 10 characters, in which the difference between upper and lower case is significant, and it must be different from all other login IDs in the network element. If ENTER is pressed, the system will default to the current login ID. (The @, #, ?, or space is not allowed.)

- The New Password parameter becomes the new password for the selected login ID. A password consists of a string of alphabetic, numeric, or symbolic characters conforming to the following rules:

- String must contain from 6 to 10 characters
- At least 2 of the characters must be nonalphabetic
- At least 1 of the characters must be symbolic (the @, #, ?, or space not allowed)
- The difference between upper and lower case is significant.

If ENTER is pressed, the system will default to the current password.

- The Re-enter New Password parameter confirms the New Password. Reenter the exact characters that were entered for the New Password parameter.

- The User Privilege parameter sets the user's security level for nonprivileged users to either:
 - "Reports Only": User will be able to execute only the **Retrieve**, **Set-Password**, and **Test-LED** inputs.
 - "General": User will be able to execute all commands, except **Initialize-System**, **Set-Login**, **Set-Network Element**, and **Set-Security**.
- The Status parameter for a privileged user can only be set to "Enabled."
- **SECURITY-Set-Login-Delete**: This input deletes a nonprivileged login. If the login is active at the time this command is executed, the user is immediately logged off the system.
 - The Login parameter selects the login that is to be deleted. The **Retrieve-Login** input will list the login IDs.
- **SECURITY-Set-Login-Enter**: This input enters a new login, including password and user privileges. A privileged user can create up to 10 nonprivileged logins per network element.

There are 2 privileged user IDs and up to 10 nonprivileged IDs allowed per network element.

- The New Login parameter is the login ID for the login being created. It must be a string of from 1 to 10 alphanumeric characters, in which the difference between upper and lower case is significant, and it must be different from all other login IDs in this network element.
- The New Password parameter is the password for the login being created. A password consists of a string of alphabetic, numeric, or symbolic characters conforming to the following rules:
 - String must contain from 6 to 10 characters
 - At least 2 of the characters must be nonalphabetic
 - At least 1 of the characters must be symbolic (the @, #, ?, or space not allowed)
 - The difference between upper and lower case is significant.
- The Re-enter New Password parameter confirms the New Password. Reenter the exact characters that were entered for the New Password parameter.
- The User Privilege parameter sets the user's security level to one of the following:
 - "Reports Only": User will be able to execute only the **Retrieve**, **Set-Password**, and **Test-LED** inputs.
 - "General": User will be able to execute all commands, except **Initialize-System**, **Set-Login**, **Set-Network Element**, and **Set-Security**.

- **SECURITY-Set-Network Element:** This input sets the Target Identifier (TID) parameter. The TID is a unique identification of the network element within a SONET network.
 - The TID parameter determines the name used to identify the local network element within a SONET network. It is used on all CIT and TL1 reports, and it is the destination address for remote logins. It is a character string of up to 20 characters, where the characters are letters, digits, or hyphens (-). The difference between upper and lower case is significant. Each TID in the network must be unique.
- **SECURITY-Set-Password:** This input changes your password. All users can change their own passwords, including Reports-Only users.
 - The Old Password parameter must match the user's current password.
 - The New Password parameter replaces the current password. The New Password must be different from the current password. A password consists of a string of alphabetic, numeric, or symbolic characters conforming to the following rules:
 - String must contain from 6 to 10 characters
 - At least 2 of the characters must be nonalphabetic
 - At least 1 of the characters must be symbolic (the @, #, ?, or space not allowed)
 - The difference between upper and lower case is significant.
 - The Re-enter New Password parameter confirms the New Password. Reenter the exact characters that were entered for the New Password parameter.
- **SECURITY-Set-Security-Network Element:** This input sets the Logins Allowed parameter. The Logins Allowed parameter specifies whether nonprivileged users are allowed to log in to the network element. When the Logins Allowed parameter is set to "No," all nonprivileged users will be immediately logged off.
 - The Logins Allowed parameter specifies whether nonprivileged users are allowed to log in to the network element. When the Logins Allowed parameter is set to "No," all nonprivileged users will be logged off immediately.
- **SECURITY-Set-Security-Port:** This input enables or disables user access via the CIT-DTE port.
 - The Type parameter selects the port(s) to enable or disable. The choices are:
 - "All"
 - "CIT-DCE": access via the user panel
 - "CIT-DTE": dial-up access.

If "All" is specified for the Type parameter, the Port Access parameter will only be set for the CIT-DTE port, since Port Access may never be disabled for the CIT-DCE.

- The Port Access parameter enables or disables access. Disabling it will terminate any active session on the port, logging out all users including privileged users.

For more information about these inputs, refer to Volume I, Section 11, "Craft Interface Terminal Usage."

Pre-provisioning Circuit Packs

To simplify circuit pack installation, parameters can be provisioned before installing the corresponding circuit pack. All system parameters and values (current and original) are preserved by the FT-2000 OC-48 Lightwave System in the nonvolatile memory of SYSMEM circuit pack. The parameters and values are protected by the nonvolatile memory if a power failure occurs and are retrievable on demand regardless of the means used for provisioning. The parameters are automatically downloaded when the affected circuit pack is installed.

Provisioning on Circuit Pack Replacement

Replacement of a failed circuit pack is simplified by the FT-2000 OC-48 Lightwave System's automatic provisioning of the original circuit pack values. The SYSCTL and SYSMEM circuit packs maintain a provisioning map of the entire system, so when a transmission or timing circuit pack is replaced, the SYSCTL and SYSMEM circuit packs automatically download provisioning parameters and current values to the new circuit pack.

Circuit Pack Slot and Low-Speed Port Provisioning States

The circuit pack slots may be in one of the following states:

- **AUTO (Auto):** AUTO is the original value of the circuit pack slot state parameter. AUTO refers to a circuit pack slot that is available for automatic provisioning. A circuit pack slot makes a transition from the AUTO state to the EQ state when the presence of a circuit pack is detected.
- **EQ (Equipped):** EQ refers to a circuit pack slot that is fully monitored and alarmed. A circuit pack slot makes a transition from the EQ state to the AUTO state when the **CONFIGURATION-Update** input is executed while the absence of a circuit pack is detected.

The low-speed ports (DS3, EC-1, OC3 and OC12) may be in one of the following states:

- **AUTO (Auto):** AUTO is the original value of the low-speed port state parameter. AUTO refers to a low-speed port that is available for automatic provisioning. A low-speed port makes a transition from the AUTO state to the IS state after a good signal is detected. A DS3 low-speed port in the AUTO state can also be provisioned to the NMON state, using the **CONFIGURATION-Set-State-T3** input. An EC-1 low-speed port in the AUTO state can be provisioned to the NMON state, using the **CONFIGURATION-Set-State-EC1** input. An OC3 low-speed port in the AUTO state can be provisioned to the NMON state using the **CONFIGURATION-Set-State-OC3** input. An OC12 low-speed port in the AUTO state can be provisioned to the NMON state using the **CONFIGURATION-Set-State-OC12** input.
- **IS (In Service):** IS refers to a low-speed port that is fully monitored and alarmed. A low-speed port makes a transition from the IS state to the AUTO state when the **CONFIGURATION-Update** input is executed while the absence of a good signal is detected. A low-speed port also makes a transition from the IS state to the AUTO state when the associated circuit pack slot makes an EQ-to-AUTO circuit pack slot state transition. A DS3 low-speed port in the IS state can also be provisioned to the AUTO or NMON state, using the **CONFIGURATION-Set-State-T3** input. An EC-1 low-speed port in the IS state can be provisioned to the AUTO or NMON state, using the **CONFIGURATION-Set-State-EC1** input. An OC3 low-speed port in the IS state can be provisioned to the AUTO or NMON state using the **CONFIGURATION-Set-State-OC3** input. An OC12 low-speed port in the IS state can be provisioned to the AUTO or NMON state using the **CONFIGURATION-Set-State-OC12** input.

- **NMON (Not Monitored):** NMON refers to a low-speed port that is not monitored and will not make a transition to the IS state when a good signal is detected. A DS3 low-speed port in the NMON state can be provisioned to the AUTO state, using the **CONFIGURATION-Set-State-T3** input. An EC-1 low-speed port in the NMON state can be provisioned to the AUTO state using the **CONFIGURATION-Set-State-EC1** input. An OC3 low-speed port in the NMON state can be provisioned to the AUTO state using the **CONFIGURATION-Set-State-OC3** input. An OC12 low-speed port in the NMON state can be provisioned to the AUTO state using the **CONFIGURATION-Set-State-OC12** input.

The electrical low-speed port state can be provisioned separately in the add (from the cross-connect) and drop (from the OC-48 line) directions.

The primary port states of OC3 and OC12 circuit packs can be provisioned in the "from OC3" and "from OC12" add directions, respectively.

The FT-2000 OC-48 Add/Drop-Rings Terminal monitors, reports, and alarms trouble conditions associated with low-speed signals according to the low-speed port state and the circuit pack slot state. The circuit pack slot state must be in the EQ state. Table 8-8 shows how the low-speed port state affects monitoring, reporting, alarming, and performance monitoring of low-speed signals.

Table 8-8. Monitoring, Reporting, Alarming, and Performance Monitoring of Low-Speed Signals

Low-Speed Port State	Direction	Cross-Connection	Monitoring, Reporting, Alarming and Performance Monitoring of Low-Speed Signals
AUTO	Add	Yes	No
		No	No
		Yes	No
		No	No
IS	Drop	Yes	Yes
		No	Yes
		Yes	Yes
		No	No
NMON	Add	Yes	No
		No	No
		Yes	No
		No	No

DS3 low-speed ports *in the drop direction* must be in the IS state, and the circuit pack slots must be in the EQ state for these trouble conditions to be reported.

- DS3 path unequipped
- incoming (from OC-48) DS3 AIS
- incoming (from OC-48) DS3 LOF
- inc.(from OC48) path RFI/yellow
- incoming (from OC-48) path AIS
- incoming (from OC-48) STS LOP

EC-1 low-speed ports *in the drop direction* must be in the IS state, and the circuit pack slots must be in the EQ state for the following trouble condition to be reported.

- incoming (from OC-48) STS LOP

Table of Contents

Overview	9-1
Remote Maintenance Philosophy	9-1
Maintenance Signals	9-2
Fault Detection, Isolation, and Reporting	9-8
Protection Switching	9-31
■ High-Speed Protection	9-31
General	9-31
2-Fiber Ring Protection Switching Architecture	9-32
2-Fiber Ring Protection Switching Example	9-35
Single and Double Failure States	9-40
■ Electrical Low Speed Protection	9-43
■ Optical Low Speed Protection — (OC-3)	9-47
General	9-47
Optical Low Speed Protection Switching Architecture	9-49
Optical Low Speed Protection Switching Example	9-50
■ Optical Low Speed Protection — (OC-12)	9-52
General	9-52
Optical Low Speed Protection Switching Architecture	9-54
Optical Low Speed Protection Switching Example	9-55
■ Dual Ring Interworking Protection	9-57
■ Synchronization Protection	9-65
General	9-65
Synchronization Hardware Protection	9-65

Table of Contents

Synchronization Reference Protection	9-68
Synchronization Mode Protection	9-71
Loopbacks	9-72
■ Low Speed (DS3, EC-1, OC-3, and OC-12)	9-72
■ High-Speed (OC-48)	9-73
Tests	9-74
■ Auto Turnup	9-74
■ Operations Interface Tests	9-78
Circuit Provisioning Audits	9-79
Performance Monitoring	9-81
■ SONET Performance Parameters	9-81
■ OC-48 Optical Parameters	9-83
■ OC-3/OC-12 Optical Parameters	9-84
■ STS-48/STS-12/STS-3 Section Parameters	9-85
■ OC-48/OC-12/OC-3 Line Parameters	9-86
■ STS-1 Path Parameters	9-87
■ EC-1 Section/Line Parameters	9-88
■ DS3 Line/Path	9-89
■ Performance-Monitoring Data Storage and Reports	9-90
■ Performance Monitoring During Failed Conditions	9-90
■ Performance-Monitoring Parameter Thresholds	9-91
■ Threshold Crossing Alert Transmission to an OS	9-91
Reports	9-92
■ Active Alarms and Status	9-92
■ Provisioning Reports	9-93
■ Performance Monitoring	9-94
■ State	9-94
■ Version/Equipment List	9-95
■ Synchronization Report	9-95

Overview

This section defines the maintenance philosophy and outlines the various features available to monitor and maintain the FT-2000 OC-48 Lightwave System.

Remote Maintenance Philosophy

The FT-2000 OC-48 Lightwave System allows operation and maintenance of a remote network element in a network from the local central office location. In Release 6 and later releases, the FT-2000 OC-48 Lightwave System also allows operation and maintenance of a remote network element (for example, a DDM-2000 Multiplexer) in a subnetwork from any FT-2000 OC-48 ADR Terminal. The FT-2000 OC-48 Lightwave System uses the synchronous optical network (SONET) OC-48, OC-3 (Release 6 and later), and OC-12 data communications channels (DCC) to provide the following:

- Craft interface terminal (CIT) remote access: The local network element provides a remote login capability from its CIT to a remote network element in the local network. In Release 6 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal also provides a remote login capability to other remote network element (for example, a DDM-2000 Multiplexer) in the same subnetwork.
- User panel remote access: The user panel provides a far-end activity (FE ACTY) LED to show that a remote network element in the local network or alarm group within a subnetwork (Release 6 and later) has a lighted near-end activity (NE ACTY) LED.

- **Telemetry remote access:** The telemetry byte-oriented serial (TBOS) protocol reports remotely as much detailed failure information as necessary to allow repair decisions to be made for networks.

A message-based operations system may also access the FT-2000 OC-48 Add/Drop-Rings Terminal, using the TL1/X.25 interface.

Maintenance Signals

The following maintenance signals are generated and detected by the FT-2000 OC-48 Lightwave System.

- **Alarm indication signals (AIS):** Alarm indication signals notify downstream equipment that a failure has been detected and alarmed by some upstream equipment, if the upstream alarm has not been suppressed. Alarm indication signals include SONET line AIS, STS-1 path AIS, and DS3 AIS.
- **Upstream failure indications signals:** Upstream failure indication signals notify upstream equipment that a downstream failure has been detected. Upstream failure indication signals include line remote defect indicator (RDI-L) and path remote defect indicator (RDI-P) signals.
- **Path unequipped signal:** The path unequipped signal is transmitted to notify downstream equipment that the STS-1 path is incomplete (for example, a low-speed interface slot in the AUTO state or the absence of a valid cross-connection). The DS3 circuit pack is the only low-speed interface circuit pack that terminates the STS-1 path overhead. Thus, the STS1E and OC3 (1.3 STD) circuit packs pass through any path unequipped signals. In Release 4.1, when a DS3 circuit pack detects a path unequipped signal on a DS3 circuit, a DS3 path unequipped condition is reported. In Release 5 and later releases, when a DS3 circuit pack detects a path unequipped signal on a DS3 circuit, a path integrity failure condition is reported.
- **Path trace signal:** The path trace signal is transmitted repetitively to verify the continuity of the STS-1 path. The DS3 circuit pack is the only low-speed interface circuit pack that generates or terminates the STS-1 path overhead. Thus, the STS1E and OC3 (1.3 STD) circuit packs pass through any path trace signals. In Release 4 and later releases, the value of the transmit and receive expected path trace signals is provisionable for DS3 circuits only. The value of the provisionable receive expected path trace signal is compared to the actual received path trace signal to verify STS-1 path continuity. For more information about provisioning path trace, refer to Volume I, Section 8, "Administration and Provisioning." For more information about the path trace report, refer to Volume I, Section 11, "Craft Interface Terminal Usage."

The maintenance signals are SONET, *ANSI*^{*}, and Telcordia compliant.

Figure 9-1 shows the maintenance signals used by the FT-2000 OC-48 Add/Drop-Rings Terminal when interfacing with DS3 low-speed signals. Figure 9-2 shows the maintenance signals used by the FT-2000 OC-48 Add/Drop-Rings Terminal when interfacing with EC-1, OC-3, IS-3, and OC-12 low-speed signals. Figure 9-3 shows the maintenance signals used by the FT-2000 OC-48 Add/Drop-Rings Terminal for through tributaries. Figure 9-4 shows the maintenance signals used by the FT-2000 OC-48 Repeater Bay. In these figures, the labels on the arrows pointing into the FT-2000 OC-48 Lightwave System show the maintenance signals and failure conditions recognized. The labels on the arrows pointing out of the FT-2000 OC-48 Lightwave System show the signals generated in response to the incoming maintenance signals or failure conditions. The Xs show failure points within the FT-2000 OC-48 Lightwave System or in upstream equipment. Unlabeled arrows show normal transmission.

* ANSI is a registered trademark of the American National Standards Institute, Inc.

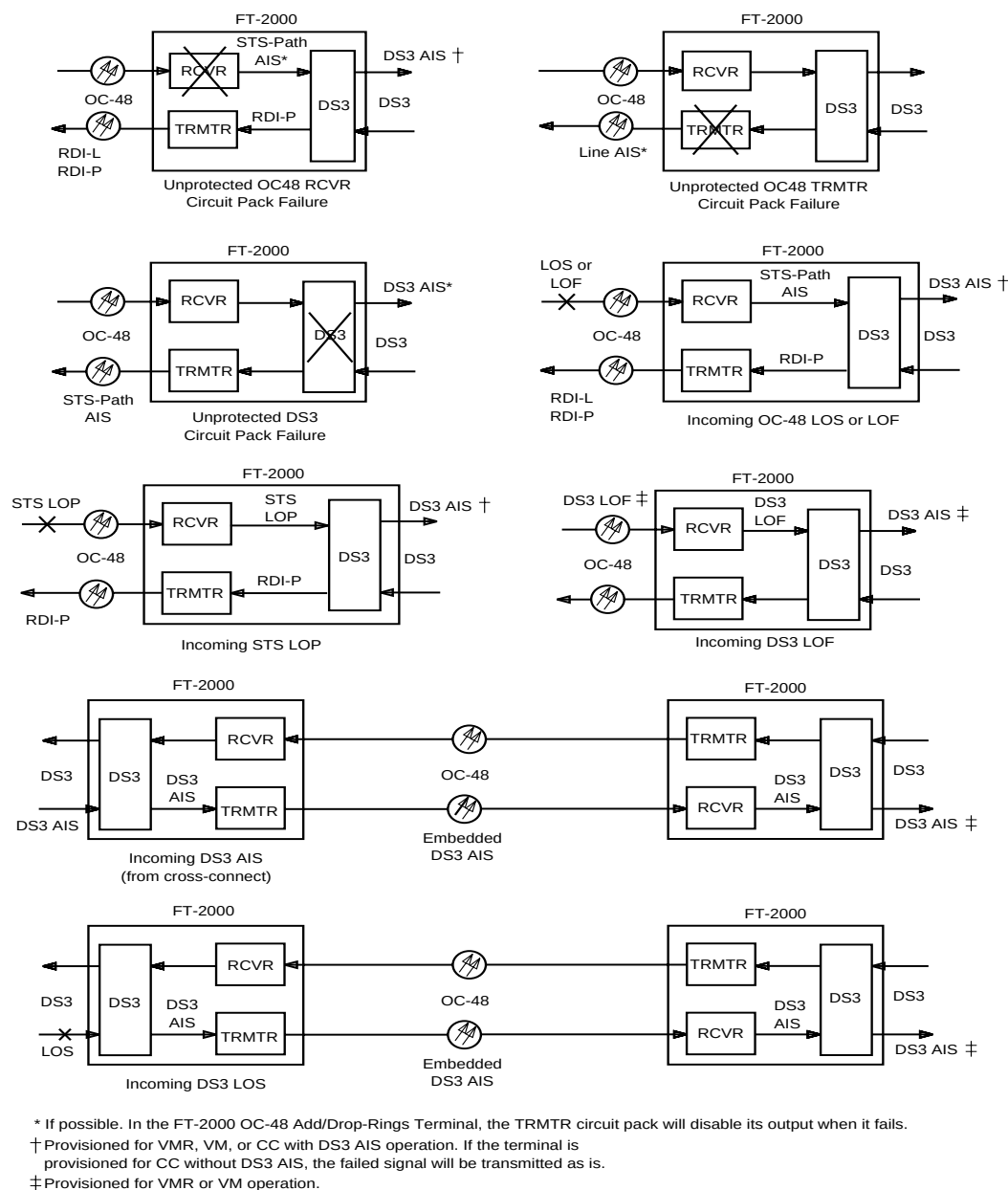


Figure 9-1. FT-2000 OC-48 Add/Drop-Rings Terminal Maintenance Signals (When Interfacing with DS3 Signals)



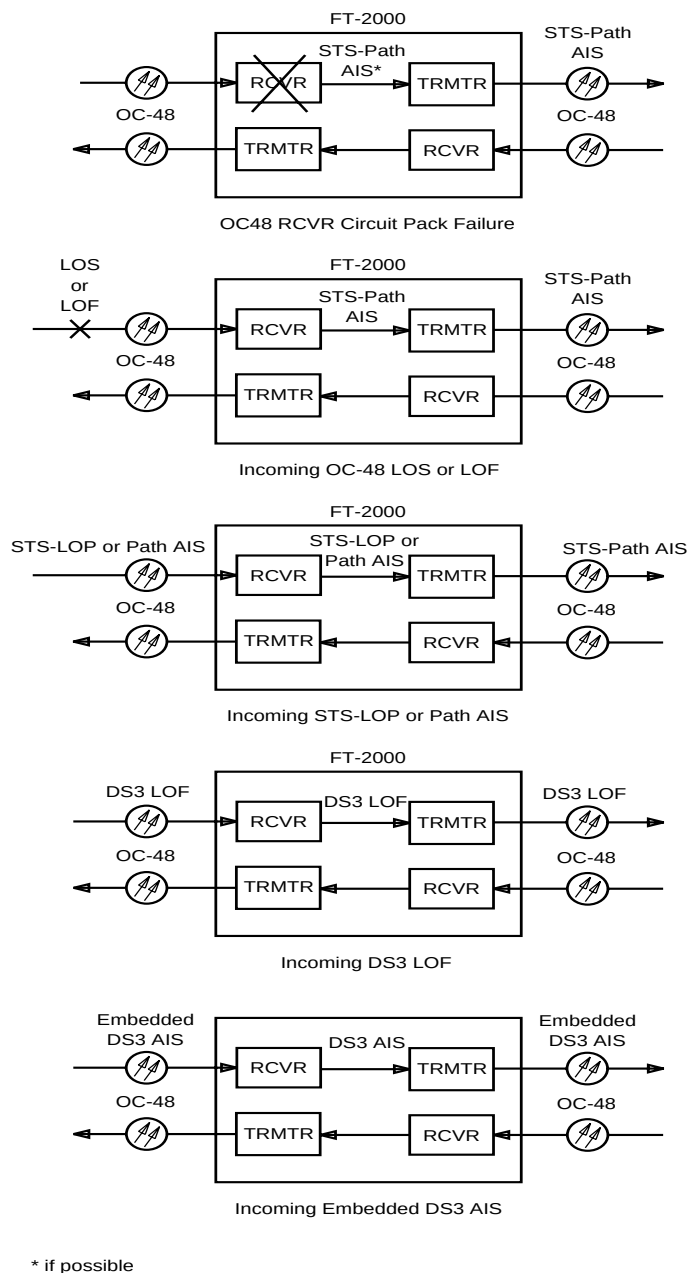


Figure 9-3. FT-2000 OC-48 Add/Drop-Rings Terminal Maintenance Signals for Through Tributaries

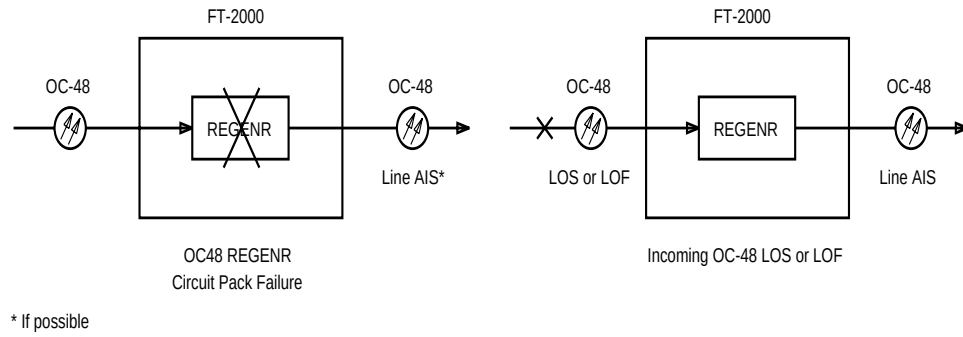


Figure 9-4. FT-2000 OC-48 Repeater Bay Maintenance Signals

Fault Detection, Isolation, and Reporting

The FT-2000 OC-48 Lightwave System continuously monitors its internal condition and incoming signals according to the state of the circuit pack slots and low-speed ports. (For information about circuit pack slot and low-speed port states and transitions, refer to Volume I, Section 8, "Administration and Provisioning.") If a circuit pack slot is in the EQ state, the FT-2000 OC-48 Lightwave System monitors the circuit pack and activates the appropriate indicators when a failure occurs. Table 9-1 shows the incoming signals and the associated conditions the FT-2000 OC-48 Lightwave System monitors.

When a trouble condition is detected, the FT-2000 OC-48 Lightwave System employs automatic diagnostics to isolate the failed circuit pack or signal. Most faults are isolated to a maximum of two circuit packs, but some faults may be isolated to more than two circuit packs. Failures are reported to the local technician and operations systems so that repair decisions can be made. If desired, operations system personnel and the local technician can use the CIT to gain more detailed information on the trouble condition.

All trouble conditions detected and isolated by the FT-2000 OC-48 Lightwave System are stored and made available to be reported, on demand, through the CIT. In addition, a history of the past 125 alarm and status conditions and CIT events is maintained and available for on-demand reporting. In Release 4 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal maintains the past 500 alarm and status conditions and CIT events. The CIT events include condition starting events, condition clearing events, autonomous actions by the system, and user actions. Each event is date and time stamped.



NOTE:

Any reset of the FT-2000 OC-48 Add/Drop-Rings Terminal erases the alarm history file (the past 500 alarm and status conditions and CIT events).

The FT-2000 OC-48 Lightwave System also automatically reports all detected alarm and status conditions and CIT events through indicators that are subject to the provisionable incoming signal alarm delay and through indicators that are *not* subject to incoming signal alarm delay.

Table 9-1. Incoming Signals and Monitored Conditions

Incoming Signal Level	Monitored Conditions
OC-48*	Alarm Indication Signals (AIS) Automatic Protection Switch (APS) Byte Failure APS Default Byte†† B1 Parity Error Invalid F-Byte Line RFI/Yellow Loss of Frame (LOF) Loss of Signal (LOS) Signal Degrade (SD) Signal Fail (SF)
OC-12*†	Alarm Indication Signals (AIS) Automatic Protection Switch (APS) Byte Failure Line RFI/Yellow Loss of Frame (LOF) Loss of Signal (LOS) STS Loss of Pointer (LOP) (from OC-12) Signal Degrade (SD) Signal Fail (SF)
OC-3†‡	Alarm Indication Signals (AIS) Automatic Protection Switch (APS) Byte Failure Line RFI/Yellow Loss of Frame (LOF) Loss of Signal (LOS) Path Integrity Failure STS Loss of Pointer (LOP) (from OC-3) Signal Degrade (SD) Signal Fail (SF)
DS3§	Loss of Signal (LOS) (from DSX) Signal Degrade (SD) (from DSX) Alarm Indication Signals (AIS) (from OC-48) Loss of Frame (LOF) (from OC-48) Path RFI/Yellow (from OC-48) STS Loss of Pointer (LOP) (from OC-48) Path Integrity Failure‡ Path Unequipped

Table 9-1. Incoming Signals and Monitored Conditions (Contd)

Incoming Signal Level	Monitored Conditions
EC-1§¶	Alarm Indication Signals (AIS) (from STSX) Line RFI/Yellow (from STSX) Loss of Frame (LOF) (from STSX) Loss of Pointer (LOP) (from STSX) Loss of Signal (LOS) (from STSX) Path Integrity Failure‡ Signal Degrade (SD) (from STSX) STS Loss of Pointer (LOP) (from OC-48) STS Loss of Pointer (LOP) (from STSX)
DS1**	DS1 Reference Failure

*If the TRMTR, RCVR, or REGENR slots are in the EQ state, the associated incoming OC-48 signals are also monitored and alarmed.

† If the low-speed interface slots are in the EQ state, the incoming low-speed signals are monitored and alarmed.

‡ Available in Release 5 and later releases.

§ If the low-speed interface slots are in the EQ state and the associated low-speed ports are in the IS state, the incoming low-speed signals are monitored and alarmed.

¶ Available in Release 4.1 and later releases.

** If a DS1 input port is in the IS state, the associated incoming DS1 synchronization reference signal is monitored and alarmed when a failure occurs.

†† Available in Release 6 and later releases.

*† Available in Release 7.1.0 and later releases.

The indicators that are subject to incoming signal alarm delay are:

- Audible and visible office alarm outputs
- Parallel telemetry outputs
- Serial telemetry (TBOS) alarm and scan points (Release 4.1 and later)
- TL1 autonomous messages*
- Critical (CR), major (MJ), and minor (MN) LEDs on the user panel
- Status field in the upper left hand corner of the CIT screen.

The indicators that are *not* subject to incoming signal alarm delay are:

- FAULT LEDs on the circuit packs
- All CIT reports

* TL1 autonomous messages for alarm and status conditions are only available on X.25 virtual circuits when the value of the OS Type parameter is provisioned "maintenance" or "other" using the **SECURITY-Set-Security-Port** input.

- Serial telemetry (TBOS) alarm and scan points (Releases 1 through 4.0 only)
- TL1 command response messages
- Abnormal (ABN), near-end activity (NE ACTY), far-end activity (FE ACTY), alarm cutoff/locator (ACO/LOCTR), line locator (LINE LOCTR), USE CIT, and POWER ON LEDs on the user panel.

The incoming signal alarm delay only applies to autonomous indications of the following incoming signal conditions:

- inc.(from DS1) sync ref fail
- inc.(from OC48) path RFI/yellow
- inc.(from OC48) sync ref fail
- inc.(from STSX) line RFI/yellow
- incoming (from DSX) DS3 LOS
- incoming (from DSX) DS3 SD
- incoming (from OC-48) DS3 AIS
- incoming (from OC-48) DS3 LOF
- incoming (from OC-48) path AIS
- incoming (from OC-48) STS LOP
- incoming (from STSX) EC1 LOF
- incoming (from STSX) EC1 LOP
- incoming (from STSX) EC1 LOS
- incoming (from STSX) EC1 SD
- incoming (from STSX) line AIS
- incoming (from STSX) path AIS*
- incoming OC-3 AIS
- incoming (from OC-3) path AIS*
- incoming OC-3 APS byte fail
- incoming OC-3 line RFI/yellow
- incoming OC-3 LOF
- incoming OC-3 LOS
- incoming OC-3 SD
- incoming OC-3 SF
- incoming OC-48 AIS
- incoming OC-48 APS byte fail

- incoming OC-48 APS default byte
- incoming OC-48 B1 parity error
- incoming OC-48 invalid F byte
- incoming OC-48 line RFI/yellow
- incoming OC-48 LOF
- incoming OC-48 LOS
- incoming OC-48 LOS/LOF
- incoming OC-48 SD
- incoming sync ref failure.
- incoming OC-12 APS byte failure
- incoming OC-12 line RFI/Yellow
- incoming OC-12 loss of frame (LOF)
- incoming OC-12 loss of signal (LOS)
- incoming OC-12 loss of pointer (LOP)
- incoming OC-12 DCC failure
- incoming OC-12 interface failure
- incoming OC-12 interface removed
- incoming OC-12 auto switch at far end
- incoming OC-12 auto switch at near end
- incoming OC-12 forced switch at far end
- incoming OC-12 forced switch at near end
- incoming OC-12 inhibit at near end
- incoming OC-12 lockout at far end
- incoming OC-12 lockout at near end
- incoming OC-12 loopback (from OC48)
- incoming OC-12 signal degrade (SD)
- incoming OC-12 signal fail (SF)
- incoming (from OC-12) path AIS

The FT-2000 OC-48 Repeater Bay conveys the following conditions using the F1-bytes in the OC-48 signal to the downstream FT-2000 OC-48 Add/Drop-Rings Terminal.

- incoming OC48 B1 parity error
- incoming OC48 LOS/LOF
- incoming OC48 invalid F byte

- optical parameter out-of-range
- repeater bay environment input
- repeater bay equipment failure.

The downstream FT-2000 OC-48 Add/Drop-Rings Terminal reports these conditions locally (via the CIT interface) and remotely (via X.25/TL1 interface).

Table 9-2 contains a list of alarm and condition descriptions and their likely causes.

**CAUTION:**

This table is for information only and should not be used for trouble clearing. Refer to the Operation and Maintenance (TOP) section (Volume II) for trouble clearing information.

Table 9-2. Condition Descriptions

Description (from Retrieve-Alarm Report)	Meaning
APS channel failure	The automatic protection switching (APS) channel on the protection line has failed. Likely causes: Failure of protection line, far-end TRMTR, near-end RCVR, or far-end LNCTL circuit packs.
APS provisioning conflict	The line protection switching configuration (1xN or 1+1) or directionality (unidirectional or bidirectional) is provisioned differently at the two ends of the line.
circuit audit suspended	The circuit provisioning audits are not functioning.
circuit provisioning error	Intermediate source/destination node TID is incorrect, or the add/drop source/destination node TID is incorrect.
clock out of range	One or both of the TG3 circuit packs supplying synchronization to the TRMTR circuit pack have gone out of the recommended range.
CKP Serial EPROM corrupted	A circuit pack in the indicated slot has corrupt data in its memory.
CP reset in progress	A circuit pack has been installed and that the necessary software is being downloaded to the circuit pack.
CP (unknown type) failure	The circuit pack in the indicated slot has failed but its type is unknown.
CP (unknown type) removed	The circuit pack in the indicated slot has been removed but its type is unknown.
CPYPGM:IP tid	Copy program in progress.
DCC startup in progress	The DCC is starting up.
DRI auto switch	An automatic DRI switch is active.
DRI manual switch	A manual DRI switch is active.
DS3 INTFC failure	A DS3 circuit pack failure.
DS3 INTFC removed	A DS3 circuit pack is missing from a slot.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
DS3 loopback	A DS3 loopback is in effect. DS3 signals coming from the cross-connect panel are preempted by the loopback.
DS3 path unequipped	A path unequipped signal has been detected on an incoming OC-48 line. Likely cause: A cross-connection was deleted in an upstream node.
DS-NE not reachable	One of the network elements in the ring network has not been provisioned "DS-NE", far end communications has been disabled to the "DS-NE", or communication to the provisioned "DS-NE" has been interrupted.
duplicate node ID defined	A recently entered node identifier (NID) value is a duplicate of the NID of another network element in the same OC-48 ring.
duplicate TID defined	A recently entered target identifier (TID) value is a duplicate of the TID of another network element in the same OC-48 ring.
EC1 loopback	An EC-1 loopback is in effect. EC-1 signals coming from the cross-connect panel are preempted by the loopback.
excessive holdover	The network element was either manually or automatically switched to internal synchronization and has remained in that synchronization mode for 24 hours. This excessive time (holdover) may cause degraded performance on the transmitted and/or received signals. Likely causes: Failure of TG3 or RCVR (when looped timed) circuit packs, or failure of an external synchronization reference.
extra traffic preempted	The extra traffic routed over the protection tributaries (9 through 16) has been preempted by a line protection switch.
fan failure	There is a tripped circuit breaker or defective fan assembly.
FT-LBA fan failure	There is a tripped circuit breaker or defective fan assembly at the FT-LBA.
FT-LBA fuse/power failure	The FT-LBA has a blown fuse, a power failure to a shelf, or a power failure within the bay.
FT-LBA 1EW failure	Failure of the 1EW LBA.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
FT-LBA 1WE failure	Failure of the 1WE LBA.
fuse/power failure A	The network element has a blown fuse in feeder A, a power failure to a shelf, or a power failure within the bay. Likely causes: A blown 10A fuse, a defective 1186A filter board in the filter panel, or a fault condition in the SYSCTL circuit pack.
fuse/power failure B	The network element has a blown fuse in feeder B, a power failure to a shelf, or a power failure within the bay. Likely causes: A blown 10A fuse, a defective 1186A filter board in the filter panel, or a fault condition in the SYSCTL circuit pack.
inc. (from DS1) sync ref fail	A failure of the incoming DS1 synchronization reference has been detected.
inc. (from OC3) DCC failure	The local network element cannot communicate through the SONET OC-3 data communications channel (DCC).
inc. (from OC3) path AIS	A Path AIS has been detected on an incoming STS-1 tributary of an OC3 signal. The OC3 port in question is provisioned as a DRI cross-connect (1-Way-DRI, 2-Way-DRI, or 2-Way-DRI-Pri). Likely causes: Failure of upstream equipment, which includes the STS-1 path (for equipment, failure of an OC-3 pack in the upstream OC3 equipment). If the upstream NE is an FT-2000 then a "delete" of a drop cross-connection may have been performed.
inc. (from OC48) DCC failure	The local network element cannot communicate through the SONET OC-48 data communications channel (DCC).
inc (from OC48) path RFI/yellow	An STS-1 path RDI signal has been detected on a DS3 signal coming from the line. Path yellow indicates the far-end terminal is receiving a "path AIS."
inc. (from OC48) sync ref fail	A failure of the incoming line synchronization reference from the incoming OC-48 signal has been detected.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
inc. (from STSX) line RFI/yellow	An incoming line RFI/yellow condition has been detected on an incoming EC-1 line from the STSX-1. Likely causes: Failure of an outgoing EC-1 line (transmit) or local STS1E circuit pack.
incoming DCC failure	The local network element cannot communicate through the SONET data communications channel (DCC). A defective incoming DCC can affect the following: - remote logins - reporting of far-end conditions on TBOS links - TL1 messages. Likely causes: Failure of far-end OHCTL, SYSMEM, or SYSCTL circuit packs; failure of both TG3 circuit packs.
incoming (from DSX) DS3 LOS	Failure of one or more of the DS3 signals coming from the low speed cross-connect panel. A DS3 signal failure causes insertion of DS3 AIS at a remote FT-2000 terminal. Likely cause: Incoming DS3 loss of signal.
incoming (from DSX) DS3 SD	A signal degrade (SD) condition has been detected on one or more of the DS3 signals coming from the low-speed cross-connect panel.
incoming (from OC3/OC12) STS LOP	An STS-1 loss-of-pointer (LOP) condition has been detected on an incoming synchronous transport signal (STS-1) from an OC-3 or OC-12 line. Likely cause: Failure of the upstream TRMTR circuit pack.
incoming OC12 AIS	An OC12 alarm condition signal (AIS) has been detected on one or more of the OC-12 signals coming from an OC-48 line. Likely causes: Failure of an upstream OC-12 source equipment or OC12 circuit pack.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
incoming (from OC12) path AIS	A Path AIS has been detected on an incoming STS-1 tributary of an OC12 signal. The OC12 port in question is provisioned as a DRI cross-connect (1-Way-DRI, 2-Way-DRI, or 2-Way-DRI-Pri). Likely causes: Failure of upstream equipment, which includes the STS-1 path (for example, failure of an OC-12 pack in the upstream OC12 equipment). If the upstream NE is an FT-2000 then a "delete" of a drop cross-connection may have been performed.
incoming (from OC-48) DS3 AIS	A DS3 alarm indication signal (AIS) has been detected on one or more of the DS3 signals coming from an OC-48 line. Likely causes: Failure of an upstream DS3 source equipment.
incoming (from OC-48) DS3 LOF	A DS3 loss of frame (LOF) condition has been detected on an incoming DS3 signal from the OC-48 line. Likely causes: Failure of an upstream DS3 source equipment or DS3 circuit pack.
incoming (from OC48) path AIS	An STS-1 path alarm indication signal (AIS) has been detected on one or more of the STS-1 tributaries of the OC-48 line. Likely cause: Failure of far-end DS3 or STS1E circuit pack.
incoming (from OC48) STS LOP	An STS-1 loss-of-pointer (LOP) condition has been detected on an incoming synchronous transport signal (STS-1) from an OC-48 line. Likely cause: Failure of the upstream TRMTR circuit pack.
incoming (from STSX1) EC1 LOF	An EC-1 loss-of-frame (LOF) condition has been detected on an incoming EC-1 line from the STSX-1.
incoming (from STSX1) EC1 LOP	An EC-1 loss-of-pointer (LOP) condition has been detected on an incoming EC-1 line from the STSX-1.
incoming(from STSX1) EC1 LOS	An EC-1 loss-of-signal (LOS) condition has been detected on an incoming EC-1 line from the STSX-1.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
incoming(from STSX1) EC1 SD	A signal degrade (SD) condition has been detected on an incoming EC-1 line.
incoming(from STSX1) line AIS	An EC-1 alarm indication signal (AIS) has been detected on an incoming EC-1 line.
incoming (from STSX1) path AIS	A Path AIS has been detected on an incoming STS-1 tributary of an OC3 signal. The OC3 port in question is provisioned as a DRI cross-connect (1-Way-DRI, 2-Way-DRI, Pri). Likely causes: Failure of an upstream EC-1 equipment.
incoming OC3/OC12 AIS	An STS-1 path alarm indication signal (AIS) has been detected on one or more of the STS-1 signals within an STS-3/STS-12 signal coming from an OC-48 line. Likely cause: Failure of an upstream REGENR or TRMTR circuit pack.
incoming OC3/OC12 APS byte fail	This condition is associated with a signal degrade (SD) or an automatic protection switch (APS) channel failure on the protection line.
incoming OC3/OC12 line RFI/yellow	The far end OC-3/OC-12 terminal is receiving a line failure from the local FT-2000 terminal. The far end terminal is notifying the local FT-2000 terminal of the failure. Likely causes: Failure of an outgoing OC-3/OC-12 line (transmit fiber). Failure of local OC3/OC-12 circuit pack or far end OC3/OC-12 circuit pack.
incoming OC3/OC12 LOF	An OC-3/OC-12 loss-of-frame (LOF) condition has been detected on an incoming OC-3/OC-12 line. Likely cause: Failure of the far end OC3/OC12 circuit pack or local OC3/OC12 circuit pack.
incoming OC3/OC12 LOS	An OC-3/OC-12 loss-of-signal (LOS) condition has been detected on an incoming OC-3/OC-12 line. Likely causes: Failure of the far end OC3/OC12 circuit pack, the incoming OC-3/OC-12 line (receive fiber) or the local OC3/OC12 circuit pack.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
incoming OC3/OC12 SD	A signal degrade (SD) condition has been detected on an incoming OC-3/OC-12 line. The line parity error rate threshold setting has been exceeded (10^{-5} through 10^{-9} with an original value of 10^{-6}). Likely causes: Failure of the far end OC3/OC12 circuit pack or the local OC3/OC12 circuit pack.
incoming OC3/OC12 SF	A signal failure (SF) condition has been detected on an incoming OC3/OC12 line. Likely causes: Failure of the far end OC3/OC12 circuit pack, the incoming OC-3/OC-12 line (receive fiber), or the local OC3/OC12 circuit pack.
OC12 mode conflict (equipment)	The alarm condition that occurs when the addressed slot or slot pair is provisioned for 1+1 mode and one or both slots are not in the "equipped" state.
OC12 mode conflict (quad pair)	The alarm condition that occurs when the addressed slot or slot pair is provisioned for 1+1 mode and the other slot is provisioned for 0x1 mode (in the same direction).
OC12 mode conflict (x-connect)	The alarm condition that occurs when the addressed slot, of the slot pair, is provisioned for 1+1 mode and the two slots are cross-connected to different tributaries.
Inconsistent DCC Values	The OC-3 or OC-12 DCC orientation (user or network) is the same at both ends of the DCC, but should be different.
OC12 INTFC failure	An OC12 circuit pack failure.
OC12 INTFC removed	An OC12 circuit pack is missing from a slot.
OC12 auto switch at far-end	An automatic switch has occurred at the far-end.
OC12 auto switch at near-end	An automatic switch has occurred at the near-end.
OC12 forced switch at the far-end	A forced switch has been performed on the far-end.
OC12 forced switch at the near-end	A forced switch has been performed at the near-end.
OC12 inhibit at near-end	Line protection switching is disabled at the near end of the OC-12 line.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
OC12 lockout at far-end	The protection line is locked out and will not be used to protect a service line failure at the near end of the OC-12 line.
OC12 lockout at near-end	The protection line is locked out and will not be used to protect a service line failure at the near end of the OC-12 line.
OC12 loopback (from OC48)	The STS-12/12c signal from the OC-48 line is looped back to the OC-48 line.
incoming OC-48 AIS	An OC-48 alarm indication signal (AIS) has been detected on an incoming OC-48 line. Likely cause: Failure of an upstream REGENR or TRMTR circuit pack.
incoming OC-48 APS byte fail	This condition is associated with a signal degrade (SD) or an automatic protection switch (APS) channel failure on the protection line.
incoming OC-48 APS default byte	This condition is normally a transient condition caused by a "Reset in Progress" condition at an upstream OC-48 source.
incoming OC48 B1 parity error	A parity error has been detected in the B1 SONET overhead byte at a repeater bay site. Likely cause: Failure of the upstream TRMTR or REGENR circuit pack, or the local RCVR or REGENR.
incoming OC-48 invalid F byte	An invalid SONET overhead F byte has been detected at a repeater bay site. Likely cause: Failure of the upstream TRMTR or REGENR circuit pack, or the local RCVR or REGENR.
LCLAN failure	This condition indicates that a failure has been detected in the Line Controller (LNCTL) or in the System Controller (SYSCTL).
incoming OC-48 line RFI/yellow	The remote FT-2000 terminal is receiving a line failure from the local FT-2000 terminal. The remote FT-2000 terminal is notifying the local FT-2000 terminal of the failure. Likely causes: Failure of an outgoing OC-48 line (transmit fiber). Failure of local TRMTR circuit pack, remote RCVR circuit pack, or intervening REGENR circuit packs.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
incoming OC-48 LOF	An OC-48 loss-of-frame (LOF) condition has been detected on an incoming OC-48 line. Likely cause: Failure of an upstream TRMTR or REGENR circuit pack or local RCVR.
incoming OC-48 LOS	An OC-48 loss-of-signal (LOS) condition has been detected on an incoming OC-48 line. Likely causes: Failure of an upstream TRMTR or REGENR circuit pack, the incoming OC-48 line (receive fiber), or the local RCVR circuit pack.
incoming OC48 LOS/LOF	An OC-48 loss-of-signal (LOS) and/or loss-of-frame (LOF) condition has been detected on an incoming OC-48 line. Likely causes: Failure of an upstream TRMTR or REGENR circuit pack, the incoming OC-48 line (receive fiber), or the local RCVR circuit pack.
incoming OC-48 SD	A signal degrade (SD) condition has been detected on an incoming OC-48 line. The line parity error rate threshold setting has been exceeded (10^{-5} through 10^{-9} with an original value of 10^{-6}). Likely causes: Failure of the upstream TRMTR or REGENR circuit pack, or the local RCVR circuit pack.
incoming OC-48 SF	A signal failure (SF) condition has been detected on an incoming OC-48 line. Likely causes: Failure of the upstream TRMTR or REGENR circuit pack, or the local RCVR circuit pack.
incoming sync ref failure	A failure of the incoming synchronization reference has been detected. Likely causes: Failure of a synchronization reference (signal source).
inhibit alarms-office alarms	Alarm reporting via the office alarms interface is disabled.
inhibit alarms-parallel telemetry	Alarm reporting via the parallel telemetry interface is disabled.
inhibit alarms-X.25	Alarm reporting via the X.25 (TL1) interface is disabled.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
laser bias current-out-of-range	This condition indicates that one or more of the optical parameters of a RCVR, TRMTR, or REGENR circuit pack has exceeded the recommended range.
line auto switch	An automatic line protection switch is active. Likely causes: Failure of incoming OC-48 line.
line FERF	The remote FT-2000 terminal is receiving a line failure from the local FT-2000 terminal. The remote FT-2000 terminal is notifying the local FT-2000 terminal of the failure. Likely causes: Failure of an outgoing OC-48 line (transmit fiber). Failure of local TRMTR circuit pack, remote RCVR circuit pack, or intervening REGENR circuit packs.
line forced switch	An automatic line protection switch is active with a priority of "forced switch."
line forced switch at far end	An automatic line protection switch is active with a priority of "forced switch" at the far end of the OC-48 line.
line forced switch at near end	An automatic line protection switch is active with a priority of "forced switch" at the near end of the OC-48 line.
line lockout	The protection line is locked out and will not be used to protect a service line failure.
line lockout at far end	The protection line is locked out and will not be used to protect a service line failure at the far end of the OC-48 line.  NOTE: If either one of the two OC48 lines are "locked-out" from any node on the ring; then, protection is inhibited throughout the whole ring.
line lockout at near end	The protection line is locked out and will not be used to protect a service line failure. Because of the architecture of OC48 Add/Drop rings, this implies that protection is unavailable throughout the ring.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
	 NOTE: If either one of the two OC48 lines are "locked-out" from any node on the ring; then, protection is inhibited throughout the whole ring.
line manual switch	An automatic line protection switch is active with a priority of "manual switch."
line manual switch at far end	An automatic line protection switch is active with a priority of "manual switch" at the far end of the OC-48 line.
line manual switch at near end	An automatic line protection switch is active with a priority of "manual switch" at the near end of the OC-48 line.
line switch inhibit	High speed line switching is disabled.
line switch inhibit - far end	High speed line switching is disabled at the far end of the OC-48 line.
line switch inhibit - near end	High speed line switching is disabled at the near end of the OC-48 line.
line TCA	A threshold crossing alert (TCA) has been issued. The alert means the signal degrade threshold has been exceeded for an incoming OC-48 line.
LNCTL failure	Failure of LNCTL circuit pack.
LNCTL removed	LNCTL circuit pack is missing from slot.
logins inhibited	Logins are disabled.
LS auto switch	A low-speed interface circuit pack has been automatically switched to protection. Likely cause: Failure of the DS3 or STS1E circuit pack.
LS forced switch	A low-speed slot was switched to the protection slot with a priority of "forced switch."
LS lockout	A low-speed slot is locked out and will not be switched.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
LS loopback	A low-speed receive signal from a high-speed line is electronically looped back to the transmit signal for the high-speed line. Signals coming from the low-speed cross-connect panel are preempted by the loopback. Likely cause: Operate-Loopback has been executed.
LS manual switch	A low-speed slot was switched to protection with a priority of "manual switch."
LSSW failure	Failure of LSSW circuit pack.
LSSW removed	LSSW circuit pack is missing from slot.
multiple DS-NEs defined	Two or more network elements in a ring network have their "Directory Server" (DS-NE) parameter provisioned to "Yes." There must be one and only one DS-NE in the ring network.
NE status comm failure	The NE is not associated with an AGNE or the AGNE cannot establish associations with all of the NEs in the alarm group. If the AGNE loses power, all NEs in the alarm group will report this condition; however, the condition may not be reported for several minutes.
node ID conflict	This normally indicates a transient condition at the local network element.
non-preemptible protection access	The Low Speed Shelf - Complementary is connected to the protection line during the switching function and cannot be preempted by an automatic switch to protection.
OC3 auto switch at far end	An automatic line protection switch is active at the far end of the OC-3 line.
OC3 auto switch at near end	An automatic line protection switch is active at the near end of the OC-3 line.
OC3 INTFC failure	Failure of OC3 circuit pack.
OC3 INTFC removed	OC3 circuit pack is missing from slot.
OC3 forced switch at far end	An automatic line protection switch is active with the priority of "forced switch" at the far end of the OC-3 line.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
OC3 forced switch at near end	An automatic line protection switch is active with the priority of "forced switch" at the near end of the OC-3 line.
OC3 inhibit at near end	Line protection switching is disabled at the near end of the OC-3 line.
OC3 lockout at far end	The protection line is locked out and will not be used to protect a service line failure at the far end of the OC-3 line.
OC3 lockout at near end	The protection line is locked out and will not be used to protect a service line failure at the near end of the OC-3 line.
OC3 loopback (from OC3)	The input from the OC-3 line is looped back to the output of the OC-3 line.
OC3 loopback (from OC48)	The STS-3/3c signal from the OC-48 line is looped back to the OC-48 line.
OC3 mode conflict (equipment)	One slot of a slot pair provisioned for 1+1 operation is equipped with an OC3 circuit pack and the other slot of the slot pair is equipped with a DS3 or STS1E circuit pack.
OC3 mode conflict (slot pair)	One slot of a slot pair is provisioned "1+1" and the other slot of the slot pair is provisioned "0x1."
OC3 mode conflict (x-connect)	The two slots of a slot pair provisioned for 1+1 operation are cross-connected to two different STS-3 tributaries.
OC3 removed	OC3 circuit pack is missing from slot.
OHCTL failure	Failure of OHCTL circuit pack.
OHCTL removed	OHCTL circuit pack is missing from slot.
optical parameter out-of-range	The laser bias current level, optical power transmit level, or optical power receive level has exceeded the allowed range at an FT-2000 repeater bay.
path AIS	An STS-1 path alarm indication signal (AIS) has been detected on one or more of the STS-1 tributaries of the OC-48 line. Likely cause: Failure of far-end DS3 or STS1E circuit pack.
path integrity failure	A cross-connection within a circuit has been broken.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
path TCA	A threshold crossing alert (TCA) has been issued. The alert means the signal degrade threshold has been exceeded.
path yellow	An STS-1 path yellow has been detected on a DS3 signal coming from the line. Path yellow indicates the far-end terminal is receiving a "path AIS." Likely causes: Unprotected failure of far-end DS3 circuit pack.
preemptible protn access	The Low Speed Shelf - Complementary is connected to the protection line during the switching function and can be preempted by an automatic switch to protection.
provisioned control pt	The specified control point (miscellaneous discrete output) is active. Note: The actual message that appears in the condition description report for this condition can be provisioned; this is the default message. See the Retrieve-Attribute-Control and Set-Attribute-Control commands. Likely cause: The system was instructed to close the control point by a TBOS control point.
provisioned environmental pt	A contact closure is present at an environmental input point of the network element. Note: The actual message that appears in the condition description report for this condition can be provisioned; this is the default message. See the Retrieve-Attribute-Control and Set-Attribute-Control commands.
RCVR failure	Failure of RCVR circuit pack.
RCVR removed	RCVR circuit pack is missing from slot.
receiver parameter-out-of-range	One or more of the operating parameters of a RCVR, TRMTR, or REGENR circuit pack has exceeded the recommended range.
REGENR failure	Failure of REGENR circuit pack.
REGENR removed	REGENR circuit pack is missing from slot.
repeater bay environment input	An environmental input has been activated at the indicated repeater site.
repeater bay equipment failure	An equipment failure has occurred at the indicated repeater site.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
reset in progress	The FT-2000 network element is in progress of downloading its software from SYSMEM to SYSCTL, to LNCTLs, to OHCTLs, and to all other circuit pack board controllers. Likely cause: Reset or Initialize-System has been executed.
ring incomplete/open	The designated DS-NE network element is unable to create a complete ring map because of a controller or transmission failure in the OC-48 ring.
ring startup in progress	This is normally a transient condition that indicates the ring network is starting up.
section TCA	A threshold crossing alert (TCA) has been issued. The alert means the signal degrade threshold has been exceeded.
site provisioning conflict	Two network elements in a network are provisioned as "Site 0." One and only one FT-2000 terminal in a network must be provisioned to be "Site 0."
software download in progress	A software download from the PC-CIT to the SYSMEM is currently in progress.
STS1 squelch map conflict	The controller circuits are unable to associate any network element's target identifier (TID) with the value supplied for a source or destination node of an STS-1 cross-connection.
STS1E INTFC failure	Failure of STS1E circuit pack.
STS1E INTFC removed	STS1E circuit pack is missing from slot.
STS1 squelch map inconsistent	The intermediate source/destination node TIDs are incorrect for the circuit.
STS3 squelch map conflict	The controller circuits are unable to associate any network element's target identifier (TID) with the value supplied for a source or destination node of an STS-3 cross-connection.
STS3 squelch map inconsistent	The intermediate source/destination node TIDs are incorrect for the circuit.
squelch map conflict	The controller circuits are unable to associate any network element's target identifier (TID) with the value supplied for a source or destination node of a cross-connection.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
sync mode auto switch	An automatic switch from the provisioned operating mode to holdover mode has occurred. Likely cause: All incoming synchronization references have failed.
sync mode forced switch	A forced switch from the provisioned operating mode to holdover mode has occurred.
sync reference auto switch	The FT-2000 terminal has automatically switched its synchronization reference. Likely causes: When provisioned for phase-locked synchronization mode, failure of the external DS1 synchronization reference. When provisioned for loop timed mode, failure of the incoming OC-48 line, failure of the RCVR circuit pack, or failure of the external DS1 synchronization reference.
sync reference forced switch	The FT-2000 terminal has switched its synchronization reference with a priority of "forced switch."
sync reference lockout	A synchronization reference signal is locked out and FT-2000 terminal will go into holdover mode if it fails.
sync reference manual switch	The FT-2000 terminal has switched its synchronization reference with a priority of "manual switch."
SYSCTL failure	Failure of SYSCTL circuit pack.
SYSMEM failure	Failure of SYSMEM circuit pack.
SYSMEM removed	SYSMEM circuit pack is missing from slot.
SYSMEM/SYSCTL code mismatch	A software mismatch has been detected between the software stored in the SYSMEM circuit pack and the software version running in the FT-2000 network element. Likely cause: A software upgrade procedure is in progress.
SYSMEM unrecognizable code	The software stored in the SYSMEM circuit pack has become corrupted and is no longer usable. Likely causes: Corrupted software download to SYSMEM circuit pack or spontaneous corruption of the software in the SYSMEM circuit pack.
test alarm in progress	An office alarm test is in progress.

Table 9-2. Condition Descriptions (Contd)

Description (from Retrieve-Alarm Report)	Meaning
test auto turnup in progress	An auto turnup test is in progress.
test telemetry in progress	A telemetry test is in progress.
TG auto switch	A TG3 circuit pack has been automatically switched to protection. Likely cause: Failure of a TG3 circuit pack, removal of a TG3 circuit pack, or a TG3 circuit pack warming up.
TG manual switch	A TG3 circuit pack was switched to protection with a priority of "manual switch."
TG warming up	A TG circuit pack has been installed and is warming up to a stable operating temperature.
TG3 failure	Failure of TG3 circuit pack.
TG3 removed	TG3 circuit pack is missing from slot.
TL-1 link failure	An incoming TL1 link failure has been detected.
TOHCTL failure	Failure of TOHCTL circuit pack.
TOHCTL removed	TOHCTL circuit pack is missing from slot.
traffic squelched	Multiple failures in the ring network have resulted in traffic being squelched to eliminate any possibility of its being misrouted.
transmit parameter-out-of-range	One or more of the operating parameters of a RCVR, TRMTR, or REGENR circuit pack has exceeded the recommended range.
TRMTR failure	Failure of TRMTR circuit pack.
TRMTR removed	TRMTR circuit pack is missing from slot.
unexpected CP type	A circuit pack has been replaced with a defective circuit pack.
x-conn provisioned but not est	A cross-connection has been entered between a tributary of an OC-48 line and a low-speed interface slot in the AUTO state.

Protection Switching

High-Speed Protection

General

The FT-2000 OC-48 Add/Drop-Rings Terminal performs 2-fiber ring protection switching in response to automatically detected faults in the OC-48 high-speed lines, as well as external commands from a technician at a local/remote CIT or operations system. Refer to the **FAULT-Switch-Line** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Table 9-3 shows the standard protection switching priorities in descending order of priority used by the FT-2000 OC-48 Add/Drop-Rings Terminal.

Table 9-3. 2-Fiber Ring Protection Switching Priorities

Switch Priority (Descending Order)	Source of Request
Reset*	CIT/OS
Automatic protection switch (APS) channel failure†	Automatic
Protection hardware failure†	Automatic
Lockout of protection	CIT/OS
Forced switch	CIT/OS
Signal fail	Automatic
Signal degrade	Automatic
Manual switch	CIT/OS
Wait to restore	Automatic

* Resetting the line switch priority (**FAULT-Switch-Line-Priority:Reset**) has no effect on any automatic switch priority except wait to restore.

† An APS channel failure and a protection hardware failure are the same priority.

The FT-2000 OC-48 Add/Drop-Rings Terminal uses bidirectional revertive 2-fiber ring protection switching. Bidirectional refers to when protection switching is performed in the transmit and receive directions. In revertive 2-fiber ring protection switching, the traffic switches from the service STS-3 tributaries of an OC-48 high-speed line to the protection STS-3 tributaries of the other OC-48 high-speed line when a fault occurs. When the fault clears, the traffic switches back (reverts) to the service STS-3 tributaries.

There are eight STS-3 tributaries carrying service on OC-48 high-speed line 1W and eight STS-3 tributaries carrying service on OC-48 high-speed line 1E. The

eight STS-3 tributaries not carrying service on each OC-48 high-speed line are the protection STS-3 tributaries. The protection STS-3 tributaries on OC-48 high-speed line 1W are used to protect the service STS-3 tributaries on OC-48 high-speed line 1E, and the protection STS-3 tributaries on OC-48 high-speed line 1E are used to protect the service STS-3 tributaries on high-speed line 1W. Table 9-4 shows the mapping between the service STS-3 tributaries and their protection STS-3 tributaries.

Table 9-4. 2-Fiber Ring Tributary Mapping

Service STS-3 Tributary	Protection STS-3 Tributary
1	9
2	10
3	11
4	12
5	13
6	14
7	15
8	16

2-Fiber Ring Protection Switching Architecture

Figure 9-5 shows the 2-fiber ring protection switching architecture of the FT-2000 OC-48 Add/Drop-Rings Terminal.

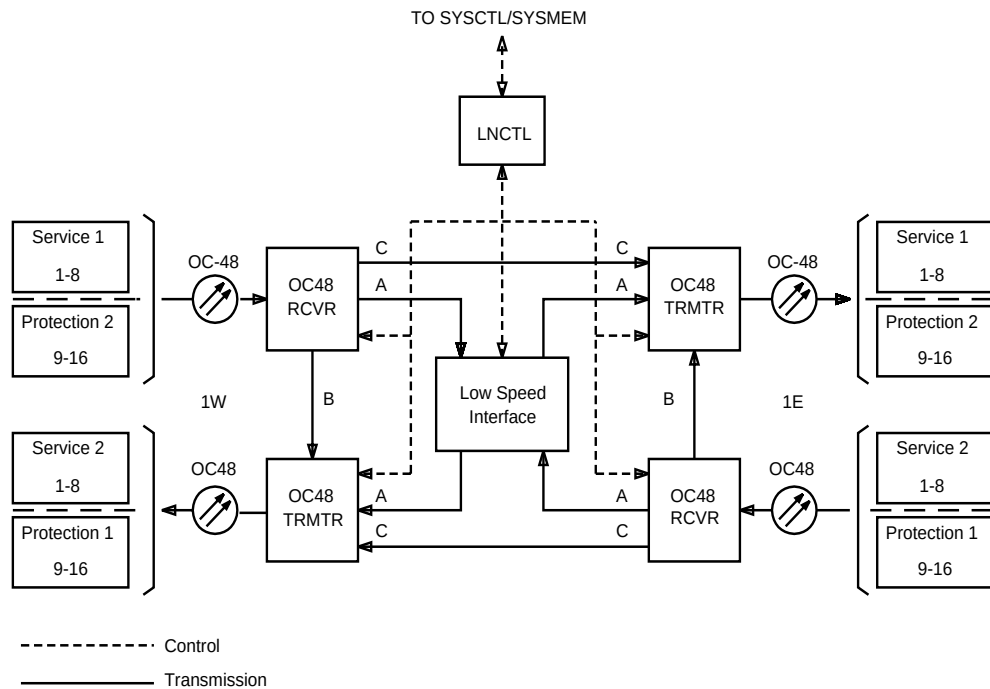


Figure 9-5. 2-Fiber Ring Protection Switching Architecture (FT-2000 OC-48 Add/Drop-Rings Terminal)

The FT-2000 OC-48 Add/Drop-Rings Terminal must have RElease 3.0.1-ADR or later software and be equipped with the following circuit packs to support bidirectional 2-fiber ring protection switching:

- **OC48 Receiver, Add/Drop [OC48 RCVR (A/D)]:** Two OC48 RCVR (A/D) circuit packs are required to interface with OC-48 high-speed lines 1W and 1E. The OC48 RCVR (A/D) circuit pack constantly monitors the incoming OC-48 signal on the OC-48 high-speed line. If a defect is detected, the OC48 RCVR circuit pack notifies the OC48 TRMTR (A/D) and LNCTL circuit packs. The OC48 RCVR (A/D) circuit packs have the following transmission buses that make 2-fiber ring protection switching possible:
 - **A bus:** This bus allows the local traffic to be dropped.
 - **B bus:** This bus allows the through traffic from the service STS-3 tributaries of the OC-48 high-speed line to be looped back to the OC48 TRMTR (A/D) circuit pack and bridged to the protection STS-3 tributaries when a 2-fiber ring protection switch is required. (Refer to Table 9-4 for the mapping between the service STS-3 tributaries and the protection STS-3 tributaries.)

- **C bus:** This bus allows through traffic to be routed from OC-48 high-speed line 1W to 1E, or from OC-48 high-speed line 1E to 1W.
- **OC48 Transmitter, Add/Drop [OC48 TRMTR (A/D)]:** Two OC48 TRMTR (A/D) circuit packs are required to interface with OC-48 high-speed lines 1W and 1E. If an incoming OC-48 signal defect or an internal equipment defect is detected, the OC48 TRMTR (A/D) circuit pack transmits maintenance signals to remote FT-2000 OC-48 Add/Drop-Rings Terminals. The OC48 TRMTR (A/D) circuit packs have the following transmission buses that make 2-fiber ring protection switching possible:
 - **A bus:** This bus allows the local traffic to be added.
 - **B bus:** This bus allows the through traffic from the OC48 RCVR (A/D) circuit pack to be looped back to the OC48 TRMTR (A/D) circuit pack and bridged to the protection STS-3 tributaries of the OC-48 high-speed line when a 2-fiber ring protection switch is required. (Refer to Table 9-4 for the mapping between the service STS-3 tributaries and the protection STS-3 tributaries.)
 - **C bus:** This bus allows through traffic to be routed from OC-48 high-speed line 1E to 1W or from OC-48 high-speed line 1W to 1E.
- **Line Controller - 4 Megabyte [LNCTL]:** One LNCTL circuit pack is required to interface with the circuit packs on OC-48 high-speed lines 1W and 1E. The LNCTL circuit pack controls 2-fiber ring protection switching and performs line specific computations. The LNCTL circuit pack uses the board controller local area network (BCLAN) to communicate with the board controllers on the OC48 TRMTR (A/D), OC48 RCVR (A/D), DS3, STS1E, OC3 and OC12 circuit packs for OC-48 high-speed lines 1W and 1E.
- **Low Speed Interface:** The FT-2000 OC-48 Add/Drop-Rings Terminal may be equipped with up to 16 service DS3 and/or STS1E circuit packs, eight 1+1 protected OC3 circuit packs (16 unprotected), two 1+1 protected OC12 circuit packs (4 unprotected), or a mixture of DS3, STS1E and OC3 circuit packs. If an incoming OC-48 signal defect occurs on the OC-48 high-speed line 1W, the service low-speed interface circuit packs switch to OC-48 high-speed line 1E in response to switch requests from the LNCTL circuit pack. When the defect clears, the service low-speed interface circuit packs switch back (revert) to OC-48 high-speed line 1W.

2-Fiber Ring Protection Switching Example

Figure 9-6 shows an example of a 2-fiber bidirectional, line-switched ring with four nodes during normal transmission. (STS-3 tributaries 1 through 8 are service STS-3 tributaries and STS-3 tributaries 9 through 16 are protection tributaries.) Service STS-3 tributary 6 is used to carry service traffic between low-speed interface slot 4A at node 0 and low-speed interface slot 5A at node 2. Service STS-3 tributary 3 is used to carry service traffic between low-speed interface slot 3A at node 1 and low-speed interface slot 2A at node 3. Protection STS-3 tributary 14 is used to carry extra traffic between low-speed interface slot 2B at node 0 and low-speed interface slot 4B at node 3. For more information about cross-connections at FT-2000 OC-48 Add/Drop-Rings Terminals, refer to Volume I, Section 8, "Administration and Provisioning."

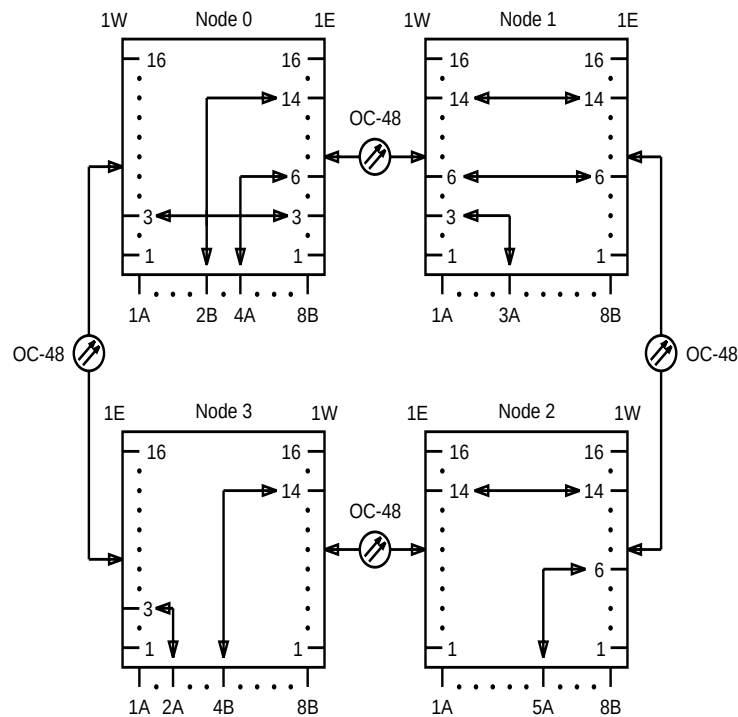


Figure 9-6. Bidirectional Line-Switched 2-Fiber Ring (Normal Transmission)

Figure 9-7 shows a 2-fiber bidirectional, line-switched ring when a fiber cut occurs between two nodes. Automatic 2-fiber ring protection switching occurs in response to a signal fail or signal degrade condition on an incoming OC-48 high-speed signal.

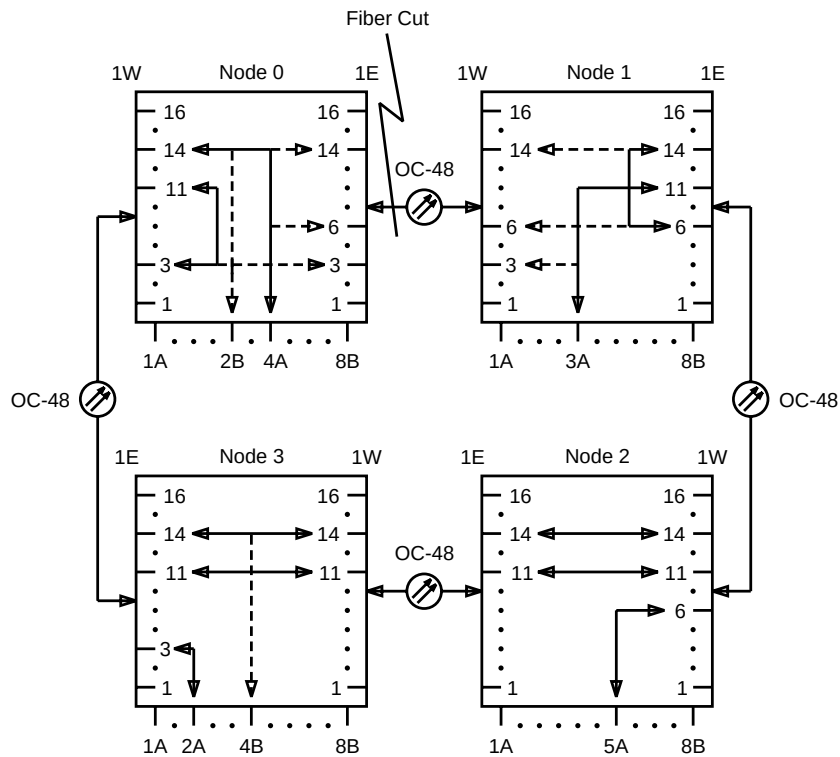


Figure 9-7. Bidirectional Line-Switched 2-Fiber Ring (Single Failure)

When a signal degrade or signal fail defect is detected by the east OC48 RCVR (A/D) circuit pack at node 0, the following occurs:

- The east OC48 RCVR (A/D) circuit pack notifies the LNCTL circuit pack.
- The LNCTL circuit pack enters a protection switch request and the provisionable alarm delay interval is started. After the alarm delay interval is reached, the autonomous indicators are enabled. The LNCTL circuit pack also notifies the west OC48 TRMTR (A/D) circuit pack.
- The west OC48 TRMTR (A/D) circuit pack transmits a switch request to the other nodes in the ring using the automatic protection switch (APS) channel on OC-48 high-speed line 1W.

Nodes 2 and 3 are not actively involved in 2-fiber ring protection switching and are referred to as intermediate nodes. At nodes 2 and 3, the following occurs:

- The east OC48 RCVR (A/D) circuit pack receives the switch request via the APS channel and notifies the LNCTL circuit pack.

- The LNCTL circuit pack transmits a request to preempt any extra traffic to the board controllers of the transmission circuit packs. After extra traffic is preempted, through cross-connections are established for the protection STS-3 tributaries. Through cross-connections are established using the C bus between the west OC48 RCVR (A/D) and east OC48 TRMTR (A/D) circuit packs and the east OC48 RCVR (A/D) and west OC48 TRMTR (A/D) circuit packs. Figure 9-7 shows that extra traffic has been preempted and through cross-connections have been established for the protection STS-3 tributaries at nodes 2 and 3. The add/drop cross-connections to the service STS-3 tributaries are not affected.
- The west OC48 TRMTR (A/D) circuit pack passes through the switch request to the other nodes using the APS channel on OC-48 high-speed line 1W.

At node 1, the following occurs:

- The east OC48 RCVR (A/D) circuit pack receives the switch request via the APS channel and notifies the LNCTL circuit pack.
- The LNCTL circuit pack transmits a protection switch request to the board controllers on all the circuit packs and notifies the east OC48 TRMTR (A/D) circuit pack.
- The low-speed interface circuit packs that add/drop traffic to/from the failure, and the east OC48 TRMTR (A/D) and OC48 RCVR (A/D) circuit packs switch traffic to the protection STS-3 tributaries of OC-48 high-speed line 1E. An add/drop cross-connection is established between the affected low-speed interface slot and the protection STS-3 tributaries of OC-48 high-speed line 1E. The add/drop cross-connection is established using the A bus between the affected low-speed interface circuit pack and the east OC48 TRMTR (A/D) and OC48 RCVR (A/D) circuit packs. Figure 9-7 shows low-speed interface slot 3A cross-connected to the protection STS-3 tributary 11 of OC-48 high-speed line 1E.
- The east OC48 TRMTR (A/D) and OC48 RCVR (A/D) circuit packs loop back all the through STS-3 tributaries transmitted toward the failure from the service STS-3 tributaries to the protection STS-3 tributaries of OC-48 high-speed line 1E. The loopback is established using the B bus between the east OC48 TRMTR (A/D) and OC48 RCVR (A/D) circuit packs. Figure 9-7 shows extra traffic is preempted and service STS-3 tributary 6 of OC-48 high-speed line 1E is looped back to protection STS-3 tributary 14. (Table 9-4 shows the mapping between the service STS-3 tributaries and their protection STS-3 tributaries.)
- The east OC48 TRMTR (A/D) circuit pack also returns a switch confirmation to node 0 using the APS channel on OC-48 high-speed line 1E.

At node 0, the following occurs:

- The west OC48 RCVR (A/D) circuit pack receives the switch confirmation and notifies the LNCTL circuit pack.
- The LNCTL circuit pack transmits a protection switch request to the board controller on all the circuit packs.
- The low-speed interface circuit packs that add/drop traffic to/from the failure, and the west OC48 TRMTR (A/D) and OC48 RCVR (A/D) circuit packs switch traffic to the protection STS-3 tributaries of OC-48 high-speed line 1W. An add/drop cross-connection is established between the affected low-speed interface slot and the protection STS-3 tributaries of OC-48 high-speed line 1W. The add/drop cross-connection is established using the A bus between the affected low-speed interface circuit pack and the west OC48 TRMTR (A/D) and OC48 RCVR (A/D) circuit packs. Figure 9-7 shows low-speed interface slot 4A cross-connected to the protection STS-3 tributary 14 of OC-48 high-speed line 1E.
- The west OC48 TRMTR (A/D) and OC48 RCVR (A/D) circuit packs loop back all the through STS-3 tributaries transmitted toward the failure from the service STS-3 tributaries to the protection STS-3 tributaries of OC-48 high-speed line 1W. The loopback is established using the B bus between the west OC48 TRMTR (A/D) and OC48 RCVR (A/D) circuit packs. Figure 9-7 shows extra traffic is preempted and service STS-3 tributary 3 of OC-48 high-speed line 1W is looped back to protection STS-3 tributary 11. (Table 9-4 shows the mapping between the service STS-3 tributaries and their protection STS-3 tributaries.)

In order to prevent misconnections during single node or multiple ring failures, the FT-2000 OC-48 Add/Drop-Rings Terminal squelches all traffic that cannot be terminated.

Figure 9-8 shows a 4-node ring with multiple ring failures that isolate node 1 and cause traffic that cannot be terminated to be squelched. Nodes 0 and 2 perform a 2-fiber ring protection switch and squelch traffic originating from or terminating at node 1 to prevent any misconnections. Squelching is done by inserting STS-1 path AIS on the protection tributaries that carry traffic terminating at or originating from node 1. The STS-1 path AIS is returned to the node(s) sending traffic to node 1. At these node(s), the STS-1 path AIS or DS3 AIS is sent to the cross-connect panels (or equivalent). If nodes 0 and 2 originate or terminate any traffic, STS-1 path AIS or DS3 AIS is also inserted and sent to the local cross-connect panels (or equivalent).

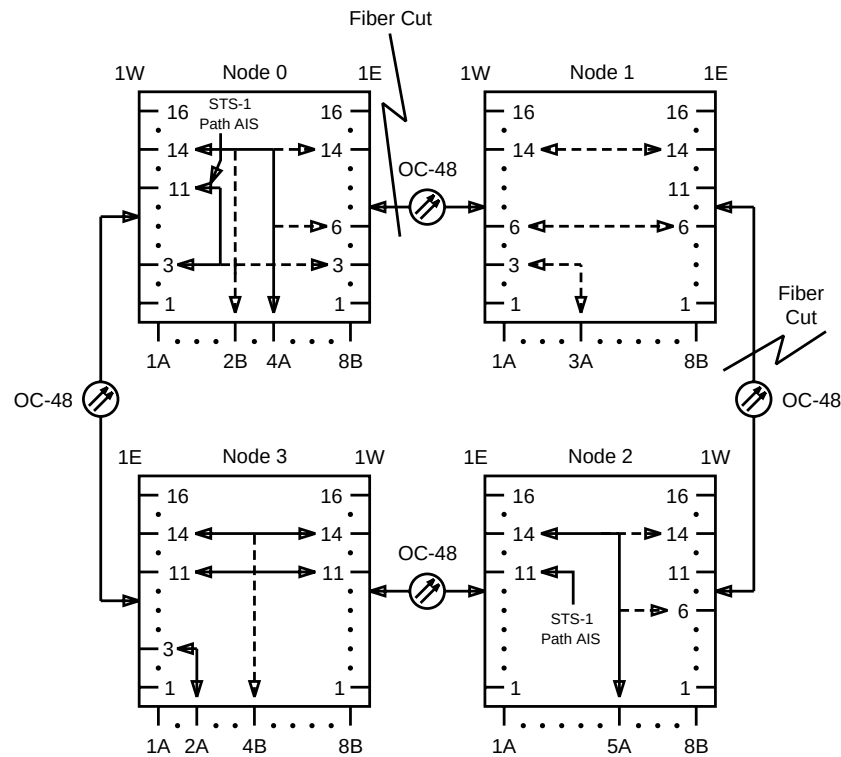


Figure 9-8. Bidirectional Line-Switched 2-Fiber Ring (Multiple Failures)

Nodes 0 and 2 do not squelch traffic that passes through node 1. Squelching is only done by nodes that have an active 2-fiber ring protection switch. An isolated node does not perform a 2-fiber ring protection switch; thus an isolated node does not squelch.

If any FT-2000 OC-48 Add/Drop-Rings Terminals in the 2-fiber ring are equipped to provide extra traffic, a 2-fiber ring protection switch will preempt extra traffic.

This applies to the secondary circuit of a DRIP circuit as well, which is carried on protection capacity, and hence treated as being extra traffic. The secondary circuit is the combination of the continue segment of the drop and continue (from the primary node to the secondary node), along with the duplicate segment from the other ring (from the secondary node to the primary node).

An exception to this occurs during isolation of the primary node of a DRIP circuit. In a DRIP circuit without failures, the secondary node of the DRIP circuit:

- Adds traffic to a protection tributary on the span towards the primary node, and

- Selects traffic from a protection tributary on the span towards the primary node.



NOTE:

This is done by provisioning the 2-Way-DRI-Sec cross-connection at the secondary node.

When the primary node of the DRIP circuit is isolated, and the other end of the circuit is visible to the secondary node, the secondary node:

- Bridges traffic on the same protection tributary on the span *away* from the primary node, and
- Selects traffic from the same protection tributary on the span *away* from the primary node.

In effect, the secondary node acts like it is protecting traffic on a *service* tributary on the span towards the primary node.

Bidirectional 2-fiber ring protection switching is completed within 60 milliseconds of the onset of a hard failure such as a fiber cut and is compliant with the applicable Telcordia Technical Advisories and Technical Requirements.

When the signal fail or signal degrade defect clears, the provisionable alarm clear delay is initiated. Before the defect is declared clear, the OC-48 signal's BER must be 10 times better than the provisioned signal degrade threshold. For example, if the provisionable signal degrade threshold is 10^{-9} , the OC-48 signal's BER must fall below 10^{-10} to clear the defect.

When the defect is declared clear, the FT-2000 OC-48 Add/Drop-Rings Terminal enters the provisionable wait-to-restore interval. This prevents any oscillating between the service and protection STS-3 tributaries. During the wait-to-restore interval, the service STS-3 tributaries are monitored and should perform better than the signal degrade threshold.

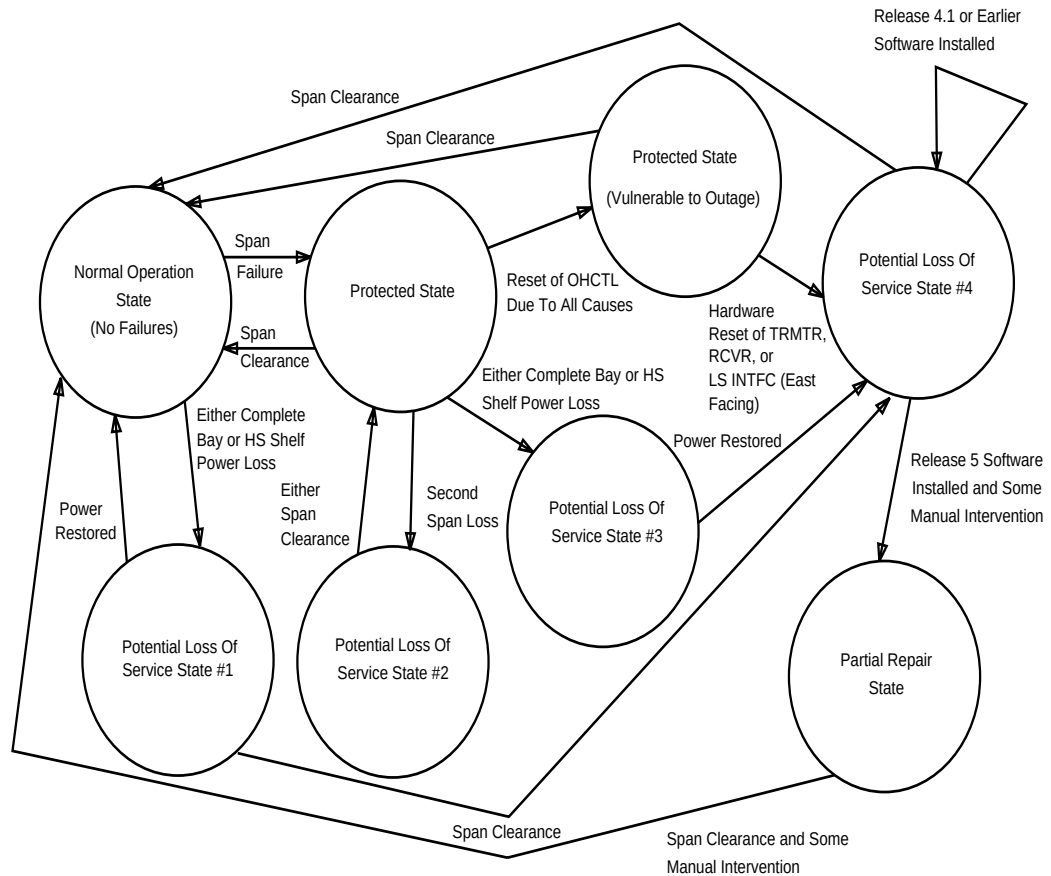
When the alarm clear delay is reached all indicators are updated. When the wait-to-restore interval is reached, traffic is revertively switched back to the service STS-3 tributaries.

Single and Double Failure States

Figure 9-9 shows how the FT-2000 OC-48 Add/Drop-Rings Terminal reacts to single and double failures. Since the probability of double failures in a ring is significantly less likely than a single failure, and the probability of three failures is even less likely than a double failure, Figure 9-9 shows how the FT-2000 OC-48 Add/Drop-Rings Terminal reacts to only single and double failures. However, some of the logic will carry over to highly unlikely multiple failure cases.

In Figure 9-9, the circles represent the states the FT-2000 OC-48 Add/Drop-Rings Terminal can enter, with respect to 2-fiber ring protection switching. The arrows

between the states show transition paths. The labels associated with the arrows are the stimulus that produce the transitions.



tpa 847883/01

Figure 9-9. Single and Double Failure States

The FT-2000 OC-48 Add/Drop-Rings Terminal may enter the following states when reacting to single and double failures (Figure 9-9):

- **Normal Operation State:** In this state there are no failures that affect 2-fiber ring protection switching. The FT-2000 OC-48 Add/Drop-Rings Terminal remains in this state the overwhelming majority of time. The following types of failures cause transitions:
 - Span failures are cable cuts and transmission electronics failures (for example, transmitter or receiver circuit pack failures)

- Failures that cause hardware resets to transmitters, receivers, and low-speed interfaces if they are the second failure within the ring. This also includes shelf power failures, removing circuit packs, and reinserting circuit packs.
- **Protected State:** Most single failures are span failures and cause the system to enter the protected state. In this state, 2-fiber ring protection switching restores all service traffic. Single failures (other than node failures) never cause service outages.
- **Potential Loss of Service State #1:** If a node has a power loss (Failure A in Figure 9-10), the system enters this state. In this state, only add/drop traffic at the node where the power loss occurs is lost. All other traffic in the ring is protected using 2-fiber ring protection switching. (For this reason, this state is labeled as a potential loss of traffic.) When power is restored, the node transitions automatically to the "Normal Operation State."
- **Potential Loss of Service State #2:** If two spans fail (Failures B and C in Figure 9-10), the system enters this state. In this state, the ring is segmented and any loss of traffic will be from paths between the two segments, if any existed. Like the "Potential Loss of Service State #1," the degree of service outage depends on how traffic is provisioned (routed) on the ring. When either of the two span failures is cleared, the system transitions automatically to the "Protected State."
- **Potential Loss of Service State #3:** If a node has a power loss and a single span failure (Failures A and B in Figure 9-10), the system enters this state. In this state, the ring is segmented and any loss of traffic will be from paths between the two segments, if any existed. Any add/drop traffic at the node where the power loss occurs is also lost. The degree of traffic loss depends on how traffic is provisioned (routed) on the ring. In some cases, traffic loss could be nonexistent.
- **Potential Loss of Service State #4:** A node enters this state when power is restored from the "Potential Loss of Service State #3", or from the "Protected State (Vulnerable to Outage)." In this state, the node "wakes up" into an incomplete ring due to the span failure. Since the ring is incomplete, some traffic, which might be thought to be restored, will not be restored until the span failure is cleared.
- **Protected State (Vulnerable to Outage):** In this state, service is fully protected following the reset of either OHCTL circuit pack (for any reason). However, any hardware reset of transmitters, receivers, or east-facing low-speed interfaces will cause the system to enter the "Potential Loss of Service State #4."
- **Partial Repair State:** In Release 5 and later releases, a node can enter this state with some manual intervention. In releases prior to Release 5, partial repair is not possible. In this state, some additional traffic can be restored before the span failure is cleared. When the span failure clears, the node can enter the "Normal Operation State" with some manual

intervention. For more information about clearing a potential loss of service due to a double failure, refer to the "Operation and Maintenance (TOP)" section (Volume II).

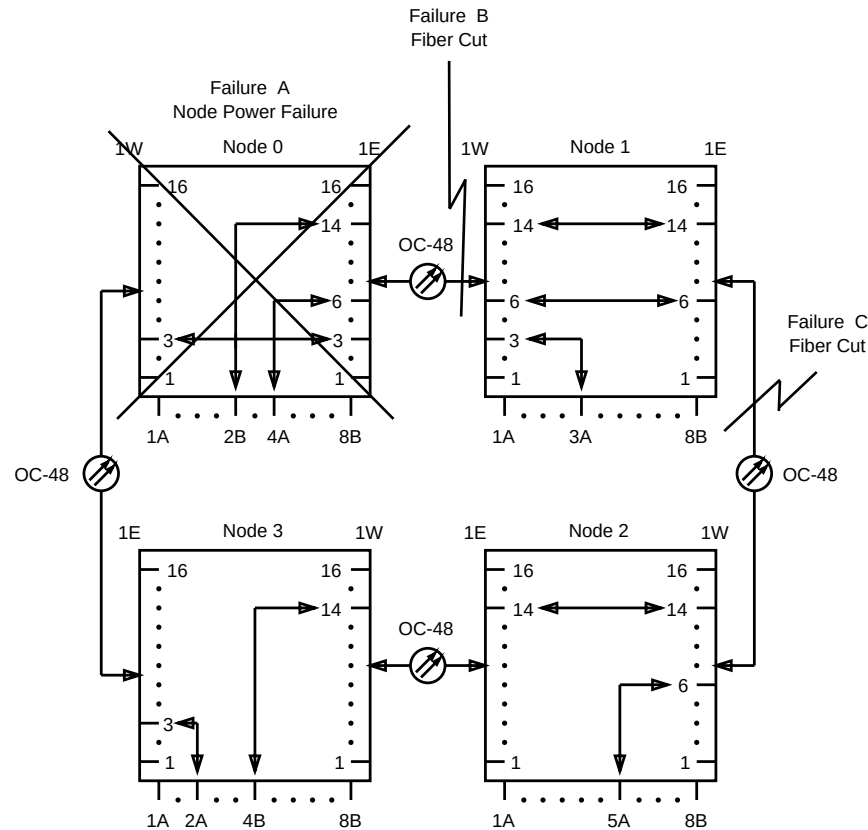


Figure 9-10. Single and Double Failures

Electrical Low Speed Protection

The FT-2000 OC-48 Lightwave System provides protection for all the low-speed interface circuit packs. The DS3 Interface circuit packs and STS-1 Electrical Interface circuit packs are 1xM ($M \leq 16$) protected and use bidirectional revertive switching. Low speed protection switching takes place in response to automatically detected faults and external commands from a local/remote CIT or operations system. Refer to the **FAULT-Switch-Low Speed** input in Volume I, Section 11, "Craft Interface Terminal Usage."

One dedicated protection DS3 circuit pack* provides protection for the service DS3 circuit packs, and one dedicated protection STS1E circuit pack* provides protection for the service STS1E circuit packs. However, only one protection low-speed interface circuit pack (DS3 or STS1E) can be switched into service at a time. Two Low Speed Switch (LSSW) circuit packs are used to perform low-speed protection switching.*

Table 9-5 shows the standard protection switching priorities in descending order of priority used by the FT-2000 OC-48 Add/Drop-Rings Terminal.

Table 9-5. Electrical low Speed Protection Switching Priorities

Switch Priority (Descending Order)	Source of Request
Reset*	CIT/OS
Protection hardware failure	Automatic
Lockout of protection	CIT/OS
Lockout of service	CIT/OS
Forced switch	CIT/OS
Circuit pack failure/removal	Automatic
Manual switch	CIT/OS
Wait to restore	Automatic

* Resetting the low-speed switch priority
(FAULT-Switch-Low Speed-Priority:Reset) has no effect
on any automatic switch priority except wait to restore.

Each Low Speed Shelf can be equipped with one protection DS3 and/or one protection STS1E circuit pack. However, only one service DS3 or STS1E circuit pack can be protected at a time.

Each service low-speed interface circuit pack (DS3 or STS1E) can be assigned a high or low protection priority. This allows a high priority circuit pack to preempt any previous switch to protection by a lower priority circuit pack. Switch requests of equal priority will be met in the order in which they are received. If two requests occur simultaneously, the one with the lower slot address will take priority.

Figure 9-11 shows the low-speed protection switching architecture.

* The protection DS3 and STS1E circuit packs may be placed in the P1 or P2 slot of the Low Speed Shelf - System Controller.

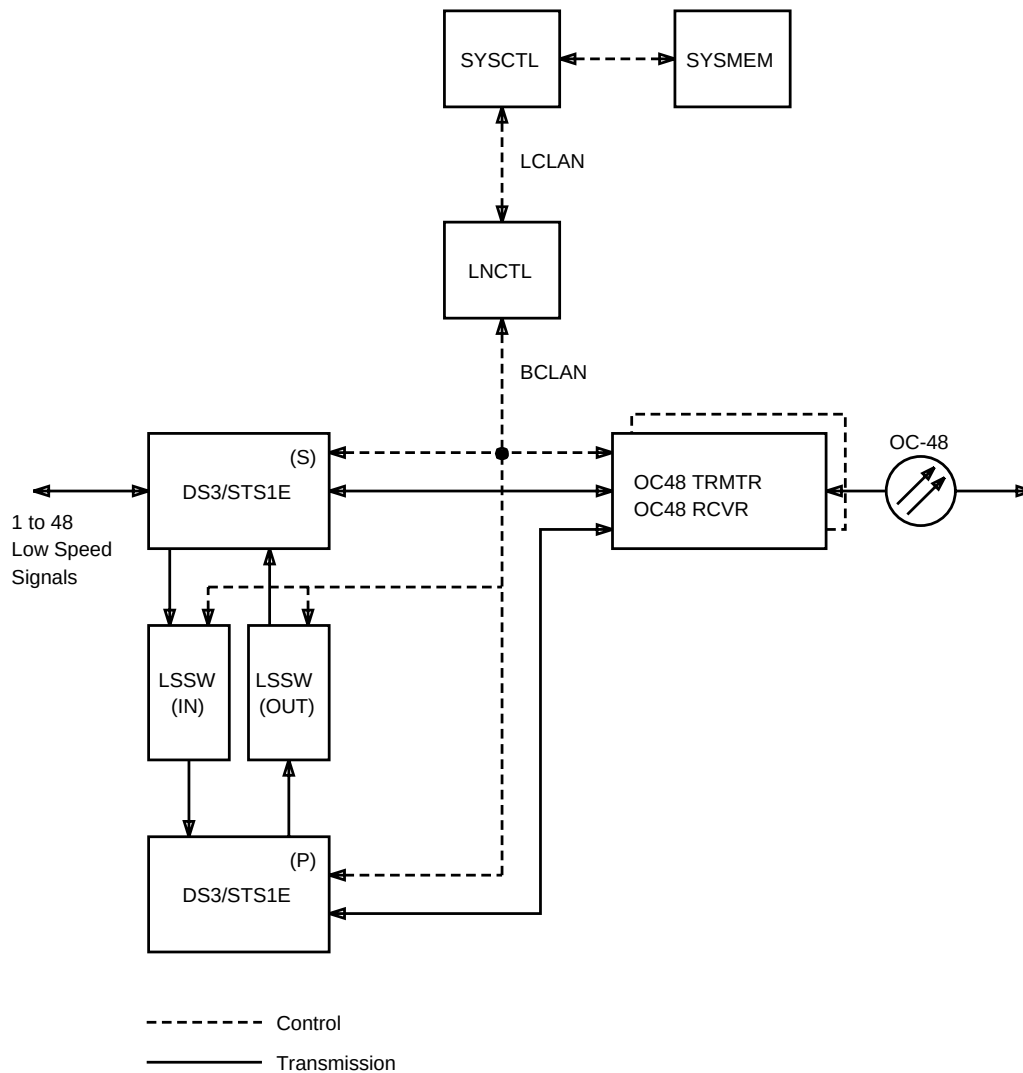


Figure 9-11. Electrical Low Speed Protection Switching

If a service low-speed interface circuit pack fails, the board controller on the circuit pack sends the information to the LNCTL circuit pack via the BCLAN. If appropriate, the LNCTL circuit pack broadcasts a bidirectional switch request to the board controllers on the LSSW circuit packs, the service OC48 TRMTR circuit pack, and the service OC48 RCVR circuit pack.

The board controller on the LSSW circuit packs activates the protection relays on the failed low-speed interface circuit pack and the relays on the LSSW circuit pack

to build a low-speed protection bus in the transmit direction. The LSSW circuit pack in the IN slot controls the protection relays on the low-speed interface circuit packs in slots 1A through 4B. The LSSW circuit pack in the OUT slot controls the protection relays on the low-speed interface circuit packs in slots 5A through 8B. The protection relays on the failed low-speed interface circuit pack switch the three low-speed signals from DSX-3 or STSX-1 through the LSSW (IN) circuit pack to the protection low-speed interface circuit pack. The protection low-speed interface circuit pack processes the low-speed signals and sends a 155 Mb/s signal to the OC48 TRMTR circuit pack. The board controller on the OC48 TRMTR circuit pack activates the selector circuitry to choose the 155 Mb/s signal from the protection low-speed interface circuit pack.

In the receive direction, the board controller on the OC48 RCVR circuit pack activates the switch circuitry to route the 155 Mb/s signal to the protection low-speed interface circuit pack instead of the failed service low-speed interface circuit pack. The board controller on the LSSW circuit packs activate the protection relays on the failed low-speed interface circuit pack and the relays on the LSSW (OUT) circuit pack to build a low-speed protection bus in the receive direction. The protection low-speed interface circuit pack accepts the 155 Mb/s signal from the OC48 RCVR circuit pack, processes the signal, and sends the three low-speed signals through the LSSW (OUT) circuit pack to the failed low-speed interface circuit pack where the switch is completed to the DSX-3 or STSX-1 panel (or equivalent).

The FT-2000 OC-48 Lightwave System also supports unprotected low-speed operation. It may be equipped without a protection DS3 or STS1E circuit pack.

Optical Low Speed Protection — (OC-3)

General

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 5.0.1-ADR or later software provides optical low-speed protection of an OC-3 line. Optical low-speed protection switching takes place in response to automatically detected faults and external commands from a local/remote CIT or operations system. Refer to the **FAULT-Switch-OC3** input in Volume I, Section 11, "Craft Interface Terminal Usage."

**NOTE:**

For intra-office applications, the IS-3 Optical Interface may be used and it retains all of the same considerations given to the OC-3.

Table 9-6 shows the standard protection switching priorities in descending order of priority used by the FT-2000 OC-48 Add/Drop-Rings Terminal.

Table 9-6. Optical Low Speed Protection Switching Priorities

Switch Priority (Descending Order)	Source of Request
Reset*	CIT/OS
Switch inhibit	CIT/OS
APS channel failure	Automatic
Lockout of protection	CIT/OS
Lockout of service	CIT/OS
Forced switch	CIT/OS
Signal fail	Automatic
Signal degrade	Automatic
Manual switch	CIT/OS

* Resetting the OC-3 switch priority

(**FAULT-Switch-OC3-Priority:Reset**) has no effect on any automatic switch priority.

An OC-3 line may operate unprotected (0x1) or use provisionable unidirectional 1+1 nonrevertive protection switching. In unidirectional 1+1 nonrevertive switching, the transmitting terminal transmits the same signal on two lines. The receiving terminal monitors the two lines independently and chooses one line as the active line and the other line as the standby line. When a protection switch occurs, the receiving terminal selects the signal from the standby line which causes the standby line to be the new active line. The original active line becomes the standby line. This status remains the same (nonrevertive) after the fault clears. Protection switching is performed in one direction only (unidirectional).

OC-3/IS-3 Specific Information

The following is required for 1+1 OC-3 line protection:

- Two adjacent slots (for example, LS INTFC slot 2A and 2B) of the Low Speed Shelf - System Controller must be equipped (EQ state) with the same OC3 Optical Interface - 1.3 μ m Standard Performance [OC3 (1.3 STD)] circuit packs (either LAA10 or LAA10B, but not a mix of the two). The Low Speed Shelf - System Controller can be equipped with 8 1+1 protected OC3 (1.3 STD) circuit packs, or 16 unprotected OC3 circuit packs, or a mixture of protected and unprotected OC3 (1.3 STD) circuit packs.
- The OC3 Protection Mode parameter of both slots must be provisioned "1+1" using the **CONFIGURATION-Set -OC3** input. To provision for "1+1" configuration, both slots **MUST** be equipped with either the OC-3 (1.3 STD) LAA10 circuit pack or the OC-3 (1.3 STD) LAA10B circuit pack (but not a mix of the two). For example, a "1+1" configuration will not work if slot 2A is equipped with an LAA10 circuit pack, while slot 2B is equipped with an LAA10B circuit pack. The OC3 Protection Mode parameter has separate values for the add (OC-3 to OC-48) and drop (OC-48 to OC-3) directions. For information about provisioning the OC3 Protection Mode parameter, refer to the information describing the **CONFIGURATION-Set -OC3** input in Volume I, Section 8, "Administration and Provisioning."
- One slot of the slot pair must be cross-connected to a service STS-3 tributary using the **CONFIGURATION-Enter -Crossconnection-STS3** input. In Release 7 and later releases, one port of the slot pair must be cross-connected to a service STS-1 tributary using the **CONFIGURATION-Enter -Crossconnection-STS1** input. The FT-2000 OC-48 Add/Drop-Rings Terminal automatically enters an active cross-connection between one slot of the slot pair and an STS-3 tributary, and a standby cross-connection between the companion slot and the same STS-3 tributary. The slot pair cannot be cross-connected to different STS-3 tributaries. In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically enters an active cross-connection between one port of the slot pair and an STS-1 tributary, and a standby cross-connection between the same port of the companion slot and the same STS-1 tributary. The ports cannot be cross-connected to different STS-1 tributaries. For information about entering cross-connections, refer to Volume I, Section 8, "Administration and Provisioning."

Unprotected (0x1) OC-3 line operation results when one or more of the following occur:

- The slot and/or the adjacent slot is equipped (EQ state) with DS3 or STS1E circuit packs, or is in the AUTO state.
- The OC3 Protection Mode parameter of the slot and/or the adjacent slot is provisioned "0x1" using the **CONFIGURATION-Set -OC3** input.
- The slot (or port) and the adjacent slot (or port) are cross-connected to different tributaries.

Optical Low Speed Protection Switching Architecture

Figure 9-12 shows the optical low-speed protection switching architecture.

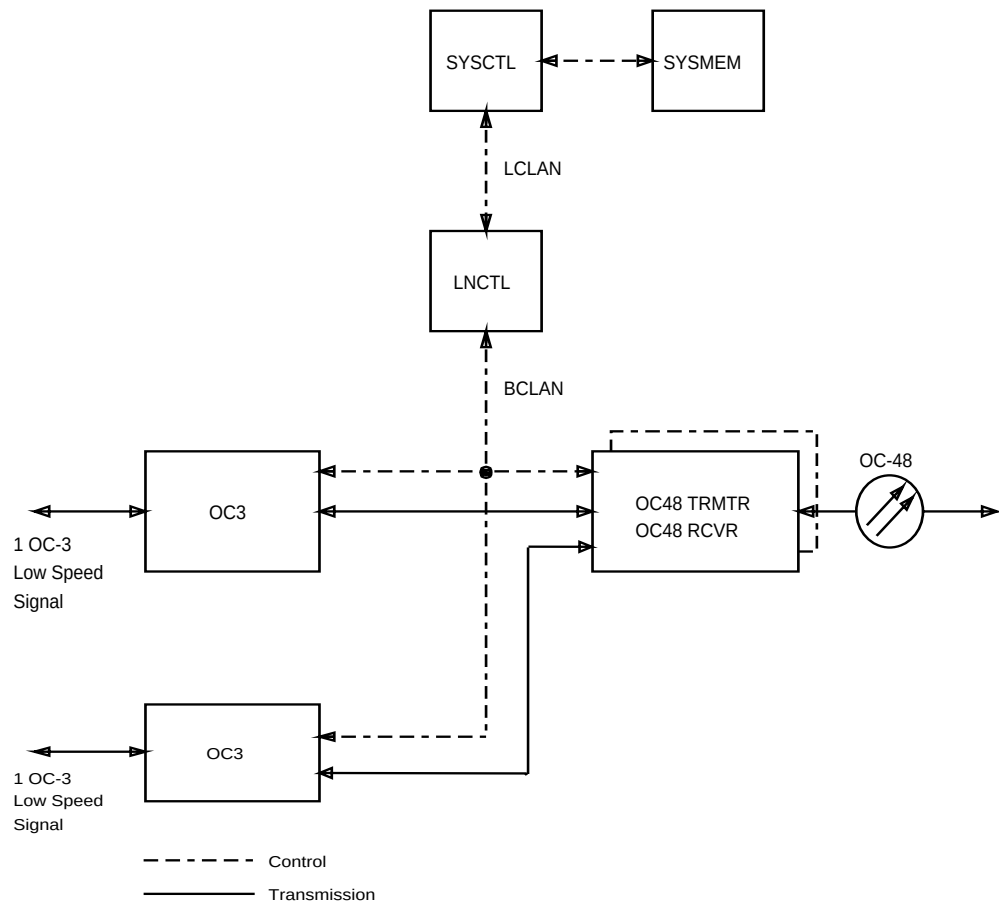


Figure 9-12. Optical Low Speed Protection Switching

Optical low-speed protection switching is only performed in the add (OC-3 to OC-48) direction by an FT-2000 OC-48 Add/Drop-Rings Terminal. If the active OC-3 line fails in the add (OC-3 to OC-48) direction, the board controller on the OC3 (1.3 STD) circuit pack sends the information to the LNCTL circuit pack via the BCLAN. If appropriate, the LNCTL circuit pack broadcasts a switch request to the board controller on the OC48 TRMTR circuit pack. The board controller on the OC48 TRMTR circuit pack activates the selector circuitry to choose the 155 Mb/s signal from the standby OC3 (1.3 STD) circuit pack. The standby OC3 (1.3 STD) circuit pack becomes the new active OC3 (1.3 STD) circuit pack.

In the drop (OC-48 to OC-3) direction, the OC48 RCVR circuit pack routes the same 155 Mb/s signal to the active and standby OC3 (1.3 STD) circuit packs.

Optical Low Speed Protection Switching Example

Figure 9-13 shows two OC-3 lines (only one direction of transmission is shown) and two FT-2000 OC-48 Add/Drop-Rings Terminals. The FT-2000 OC-48 Add/Drop-Rings Terminal at Office A transmits service traffic on both OC-3 lines. The FT-2000 OC-48 Add/Drop-Rings Terminal at Office B monitors the two OC-3 lines independently and chooses one OC-3 line as the active line and the other OC-3 line as the standby line.

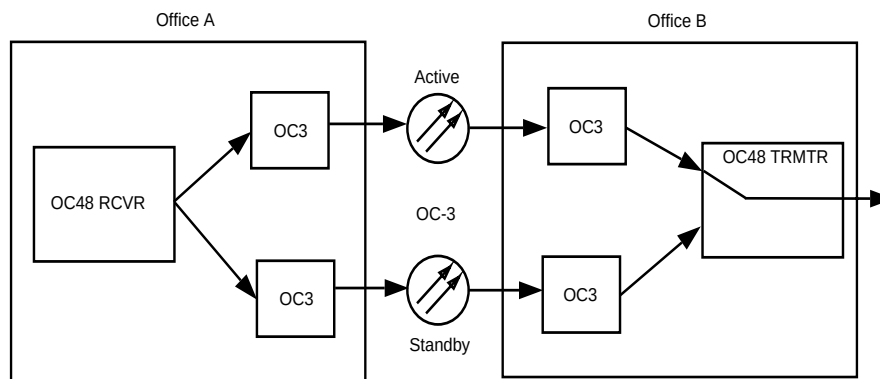


Figure 9-13. 1+1 OC-3 Line (Normal Transmission)

Figure 9-14 shows two OC-3 lines (only one direction of transmission is shown) and two FT-2000 OC-48 Add/Drop-Rings Terminals when a fiber cut occurs on the active OC-3 line. Automatic 1+1 OC-3 line protection switching occurs in response to a signal fail or signal degrade condition on the incoming OC-3 signal.

Optical low-speed protection switching is only performed in the add (OC-3 to OC-48) direction by the FT-2000 OC-48 Add/Drop-Rings Terminal at Office B. The board controller on the OC48 TRMTR circuit pack activates the selector circuitry to choose the 155 Mb/s or 622 Mb/s signal from the standby OC3 (1.3 STD) or OC12 (1.3 STD) circuit pack. The standby OC3 (1.3 STD) circuit pack becomes the new active OC3 (1.3 STD) circuit pack. The original active OC-3 line becomes the standby OC-3 line.

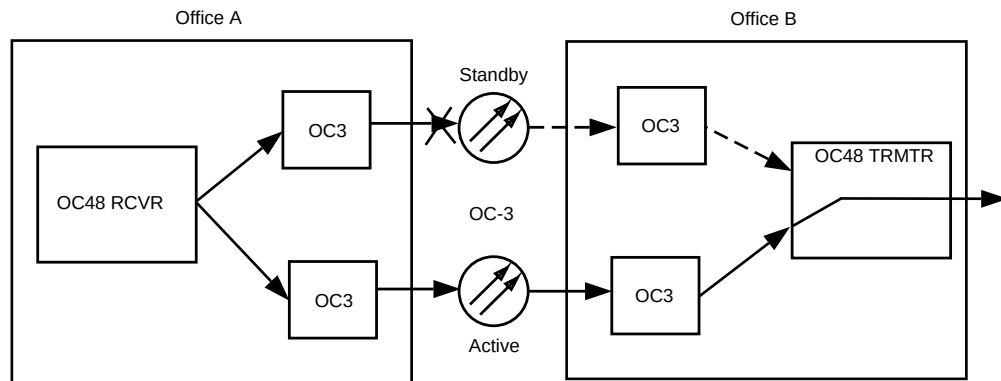


Figure 9-14. 1+1 OC-3 Line (Fiber Cut)

Optical Low Speed Protection — (OC-12)

General

The FT-2000 OC-48 Add/Drop-Rings Terminal equipped with Release 5.0.1-ADR or later software provides optical low-speed protection of an OC-12 line. Optical low-speed protection switching takes place in response to automatically detected faults and external commands from a local/remote CIT or operations system. Refer to the **FAULT-Switch-OC12** input in Volume I, Section 11, "Craft Interface Terminal Usage."

**NOTE:**

For intra-office applications, the IS-3 Optical Interface may be used and it retains all of the same considerations given to the OC-12.

Table 9-7 shows the standard protection switching priorities in descending order of priority used by the FT-2000 OC-48 Add/Drop-Rings Terminal.

Table 9-7. Optical Low Speed Protection Switching Priorities

Switch Priority (Descending Order)	Source of Request
Reset*	CIT/OS
Switch inhibit	CIT/OS
APS channel failure	Automatic
Lockout of protection	CIT/OS
Lockout of service	CIT/OS
Forced switch	CIT/OS
Signal fail	Automatic
Signal degrade	Automatic
Manual switch	CIT/OS

* Resetting the OC-12 switch priority

(**FAULT-Switch-OC12-Priority:Reset**) has no effect on any automatic switch priority.

An OC-12 line may operate unprotected (0x1) or use provisionable unidirectional 1+1 nonrevertive protection switching. In unidirectional 1+1 nonrevertive switching, the transmitting terminal transmits the same signal on two lines. The receiving terminal monitors the two lines independently and chooses one line as the active line and the other line as the standby line. When a protection switch occurs, the receiving terminal selects the signal from the standby line which causes the standby line to be the new active line. The original active line becomes the standby line. This status remains the same (nonrevertive) after the fault clears. Protection switching is performed in one direction only (unidirectional).

OC-12/IS-3 Specific Information

The following is required for 1+1 OC-12 line protection:

- Two adjacent slots (for example, LS INTFC slot 2A and 2B) of the Low Speed Shelf - System Controller must be equipped (EQ state) with the same OC12 Optical Interface - 1.3 μ m Standard Performance [OC12 (1.3 STD)] circuit packs (either LAA10 or LAA10B, but not a mix of the two). The Low Speed Shelf - System Controller can be equipped with 8 1+1 protected OC12 (1.3 STD) circuit packs, or 16 unprotected OC12 circuit packs, or a mixture of protected and unprotected OC12 (1.3 STD) circuit packs.
- The OC12 Protection Mode parameter of both slots must be provisioned "1+1" using the **CONFIGURATION-Set -OC12** input. To provision for "1+1" configuration, both slots **MUST** be equipped with either the OC-12 (1.3 STD) LAA10 circuit pack or the OC-12 (1.3 STD) LAA10B circuit pack (but not a mix of the two). For example, a "1+1" configuration will not work if slot 2A is equipped with an LAA10 circuit pack, while slot 2B is equipped with an LAA10B circuit pack. The OC12 Protection Mode parameter has separate values for the add (OC-12 to OC-48) and drop (OC-48 to OC-12) directions. For information about provisioning the OC12 Protection Mode parameter, refer to the information describing the **CONFIGURATION-Set -OC12** input in Volume I, Section 8, "Administration and Provisioning."
- One slot of the slot pair must be cross-connected to a service STS-3 tributary using the **CONFIGURATION-Enter -Crossconnection-STS3** input. In Release 7 and later releases, one port of the slot pair must be cross-connected to a service STS-1 tributary using the **CONFIGURATION-Enter -Crossconnection-STS1** input. The FT-2000 OC-48 Add/Drop-Rings Terminal automatically enters an active cross-connection between one slot of the slot pair and an STS-3 tributary, and a standby cross-connection between the companion slot and the same STS-3 tributary. The slot pair cannot be cross-connected to different STS-3 tributaries. In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal automatically enters an active cross-connection between one port of the slot pair and an STS-1 tributary, and a standby cross-connection between the same port of the companion slot and the same STS-1 tributary. The ports cannot be cross-connected to different STS-1 tributaries. For information about entering cross-connections, refer to Volume I, Section 8, "Administration and Provisioning."

Unprotected (0x1) OC-12 line operation results when one or more of the following occur:

- The slot and/or the adjacent slot is equipped (EQ state) with DS3 or STS1E circuit packs, or is in the AUTO state.
- The OC12 Protection Mode parameter of the slot and/or the adjacent slot is provisioned "0x1" using the **CONFIGURATION-Set -OC12** input.
- The slot (or port) and the adjacent slot (or port) are cross-connected to different tributaries.

Optical Low Speed Protection Switching Architecture

Figure 9-15 shows the optical low-speed protection switching architecture.

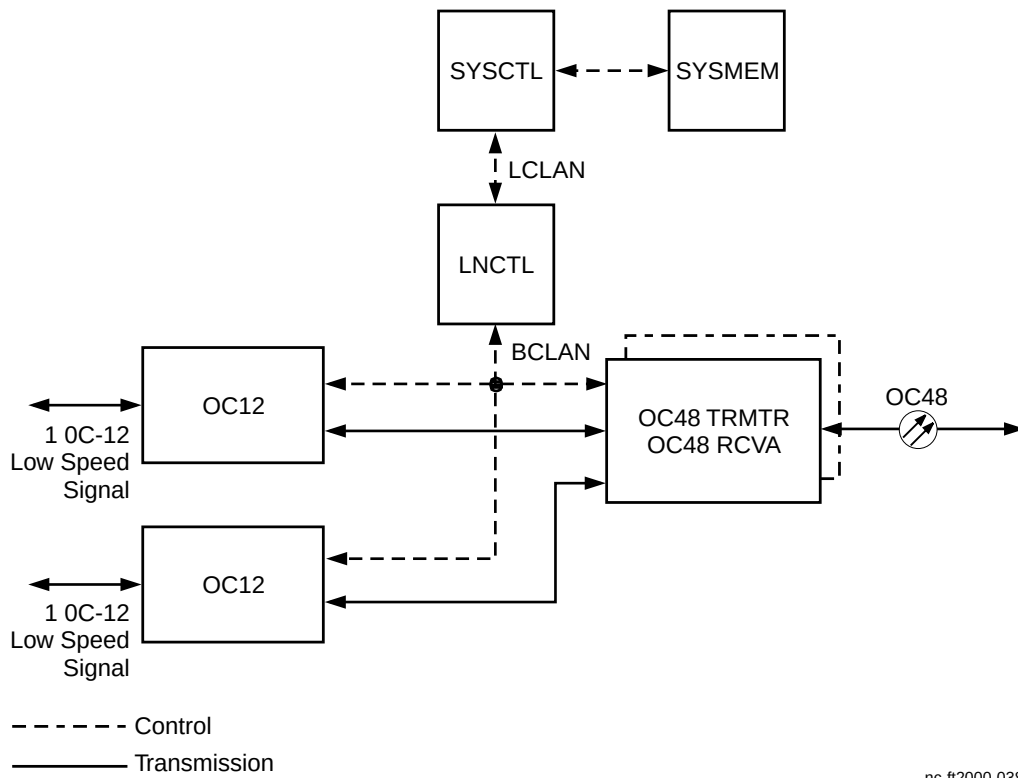


Figure 9-15. Optical Low Speed Protection Switching

Optical low-speed protection switching is only performed in the add (OC-12 to OC-48) direction by an FT-2000 OC-48 Add/Drop-Rings Terminal. If the active OC-12 line fails in the add (OC-12 to OC-48) direction, the board controller on the OC12 (1.3 STD) circuit pack sends the information to the LNCTL circuit pack via the BCLAN. If appropriate, the LNCTL circuit pack broadcasts a switch request to the board controller on the OC48 TRMTR circuit pack. The board controller on the OC48 TRMTR circuit pack activates the selector circuitry to choose the 155 Mb/s signal from the standby OC12 (1.3 STD) circuit pack. The standby OC12 (1.3 STD) circuit pack becomes the new active OC12 (1.3 STD) circuit pack.

In the drop (OC-48 to OC-12) direction, the OC48 RCVR circuit pack routes the same 155 Mb/s signal to the active and standby OC12 (1.3 STD) circuit packs.

Optical Low Speed Protection Switching Example

Figure 9-16 shows two OC-12 lines (only one direction of transmission is shown) and two FT-2000 OC-48 Add/Drop-Rings Terminals. The FT-2000 OC-48 Add/Drop-Rings Terminal at Office A transmits service traffic on both OC-12 lines. The FT-2000 OC-48 Add/Drop-Rings Terminal at Office B monitors the two OC-12 lines independently and chooses one OC-12 line as the active line and the other OC-12 line as the standby line.

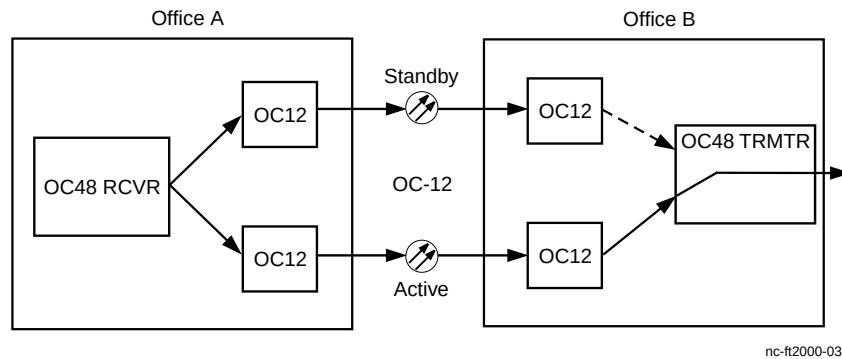


Figure 9-16. 1+1 OC-12 Line (Normal Transmission)

Figure 9-14 shows two OC-12 lines (only one direction of transmission is shown) and two FT-2000 OC-48 Add/Drop-Rings Terminals when a fiber cut occurs on the active OC-12 line. Automatic 1+1 OC-12 line protection switching occurs in response to a signal fail or signal degrade condition on the incoming OC-12 signal.

Optical low-speed protection switching is only performed in the add (OC-12 to OC-48) direction by the FT-2000 OC-48 Add/Drop-Rings Terminal at Office B. The board controller on the OC48 TRMTR circuit pack activates the selector circuitry to choose the 155 Mb/s or 622 Mb/s signal from the standby OC12 (1.3 STD) or OC12 (1.3 STD) circuit pack. The standby OC12 (1.3 STD) circuit pack becomes the new active OC12 (1.3 STD) circuit pack. The original active OC-12 line becomes the standby OC-12 line.

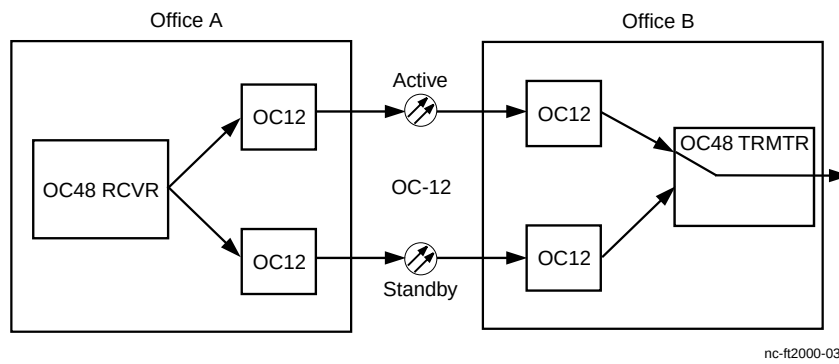


Figure 9-17. 1+1 OC-12 Line (Fiber Cut)

Dual Ring Interworking Protection

In Release 4.1 and later releases, the FT-2000 OC-48 Add/Drop-Ring Terminal supports dual ring interworking (DRI) protection switching. DRI protection switching protects a circuit with a termination in one ring and the other termination in another ring when an interconnecting node fails. DRI protection switching takes place in response to an automatically detected incoming SONET EC-1 or DS3 loss-of-signal (LOS) condition or external commands from a local/remote CIT or operations system. In Release 5 and later releases, DRI protection switching also takes place in response to an automatically detected unprotected OC-3/IS-3/OC-12 LOS or loss-of-frame (LOF) condition. Additionally, a path AIS (AIS-P) triggers a DRI switch. Refer to the **FAULT-Switch-Path-STS3** input in Volume I, Section 11, "Craft Interface Terminal Usage."

**NOTE:**

With Release 7.1.0 and later software, a path AIS (AIS-P) triggers a DRI switch. Furthermore, Release 7.1.0 and later releases provide revertive DRI and thus do automatically switch back to the service fiber.

Table 9-8 shows the standard protection switching priorities in descending order of priority used by the FT-2000 OC-48 Add/Drop-Rings Terminal.

Table 9-8. Dual Ring Interworking Protection Switching Priorities

Switch Priority (Descending Order)	Source of Request
Reset*	CIT/OS
DS3/EC1 loss of signal (LOS)†	Automatic
Unprotected OC-3/OC-12 LOS or loss of frame (LOF)†	Automatic
Manual switch	CIT/OS

* Reset performed using the **FAULT-Switch-Path-STS3** or the **FAULT-Switch-Path-STS1** input.

† A DS3/EC1 LOS and an unprotected OC-3/OC-12 LOS or LOF are the same priority.

For Release 7.0 and earlier releases, DRI protection switching is unidirectional and manually revertive. Unidirectional refers to when protection switching is *performed in one direction only*. For a given 1-way circuit, DRI protection switching is only performed in the add direction. When a fault clears, traffic switches from the primary to secondary node. When the fault clears, traffic does *not* automatically switch back (revert) to the primary node. Therefore, if the secondary node fails, traffic will not automatically switch back (revert) to the primary node. A reset *must* be performed using the **FAULT - SWITCH - PATH - STS3** or **FAULT - SWITCH - PATH - STS1** input to clear an active manual or automatic DRI switch.

The DRI Auto Switch or DRI Manual Switch condition indicates a DRI switch is active. These conditions are shown in the reports generated by the **FAULT-Retrieve-Alarm (and Status)** and **FAULT-Retrieve-Network Alarms** inputs. Table 9-9 provides a summary of the DRI feature capabilities.

Table 9-9. Summary of DRI Feature Capabilities

Parameter Functionality	Release				
	4.01	5.01	7.01	7.10	7.2
Unidirectional	X	X	X		
Revertive				X	X
Alarm/Event					
*LOS(DS3)	X	X	X	X	X
*LOS(EC1, OC3)	X	X	X	X	X
*LOS(IS3/OC12)				X	X
*LOF(EC1, OC3/IS3, OC12)	X	X	X	X	X
AIS-P(EC1, OC3/IS3, OC12)				X	X*
*STS Path Unequipped					X
*SD (Signal Degrade)					X
*SF (Signal Fail)					X
* Generates an alarm (or near end activity) to the local CIT and corresponding TL1 autonomous messages (alarm or event) are sent to the OS.					
+ In the event an AIS-P signal is detected on an STS-1 that is not DRI protected——no CIT reports and TL1 autonomous messages will NOT be generated.					

Figure 9-18 shows a dual ring interworking application with two rings and a 2-way DRI circuit. One ring is a line-switched ring equipped with the FT-2000 OC-48 Add/Drop-Rings Terminals. The other ring could be another line-switched ring equipped with FT-2000 OC-48 Add/Drop-Rings Terminals or a path-switched ring equipped with DDM-2000 OC-3 Multiplexers (Release 7.0) or DDM-2000 OC-12 Multiplexers (Release 3.0).

The 2-way DRI circuit is routed to/from the termination node (node 5) in the FT-2000 line-switched ring through the primary node (node 4) and secondary node (node 3) to the other ring. (The primary and secondary nodes are not required to be adjacent nodes.)

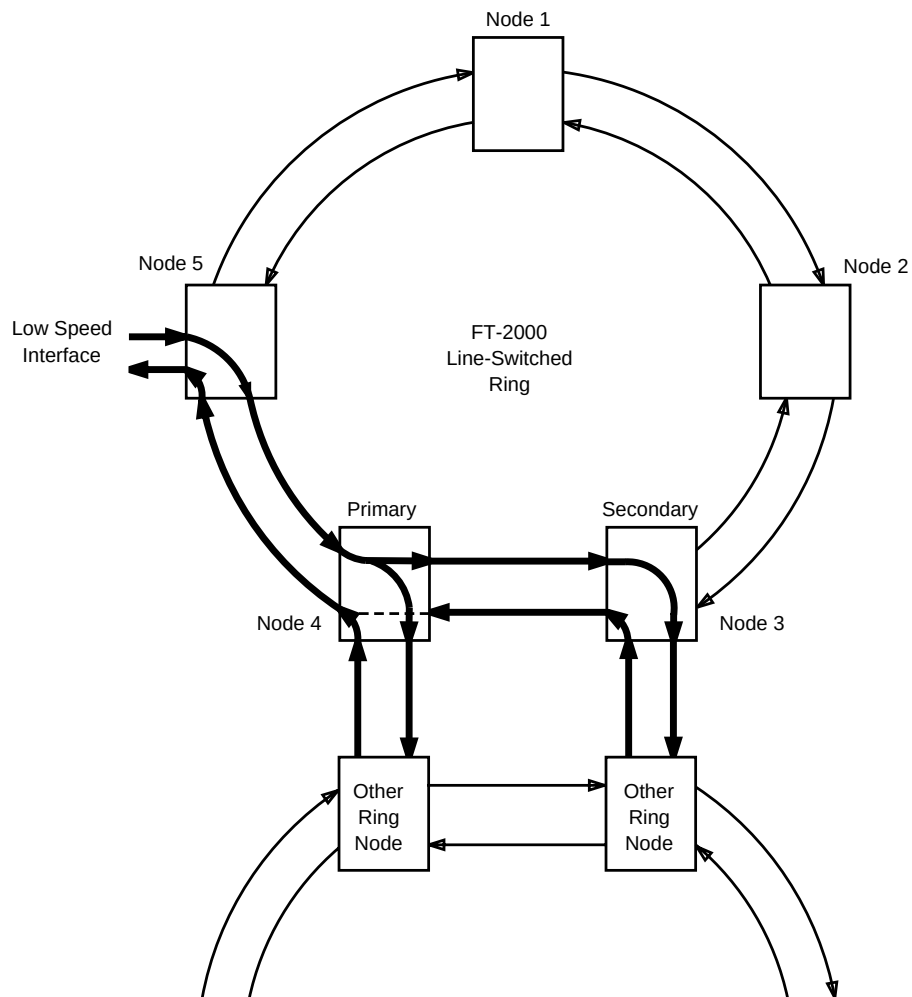


Figure 9-18. Dual Ring Interworking Circuit (Normal Transmission)

A 2-way DRI circuit consists of cross-connections in the drop direction (from the FT-2000 line-switched ring to another ring) and cross-connections in the add direction (to the FT-2000 line-switched ring from another ring). The following cross-connections must be entered in the drop direction (Figure 9-18):

- At the termination node (node 5), a 1-way add cross-connection is entered. When entering the 1-way add cross-connection, the secondary node (node 3) must be specified as the destination node. (If an STS-3 tributary is bridged at one or more nodes in a ring, the last node where the low-speed signals are terminated must be specified as the destination node.)

- At the primary node (node 4), a 1-way drop cross-connection and a 1-way through cross-connection from the same STS-3 tributary are entered. This creates a bridge (drop and continue) cross-connection. When entering the drop and through cross-connections, the termination node (node 5) must be specified as the source node. When entering the through cross-connection, the secondary node (node 3) must be specified as the destination node.
- At the secondary node (node 3), a 1-way drop cross-connection from the same STS-3 tributary is entered. When entering the drop cross-connection, the termination node (node 5) must be specified as the source node.

The following cross-connections are entered in the add direction (Figure 9-18):

- At the primary node (node 4), a 1-way-DRI cross-connection is entered. A 1-way-DRI cross-connection consists of a 1-way add cross-connection and a 1-way through cross-connection. When entering the 1-way-DRI cross-connection, the termination node (node 5) must be specified as the destination node, and node 3 must be specified as the secondary node. Initially, the add cross-connection is in the active state and the through cross-connection is in the standby state.
- At the secondary node (node 3), a 1-way add cross-connection to the same STS-3 tributary is entered. When entering the 1-way add cross-connection, the termination node (node 5) must be specified as the destination node.
- At the termination node (node 5), a 1-way drop cross-connection from the same STS-3 tributary is entered. When entering the drop cross-connection, the secondary node (node 3) must be specified as the source node.

The add and drop directions are completely independent. During normal transmission, traffic is routed from the termination node (node 5) through the primary node (node 4) and secondary node (node 3) to the other ring. Traffic from the other ring is routed through the primary node (node 4) to the termination node (node 5).

Figure 9-19 shows a dual ring interworking application with a node failure in the other ring causing an unprotected incoming low-speed loss-of-signal condition at the primary node (node 4) in the FT-2000 line-switched ring.

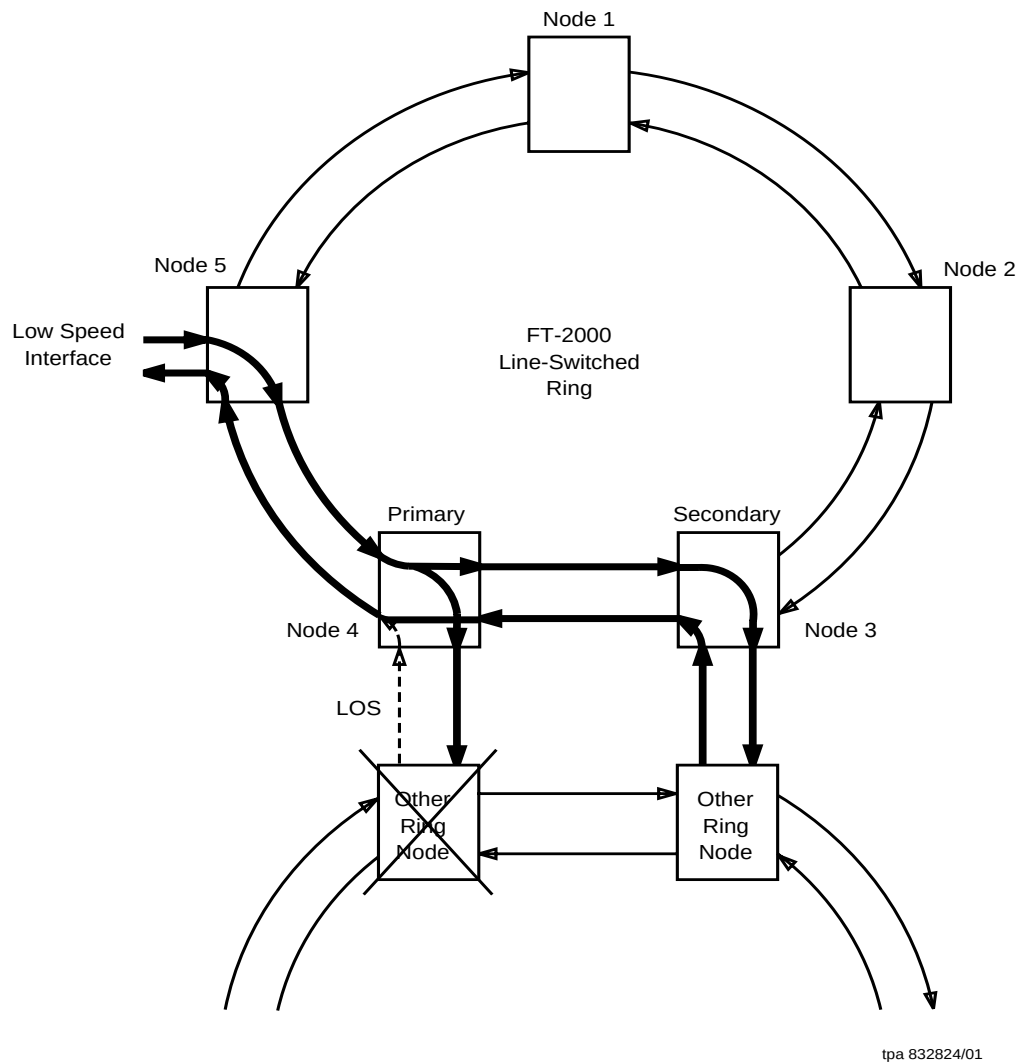


Figure 9-19. Dual Ring Interworking Circuit (Node Failure in Other Ring)

When an unprotected incoming low-speed loss of signal (LOS) or OC-3 loss of frame (LOF) is detected by the low-speed interface circuit pack at the primary node (node 4), the low-speed interface circuit pack notifies the LNCTL circuit pack via the BCLAN. The LNCTL circuit pack enters an automatic DRI switch request and sends it to the board controller on the OC48 TRMTR (A/D) circuit pack. An LOS condition on one or more of the DS3 or EC-1 inputs to a low-speed interface circuit pack or an unprotected OC-3 LOS/LOF condition results in an automatic DRI switch request for that STS-3 tributary or STS-1 tributary (Release 7 and

later). A DRI protection switch will not occur in response to any other low-speed line failure (for example, AIS or signal degrade).

The board controller on the OC48 TRMTR (A/D) circuit pack activates the selector circuitry to choose the incoming STS-3 tributary or STS-1 tributary from the secondary node. This causes the through cross-connection at the primary node to switch to the active state and the add cross-connection to switch to the standby state. This completes the DRI circuit through the secondary node.

An automatic or manual DRI protection switch request will complete successfully even if there is a corresponding incoming loss of signal condition at the secondary node (node 3). The primary node does not monitor the condition of the incoming low-speed signal at the secondary node.

The DRI protection switch will not work if the DRI circuit is being used for extra traffic and a high-speed protection switch is active.

Figure 9-20 shows a dual ring interworking application with a primary node failure.

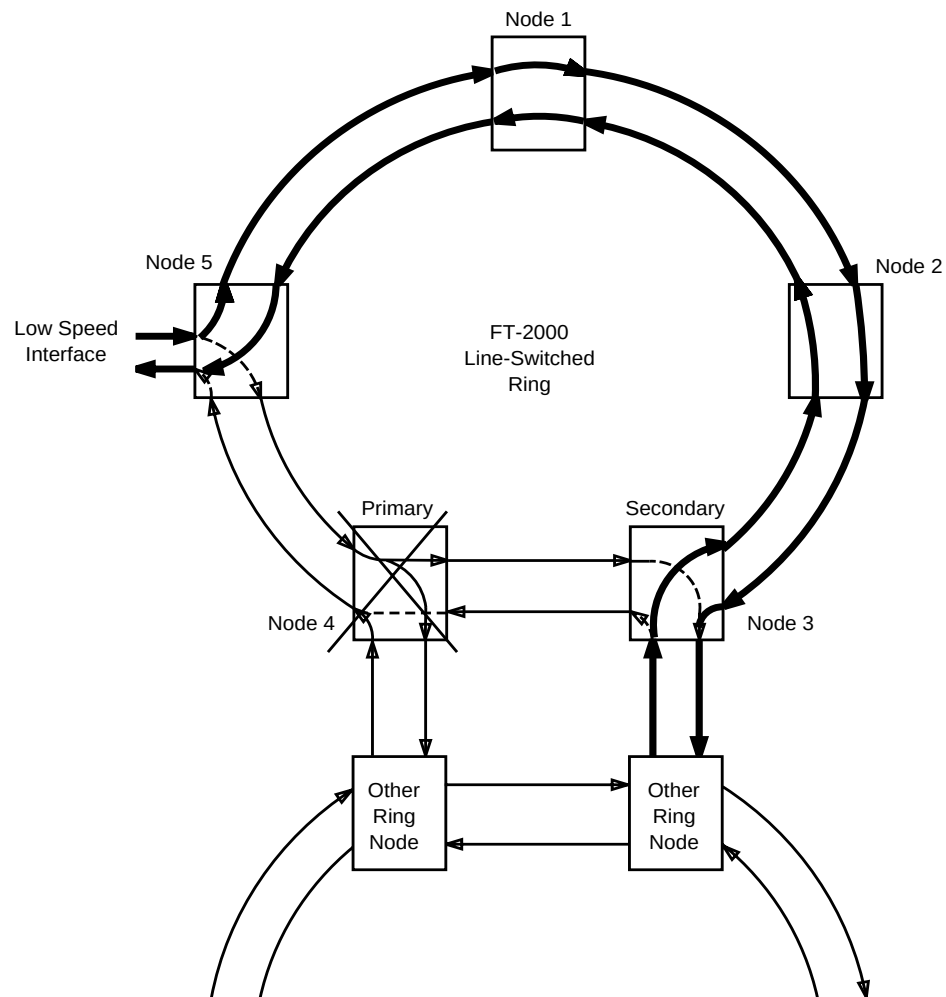


Figure 9-20. Dual Ring Interworking Circuit (Primary Node Failure)

When an incoming OC-48 loss-of-signal condition is detected by the secondary node (node 3) and the termination node (node 5), the secondary node and the termination node perform 2-fiber ring protection switching. The termination node routes traffic on the DRI circuit around the ring through the secondary node to the other ring. Traffic from the other ring is routed through the secondary node (node 3) to the termination node (node 5). For more information about 2-fiber ring protection switching, refer to "2-Fiber Ring Protection Switching" in this section.

Figure 9-21 shows a dual ring interworking application with a secondary node failure.

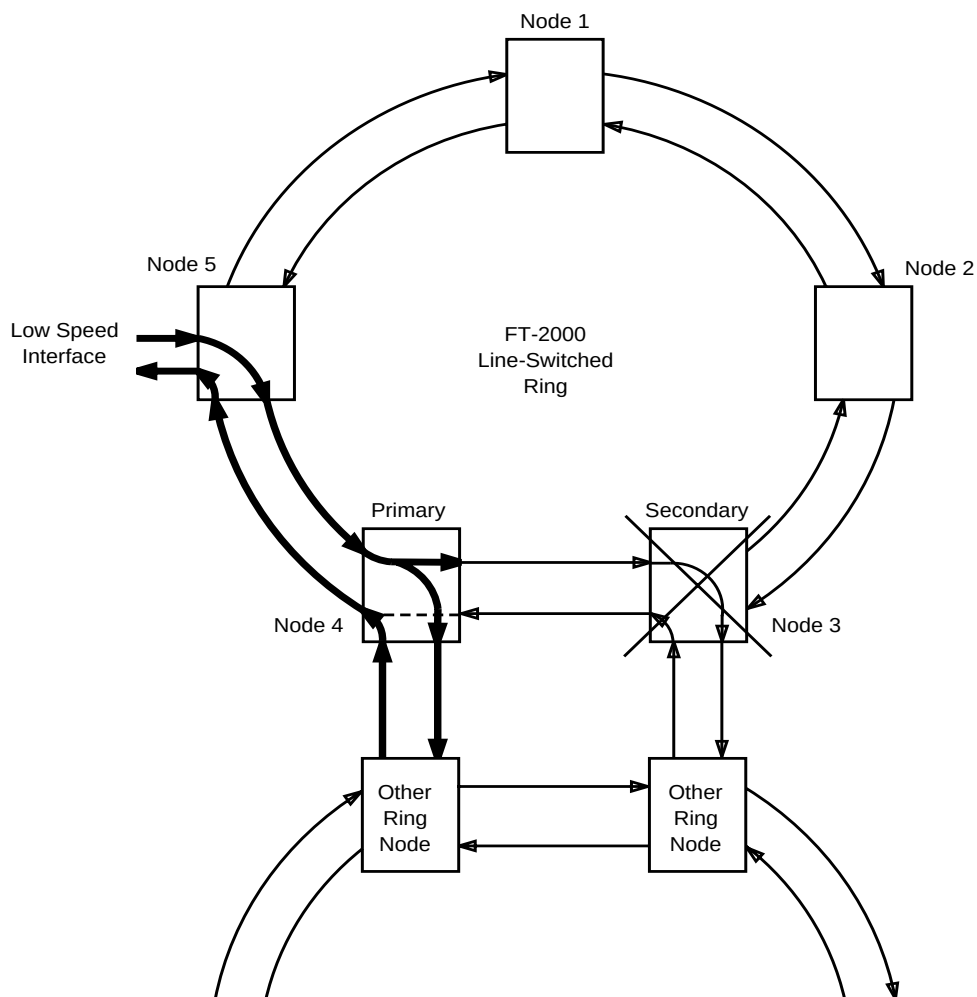


Figure 9-21. Dual Ring Interworking Circuit (Secondary Node Failure)

When an incoming OC-48 loss of signal condition is detected by the primary node (node 4) and node 2, the primary node (node 4) and node 2 perform 2-fiber ring protection switching. The 2-fiber ring protection switch does not affect the DRI circuit.

For more information about the DRI application, refer to Volume I, Section 3, "Applications." For more information about cross-connections, refer to Volume I, Section 8, "Administration and Provisioning."

Synchronization Protection

General

The FT-2000 OC-48 Lightwave System provides three types of synchronization protection: synchronization hardware protection, synchronization reference protection, and synchronization mode protection.

Synchronization Hardware Protection

General

Synchronization hardware protection switching occurs in response to automatically detected faults and external commands from a local/remote CIT or operations system. Refer to the **FAULT-Switch-Synchronization** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Table 9-10 shows the standard protection switching priorities in descending order of priority used by the FT-2000 OC-48 Add/Drop-Rings Terminal.

Table 9-10. Synchronization Hardware Protection Switching Priorities

Switch Priority (Descending Order)	Source of Request
Reset*	CIT/OS
Circuit pack failure/removal	Automatic
Manual switch	CIT/OS
Circuit pack warming up	Automatic

* Resetting the synchronization switch priority (**FAULT-Switch-Synchronization**- Hardware-Priority:Reset) has no effect on any automatic switch priority.

The FT-2000 OC-48 Lightwave System provides protection for all the synchronization circuit packs. The Timing Generator, Stratum 3 - DS1 [TG3 (DS1)] circuit packs are 1x1 protected and use revertive switching. The FT-2000 OC-48 Add/Drop-Rings Terminal must be equipped with two TG3 (DS1) circuit packs on the Low Speed Shelf - System Controller.

FT-2000 OC-48 Add/Drop-Rings Terminal

Figure 9-22 shows the TG3 (DS1) circuit pack protection architecture of the FT-2000 OC-48 Add/Drop-Rings Terminal.

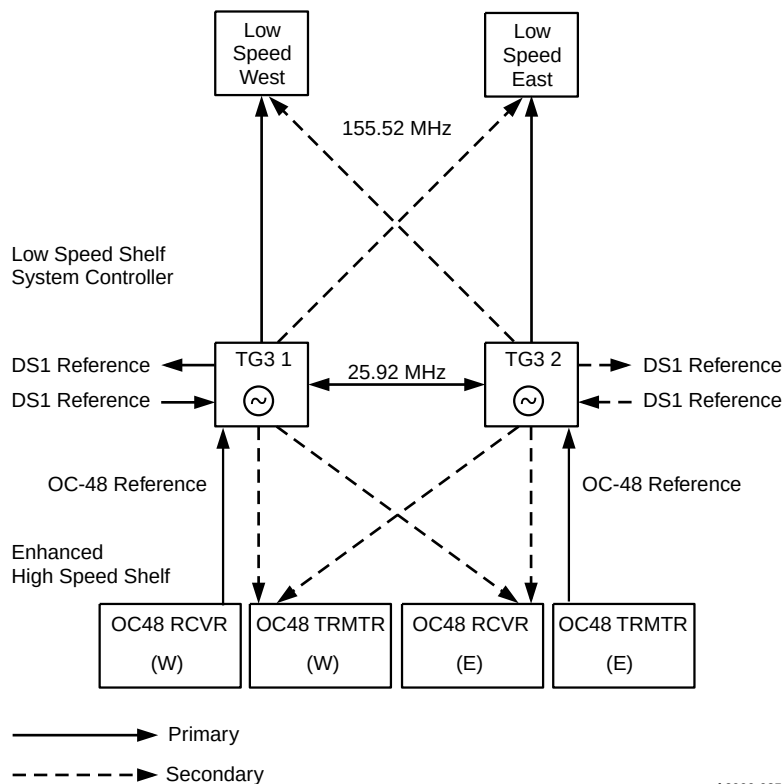


Figure 9-22. TG3 (DS1) Circuit Pack Protection (FT-2000 OC-48 Add/Drop-Rings Terminal)

The number 1 TG3 (DS1) circuit pack on the Low Speed Shelf - System Controller provides the following:

- Primary timing to the STS1E, IS3, OC3 (1.3 STD) and OC12 (1.3 STD) circuit packs that terminate add/drop traffic to/from the OC-48 high-speed line 1W and the DS3 circuit packs that terminate add/drop traffic to/from the OC-48 high-speed line 1E
- Primary timing to the east OC48 TRMTR circuit pack on the Enhanced High Speed Shelf (or high-speed Shelf)
- Secondary timing to the STS1E, IS3, OC3 (1.3 STD) and OC12 circuit packs that terminate add/drop traffic to/from the OC-48 high-speed line 1E and the DS3 circuit packs that terminate add/drop traffic to/from the OC-48 high-speed line 1W
- Secondary timing to the west OC48 TRMTR circuit pack on the Enhanced High Speed Shelf (or high-speed Shelf)

- An internal synchronization signal to the number 2 TG3 (DS1) on the Low Speed Shelf - System Controller.

The number 2 TG3 (DS1) circuit pack on the Low Speed Shelf - System Controller provides the following:

- Primary timing to the STS1E, IS3, OC3 (1.3 STD) and OC12 (1.3 STD) circuit packs that terminate add/drop traffic to/from the OC-48 high-speed line 1E and the DS3 circuit packs that terminate add/drop traffic to/from the OC-48 high-speed line 1W
- Primary timing to the west OC48 TRMTR circuit pack on the Enhanced High Speed Shelf (or High Speed Shelf)
- Secondary timing to the STS1E, IS3, OC3 (1.3 STD) and OC12 (1.3 STD) circuit packs that terminate add/drop traffic to/from the OC-48 high-speed line 1W and the DS3 circuit packs that terminate add/drop traffic to/from the OC-48 high-speed line 1E
- Secondary timing to the east OC48 TRMTR circuit pack on the Enhanced High Speed Shelf (or High Speed Shelf)
- An internal synchronization signal to the number 1 TG3 (DS1) on the Low Speed Shelf - System Controller.

If the number 1 TG3 (DS1) circuit pack fails, the number 2 TG3 (DS1) circuit pack will provide timing for the transmit circuits. Conversely, if the number 2 TG3 (DS1) circuit pack fails, the number 1 TG3 (DS1) circuit pack will provide timing.

Synchronization Reference Protection

General

Synchronization reference protection switching occurs in response to automatically detected faults and external commands from a local/remote CIT or operations system. Refer to the **FAULT-Switch-Synchronization** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Table 9-11 shows the standard protection switching priorities in descending order of priority used by the FT-2000 OC-48 Add/Drop-Rings Terminal.

Table 9-11. Synchronization Reference Protection Switching Priorities

Switch Priority (Descending Order)	Source of Request
Reset*	CIT/OS
Lockout	CIT/OS
Forced switch	CIT/OS
Signal failure	Automatic
Manual switch	CIT/OS
Wait to restore	Automatic

* Resetting the synchronization switch priority
(**FAULT-Switch-Synchronization**- Reference-Priority:Reset) has no effect on any automatic switch priority except wait to restore.

DS1 Reference Protection

In the externally timed mode (phase locked mode in releases prior to Release 5), the TG3 (DS1) circuit packs on the Low Speed Shelf - System Controller derive timing from two external DS1 reference signals as shown in Figure 9-23.

The number 1 TG3 (DS1) circuit pack accepts the primary DS1 reference signal and the number 2 TG3 (DS1) circuit pack accepts the secondary DS1 reference signal.

If the primary DS1 reference signal fails, the FT-2000 OC-48 Add/Drop-Rings Terminal switch to the secondary DS1 reference signal and the number 2 TG3 (DS1) circuit pack. If both DS1 reference signals fail, the TG3 (DS1) circuit pack holds the on-board oscillator frequency at the last good reference sample (holdover mode) until the DS1 reference signals are repaired. In the holdover mode, the on-board oscillator frequency will not degrade below the stratum 3 level.

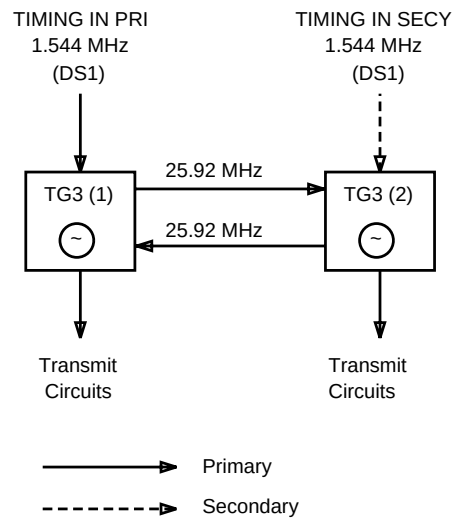


Figure 9-23. DS1 Reference Protection

OC-48 Line Reference Protection

FT-2000 OC-48 Add/Drop-Rings Terminal . When the FT-2000 OC-48 Add/Drop-Rings Terminal is provisioned for the through timed mode, the TG3 (DS1) circuit packs on the Low Speed Shelf - System Controller derive timing from two 25.92 MHz reference signals obtained from the incoming OC-48 signals as shown in Figure 9-24.

The west OC48 RCVR circuit pack recovers the 2.5 GHz clock signal from the incoming OC-48 signal on OC-48 high-speed line 1W and generates the primary 25.92 MHz reference signal for the number 1 TG3 (DS1) circuit pack. The east OC48 RCVR circuit pack recovers the 2.5 GHz clock signal from the incoming OC-48 signal on OC-48 high-speed line 1E and generates the primary 25.92 MHz reference signal for the number 2 TG3 (DS1) circuit pack.

If an OC-48 line reference signal or the OC-48 high-speed line fails, the TG3 (DS1) circuit pack switches to the secondary OC-48 line reference signal. If both OC-48 line reference signals fail, the TG3 (DS1) circuit pack holds the on-board oscillator frequency at the last good reference sample (holdover mode). In the holdover mode, the on-board oscillator frequency will not degrade below the stratum 3 level.

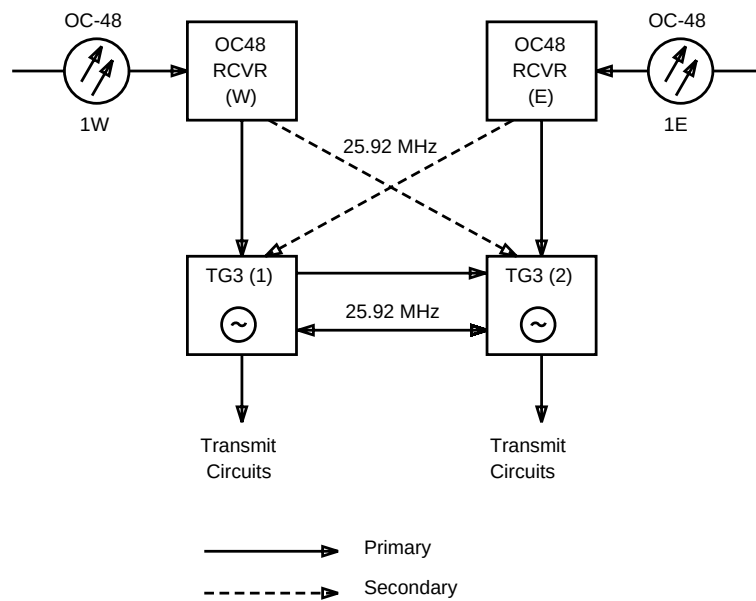


Figure 9-24. OC-48 Line Reference Protection (FT-2000 OC-48 Add/Drop-Rings Terminal)

Synchronization Mode Protection

Synchronization mode protection switching occurs in response to automatically detected faults and external commands from a local/remote CIT or operations system. Refer to the **FAULT-Switch-Synchronization** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Table 9-12 shows the standard protection switching priorities in descending order of priority used by the FT-2000 OC-48 Add/Drop-Rings Terminal.

Table 9-12. Synchronization Mode Protection Switching Priorities

Switch Priority (Descending Order)	Source of Request
Reset*	CIT/OS
Forced switch	CIT/OS
Synchronization reference failure	Automatic

* Resetting the synchronization switch priority
(**FAULT-Switch-Synchronization- Mode-Priority:Reset**) has no effect on any automatic switch priority.

If the FT-2000 OC-48 Add/Drop-Rings Terminal is provisioned to operate in the through timed mode, or externally timed mode (phase locked in releases prior to Release 5) and all reference signals fail, the TG3 (DS1) circuit pack holds the on-board oscillator frequency at the last good reference sample (holdover mode) until the DS1 reference signals are repaired. In the holdover mode, the on-board oscillator frequency will not degrade below the stratum 3 level.

Loopbacks

Low Speed (DS3, EC-1, OC-3, and OC-12)

The FT-2000 OC-48 Lightwave System allows the following loopbacks to be performed:

- A low-speed DS3 loopback allows a single DS3 signal or all the DS3 signals to be looped back toward the OC-48 high-speed line. The DS3 loopback only allows a single DS3 signal to be looped back toward the OC-48 high-speed line. The input DS3 signal(s) from the DSX-3 cross-connect panel are preempted by this loopback, but the output DS3 signal(s) to the DSX-3 cross-connect panel remain connected.
- A low-speed EC-1 loopback allows an EC-1 signal or all the EC-1 signals to be looped back toward the OC-48 high-speed line. In Release 6.0.1, the EC-1 loopback only allows a single EC-1 signal to be looped back toward the OC-48 high-speed line. The input EC-1 signal(s) from the STSX-1 cross-connect panel are preempted by this loopback, but the output EC-1 signal(s) to the STSX-1 cross-connect panel remain connected.
- Low-speed OC-3 loopbacks allow a single OC-3 signal to be looped back toward the OC-48 high-speed line or toward the OC-3 line. A loopback may be active in only one direction at a time. Simultaneous loopbacks in both directions are not supported. When a loopback is established toward the OC-48 high-speed line, the input OC-3 signal from the OC-3 line is preempted, but the output OC-3 signal transmitted toward the OC-3 line remains connected. When a loopback is established toward the OC-3 line, the OC-3 signal from the OC-48 high-speed line is preempted, but the OC-3 signal transmitted toward the OC-48 high-speed line remains connected.
- Low-speed OC-12 loopbacks allow a single OC-12 signal to be looped back toward the OC-48 high-speed line or toward the OC-12 line. A loopback may be active in only one direction at a time. Simultaneous loopbacks in both directions are not supported. When a loopback is established toward the OC-48 high-speed line, the input OC-12 signal from the OC-12 line is preempted, but the output OC-12 signal transmitted toward the OC-12 line remains connected. When a loopback is established toward the OC-12 line, the OC-12 signal from the OC-48 high-speed line is preempted, but the OC-12 signal transmitted toward the OC-48 high-speed line remains connected.

These loopbacks are made electronically within the DS3, STS1E, OC3 (1.3 STD), and OC12 (1.3 STD) circuit packs in response to the

FAULT-Operate-Loopback-T3, FAULT-Operate-Loopback-EC1, FAULT-Operate-Loopback-OC3 and **FAULT-Operate-Loopback-OC12** inputs entered at the CIT. The **FAULT-Release-Loopback-T3, FAULT-Release-Loopback-EC1, FAULT-Release-Loopback-OC3, FAULT-Release-Loopback-OC12** inputs are used to take down loopbacks.

⇒ **NOTE:**
The **FAULT-Operate-Loopback-T3**, **FAULT-Operate-Loopback-EC1**, **FAULT-Operate-Loopback-OC3** and **FAULT-Operate-Loopback-OC12** inputs will complete regardless of whether the selected low-speed interface slot(s) is equipped (EQ).

⇒ **NOTE:**
For Release 7.2 and later, the **FAULT-Operate-Loopback-T3**, **FAULT-Operate-Loopback-EC1**, **FAULT-Operate-Loopback-OC12**, and **FAULT-Operate-Loopback-OC3** inputs will be denied if the pack inserted in the addressed slot is of the wrong type or the addressed slot is not equipped.

⇒ **NOTE:**
For Release 7.2 and later, the **FAULT-Operate-Loopback-T3**, **FAULT-Operate-Loopback-EC1**, **FAULT-Operate-Loopback-OC12**, and **FAULT-Operate-Loopback-OC3** inputs require that a LNCTL circuit pack be installed and operating in order to execute. If the LNCTL slot is in the AUTO state, if the LNCTL has been removed, or if the LNCTL has failed, the command shall be denied with error code EMCD_NOLNCTL.

In Release 4.1 and later releases, DS3 and EC-1 loopbacks can also be made through the message-based operations system interface, using X.25 protocol and Transaction Language 1 (TL1) messages. In Release 5 and later releases, OC-3 and OC-12 loopbacks can be made through the message-based operations system interface.

Active electronic loopbacks are noted by the abnormal (ABN) LED on the user panel and in the alarm and status report.

A remote DS3, EC-1, OC-3 or OC-12 loopback can be done through a remote login. This loops the remote low-speed signal back toward the local terminal.

High-Speed (OC-48)

The FT-2000 OC-48 Lightwave System allows loopbacks to be performed on all high-speed interfaces. Front access to the optical connectors on the circuit pack faceplates allows a manual OC-48 optical loopback. This loopback is performed by connecting the optical connector on the faceplate of the OC48 TRMTR circuit pack to the optical connector on the faceplate of the OC48 RCVR circuit pack with a single-mode fiber jumper and lightguide buildouts. A loopback can also be performed on an OC48 REGENR circuit pack by connecting the IN and OUT connector on the circuit pack faceplate with a single-mode fiber jumper and lightguide buildouts.

Tests

Auto Turnup

The auto turnup tests are performed by the FT-2000 OC-48 Lightwave System to verify that the system has been installed correctly. These tests verify the system transmission capabilities and include testing all the service and protection transmission paths to detect any cabling errors.

The auto turnup tests are controlled using the CIT and should only be performed on an out-of-service system. Before executing an auto turnup test at the CIT, no trouble conditions should be present in the system except conditions associated with the test setup. After executing an auto turnup test, the CIT will not accept any commands until the test is complete. An auto turnup test can be aborted at any time or terminated prematurely if a condition exists that prevents the test from completing. The test results are displayed on the CIT. For more information about auto turnup tests, refer to the "Operation and Maintenance (TOP)" section (Volume II).

The following auto turnup tests are provided:

- **Local (Terminal Self-Test):** The **FAULT-Test-Auto Turnup-Local** input verifies the internal health of the FT-2000 OC-48 Add/Drop-Rings Terminal itself. It tests the transmission path between the low-speed interface circuit packs [DS3, STS1E, OC-3, and OC12 (1.3 STD)] and the OC48 RCVR and OC48 TRMTR circuit packs (Figure 9-25). The FT-2000 OC-48 Add/Drop-Rings Terminal also tests the through and loopback transmission paths between OC48 RCVR and OC48 TRMTR circuit packs.

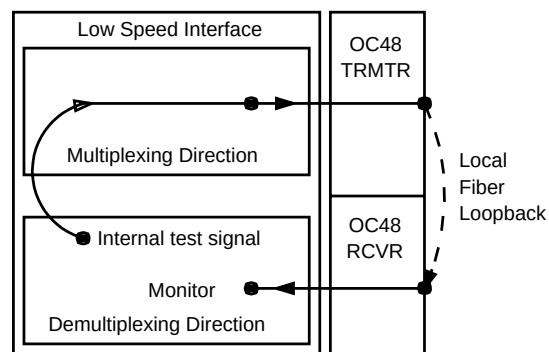


Figure 9-25. Local (Terminal Self-Test)

For the local test, the output of the OC48 TRMTR circuit pack must be looped back with a single-mode fiber jumper and a lightguide buildout to the input of the OC48 RCVR circuit pack on the line(s) to be tested. For example, when all lines are being tested, all OC48 TRMTR and OC48 RCVR circuit pack pairs must be manually looped back.

The FT-2000 OC-48 Add/Drop-Rings Terminal performs the following tests:

- To verify the continuity of each add/drop transmission path, each DS3 circuit pack inserts one parity bit error on each DS3 signal toward each high-speed line. The output of the OC48 TRMTR circuit pack is looped back to the OC48 RCVR circuit pack and the DS3 circuit pack monitors each DS3 signal from the high-speed line. If only one parity bit error is detected within 1 second, the test passes.

Rather than checking parity, the STS1E and OC3/OC12 (1.3 STD) circuit packs use special test signals with a known content to verify the continuity of add/drop transmission path.

- To verify the parity error rate of each add/drop transmission path, each DS3 circuit pack monitors each DS3 signal from the high-speed line for errors over a 4-second period. If no errors are detected in the 4-second period, the test passes.
- To verify the continuity of each through transmission path, the east OC48 RCVR circuit pack inserts one parity bit error on each STS-3 tributary toward the west OC48 TRMTR circuit pack. The west OC48 RCVR circuit pack also inserts one parity bit error on each STS-3 tributary toward the east OC48 TRMTR circuit pack. The OC48 TRMTR circuit packs monitor each STS-3 tributary. If only one parity bit error is detected within 1 second, the test passes.
- To verify the parity error rate of each through transmission path, each OC48 TRMTR circuit pack monitors each STS-3 tributary (from the OC48 RCVR circuit pack of the other high-speed line) for errors over a 4-second period. If no errors are detected in the 4-second period, the test passes.
- To verify the continuity of each loopback transmission path, the east OC48 RCVR circuit pack inserts one parity bit error on each STS-3 tributary toward the east OC48 TRMTR circuit pack. The west OC48 RCVR circuit pack also inserts one parity bit error on each STS-3 tributary toward the west OC48 TRMTR circuit pack. The OC48 TRMTR circuit packs monitor each STS-3 tributary. If only one parity bit error is detected within 1 second, the test passes.
- To verify the parity error rate of each loopback transmission path, each OC48 TRMTR circuit pack monitors each STS-3 tributary (from the OC48 RCVR circuit pack of the same high-speed line) for errors over a 4-second period. If no errors are detected in the 4-second period, the test passes.

The LSSW circuit packs are tested to verify they can perform a low-speed protection switch for each low-speed interface circuit pack. The transmission path through the LSSW circuit packs is tested in the DSX test.

Each TG3 (DS1) circuit pack is also tested individually to detect transmission and cross-cabling errors. The local test disables the reference signals on each TG3 (DS1) circuit pack and verifies the correct TG3 (DS1) circuit pack detects the loss of reference signal. If the correct TG3 (DS1) circuit pack detects the loss of reference signal, the test passes.

- **DSX (Low Speed Cabling Self-Test):** The **FAULT-Test-Auto Turnup-DSX** input verifies the low-speed cabling between the FT-2000 OC-48 Add/Drop-Rings Terminal and the cross-connect panel (or equivalent) was installed properly and supports transmission. The test verifies that the input and output low-speed interface cables are connected to the same port at the terminal and the cross-connect panel (or equivalent). The DSX test also performs low-speed switches for each electrical low-speed interface circuit pack (DS3 and STS1E) and tests the transmission path through the LSSW circuit packs. The LSSW circuit packs are in the transmission path between the protection electrical low-speed interface circuit pack and the cross-connect panel (or equivalent).

For the DSX test, the fiber(s) for the line(s) to be tested must be disconnected and each low-speed port of the line(s) to be tested must be looped back at the DSX-3 cross-connect panel (or equivalent). See Figure 9-26.

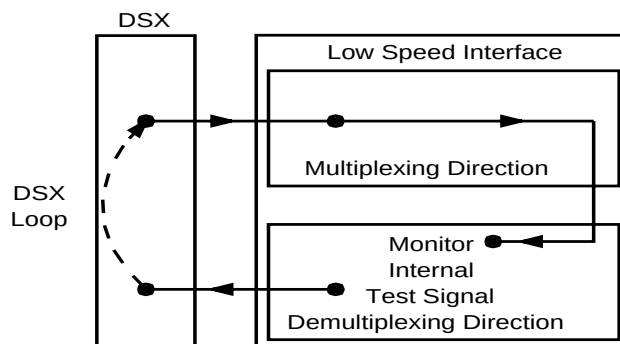


Figure 9-26. DSX (Low Speed Cabling Self-Test)

The following tests are performed:

- To verify the continuity of each transmission path, each DS3 circuit pack inserts one parity bit error on each DS3 signal toward the cross-connect panel (or equivalent). The DS3 signals are looped

back at the cross-connect panel and the DS3 circuit pack monitors each DS3 signal. If only one parity bit error is detected within 1 second, the test passes.

Rather than checking parity, the STS1E and OC3 (1.3 STD) circuit packs use special test signals with a known content to verify the continuity of each transmission path.

- To verify the parity error rate of each transmission path, each DS3 circuit pack monitors each DS3 signal from the cross-connect panel (or equivalent) for errors over a 4-second period. If no errors are detected in the 4-second period, the test passes.

- **Regenerator Self-Test:** The **FAULT-Test-Auto Turnup** input verifies each individual OC48 REGENR circuit pack in an FT-2000 OC-48 Repeater Bay. The regenerator test verifies the transmission and fault isolation capabilities of an OC48 REGENR circuit pack.

For the regenerator test, the output of each OC48 REGENR circuit pack to be tested must be looped back to its input with a single-mode fiber jumper and a lightguide buildout in the optical path simulating the outside plant loss (Figure 9-27).

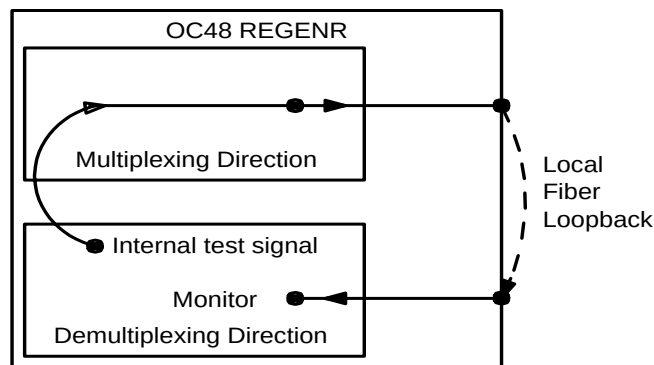


Figure 9-27. Regenerator Self-Test

The OC48 REGENR circuit pack inserts an AIS signal embedded with F1-bytes (site address and line status codes) in the outgoing high-speed signal. The OC48 REGENR circuit pack reads the F1-bytes on the incoming high-speed signal. If the F1-bytes line status codes are read and no conditions are observed that would set the outgoing F1-byte to anything other than normal, the test passes.

Operations Interface Tests

The FT-2000 OC-48 Lightwave System provides the following inputs to test LEDs, serial and parallel telemetry interfaces, and office alarms:

- **FAULT-Test-LED:** This input lights the specified LEDs. The test consists of one or more test cycles repeated as many times as specified. For each test cycle, the LED(s) is lighted for 10 seconds, then turned off for 10 seconds. The LED(s) reverts to normal operation after the test is completed. The PWR ON LED is always on if the bay is powered and cannot be tested with this input.
- **FAULT-Test-Telemetry-Parallel:** This input tests the operation of the local parallel telemetry and miscellaneous discrete control output points. Each test cycle activates the selected control output point(s) for 20 seconds, then clears the control output point(s) for 20 seconds. The control output points revert to normal operation after the test is completed. This input is not available at the FT-2000 OC-48 Repeater Bay.
- **FAULT-Test-Alarm:** This input tests the audible and visible office alarm outputs and their corresponding user panel LEDs (CR, MJ, and MN). The test consists of one or more test cycles repeated as many times as specified. The alarms revert to normal operation after the test is completed.

Circuit Provisioning Audits

In Release 5 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal audits (monitors) the following circuit provisioning information related to ring administration and cross-connections.

- **Target Identifier (TID) and Node Identifier (NID):** Each FT-2000 OC-48 Add/Drop-Rings Terminal in an OC-48 ring must have a unique TID. The FT-2000 OC-48 Add/Drop-Rings Terminal automatically detects and reports any duplicated TIDs.
- **Circuit Add/Drop Continuity:** Each FT-2000 OC-48 Add/Drop-Rings Terminal in a ring checks each local drop cross-connection. The FT-2000 OC-48 Add/Drop-Rings Terminal ensures that there is a continuous path from each local drop cross-connection back through the ring to a valid add cross-connection. The FT-2000 OC-48 Add/Drop-Rings Terminal automatically detects and reports breaks in circuit continuity. Conditions are reported when cross-connections are missing, intermediate source/destination TIDs are incorrect, and add/drop source/destination TIDs are incorrect.
- In Release 5, if a 1-way DRI cross-connection is in the circuit, only the active cross-connection (add or through) is checked. To check the standby cross-connection, the **FAULT-SWITCH-PATH-STS3** or **FAULT-SWITCH-PATH-STS1** (Release 7 and later) input must be used to switch the standby cross-connection to the active state.

The FT-2000 OC-48 Add/Drop-Rings Terminal also makes circuit provisioning information available with the following reports.

- **STS-3 Tributary Map Report:** In releases prior to Release 7, the STS-3 Tributary Map Report displays the cross-connections involving a single time slot throughout the ring. For more information about the STS-3 Tributary Map Report, refer to the **CONFIGURATION-Retrieve-Timeslot-STS1** input in Volume I, Section 11, "Craft Interface Terminal Usage."
- **STS-1 Tributary Map Report:** In Release 7 and later releases, the STS-1 Tributary Map Report displays the cross-connections involving a single time slot throughout the ring. For more information about the STS-1 Tributary Map Report, refer to the **CONFIGURATION-Retrieve-Timeslot-STS1** input in Volume I, Section 11, "Craft Interface Terminal Usage."
- **STS-3 Circuit Map Report:** In releases prior to Release 7, the STS-3 Circuit Map Report follows a circuit path and reports all cross-connections that make up a circuit. For more information about the STS-3 Circuit Map Report, refer to the **CONFIGURATION-Retrieve-Circuit-STS3** input in Volume I, Section 11, "Craft Interface Terminal Usage."

- **STS-1 Circuit Map Report:** In Release 7 and later releases, the STS-1 Circuit Map Report follows a circuit path and reports all cross-connections that make up a circuit. For more information about the STS-1 Circuit Map Report, refer to the **CONFIGURATION-Retrieve-Circuit-STs1** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Performance Monitoring

SONET Performance Parameters

The FT-2000 OC-48 Lightwave System provides performance monitoring to support proactive maintenance of a network. Proactive maintenance refers to the process of detecting degrading conditions not severe enough to initiate protection switching or alarming, but indicative of an impending hard or soft failure.

Proactive maintenance consists of monitoring performance parameters associated with the SONET sections, lines, and paths within the SONET network (Figure 9-28). (Refer to Appendix A, "A SONET Overview," for definitions of SONET sections, lines, and paths.) Table 9-13 lists the SONET performance parameters monitored.

These parameters are thresholded to show degraded performance. When a performance-monitoring threshold is crossed, it is reported to the operations system where all threshold crossings associated with a particular path can be correlated, and the likely source of the degradation can be identified.

For more information about provisioning thresholds and enabling/disabling the reporting of threshold crossing alerts (TCAs) to an operations system, refer to Volume I, Section 8, "Administration and Provisioning."

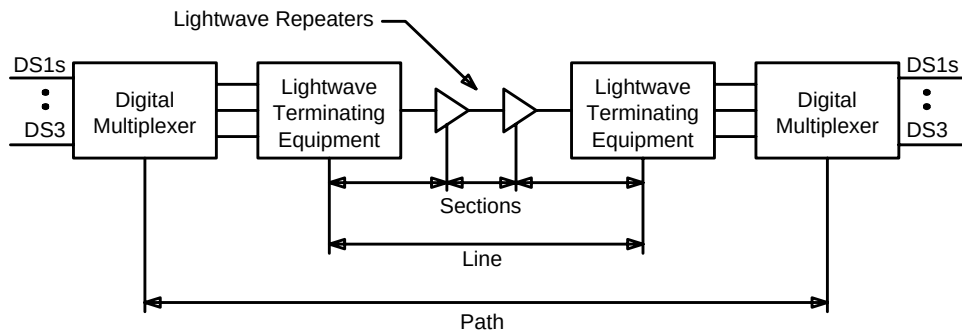


Figure 9-28. SONET Section, Line, and Path

Table 9-13. Performance-Monitoring Parameters

Facility	Measured Parameter	Maximum Counts	
		Current Quarter Hour	Current Day
OC-48/ OC-3/OC-12 Optics	Laser Bias Current	—	—
	Optical Power Transmit	—	—
	Optical Power Receive	—	—
STS-48/STS-12/ STS-3 Section	STS-3 Section (B1) Coding Violations (CV-S)	14000	1343750
	STS-3 Section (B1) Errored Seconds (ES-S)	900	65535
	STS-3 Section (B1) Severely Errored Seconds (SES-S)	900	65535
	STS-3 Section (B1) Severely Errored Frame Seconds (SEFS-S)	900	65535
	STS-12 Section (B1) Coding Violations (CV-S)	56000	5375000
	STS-12 Section (B1) Errored Seconds (ES-S)	900	65535
	STS-12 Section (B1) Severely Errored Seconds (SES-S)	900	65535
	STS-12 Section (B1) Severely Errored Frame Seconds (SEFS-S)	900	65535
	STS-48 Section (B1) Coding Violations (CV-S)	224000	21500000
	STS-48 Section (B1) Errored Seconds (ES-S)	900	65535
	STS-48 Section (B1) Severely Errored Seconds (SES-S)	900	65535
	STS-48 Section (B1) Severely Errored Frame Seconds (SEFS-S)	900	65535
OC-48/OC-12/ OC-3 Line	OC-3 Line (B2) Coding Violations (CV-L)	14000	1343750
	OC-3 Line (B2) Errored Seconds (ES-L)	900	65535
	OC-3 Line (B2) Severely Errored Seconds (SES-L)	900	4095
	OC-3 Line (B2) Unavailable Seconds (UAV-L)	900	4095
	OC-3 Line (B2) Protection Switch Count (PSC-L)	63	255
	OC-12 Line (B2) Coding Violations (CV-L)	56000	5375000
	OC-12 Line (B2) Errored Seconds (ES-L)	900	65535
	OC-12 Line (B2) Severely Errored Seconds (SES-L)	900	4095
	OC-12 Line (B2) Unavailable Seconds (UAV-L)	900	4095
	OC-12 Line (B2) Protection Switch Count (PSC-L)	63	255
	OC-48 Line (B2) Coding Violations (CV-L)	224000	21500000
	OC-48 Line (B2) Errored Seconds (ES-L)	900	65535
	OC-48 Line (B2) Severely Errored Seconds (SES-L)	63	4095
	OC-48 Line (B2) Unavailable Seconds (UAS-L)	63	4095
	OC-48 Line (B2) Protection Switch Count (PSC-L)	63	255
STS-1 Path/ Far End	Path (B3) Coding Violations (CV-P)	16383	1048575
	Path (B3) Errored Seconds (ES-P)	900	65535
	Path (B3) Severely Errored Seconds (SES-P)	900	65535
	Path (B3) Unavailable Seconds (UAS-P)	900	65535
	Far End (FEBE) Coding Violations (CV-F)	16383	1048575
	Far End (FEBE) Errored Seconds (ES-F)	900	65535
	Far End (FEBE) Severely Errored Seconds (SES-F)	900	65535
	Far End (FEBE) Unavailable Seconds (UAS-F)	900	65535

Table 9-13. Performance-Monitoring Parameters

Facility	Measured Parameter	Maximum Counts	
		Current Quarter Hour	Current Day
EC-1 Section/ Line	Section Severely Errored Frame Seconds (SEFS-S)	900	65535
	EC-1 Line (B2) Coding Violations (CV-L)	16383	1048575
	EC-1 Line (B2) Errored Seconds (ES-L)	900	65535
	EC-1 Line (B2) Severely Errored Seconds (SES-L)	900	65535
	EC-1 Line (B2) Unavailable Seconds (UAS-L)	900	65535
DS3 Line/Path	DS3 Line Errored Seconds (ES-L)	900	65535
	DS3 Path Coding Violations (CV-P)	16383	1048575
	DS3 Path Errored Seconds (ES-P)	900	65535
	DS3 Path Severely Errored Seconds (SES-P)	900	65535
	DS3 Path Severely Errored Frame/AIS Seconds (SAS-P)	900	65535
	DS3 Path Unavailable Seconds (UAS-P)	900	65535

OC-48 Optical Parameters

The following OC-48 optical parameters are monitored continuously to detect degraded performance of the laser:

- **Laser Bias Current:** This parameter indicates whether the system optics are working within normal margins (in range or out of range). The upper limit is 1.5 times the initial factory (nominal) value. There is no lower limit.
- **Optical Transmit Power:** This parameter indicates whether the signal power (facet power) generated by the laser is within normal margins (in range and out of range). The upper limit is 0.8 dBm above the initial factory (nominal) value, and the lower limit is 1.0 dBm below the initial factory (nominal) value.
- **Optical Power Receive:** This parameter is not an absolute measure of the received optical power and, therefore, should not be used as a proactive performance-monitoring parameter. The optical power receive parameter indicates whether the received optical power is currently within the nominal sensitivity limits of the receiver (in range or out of range). The optical power receive parameter can be used with other proactive performance-monitoring parameters to isolate the source of an impending high-speed failure.

Threshold crossing alerts (TCAs) are generated if an OC-48 optical parameter exceeds the set thresholds. The generation of TCAs can be enabled or disabled using the **PERFORMANCE-Set-PM Threshold-Optics** input.

OC-3/OC-12 Optical Parameters

The following OC-3/OC-12 optical parameters are monitored continuously to detect degraded performance of the laser:

- **Laser Bias Current:** This parameter indicates whether the system optics are working within normal margins (in range or out of range). The OC-3/OC-12 laser bias current varies widely with temperature because the laser does not have a thermoelectric cooler to maintain a constant laser temperature. Typical nominalized values are expected to range from 0.5 to 2, or even higher. The upper limit is 1.5 times the maximum (beginning of life) laser bias current limit. There is no lower limit.
- **Optical Transmit Power:** This parameter indicates whether the signal power (facet power) generated by the laser is within normal margins (in range and out of range). The upper limit is 0.8 dBm above the initial factory (nominal) value, and the lower limit is 1.0 dBm below the initial factory (nominal) value.
- **Optical Power Receive:** This parameter indicates whether or not the received optical power is currently within the nominal sensitivity limits of the receiver (in range or out of range). The upper limit is -6 dBm (nominal), and the lower limit is -35 dBm (nominal).

Threshold crossing alerts (TCAs) are generated if an OC-3/OC-12 optical parameter exceeds the set thresholds. The generation of TCAs can be enabled or disabled using the **PERFORMANCE-Set-PM Threshold-Optics** input.

STS-48/STS-12/STS-3 Section Parameters

The following STS-48, STS-12 and STS-3 section parameters are monitored:

- **Section (B1) Coding Violations (CV-S):** To monitor the performance of an STS-48, STS-12 or STS-3 section, the section B1 parity is calculated, written, and checked for errors. The section B1 parity violation counter is incremented for each section error detected.
- **Section (B1) Errored Seconds (ES-S):** An errored second (ES) is a second in which there are one or more B1 parity violations detected, one or more LOS defects, or one or more LOF defects.
- **Section (B1) Severely Errored Seconds (SES-S):** A severely errored second (SES) is a second in which there are 249 or more B1 parity violations detected, one or more LOS defects, or one or more LOF defects.
- **Section (B1) Severely Errored Frame Seconds (SEFS-S):** A section severely errored frame second (SEFS) is the number of seconds during which an out-of-frame event occurred.

A threshold crossing alert (TCA) is generated if an STS-48 or STS-3 section parameter exceeds the provisionable threshold. The generation of TCAs can be enabled or disabled using the **PERFORMANCE-Set-PM Threshold-Section** input.

OC-48/OC-12/OC-3 Line Parameters

The following OC-48, OC-12, and OC-3 line parameters are monitored:

- **Line (B2) Coding Violations (CV-L):** To monitor the performance of the OC-48, OC-12, or OC-3 line, the line B2 parity is calculated, written, and checked for errors. The line B2 parity violation counter is incremented for each line bit interleaved parity (BIP) error detected. Each line BIP-8 can detect up to eight errors per STS-1 frame. The contents of the B2 parity violation counters associated with the OC-48, OC-12, or OC-3 line are added to form a composite B2 parity violation count.
- **Line (B2) Errored Seconds (ES-L):** An errored second (ES) is a second in which there are one or more B2 parity violations or a line AIS defect detected.
- **Line (B2) Severely Errored Seconds (SES-L):** A severely errored second (SES) is a second in which there are 494 or more B2 parity violations or a line AIS defect is detected.
- **Line (B2) Unavailable Seconds (UAS-L):** An unavailable second (UAS-L) is a second during which the OC-48, OC-12 or OC-3 line is "unavailable." Unavailable seconds are counted after 10 consecutive line (B2) severely errored seconds (SES-L). These 10 severely errored seconds are counted, and each subsequent second is counted until there are 10 consecutive seconds without a severely errored second. The 10 consecutive seconds without a severely errored second are not counted as unavailable seconds.
- **Protection Switch Counts (PSC-L):** The protection switch count (PSC-L) parameter is the number of times that a protection switch has been performed. This count includes the actual automatic and manual switches (not switch requests) to protection and the revertive switches back to service.

A threshold crossing alert (TCA) is generated if an OC-48, OC-12, or OC-3 line parameter exceeds the provisionable threshold. (The protection switch counts parameter does not have a provisionable or fixed threshold.) The generation of TCAs can be enabled or disabled using the **PERFORMANCE-Set-PM Threshold-Line** input.

STS-1 Path Parameters

The following STS-1 path parameters are monitored:

- **Path (B3)/Path Far End (FEBE) Coding Violations (CV-P and CV-F):** To monitor the performance of the STS-1 path, the "B3" byte in the STS-1 path overhead is written when the path is originated and is checked for errors when the path is terminated. The B3 coding violation counter is incremented for each error detected. Up to eight errors can be detected in each STS-1 path. The near end path (B3) terminating status is relayed back to the originating end where it is monitored as far end path (FEBE).
- **Path (B3)/Path Far End (FEBE) Errored Seconds (ES-P and ES-F):** An STS-1 path (B3) errored second (ES) is a second in which there are one or more B3 parity violations, one or more STS-1 path AIS defects, or one or more STS-1 path LOP defects. A path far end (FEBE) errored second (ES) is a second in which there are one or more FEBE coding violations or one or more remote defect indicator-path (RDI-P) defects.
- **Path (B3)/Path Far End (FEBE) Severely Errored Seconds (SES-P and SES-F):** An STS-1 path (B3) severely errored second (SES) is a second in which there are nine or more B3 parity violations, one or more STS-1 path AIS defects, or one or more STS-1 path LOP defects. A path far end (FEBE) severely errored second (SES) is a second in which there are nine or more FEBE coding violations or one or more RDI-P defects.
- **Path (B3)/Path Far End (FEBE) Unavailable Seconds (UAS-P and UAS-F):** An unavailable second (UAS) is a second during which the STS-1 path is "unavailable." Unavailable seconds are counted after 10 consecutive path (B3)/path far end (FEBE) severely errored seconds (SES). These 10 severely errored seconds are counted, and each subsequent second is counted until there are 10 consecutive seconds without a severely errored second. The 10 consecutive seconds without a severely errored second are not counted as unavailable seconds.

A threshold crossing alert (TCA) is generated if an STS-1 path (B3)/far end (FEBE) parameter exceeds the provisionable threshold. The generation of TCAs can be enabled or disabled using the **PERFORMANCE - Set - PM Threshold - STS1** input.

EC-1 Section/Line Parameters

The following EC-1 section/line parameters are monitored:

- **Section (B1) Severely Errored Frame Seconds (SEFS-S):** A section severely errored frame second (SEFS) is the number of seconds during which an out-of-frame event occurred.
- **Line (B2) Coding Violations (CV-L):** To monitor the performance of the EC-1 line, the line B2 parity is calculated, written, and checked for errors. The line B2 parity violation counter is incremented for each line bit interleaved parity (BIP) error detected. Each line BIP-8 can detect up to eight errors per STS-1 frame.
- **Line (B2) Errored Seconds (ES-L):** An errored second (ES) is a second in which there are one or more B2 parity violations or an EC-1 line AIS defect detected.
- **Line (B2) Severely Errored Seconds (SES-L):** A severely errored second (SES) is a second in which there are 9 or more B2 parity violations or an EC-1 line AIS defect detected.
- **Line (B2) Unavailable Seconds (UAS-L):** An unavailable second (UAS) is a second during which the EC-1 line is "unavailable." Unavailable seconds are counted after 10 consecutive line (B2) severely errored seconds (SES). These 10 severely errored seconds are counted, and each subsequent second is counted until there are 10 consecutive seconds without a severely errored second. The 10 consecutive seconds without a severely errored second are not counted as unavailable seconds.

A threshold crossing alert (TCA) is generated if an EC-1 section/line parameter exceeds the provisionable threshold. The generation of TCAs can be enabled or disabled using the **PERFORMANCE-Set -PM Threshold-EC1** input.

DS3 Line/Path

The following DS3 line/path parameters are monitored:

- **DS3 Line Errored Seconds (ES-L):** An errored second (ES) is a second in which there are one or more B3ZS coding violations or a DS3 LOS defect detected in the DS3 signal input from the cross-connect panel.
- **DS3 Path Coding Violations (CV-P):** To monitor the performance of the DS3 path, the P-bit parity of the DS3 signal received from the OC-48 line is calculated, written, and checked for errors. Path coding violation may be based on P bits or F and M framing bits (provisionable on a per DS3 basis). The F and M bit error counts are adjusted to estimate the equivalent P-bit error count in the DS3 path payload. This may be used as an approximate end-to-end DS3 path performance measure, even if the path passes through intermediate equipment that corrects P-bit errors. For more information about provisioning the DS3 performance-monitoring mode, refer to Volume I, Section 8, "Administration and Provisioning."
- **DS3 Path Errored Seconds (ES-P):** An errored second (ES) is a second in which there are one or more DS3 coding violations, one or more DS3 path AIS defects, or one or more DS3 path LOF defects detected in the DS3 signal received from the OC-48 line.
- **DS3 Path Severely Errored Seconds (SES-P):** A severely errored second (SES) is a second in which there are 44 or more DS3 coding violations, one or more DS3 path AIS defects, or one or more DS3 path LOP defects in the DS3 signal received from the OC-48 line.
- **DS3 Path Severely Errored Frame/AIS Seconds (SAS-P):** A severely errored frame/AIS second (SAS) is a second in which there is an out-of-frame or AIS defect detected in the DS3 signal received from the OC-48 line.
- **DS3 Path Unavailable Seconds (UAS-P):** An unavailable second (UAS) is a second during which the DS3 path is "unavailable." Unavailable seconds are counted after 10 consecutive DS3 path severely errored seconds (SES). These 10 severely errored seconds are counted, and each subsequent second is counted until there are 10 consecutive seconds without a severely errored second. The 10 consecutive seconds without a severely errored second are not counted as unavailable seconds.

A threshold crossing alert (TCA) is generated if a DS3 line/path parameter exceeds the provisionable threshold. The generation of TCAs can be enabled or disabled using the **PERFORMANCE-Set -PM Threshold-T3** input.

Performance-Monitoring Data Storage and Reports

The FT-2000 OC-48 Lightwave System provides current quarter-hour and current day registers for all accumulated performance parameters. There is also 1 previous day register and 32 previous quarter-hour registers (8 hours).

The FT-2000 OC-48 Lightwave System can initialize the current quarter-hour and/or current day registers through the CIT locally or remotely at any time, as well as retrieve and report the contents of any parameter storage register at any time. When performance-monitoring data is retrieved, any previous day or quarter-hour register with all zero data is not reported. The current day and quarter-hour registers are always reported, even when they contain all zero data. For more information, refer to the **PERFORMANCE-Initialize-PM**, **PERFORMANCE-Retrieve-PM-Line**, **PERFORMANCE-Retrieve-PM-Optics**, **PERFORMANCE-Retrieve-PM-Section**, **PERFORMANCE-Retrieve-PM-STS1**, **PERFORMANCE-Retrieve-PM-EC1**, and **PERFORMANCE-Retrieve-PM-T3** inputs in Section 11, "Craft Interface Terminal Usage."

A single start time can be provisioned for measuring all the current day performance-monitoring parameters. The start time can be provisioned to start at any hour. The performance-monitoring parameters will reset and begin daily at the provisioned start time. For more information about provisioning the performance-monitoring start time, refer to Volume I, Section 8, "Administration and Provisioning."

Performance Monitoring During Failed Conditions

When a trouble condition is detected, the FT-2000 OC-48 Lightwave System stops accumulating affected performance parameters per Telcordia requirements. All unaffected performance parameters continue to be accumulated during the trouble condition.

The coding violations (CV) and errored seconds (ES) counts are accumulated until a severely errored second (SES) is counted. When a severely errored second (SES) is counted, the coding violations (CV) count is inhibited. The errored seconds (ES) and severely errored seconds (SES) counts continue to be accumulated until an unavailable second (UAS) is counted. When an unavailable second (UAS) is counted, the coding violations (CV), errored seconds (ES), and severely errored seconds (SES) counts are inhibited.

The unavailable seconds (UAS) and protection switch count (PSC-L) parameter counts are not inhibited.

Performance-Monitoring Parameter Thresholds

The current quarter-hour and current day thresholds for each performance-monitoring parameter are provisionable using the CIT. Whenever the current quarter-hour or the current day threshold for a given performance-monitoring parameter is reached or exceeded, the FT-2000 OC-48 Add/Drop-Rings Terminal generates a threshold crossing alert (TCA). The TCA is then entered into the appropriate performance-monitoring report. For more information about provisioning thresholds using the CIT, refer to the **PERFORMANCE-Set-PM Threshold-Optics**, **PERFORMANCE-Set-PM Threshold-Section**, **PERFORMANCE-Set-PM Threshold-Line**, **PERFORMANCE-Set-PM Threshold-STs1**, **PERFORMANCE-Set-PM Threshold-EC1**, and **PERFORMANCE-Set-PM Threshold-T3** inputs in the "Local Provisioning" part of Volume I, Section 8, "Administration and Provisioning." Also refer to the **PERFORMANCE-Retrieve-PM Threshold-Optics**, **PERFORMANCE-Retrieve-PM Threshold-Section**, **PERFORMANCE-Retrieve-PM Threshold-Line**, **PERFORMANCE-Retrieve-PM Threshold-STs1**, **PERFORMANCE-Retrieve-PM Threshold-EC1**, and **PERFORMANCE-Retrieve-PM Threshold-T3** inputs in Volume I, Section 11, "Craft Interface Terminal Usage."

Threshold Crossing Alert Transmission to an OS

Threshold crossing alerts are also reported to an OS via the X.25 interface using TL1 messages.

Reports

Active Alarms and Status

The FT-2000 OC-48 Add/Drop-Rings Terminal and FT-2000 OC-48 Repeater Bay provide a report that shows all the local active alarm and status conditions. The FT-2000 OC-48 Add/Drop-Rings Terminal also provides a report that shows the active alarm level at each FT-2000 OC-48 Add/Drop-Rings Terminal with active alarm and status conditions. This report also shows the actual alarm and status conditions at FT-2000 OC-48 Repeater Bays with active conditions.

The local active alarms and status report is automatically displayed on the CIT immediately after a user logs in. The report shows the following alarm levels:

- Critical (CR)
- Major (MJ)
- Minor (MN)
- Abnormal (ABN)
- Near-end activity (NE ACTY).

The source address and description of the alarm condition (for example, controller failure, incoming high-speed signal failure, etc.) are included in the report, along with the date and time detected. The report also shows if the condition affects service. The option to display specified subsets of conditions by alarm level is also provided.

If no LEDs are lighted on the user panel (no active conditions), the report states "There are no active alarms or status conditions."

If the NE ACTY LED is lighted at the local FT-2000 OC-48 Repeater Bay or FT-2000 OC-48 Add/Drop-Rings Terminal, use the local active alarm and status report. For more information, refer to the **FAULT-Retrieve-Alarm (and Status)**, inputs in Volume I, Section 11, "Craft Interface Terminal Usage."

Provisioning Reports

Provisioning reports list the current state of all provisionable options in the FT-2000 OC-48 Lightwave System. For more information, refer to the following inputs in Volume I, Section 11, "Craft Interface Terminal Usage" within the **CONFIGURATION** category:

Retrieve-Assignment-DCC	Retrieve-Attribute-Alarm
Retrieve-Attribute-Control	Retrieve-Attribute-Environment
Retrieve-Circuit-STs1	Retrieve-Crossconnection-All
Retrieve-Crossconnection- <i>TIRKS</i>	
Retrieve-Date	Retrieve-EC1
Retrieve-Equipment	Retrieve-Low Speed
Retrieve-Map-Neighbor	Retrieve-Map-Network
Retrieve-Map-Ring	
Retrieve-OC12	Retrieve-OC3
Retrieve-OC48	Retrieve-Path-Trace
Retrieve-Protection-Line	Retrieve-Protection-Low Speed
Retrieve-Protection-STs1	Retrieve-Synchronization
Retrieve-T3	Retrieve-Timeslot-STs1
Retrieve-ULSDCC-L3	Retrieve-ULSDCC-L4

Also refer to the following inputs in Volume I, Section 11, "Craft Interface Terminal Usage" within the **SECURITY** category:

Retrieve-Security-Network Element
Retrieve-Security-Port
Retrieve-Login
Retrieve-Network Element

Also refer to the **FAULT-Retrieve-State** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Performance Monitoring

The FT-2000 OC-48 Add/Drop-Rings Terminal provides a report that contains a snapshot of all the performance-monitoring registers at the time the report is requested. The time each register was last reinitialized is also included. The report contains a summary of all requested performance parameters that have crossed their quarter-hour and day threshold within the last 24 hours. For more information, refer to the **PERFORMANCE-Retrieve-PM-Line**, **PERFORMANCE-Retrieve-PM-Threshold-Line**, **PERFORMANCE-Retrieve-PM-Optics**, **PERFORMANCE-PM-Threshold-Optics**, **PERFORMANCE-Retrieve-PM-Section**, **PERFORMANCE-PM-Threshold-Section**, **PERFORMANCE-Retrieve-PM-STS1**, **PERFORMANCE-Retrieve-PM-Threshold-STS1**, **PERFORMANCE-Retrieve-PM-EC1**, **PERFORMANCE-Retrieve-PM-Threshold-EC1**, **PERFORMANCE-Retrieve-PM-Start Time**, **PERFORMANCE-Retrieve-PM-STS1**, **PERFORMANCE-Retrieve-PMMode-T3**, **PERFORMANCE-Retrieve-PM-T3**, and **PERFORMANCE-Retrieve-PM-Threshold-T3** inputs in Volume I, Section 11, "Craft Interface Terminal Usage."

State

An on-demand report displays the state of each addressed slot and the external DS1 references in the FT-2000 OC-48 Add/Drop-Rings Terminal. For each slot and external DS1 reference, the report includes the following:

- Address: This shows the slot or external DS1 reference address.
- Circuit pack: This shows the functional name of the circuit pack.
- Port state: This shows the state of low-speed ports as AUTO, NMON, or IS. The port states for the add (from the cross-connect) and drop (from the OC-48 line) directions are shown separately. For more information about circuit pack slot and low-speed port states and transitions, refer to Volume I, Section 8, "Administration and Provisioning."
- Switch state: This shows whether the circuit pack or external DS1 reference is active (carrying traffic or providing timing) or standby (not in use).
- Switch priority: This shows the priority of an automatic protection switch, manual switch, an inhibit switch, or a lockout switch.
- Active switch priority: This shows the current protection switching priority of circuit pack or external DS1 reference. The "ring" designation for a high-speed switch priority means that the circuit pack is signaling a switch request for a switch at the other side of the node or at another node. An external high-speed switch request results in an "ne" (near end)

designation at the node where the request was entered, and a "fe" (far end) designation at the other end of the line. If the same priority request was entered at both ends of a line, both ends show "ne."

- Deferred switch priority: This shows the priority of any pending switch request for a circuit pack or external DS1 reference.

For more information, refer to the **FAULT-Retrieve-State** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Version/Equipment List

The version/equipment list report is an on-demand report that lists the circuit pack version and the software release (if applicable) for the FT-2000 OC-48 Add/Drop-Rings Terminal and FT-2000 OC-48 Repeater Bay. This report also lists all the circuit packs that are present. For more information, refer to the **CONFIGURATION-Retrieve-Equipment** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Synchronization Report

The synchronization report is an on-demand report that lists the status of the system synchronization at the FT-2000 OC-48 Add/Drop-Rings Terminal. This report lists all the clock parameters that can be interrogated from a craft interface terminal or from remote telemetry. For more information, refer to the **CONFIGURATION-Retrieve-Synchronization** input in Volume I, Section 11, "Craft Interface Terminal Usage."

Table of Contents

Overview	10-1
Optical Line Interface	10-1
Optical Connector Interfaces	10-1
Transmission Medium	10-1
Lightguide Jumpers	10-2
Optical Source	10-2
Optical Detector	10-2
Optical Safety (CDRH Classification)	10-2
Operating Wavelength	10-3
Optical Dispersion	10-4
Optical Return Loss	10-4
Outside Plant Loss	10-5
Optical Line Loss Budgets	10-5
■ FT-2000 OC-48 Lightwave System	10-5
■ FT-2000 OC-48 Lightwave System and FT-Lightwave Booster Amplifier	10-11
■ FT-2000 OC-48 Lightwave System and Wavelength Division Multiplexing Systems	10-12
■ FT-2000 OC-48 Lightwave System with Wavelength Division Multiplexing and FT-LBA Systems	10-13
Transmission Distances	10-14
■ FT-2000 OC-48 Lightwave System without Optical Amplifiers	10-14

Table of Contents

■ FT-2000 OC-48 Lightwave System and the FT-Lightwave Booster Amplifier	10-15
■ FT-2000 OC-48 Lightwave System with Wavelength Division Multiplexing and FT-LBA Systems	10-16
Capacity	10-16
Transmission Characteristics	10-17
DS3 Access	10-17
EC-1 Access	10-18
OC-3 Access	10-19
■ Optical Line Interface	10-19
■ Optical Connector Interfaces	10-19
■ Transmission Medium	10-19
■ Lightguide Jumpers	10-19
■ Optical Source	10-19
■ Optical Detector	10-20
■ Optical Safety (BRH Classification)	10-20
■ Operating Wavelength	10-20
■ Spectral Width	10-20
■ Optical Dispersion	10-20
■ OC-3 Line Loss Budgets	10-20
IS-3 Access	10-22
■ Optical Line Interface	10-22
■ Optical Connector Interfaces	10-22
■ Transmission Medium	10-22
■ Lightguide Jumpers	10-22
■ Optical Source	10-22
■ Optical Detector	10-22
■ Optical Safety (BRH Classification)	10-23
■ Operating Wavelength	10-23
■ Spectral Width	10-23
■ Optical Dispersion	10-23
■ IS-3 Line Loss Budgets	10-24

Table of Contents

OC-12 Access	10-25
■ Optical Line Interface	10-25
■ Optical Connector Interfaces	10-25
■ Transmission Medium	10-25
■ Lightguide Jumpers	10-25
■ Optical Source	10-25
■ Optical Detector	10-25
■ Optical Safety (BRH Classification)	10-26
■ Operating Wavelength	10-26
■ Spectral Width	10-26
■ Optical Dispersion	10-26
■ OC-12 Line Loss Budgets	10-27
OC-3 to IS-3 Interface Interconnection	10-28
Transmission Delay	10-28
Protection Switching (OC-3, OC-12, OC-48 Lines)	10-29
Electrical Line Buildout Specifications	10-30
Cable Access	10-31
Power Specifications	10-32
Bay/Shelf Dimensions	10-33
Circuit Pack Dimensions	10-35
Floor Loading Specifications	10-38
Environmental Specifications	10-38
Craft Interface Terminal	10-39
■ Personal Computer Requirements	10-39
■ ASCII-Based Terminal Requirements	10-41
Modem Requirements	10-41
Operations Interfaces	10-42
TL1/X.25 Interface	10-43
■ General	10-43

Table of Contents

■ TL1 Autonomous Messaging in Release 4.1 and Earlier	10-43
■ Provisioning TL1 Autonomous Messages for Release 5 and Later Releases	10-44
■ DTE Configuration	10-44
Circuit Pack FIT Rates	10-50

Overview

This section contains the technical specifications for the FT-2000 OC-48 Lightwave System. Telcordia Technical Reference 253 (GR-253-CORE) is the basis for this information.

Optical Line Interface

Optical Output.....	2.488 Gb/s
Optical Line Code	Scrambled nonreturn to zero (NRZ)

Optical Connector Interfaces

The OC48 Transmitter, OC48 Receiver, and OC48 Regenerator circuit packs use ST® lightguide connectors (standard), FC/PC lightguide connectors, or SC lightguide connectors.

Transmission Medium

Single-Mode Fiber

Lightguide Jumpers

Single-mode lightguide jumpers are used on the OC48 Transmitter, OC48 Receiver, and OC48 Regenerator circuit packs.

Optical Source

Distributed Feedback (DFB) Laser

Optical Detector

Avalanche Photodiode (APD)

Optical Safety (CDRH Classification)

OC-48 standard performance (1.310 μm)	Class IIIB
OC-48 high performance (1.310 μm)	Class IIIB
OC-48 standard performance (1.550 μm)	Class I
OC-48 WDM/LBA compatible (1.538 μm)	Class I
OC-48 WDM/LBA compatible (1.558 μm)	Class I
OC-48 739E_ standard performance (1.5 μm)	Class I
OC-48 739G_ high performance (1.5 μm)	Class I

Operating Wavelength

739B_/C_	1.310 μm ($\pm 0.020 \mu\text{m}$)
739J_/P_	1.550 μm ($\pm 0.015 \mu\text{m}$)
739R2	1.538 μm ($\pm 0.003 \mu\text{m}$)
739S2	1.558 μm ($\pm 0.003 \mu\text{m}$)
739E1	1.54932 μm
739E2	1.55092 μm
739E3	1.55252 μm
739E4	1.55413 μm
739E5	1.55575 μm
739E6	1.55737 μm
739E7	1.55898 μm
739E8	1.56061 μm
739E9	1.54851 μm
739E10	1.55012 μm
739E11	1.55172 μm
739E12	1.55333 μm
739E13	1.55494 μm
739E14	1.55655 μm
739E15	1.55817 μm
739E16	1.55979 μm
739H1	1.56061 μm
739H2	1.55898 μm
739H3	1.55736 μm
739H4	1.55575 μm
739H5	1.55252 μm
739H6	1.55092 μm
739H7	1.54932 μm
739H8	1.54772 μm
739H9	1.54294 μm
739H10	1.54135 μm
739H11	1.53977 μm
739H12	1.53819 μm
739H13	1.53504 μm
739H14	1.53347 μm
739H15	1.53190 μm
739H16	1.53033 μm

Optical Dispersion

OC-48 standard performance (1.310 μm)	300 ps/nm
OC-48 high performance (1.310 μm)	300 ps/nm
OC-48 standard performance (1.550 μm)	1800 ps/nm
OC-48 WDM/LBA compatible (1.538 μm)	2520 ps/nm
OC-48 WDM/LBA compatible (1.558 μm)	2520 ps/nm
OC-48 739E_ TRMTR (1.550 μm)	6800 ps/nm
OC-48 739G_ TRMTR (1.550 μm)	10900 ps/nm
OC-48 739H_ TRMTR (1.550 μm)	1560 ps/nm

Optical Return Loss

Maximum system optical return loss	24.0 dB
Maximum receiver reflectance	-27.0 dB
Maximum discrete reflectance.....	-27.0 dB

Lucent Technologies has a computer tool that determines in-depth return loss characteristics. Users who have concerns about return loss limitations should contact their local Account Executive.

Outside Plant Loss

OC-48 standard performance (1.310 μm)	24.0 dB
OC-48 high performance (1.310 μm).....	27.0 dB
OC-48 standard performance (1.550 μm)	23.0 dB
OC-48 WDM/LBA compatible (1.538 μm)	20.5 dB
OC-48 WDM/LBA compatible (1.558 μm)	20.5 dB

Optical Line Loss Budgets

FT-2000 OC-48 Lightwave System

The FT-2000 OC-48 Lightwave System is designed to meet the OC-48 loss budget specifications shown in Table 10-1, Table 10-2, and Table 10-7. The transmit and receive powers are referenced to points S and R in Figure 10-1.

Table 10-1. OC-48 Loss Budget Specifications

Parameter	1.310 μm		1.550 μm Performance
	Standard Performance	High Performance	
Maximum Transmitter Power (P_{Tmax}) [*]	2.5 dBm	5.5 dBm	4.0 dBm
Minimum Transmitter Power (P_{Tmin}) [†]	−2.0 dBm	1.0 dBm	−2.0 dBm
Maximum Received Power (P_{Rmax}) [‡]	−10.0 dBm	−10.0 dBm	−9.0 dBm
Receiver Sensitivity (P_{Rmin}) [§]	−27.0 dBm	−27.0 dBm	−27.0 dBm
Minimum System Gain (S-R)	+25.0 dB	28.0 dB	25.0 dB
Optical Path Penalty (P_{O})	1.0 dB	1.0 dB	2.0 dB
Maximum Loss Budget	24.0 dB	27.0 dB	23.0 dB
Minimum Loss Budget [¶]	10.0 dB	10.0 dB	10.0 dB

* Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

† These values include transmitter/receiver connectors @ 0.7 dB each (worst case) and the system margins

‡ The receiver sensitivity and maximum received power values are measured at a Bit-Error-Rate (BER) of 1×10^{-10} .

§ The 1.5 μm Standard Performance optics is dispersion limited to 92 kilometers.

¶ These values assume that the maximum received power limitations are not exceeded.

For detailed circuit pack information and software compatibility, refer to Table 7-1 and the circuit pack descriptions in Section 7, "Circuit Pack Descriptions."

Table 10-2. OC-48 Loss Budget Specifications for 739E/G[1-8] OC48 TRMTR Circuit Packs

Parameter	OC48 Transmitter Circuit Pack Code and Wavelength (in nm)							
	739E1/G1 1549.32	739E2/G2 1550.92	739E3/G3 1552.52	739E4/G4 1554.13	739E5/G5 1555.75	739E6/G6 1557.37	739E7/G7 1558.98	739E8/G8 1560.61
Maximum Transmitter Power (P_{Tmax})*	-3.3 dBm	-4.6 dBm	-5.8 dBm	-6.5 dBm	-7.0 dBm	-7.5 dBm	-7.3 dBm	-5.8 dBm
Minimum Transmitter Power (P_{Tmin})†	-6.2 dBm	-7.5 dBm	-8.7 dBm	-9.4 dBm	-9.9 dBm	-10.4 dBm	-10.2 dBm	-8.7 dBm
Maximum Received Power (P_{Rmax})‡	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm
Receiver Sensitivity (P_{Rmin})†‡	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm
Minimum System Gain (S-R)	20.8 dB	19.5 dB	18.3 dB	17.6 dB	17.1 dB	16.6 dB	16.8 dB	18.3 dB
Optical Path Penalty Penalty (P_O)	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB
Maximum Loss Budget	20.3 dB	19.0 dB	17.8 dB	17.1 dB	16.6 dB	16.1 dB	16.3 dB	17.8 dB
Minimum Loss Budget§	10.0 dB	10.0 dB	10.0 dB	10.0 dB	10.0 dB	10.0 dB	10.0 dB	10.0 dB
Typical Maximum Distance¶	81.2 km	76 km	71.2 km	68.4 km	66.4 km	64.4 km	65.2 km	71.2 km

* Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

† These values include transmitter/receiver connectors @ 0.7 dB each (worst case) and the system margins.

‡ The receiver sensitivity and maximum received power values are measured at a BER of 1×10^{-10} .

§ These values assume that the maximum received power limitations are not exceeded.

¶ These distances are based on an assumption of 0.25 dB/km loss, including fiber loss and splice loss.

Table 10-3. OC-48 Loss Budget Specifications for 739E/G[9-16] OC48 TRMTR Circuit Packs

Parameter	OC48 Transmitter Circuit Pack Code and Wavelength (in nm)							
	739E9 1548.51	739E10 1550.12	739E11 1551.72	739E12 1553.33	739E13 1554.94	739E14 1556.55	739E15 1558.17	739E16 1559.79
Maximum Transmitter Power (P_{Tmax})*	-1.3 dBm	-3.9 dBm	-5.1 dBm	-6.1 dBm	-6.7 dBm	-7.2 dBm	-7.4 dBm	-6.5 dBm
Minimum Transmitter Power (P_{Tmin})†	-4.2 dBm	-6.8 dBm	-8.0 dBm	-9.0 dBm	-9.6 dBm	-10.1 dBm	-10.3 dBm	-9.4 dBm
Maximum Received Power (P_{Rmax})‡	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm
Receiver Sensitivity (P_{Rmin})†‡	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm
Minimum System Gain (S-R)	22.8 dB	20.2dB	19.0 dB	18.0 dB	17.4 dB	16.9 dB	16.7 dB	17.6 dB
Optical Path Penalty (P_O)	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB
Maximum Loss Budget	22.3 dB	19.7 dB	18.5 dB	17.5 dB	16.9 dB	16.4 dB	16.2 dB	17.1 dB
Minimum Loss Budget§	10.0 dB	10.0 dB	10.0 dB	10.0 dB	10.0 dB	10.0 dB	10.0 dB	10.0 dB
Typical Maximum Distance¶	89.2 km	78.8 km	74.0 km	70.0 km	67.6 km	65.6 km	64.8 km	68.4 km

* Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

† These values include transmitter/receiver connectors @ 0.7 dB each (worst case) and the system margins.

‡ The receiver sensitivity and maximum received power values are measured at a BER of 1×10^{-10} .

§ These values assume that the maximum received power limitations are not exceeded.

¶ These distances are based on an assumption of 0.25 dB/km loss, including fiber loss and splice loss.

Table 10-4. OC-48 Loss Budget Specifications for 739H[1-8] OC48 TRMTR Circuit Packs

Parameter	OC48 Transmitter Circuit Pack Code and Wavelength (in nm)							
	739H1 1560.61	739H2 1558.98	739H3 1557.36	739H4 1555.75	739H5 1552.52	739H6 1550.92	739H7 1549.32	739H8 1547.72
Maximum Transmitter Power (P_{Tmax})*	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm
Minimum Transmitter Power (P_{Tmin})†	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm
Maximum Received Power (P_{Rmax})‡	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm
Receiver Sensitivity (P_{Rmin})†‡	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm
Minimum System Gain (S-R)	23.7 dB	23.7 dB	23.7 dB	23.7 dB	23.7 dB	23.7 dB	23.7 dB	23.7 dB
Optical Path Penalty (P_O)	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB
Maximum Loss Budget	23.2 dB	23.2 dB	23.2 dB	23.2 dB	23.2 dB	23.2 dB	23.2 dB	23.2 dB
Minimum Loss Budget§	10 dB	10 dB	10 dB	10 dB	10 dB	10 dB	10 dB	10 dB
Typical Maximum Distance¶	60 km	60 km	60 km	60 km	60 km	60 km	60 km	60 km

* Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

† These values include transmitter/receiver connectors @ 0.7 dB each (worst case) and the system margins.

‡ The receiver sensitivity and maximum received power values are measured at a BER of 1×10^{-10} .

§ These values assume that the maximum received power limitations are not exceeded.

¶ These distances are based on an assumption of 0.25 dB/km loss, including fiber loss and splice loss.

Table 10-5. OC-48 Loss Budget Specifications for 739H[9-16] OC48 TRMTR Circuit Packs

Parameter	OC48 Transmitter Circuit Pack Code and Wavelength (in nm)							
	739H9 1542.92	739H10 1541.35	739H11 1539.77	739H12 1538.19	739H13 1535.04	739H14 1533.47	739H15 1531.90	739H16 1530.33
Maximum Transmitter Power (P_{Tmax})*	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm	-0.6 dBm
Minimum Transmitter Power (P_{Tmin})†	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm	-3.3 dBm
Maximum Received Power (P_{Rmax})‡	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm	-11.0 dBm
Receiver Sensitivity (P_{Rmin})† ‡	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm
Minimum System Gain (S-R)	23.7 dB	23.7 dB	23.7 dB	23.7 dB	23.7 dB	23.7 dB	23.7 dB	23.7 dB
Optical Path Penalty (P_O)	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB	2 dB
Maximum Loss Budget	23.2 dB	23.2 dB	23.2 dB	23.2 dB	23.2 dB	23.2 dB	23.2 dB	23.2 dB
Minimum Loss Budget§	10 dB	10 dB	10 dB	10 dB	10 dB	10 dB	10 dB	10 dB
Typical Maximum Distance¶	60 km	60 km	60 km	60 km	60 km	60 km	60 km	60 km

* Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

† These values include transmitter/receiver connectors @ 0.7 dB each (worst case) and the system margins.

‡ The receiver sensitivity and maximum received power values are measured at a BER of 1×10^{-10} .

§ These values assume that the maximum received power limitations are not exceeded.

¶ These distances are based on an assumption of 0.25 dB/km loss, including fiber loss and splice loss.

Table 10-6. OC-48 Loss Budget Specifications for 839B5 and 839E5 OC48 RCVR Circuit Packs

Parameter	OC48 Receiver Circuit Pack Code	
	839B5	839E5
Maximum Received Power (P_{Rmax})*	-11 dBm	-11 dBm
Receiver Sensitivity (P_{Rmin})† ‡	-27 dBm	-27 dBm
Optical Signal to Noise Ratio (OSNR)	17.5 dB	14.5 dB

* The receiver sensitivity and maximum received power values are measured at a BER of 1×10^{-10} .

† These values include transmitter/receiver connectors @ 0.7 dB each (worst case) and the system margins.

‡ The receiver sensitivity and maximum received power values are measured at a BER of 1×10^{-10} .

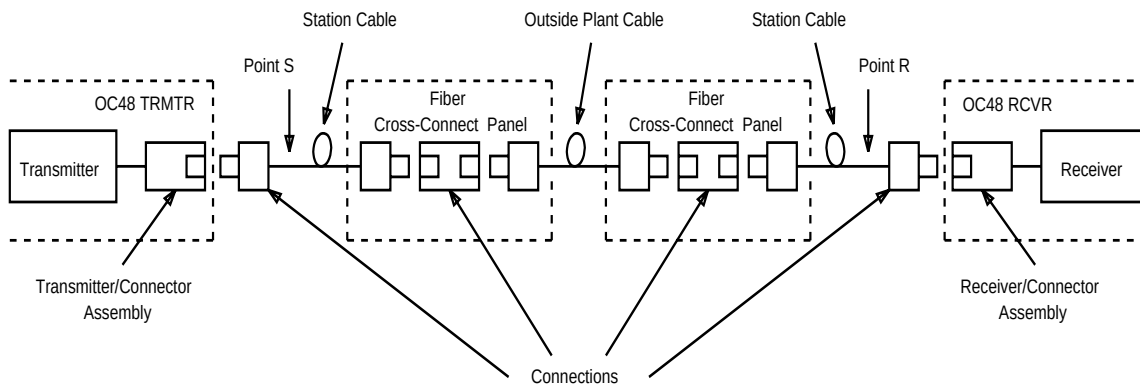


Figure 10-1. Optical System Interfaces

FT-2000 OC-48 Lightwave System and FT-Lightwave Booster Amplifier

The FT-2000 OC-48 Lightwave System is also designed to meet the OC-48 loss budget specifications shown in Table 10-7 when operating with the FT-Lightwave Booster Amplifier.

Table 10-7. FT-2000 OC-48 Lightwave System and FT-Lightwave Booster Amplifier Loss Budgets

Parameter	9-dBm FT-LBA		12-dBm FT-LBA		16-dBm FT-LBA	
	Standard Fiber	Dispersion Shifted Fiber	Standard Fiber	Dispersion Shifted Fiber	Standard Fiber	Dispersion Shifted Fiber
Maximum Transmitter Power (P_{Tmax})	11.0 dBm	11.0 dBm	15.0 dBm	15.0 dBm	17.0 dBm	17.0 dBm
Nominal Transmitter Power	9.0 dBm	9.0 dBm	12.0 dBm	12.0 dBm	16.0 dBm	16.0 dBm
Minimum Transmitter Power (P_{Tmin})*	7.0 dBm	7.0 dBm	10.0 dBm	10.0 dBm	14.0 dBm	14.0 dBm
Receiver Sensitivity (P_{Rmin})*	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm	-27.0 dBm
Minimum System Gain (S-R)	34.0 dB	34.0 dB	37.0 dB	37.0 dB	41.0 dB	41.0 dB
Optical Path Penalty (P_O)	2.0 dB	1.0 dB	2.0 dB	1.0 dB	2.0 dB	1.0 dB
Maximum Loss Budget	32.0 dB	33.0 dB	35.0 dB†	36.0 dB†	39.0 dB†	40.0 dB†
Dispersion Limit	120 km‡	—	140 km§	—	140 km§	—

* These values include transmitter/receiver connectors @ 0.7 dB each (worst case) and the system margins.

† Optional: Subtract 3.5 dB for pump redundancy.

‡ These values are based on a system performance of 2100 psec/nm which yields 120 km for a fiber dispersion of 18 psec/nm km.

§ These values are based on a system performance of 2520 psec/nm which yields 140 km for a fiber dispersion of 18 psec/nm km.

FT-2000 OC-48 Lightwave System and Wavelength Division Multiplexing Systems

The FT-2000 OC-48 Lightwave System is also designed to meet the OC-48 loss budget specifications shown in Table 10-8 when operating with wavelength division multiplexing (WDM) systems.

Table 10-8. FT-2000 OC-48 Lightwave System and Wavelength Division Multiplexing Systems Loss Budgets

Parameter	OC48 TRMTR (A/D 1.5 LBA, W1 and W2) OC48 TRMTR (A/D STS1 1.5 LBA, W1 and W2)
Maximum Transmitter Power (P_{Tmax})*	3.3 dBm
Minimum Transmitter Power (P_{Tmin})†	0.0 dBm
Maximum Received Power (P_{Rmax})‡	-11.0 dBm
Receiver Sensitivity (P_{Rmin})††	-27.0 dBm
Minimum System Gain (S-R)	27.0 dB
MXR/DMXR Loss	4.5 dB
Optical Path Penalty (P_O)	2.0 dB
Maximum Loss Budget§	20.5 dB
Minimum Loss Budget¶	10.0 dB
Optical Dispersion	2520 ps/nm

*Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

†These values include transmitter/receiver connectors @ 0.7 dB each (worst case) and the system margins.

‡ The receiver sensitivity and maximum received power values are measured at a BER of 1×10^{-10} .

§ The 1.5 μm Standard Performance optics is dispersion limited to 92 kilometers.

¶ These values assume that the maximum received power limitations are not exceeded.

FT-2000 OC-48 Lightwave System with Wavelength Division Multiplexing and FT-LBA Systems

The FT-2000 OC-48 Lightwave System is also designed to meet the OC-48 loss budget specifications shown in Table 10-9 when operating with wavelength division multiplexing (WDM) systems and the FT-LBA (+16 dBm system).

Table 10-9. FT-2000 OC-48 Lightwave System with Wavelength Division Multiplexing Systems and FT-LBA Loss Budgets

Parameter	OC48 TRMTR (A/D 1.5 LBA, W1 and W2) OC48 TRMTR (A/D STS1 1.5 LBA, W1 and W2)
Maximum Transmitter Power (P_{Tmax})*	17.0 dBm
Minimum Transmitter Power (P_{Tmin})†	14.0 dBm
Minimum Transmitter Power per Channel	11.0 dBm
Maximum Received Power (P_{Rmax})‡	-11.0 dBm
Receiver Sensitivity (P_{Rmin})†‡	-27.0 dBm
Minimum System Gain (S-R)	38.0 dB
FT-LBA Asymmetric Gain Penalty	2.5 dB
DMXR Loss	2.5 dB
Optical Path Penalty (P_O)	2.0 dB
Maximum Loss Budget§	31.0 dB
Minimum Loss Budget¶	10.0 dB
Optical Dispersion	2520 ps/nm

* Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

† These values include transmitter/receiver connectors @ 0.7 dB each (worst case) and the system margins.

‡ The receiver sensitivity and maximum received power values are measured at a BER of 1×10^{-10} .

§ The 1.5 μm Standard Performance optics is dispersion limited to 92 kilometers.

¶ These values assume that the maximum received power limitations are not exceeded.

Transmission Distances

FT-2000 OC-48 Lightwave System without Optical Amplifiers

Table 10-10 shows the transmission distances of the FT-2000 OC-48 Lightwave System without optical amplifiers (for example, the FT-Lightwave Booster Amplifier).

Table 10-10. Transmission Distances without Optical Amplifiers

Circuit Pack	Loss Budget	Standard Fiber (Note 1)	Low Loss Fiber (Note 2)	Dispersion-Shifted Fiber (Note 3) (Note 4)
OC48 TRMTR (A/D 1.3 STD)	24 dB	53 km	64 km	—
OC48 TRMTR (A/D STS1 1.3 STD)		33 miles	40 miles	
OC48 TRMTR (A/D 1.3 HI PERF)	27 dB	60 km	72 km	—
OC48 TRMTR (A/D STS1 1.3 HI PERF)		37 miles	45 miles	
OC48 TRMTR (A/D 1.5 STD)*	23 dB (SF)	92 km	105 km	96 km
OC48 TRMTR (A/D STS1 1.5 STD)	24 dB (DSF)	57 miles	65 miles	60 miles
OC3 (1.3 STD)	23 dB	51 km	61 km	—
		32 miles	38 miles	
OC12 (1.3 STD)	23 dB	51 km	61 km	—
		32 miles	38 miles	
OC48 739E_ TRMTR (A/D 1.5 STD)	23 dB			
OC48 739G_ TRMTR (A/D 1.5 STD)	23 dB			
OC48 739H_ TRMTR (A/D 1.5 STD)	23 dB			

Notes:

- Standard fiber assumes the following fiber loss (including splice loss) with a dispersion limit of 18 psec/nm per kilometer:
1.3 μm — 0.45 dB/km
1.5 μm — 0.25 dB/km.
- Low loss fiber assumes the following fiber loss (including splice loss) with a dispersion limit of 18 psec/nm per kilometer:
1.3 μm — 0.375 dB/km
1.5 μm — 0.22 dB/km.
- Dispersion-shifted fiber assumes a 0.25 dB/km loss at 1.5 μm .
- Only 1.5 μm transmitters will work with dispersion-shifted fiber.

* The dispersion limit applies only to the OC48 TRMTR (A/D 1.5 STD) circuit pack using standard fiber. The limit is 1800 psec/nm or 100 km.

FT-2000 OC-48 Lightwave System and the FT-Lightwave Booster Amplifier

Table 10-11 shows the transmission distances of the FT-2000 OC-48 Lightwave System with the FT-Lightwave Booster Amplifier.

Table 10-11. Transmission Distances with the FT-Lightwave Booster Amplifier

FT-LBA Loss Budget		Dispersion Limit (Note 1)		Maximum Distance		
				Standard Fiber (SF) (Note 2)		Dispersion-Shifted Fiber (DSF) (Note 3)
		OC48 TRMTR (A/D 1.5 STD)	OC48 TRMTR A/D (1.5 LBA) (1.5 LBA, W1) (1.5 LBA, W2) (STS1 1.5 LBA, W1) (STS1 1.5 LBA, W2) (Note 4)	OC48 TRMTR (A/D 1.5 STD)	OC48 TRMTR A/D (1.5 LBA) (1.5 LBA, W1) (1.5 LBA, W2) (STS1 1.5 LBA, W1) (STS1 1.5 LBA, W2) (Note 4)	OC48 TRMTR A/D (1.5 STD) (1.5 LBA) (1.5 LBA, W1) (1.5 LBA, W2) (STS1 1.5 LBA, W1) (STS1 1.5 LBA, W2)
9 dBm	32 dB (SF)	1800 psec/nm	2160 psec/nm	100 km	120 km	132 km
	33 dB (DSF)	100 km	120 km	62 miles	75 miles	82 miles
12 dBm	35 dB (SF)	1800 psec/nm	2510 psec/nm	100 km	140 km	144 km
	36 dB (DSF)	100 km	140 km	62 miles	87 miles	89 miles
16 dBm	39 dB (SF)	1800 psec/nm	2520 psec/nm	100 km	140 km	160 km
	40 dB (DSF)	100 km	140 km	62 miles	87 miles	99 miles

Notes:

1. This dispersion limit applies only to 1.5 μm STD and LBA performance transmitters used with standard fiber.
2. Standard fiber assumes a dispersion limit of 18 psec/nm per km. It also assumes a fiber loss (including splice loss) of 0.25 dB/km at 1.5 μm .
3. Dispersion-shifted fiber assumes a 0.25 dB/km loss at 1.5 μm . On dispersion-shifted fiber, both versions of the 1.5 μm transmitters support the same distance.
4. The 1.5 μm LBA transmitter is screened for dispersion.

FT-2000 OC-48 Lightwave System with Wavelength Division Multiplexing and FT-LBA Systems

Table 10-12 shows the transmission distances of the FT-2000 OC-48 Lightwave System with wavelength division multiplexing only, and with wavelength division multiplexing and the FT-Lightwave Booster Amplifier.

Table 10-12. Transmission Distances with Wavelength Division Multiplexing and the FT-LBA

OC48 TRMTR A/D (1.5 LBA, W1 and W2) (STS1 1.5 LBA, W1 and W2) Circuit Packs (Note 1)	Loss Budget	Standard Fiber (SF) (Note 2)	Low Loss Fiber (LLF) (Note 3)	Dispersion- Shifted Fiber (DSF) (Note 4) (Note 5)
Used with Wavelength Division Multiplexing	20.5 dB (SF, LLF) 21.5 dB (DSF)	82 km 51 miles	93 km 58 miles	86 km 53 miles
Used with Wavelength Division Multiplexing and FT-LBA	31.0 dB (SF, LLF)	124 km 77 miles	140 km 87 miles	—

Notes:

1. The dispersion limit only applies to the 1.5 μ m standard performance transmitters using standard fiber. The limit is 2520 psec/nm or 140 km.
2. Standard fiber assumes the following fiber loss (including splice loss) with a dispersion limit of 18 psec/nm per kilometer:
1.3 μ m — 0.45 dB/km
1.5 μ m — 0.25 dB/km.
3. Low loss fiber assumes the following fiber loss (including splice loss) with a dispersion limit of 18 psec/nm per kilometer:
1.3 μ m — 0.375 dB/km
1.5 μ m — 0.22 dB/km.
4. Dispersion-shifted fiber assumes a 0.25 dB/km loss at 1.5 μ m.
5. Only 1.5 μ m transmitters will work with dispersion-shifted fiber. On dispersion-shifted fiber, both versions of 1.5 μ m transmitters support the same distances.

Capacity

48 DS3 Equivalent Signals (32,256 two-way voice circuits per fiber pair)

Transmission Characteristics

The FT-2000 OC-48 Lightwave System meets single-mode interoffice digital fiber optic systems requirements and objectives as specified in GR-253-CORE and TR 499.

Error Rate	<10 ⁻⁹ accumulated error rate for systems up to 400 kilometers (250 miles)
Maximum Repeater Spacing	60 kilometers at 1.3 μm on 0.46 dB/km fiber
Maximum Repeater Spacing	92 kilometers at 1.5 μm on 0.28 dB/km fiber (dispersion limited)

DS3 Access

Interface.....	Standard DS3 interface as specified in T1.102-1987
Growth Increment	Three DS3s per circuit pack; ports can be provisioned one at a time
Line Code	Bipolar 3-zero substitution (B3ZS)
Protection Ratio	1xM (M≤16)



CAUTION:

All DS3 signal outputs from the FT-2000 OC-48 Lightwave System must be properly terminated at all times. Therefore, these outputs must be connected to DSX-3 cross-connect panels that have internal 75-ohm terminations (for example, Lucent Technologies cross-connect panels meet this requirement) or other working transmission equipment (for example, DACS III-2000 Cross-Connect System or DDM-2000 Multiplexer) where the associated low speed interfaces are equipped. When operating the FT-2000 OC-48 Lightwave System with cross-connect panels that do not automatically terminate the DS3 signal outputs, 75-ohm termination plugs must be placed at the cross-connect panel.

In releases prior to Release 7, if DS3 output ports are not terminated with 75-ohms, false trouble conditions could be reported by the FT-2000 OC-48 Add/Drop-Rings Terminal. In Release 7 and later releases, it is still recommended that DS3 output ports be terminated properly. However, if unterminated ports in the drop direction (from OC-48 line) are in the AUTO state, false trouble conditions will not be reported by the FT-2000 OC-48 Add/Drop-Rings Terminal.

EC-1 Access

Interface.....	Standard EC-1 interface as specified by GR-253-CORE and T1X1.4
Growth Increment.....	Three EC-1s per circuit pack; ports can be provisioned one at a time
Line Code	Scrambled B3ZS
Protection Ratio	1xM (M≤16)



CAUTION:

All EC-1 signal outputs from the FT-2000 OC-48 Lightwave System must be properly terminated at all times. Therefore, these outputs must be connected to STSX-1 cross-connect panels that have internal 75-ohm terminations (for example, Lucent Technologies cross-connect panels meet this requirement) or other working transmission equipment (for example, DACS III-2000 Cross-Connect System or DDM-2000 Multiplexer) where the associated low speed interfaces are equipped. When operating the FT-2000 OC-48 Lightwave System with cross-connect panels that do not automatically terminate the EC-1 signal outputs, 75-ohm termination plugs must be placed at the cross-connect panel.

In releases prior to Release 7, if EC-1 output ports are not terminated with 75-ohm, false trouble conditions could be reported by the FT-2000 OC-48 Add/Drop-Rings Terminal. In Release 7 and later releases, it is still recommended that EC-1 output ports be terminated properly. However in Release 7 and later releases, if unterminated ports in the drop direction (from OC-48 line) are in the AUTO state, false trouble conditions will not be reported by the FT-2000 OC-48 Add/Drop-Rings Terminal.

OC-3 Access

Optical Line Interface

Optical output.....	155.52 Mb/s
Optical line code	Scrambled nonreturn to zero (NRZ)
Growth Increment	One OC-3 per circuit pack
Protection Ratio	1+1



NOTE:

The protection ratio of 1+1 is not valid when two adjacent slots of an OC-3 Optical Interface are equipped with both an OC-3 (1.3 STD) LAA10 circuit pack and an OC-3 (1.3 STD) LAA10B circuit pack. To operate in a 1+1 configuration, both adjacent slots (for example, 2A and 2B) **MUST** be equipped with either the LAA10 or the LAA10B, but not a mix of the two.

The OC3 Optical Interface - 1.3 μ m Standard Performance [OC3 (1.3 STD)] circuit pack complies with the OC-3 short reach and intermediate reach optical interface standards (TR-253). The OC3 (1.3 STD) circuit pack also supports applications with span lengths up to 51 kilometers.

Optical Connector Interfaces

The OC3 (1.3 STD) circuit pack uses *ST* lightguide connectors (standard), *FC/PC* lightguide connectors, or *SC* lightguide connectors.

Transmission Medium

Single-mode fiber or multimode fiber

Lightguide Jumpers

Single-mode or multimode lightguide jumpers

Optical Source

Multi-longitudinal mode (MLM) laser

Optical Detector

PIN diode

Optical Safety (BRH Classification)

OC-3 standard performance (1.31 μm) Class I

Operating Wavelength

OC-3 receiver	1.31 μm +0.070 or –0.050 μm
OC-3 transmitter	1.31 μm +0.040 or –0.038 μm

Spectral Width

3.0 nm (RMS)

Optical Dispersion

OC-3 standard performance (1.31 μm) 270 ps/nm

OC-3 Line Loss Budgets

The FT-2000 OC-48 Lightwave System is designed to meet the OC-3 loss budget specifications shown in Table 10-13. The transmit and receive powers are referenced to points S and R in Figure 10-1.

Table 10-13. OC-3 Line Loss Budget Specifications (Note)

Parameter	1.31 μ m Standard Performance
Maximum Transmitter Power (P_{Tmax})*	0.0 dBm
Minimum Transmitter Power (P_{Tmin})	-7.0 dBm
Maximum Received Power (P_{Rmax})	-7.0 dBm
Receiver Sensitivity (P_{Rmin})	-34.0 dBm
Minimum System Gain (S-R)†	27.0 dB
Optical Path Penalty (P_O)‡	1.0 dB
Connector Loss§	1.5 dB
Unallocated Margin¶	1.5 dB
Maximum Loss Budget**	23.0 dB
Minimum Loss Budget	7.0 dB

Note: The output of the OC3 (1.3 STD) circuit pack is shipped with a 7.1-dB LBO. For spans greater than 15-dB, replace the 7.1-dB LBO with a 0-dB LBO. The 7.1-LBO (or equivalent) optical attenuation must be used for optical loopbacks.

* Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

† The minimum system gain allows for account aging, temperature, and manufacturing tolerances (these figures are built into the minimum transmitter power).

‡ Optical path penalty includes the effects of dispersion, reflection, and jitter that occur on the optical path.

§ One connector (0.75 dB) on each end is assumed to connect station cable to outside plant.

¶ Unallocated margin, or safety margin, is usually used for fiber repairs. This can be used to increase the loss budget if needed.

** The budget is available for both station and transmission cable and splices.

IS-3 Access

Optical Line Interface

Optical output	155.52 Mb/s
Optical line code.....	Scrambled nonreturn to zero (NRZ)
Growth Increment.....	One IS-3 per circuit pack
Protection Ratio.....	1+1

The IS3 Optical Interface (IS3) circuit pack supports intraoffice signal transmission distances up to 2000 feet. The IS-3 signal is based on proprietary optical specifications that are common across the SONET family of products.

Optical Connector Interfaces

The IS3 circuit pack uses *ST* lightguide connectors (standard), FC/PC lightguide connectors, or SC lightguide connectors.

Transmission Medium

Multimode fiber

Lightguide Jumpers

Multimode lightguide jumpers

Optical Source

LED

Optical Detector

PIN diode

Optical Safety (BRH Classification)

IS3 Optical Interface Class I

Operating Wavelength

IS-3 1.31 μm +0.070 or –0.040 μm

Spectral Width

170.0 nm (RMS)

Optical Dispersion

IS-3 (1.31 μm)..... 270 ps/nm

IS-3 Line Loss Budgets

The FT-2000 OC-48 Lightwave System is designed to meet the IS-3 loss budget specifications shown in Table 10-14. The transmit and receive powers are referenced to points S and R in Figure 10-1.

Table 10-14. IS-3 Line Loss Budget Specifications

Parameter	1.31 μm Standard Performance
Maximum Transmitter Power ($P_{T\text{max}}$)*	–14.0 dBm
Minimum Transmitter Power ($P_{T\text{min}}$)	–20.8 dBm
Maximum Received Power ($P_{R\text{max}}$)	–14.0 dBm
Receiver Sensitivity ($P_{R\text{min}}$)	–30.8 dBm
Minimum System Gain (S-R)†	10.0 dB
Optical Path Penalty (P_O)‡	1.0 dB
Connector Loss§	1.5 dB
Unallocated Margin¶	2.0 dB
Maximum Loss Budget**	5.5 dB
Minimum Loss Budget	0.0 dB

* Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

† The minimum system gain allows for account aging, temperature, and manufacturing tolerances (these figures are built into the minimum transmitter power).

‡ Optical path penalty includes the effects of dispersion, reflection, and jitter that occur on the optical path.

§ One connector (0.75 dB) on each end is assumed to connect station cable to outside plant.

¶ Unallocated margin, or safety margin, is usually used for fiber repairs. This can be used to increase the loss budget if needed.

** The budget is available for both station and transmission cable and splices.

OC-12 Access

Optical Line Interface

Optical output.....	622.08 Mb/s
Optical line code	Scrambled nonreturn to zero (NRZ)
Growth Increment	One OC-12 per circuit pack
Protection Ratio	0x1
Protection Ratio	1+1

The OC12 Optical Interface - 1.3 μ m Standard Performance [OC12 (1.3 STD)] circuit pack complies with the OC-12 long reach standard (GR-253-CORE).

Optical Connector Interfaces

The OC12 (1.3 STD) circuit pack uses *ST* lightguide connectors (standard), FC/PC lightguide connectors, or SC lightguide connectors.

Transmission Medium

Single-mode fiber or multimode fiber

Lightguide Jumpers

Single-mode or multimode lightguide jumpers

Optical Source

InGaAsP, Multi-longitudinal mode (MLM) laser

Optical Detector

PIN diode

Optical Safety (BRH Classification)

OC-12 standard performance (1.31 μm) Class I

Operating Wavelength

OC-12* 1.31 μm +0.015 or -0.012 μm

* Available in Release 7.1, but the DCC channel cannot be provisioned for 1+1 connections.

Spectral Width

2.0 nm (RMS)

Optical Dispersion

OC-12 standard performance (1.31 μm) 270 ps/nm

OC-12 Line Loss Budgets

The FT-2000 OC-48 Lightwave System is designed to meet the OC-12 loss budget specifications shown in Table 10-15. The transmit and receive powers are referenced to points S and R in Figure 10-1.

Table 10-15. OC-12 Line Loss Budget Specifications

Parameter	1.31 mm Standard Performance
Maximum Transmitter Power (P_{Tmax})*	2.0 dBm
Minimum Transmitter Power (P_{Tmin})	-2.5 dBm
Maximum Received Power (P_{Rmax})	-8.0 dBm
Receiver Sensitivity (P_{Rmin})	-30.5 dBm
Minimum System Gain (S-R)†	28.0 dB
Optical Path Penalty (P_O)‡	2.0 dB
Connector Loss§	1.5 dB
Unallocated Margin¶	1.5 dB
Maximum Loss Budget**	23.0 dB
Minimum Loss Budget	10.0 dB

* Transmit and receive powers are referenced to points S and R as shown in Figure 10-1.

† The minimum system gain allows for account aging, temperature, and manufacturing tolerances (these figures are built into the minimum transmitter power).

‡ Optical path penalty includes the effects of dispersion, reflection, and jitter that occur on the optical path.

§ One connector (0.75 dB) on each end is assumed to connect station cable to outside plant.

¶ Unallocated margin, or safety margin, is usually used for fiber repairs. This can be used to increase the loss budget if needed.

** The budget is available for both station and transmission cable and splices.

OC-3 to IS-3 Interface Interconnection

For intraoffice applications, the OC-3 interface can interwork with another OC-3 interface or an IS-3 interface using multimode fiber.

The IS-3 interface is a Lucent Technologies proprietary interface that operates at the same rate and format as the OC-3 interface.

Table 10-16 shows the span maximum distances, using dispersion limited fiber, for OC-3 to OC-3 connections and OC-3 to IS-3 connections.

Table 10-16. Maximum Distances

Fiber (Note) Bandwidth	OC-3 to OC-3 Connections	OC-3 to IS-3 Connections
1000 MHz-km	6.5 km	3.4 km
800 MHz-km	5.1 km	3.3 km
500 MHz-km	3.1 km	2.7 km
300 MHz-km	1.8 km	1.9 km

For details (including LBO values) and ordering information about connecting to a DDM-2000 IS-3 interface, refer to 365-206-200, *DDM-2000 Multiplexer, Applications, Planning, and Ordering Guide*.

Transmission Delay

The FT-2000 OC-48 Add/Drop-Rings Terminal has the following worst-case measured one-way transmission delay times:

Add or Drop DS3 Circuit	8.2 μ sec
Add or Drop EC-1 Circuit	4.0 μ sec
Add or Drop OC-3 Circuit	4.5 μ sec
Add or Drop IS-3 Circuit	4.5 μ sec
Add or Drop OC-12 Circuit	4.5 μ sec
Through Circuit (OC-48 to OC-48)	5.2 μ sec

Protection Switching (OC-3, OC-12, OC-48 Lines)

Switching Bit Error Rate (BER)	10^{-5} to 10^{-9} (user provisionable)
Restoral BER	One-tenth of the switching BER
Switching Time	60 msec ($\text{BER} \geq 10^{-3}$ line signal failure)

Electrical Line Buildout Specifications

Electrical line buildouts may be required to be connected in series with the transmit LS INTFC cable connections depending on the application, cable type, and cable length associated with that particular LS INTFC cable. Table 10-17 shows when electrical line buildouts should be used based on the actual cable length (or equivalent measured signal level) for each transmit cable.

Table 10-17. Electrical Line Buildout Specifications

Application Connection to FT-2000	Length in Feet from FT-2000 Eqpt. to Connected Eqpt. for LS INTFC Signal In/Out Cables		LBO Req'd. for LS INTFC Signal Out Cable
	735A Cable (Note 1)	734D (Notes 2 and 3) or 735A/734D (Note 4) or 735A/734D/735A (Note 5) Combination Cable	
DS3 or EC-1 Cross-Connect Equipment	0-125	0-225*	Yes
	>125-250	>225-450	No
DACS III-2000, DACS IV-2000, DDM-2000, FT-2000, or Equiv. Non-Cross-Connect Equipment	0-125	0-225*	Yes
	>125-500	>225-900	No

Notes:

1. The exclusive use of 735A coaxial cable for connections between the FT-2000 and terminating equipment is strongly recommended. This is an important consideration when deciding office layout and equipment arrangements during site planning. Use the cable length figures in the table to aid in office layout.
2. 734D coaxial cable is electrically equivalent to 734A cable and has a silver coating on the center conductor to enhance crimping the connectors.
3. Direct connection of 734D cables to FT-2000 equipment is *not* recommended due to space limitations in the interconnection panel. 735A cables should be used for these connections.
4. If cable length requirements exceed the lengths specified for 735A cable, 735A/734D or 735A/734D/735A cable combinations may be used.

* This is a nonpreferred cable combination; a preferred cable type that does not require an LBO should be used in its place if possible.

Cable Access

- Rear only required for adding shelves or upgrading FT-2000 terminal bays
- Front for all customer access
- Connectorized cabling with commercially available connectors

Figure 10-2 depicts various “Inter-office” options for connecting two NEs via coaxial cable.

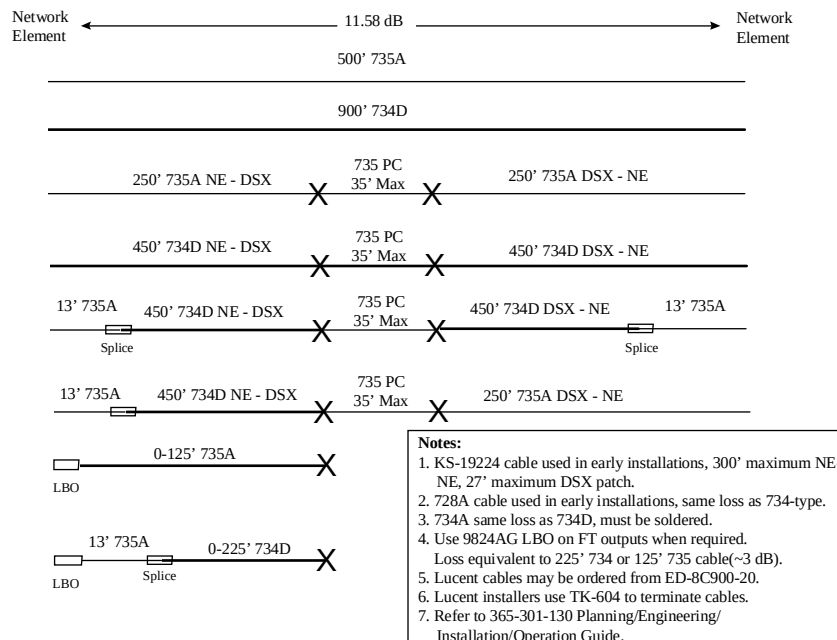


Figure 10-2. Common Coaxial Cable Interoffice Cross-Connect Options for DS-3 and STS-1E

Power Specifications


NOTE:

Consult FPD 804-911-168 for complete engineering of battery plant and feeders.

Voltage Range, all components	–42.75 to –60 V DC
Power Feeders	Two –48 V power feeders ("A" and "B")
Fuse Size (per shelf)	10 amps
Maximum Power Dissipation:	
— FT-2000 OC-48 Add/Drop-Rings Terminal	507 watts (72 wts/ft ²)
for 2–Fiber Rings	
— FT-2000 OC-48 Dual Lightwave	1142 watts (162 watts/ft ²)
Terminating Bay	
— FT-2000 OC-48 Repeater Shelf	175 watts (25 watts/ft ²)
— FT-2000 OC-48 Repeater Shelf	175 watts (25 watts/ft ²)
(One Repeater Shelf - System Controller)	
— FT-2000 OC-48 Repeater Shelf	350 watts (50 watts/ft ²)
(Two Repeater Shelf - System Controllers)	
— FT-2000 OC-48 Repeater Shelf	525 watts (75 watts/ft ²)
(Three Repeater Shelf - System Controllers)	
Current Drains per Feeder Cable:	
Nominal (List 1*) (@ –48 V DC)	
—FT-2000 OC-48 Add/Drop-Rings	
Terminal for 2-Fiber Ring	5.3 amps
—FT-2000 OC-48 Dual Lightwave Terminating Bay..	11.9 amps
— FT-2000 OC-48 Repeater Shelf	1.8 amps
— FT-2000 OC-48 Repeater Shelf	1.8 amps
(One Repeater Shelf - System Controller)	
— FT-2000 OC-48 Repeater Shelf	3.6 amps
(Two Repeater Shelf - System Controllers)	
— FT-2000 OC-48 Repeater Shelf	5.5 amps
(Three Repeater Shelf - System Controllers)	
Maximum (List 2†) (@ –42.75 V DC)	
—FT-2000 OC-48 Add/Drop-Rings	
Terminal for 2-Fiber Ring	11.9 amps
—FT-2000 OC-48 Dual Lightwave Terminating Bay..	26.7 amps
— FT-2000 OC-48 Repeater Shelf	4.1 amps

Voltage Range, all components	–42.75 to –60 V DC
— FT-2000 OC-48 Repeater Shelf (One Repeater Shelf - System Controller)	4.1 amps
— FT-2000 OC-48 Repeater Shelf (Two Repeater Shelf - System Controllers)	8.2 amps
— FT-2000 OC-48 Repeater Shelf (Three Repeater Shelf - System Controllers)	12.3 amps

* Nominal (List 1) current drains used to size batteries and rectifiers. To size batteries and rectifiers, use twice the nominal (List 1) current drain per feeder. These current drains represent the average busy-hour current at normal operating voltages.

† Maximum (List 2) current drains used to size each feeder cable and fuse. To size feeder cables and fuses, use the maximum (List 2) current drain per feeder. These current drains represent the peak current under worst-case operating conditions. Normally the current for the system is shared equally by both feeders. If one feeder fails, the other feeder carries the total load for both feeders (feeder A + feeder B current).

Bay/Shelf Dimensions

FT-2000 OC-48 Lightwave System Bays*	84 inches (213 cm) high 26 inches (66 cm) wide 12 inches (30 cm) deep
FT-2000 OC-48 Lightwave System Bays (Packaged)	33 inches (84 cm) high 90 inches (229 cm) wide 29 inches (74 cm) deep
Enhanced High-Speed Shelf.....	21.5 inches (54 cm) high 21.5 inches (54 cm) wide 12 inches (30 cm) deep
Condensed High-Speed Shelf	14.3 inches (36 cm) high 21.5 inches (54 cm) wide 12 inches (30 cm) deep
Low Speed Shelf—System Controller	21.5 inches (54 cm) high 21.5 inches (54 cm) wide 12 inches (30 cm) deep
Low Speed Shelf—Complementary.....	21.5 inches (54 cm) high 21.5 inches (54 cm) wide 12 inches (30 cm) deep
Repeater Shelf—System Controller.....	21.5 inches (54 cm) high 21.5 inches (54 cm) wide 12 inches (30 cm) deep
FT-2000 OC-48 Repeater Shelf.....	25.5 inches (65 cm) high 21.5 inches (54 cm) wide 12 inches (30 cm) deep

Bay/Shelf Dimensions (Contd)

PC Tray	4 inches (10 cm) high 21.5 inches (54 cm) wide 10 inches (25 cm) deep
Heat Baffle.....	4 inches (10 cm) high 21.5 inches (54 cm) wide 10 inches (25 cm) deep
Fan Assembly.....	4 inches (10 cm) high 21.5 inches (54 cm) wide 10 inches (25 cm) deep

* The shelves in an FT-2000 bay are central office white and the framework is central office soft blue.

Circuit Pack Dimensions

I	LAA2/LAA2B DS3.....	13 inches (32 cm) high 0.75 inch (1.9 cm) wide 7 inches (19 cm) deep
	LAA4 STS1E.....	13 inches (32 cm) high 0.75 inch (1.9 cm) wide 7 inches (19 cm) deep
I	LAA10/LAA10B OC3	13 inches (32 cm) high 0.75 inch (1.9 cm) wide 7 inches (19 cm) deep
	LAA5 IS3.....	13 inches (32 cm) high 0.75 inch (1.9 cm) wide 7 inches (19 cm) deep
	T939A OC12.....	13 inches (32 cm) high 3.00 inch (7.5 cm) wide 7 inches (19 cm) deep
	LAA12 LSSW	13 inches (32 cm) high 0.75 inch (1.9 cm) wide 7 inches (19 cm) deep
	LAA14 OW	13 inches (32 cm) high 0.75 inch (1.9 cm) wide 7 inches (19 cm) deep
	LAA18 TG3 (DS1).....	13 inches (32 cm) high 1 inch (2.5 cm) wide 7 inches (19 cm) deep
	LAA21 OHCTL (TERM)	13 inches (32 cm) high 0.75 inch (1.9 cm) wide 7 inches (19 cm) deep
	LAA22 OHCTL (TERM)	13 inches (32 cm) high 0.75 inch (1.9 cm) wide 7 inches (19 cm) deep
	LAA23B SYSCTL/LAA23C	13 inches (32 cm) high 1 inch (2.5 cm) wide 7 inches (19 cm) deep
	LAA25 SYSMEM	13 inches (32 cm) high 0.75 inch (1.9 cm) wide 7 inches (19 cm) deep
	LAA26 TOHCTL.....	13 inches (32 cm) high 1 inch (2.5 cm) wide 7 inches (19 cm) deep

LAA28 LNCTL	13 inches (32 cm) high .75 inch (1.9cm) wide 7 inches (19 cm) deep
39B2 OC48 REGENR	13 inches (32 cm) high 3.25 inches (8.3 cm) wide 7 inches (19 cm) deep
39C2 OC48 REGENR	13 inches (32 cm) high 3.25 inches (8.3 cm) wide 7 inches (19 cm) deep
39J2 OC48 REGENR	13 inches (32 cm) high 3.25 inches (8.3 cm) wide 7 inches (19 cm) deep
39R2 OC48 REGENR	13 inches (32 cm) high 3.25 inches (8.3 cm) wide 7 inches (19 cm) deep
39S2 OC48 REGENR	13 inches (32 cm) high 3.25 inches (8.3 cm) wide 7 inches (19 cm) deep
739B2 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739B4 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739B5 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739C2 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739C5 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739E_ OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739J2 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep

739J3 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739J4 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739J5 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739P5 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739R2 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739R5 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739S2 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
739S5 OC48 TRMTR	13 inches (32 cm) high 4.5 inches (11.4 cm) wide 7 inches (19 cm) deep
839B2 OC48 RCVR	13 inches (32 cm) high 2.25 inches (5.7 cm) wide 7 inches (19 cm) deep
839B4B OC48 RCVR	13 inches (32 cm) high 2.25 inches (5.7 cm) wide 7 inches (19 cm) deep
839B5 OC48 RCVR.....	13 inches (32 cm) high 2.25 inches (5.7 cm) wide 7 inches (19 cm) deep
839E5 OC48 RCVR.....	13 inches (32 cm) high 2.25 inches (5.7 cm) wide 7 inches (19 cm) deep

Floor Loading Specifications

1 Shelf Bay	< 220 lbs (31 lbs/ft ²)
2 Shelf Bay	< 375 lbs (53 lbs/ft ²)
3 Shelf Bay	< 420 lbs (60 lbs/ft ²)
4 Shelf Bay	< 465 lbs (67 lbs/ft ²)
Packaged Bay	< 520 lbs

Environmental Specifications

Normal Operating Temperature	41°F to 104°F (5°C to 40°C)
Short Term* Operating Temperature	23°F to 122°F (–5°C to 50°C)
Operating Temperature Change Rate.....	1.8°F/minute (1°C/minute)
Normal Operating Humidity [†]	5 to 85 percent
Short Term* Operating Humidity [†]	5 to 90 percent

* Short term refers to a period of up to 72 consecutive hours and a total of up to 15 days in 1 year.

† Noncondensing.

Earthquake and vibration, fire resistance, and airborne contaminant requirements meet GR-63-CORE Network Equipment Building Systems (NEBS) Generic Equipment Requirements. Electrostatic, electromagnetic, electrical grounding, and safety requirements meet GR-1089-CORE standards. The FT-2000 OC-48 Lightwave System is also designed to meet the electromagnetic compatibility requirements of FCC Title 47, Part 15, Subpart J for Class A equipment.

The FT-2000 OC-48 Lightwave System is *UL*^{*} and *CSA*[†] listed.

* Registered trademark of Underwriters Laboratories Inc.

† Registered trademark of Canadian Standards Association.

Craft Interface Terminal

Personal Computer Requirements

In Release 5.0.2 and later releases, a PC must meet the following minimum requirements to be loaded with FT-2000 CIT-PC software and used as a CIT:

- 386SX *IBM*^{*} compatible computer (desktop or lap top) (25 MHz clock or greater)
- Floppy Disk drive — one 1.44 Megabyte (3.5 inch)
- Hard disk with approximately 5 Megabytes of free space
- 4 Megabytes RAM[†]
- *MS-DOS*[‡] computer program, version 5.0 or later
- Serial port (EIA-232-D) — configured as COM1, COM2, COM3, or COM4
- Parallel port — configured as LPT1
- VGA color monitor.

* Registered trademark of International Business Machine Corporation.

† No memory resident programs running in the background. 565 kilobytes of conventional memory is also required. In Release 7 and later releases, 2 megabytes of extended extended memory is also required. If less than 565 kilobytes of free conventional memory is available, a memory manager (for example, HIMEM.SYS with *MS-DOS* 5.0 and later) is required to increase the free conventional memory.

‡ Registered trademark of Microsoft Corporation.

In releases prior to Release 5.0.2, a PC must meet the following minimum requirements to be loaded with FT-2000 CIT-PC software and used as a CIT:

- Any *IBM*^{*} compatible computer (desktop or laptop)
- Disk drive – one 720 kilobyte (3.5 inch) or one 1.2 megabyte (5.25 inch)
- Hard disk with approximately 5 megabytes of free space
- 640 kilobyte RAM[†]
- *MS-DOS* computer program, version 3.1 or later
- Serial port (EIA-232-D) – configured as COM1, COM2, COM3[‡], or COM4
- Parallel port – configured as LPT1
- 4.77 MHz clock speed or greater
- Monochrome screen monitor – Standard-size American Standard Code for Information Exchange (ASCII) 25 line x 80 column

A PC must meet the following minimum requirements to be loaded with the CPro-2000 software and used as a CIT:

- 386SX *IBM* compatible computer (desktop or lap top) (25 MHz clock or greater)
- Disk drive — one 1.44 Megabyte (3.5 inch)
- Hard disk with approximately 5 Megabytes of free space
- 8 Megabyte RAM *
- *MS-DOS* computer program, version 5.0 or later
- *Windows*[§] program version 3.1
- Serial port (EIA-232-D) — configured as COM1 or COM2
- Mouse
- VGA color monitor.

* Registered trademark of International Business Machines Corporation.

† No memory resident programs running in the background. 565 Kilobytes of free conventional memory is required. In Release 7 and later releases 2 megabytes of extended memory is also required. If less than 565 kilobytes of free conventional memory is available, a memory manager (for example, HIMEM.SYS with MS-DOS 5.0 and later) is required to increase the free conventional memory

‡ Not supported in Releases 1 and 2

§ Registered trademark of Microsoft Corporation.

ASCII-Based Terminal Requirements

In Release 5 and later releases, an ASCII-based (dumb) terminal may be used as a CIT. The ASCII-based terminal must meet the following minimum requirements:

- ASCII-based data communications terminal with an 80-column display
- Scroll capability
- Local echo
- Serial port (EIA-232-D)
- Data transmission speed should be from 1200 to 9600 baud
- XON/XOFF flow control capability.

The ASCII-based terminal should be configured for byte serial data transmission of eight-bit bytes with one start bit and one stop bit. Parity should be "none" and the flow control should be "DC1/DC3."

Modem Requirements

A compatible modem or digital data network must meet the following minimum requirements:

- Full duplex
- Asynchronous
- Byte serial data transmission of eight-bit bytes
- One start bit
- One stop bit
- Data transmission speed should be from 1200 to 9600 baud.

The modem must comply with one of the following transmission standards, depending on the desired baud rate.

Standard	Baud Rate(s)
Bell 212A	1200
V.22	1200
V.22 bis	2400
V.32	4800,9600
V.32 bis	9600 (fax), 14400 (data)

Operations Interfaces

- Office alarms: The steady state current for office alarm connections should not exceed 0.9 A at 60 V or 1.8 A at 30 V. The maximum transient currents (20 msec duration) during initial contact closure should not exceed 9 A at 60 V or 18 A at 30 V.
- Parallel Telemetry: The parallel telemetry interface is designed for a maximum open circuit voltage of 60 V and a maximum current of 35 mA.
- Miscellaneous Discretes: Any external customer equipment to be monitored by the FT-2000 OC-48 Lightwave System must provide the electrical equivalent of a contact closure across the corresponding environmental input wiring pairs. The contact closure must be capable of passing at least 10 mA of drive current.

The power source to enable the FT-2000 OC-48 Lightwave System to control external customer equipment may be from a minimum of 3 V to a maximum of 72 V. The FT-2000 OC-48 Lightwave System provides a unidirectional opto-isolator connection across each corresponding control output wiring pair. The load current across each control output wiring pair must be limited by the external customer equipment and must not exceed 35 mA.

- Telemetry Byte-Oriented Serial (TBOS) Protocol: The TBOS interface operates at 2400 baud through an EIA-422 port.
- X.25 Protocol/Transaction Language 1 (TL1): The TL1/X.25 interface operates at 1200 to 19,200 baud through an EIA-232-D port.

TL1/X.25 Interface

General

FT-2000 OC-48 Lightwave System supports an TL1/X.25 interface to report alarm and status conditions and performance-monitoring data to operations systems (OS) such as Telcordia's *NMA*^{*} OS. (See Telcordia's SR-STS-OO1665 for a list of NMA releases compatible with the FT-2000 OC-48 Lightwave System.) The FT-2000 OC-48 Lightwave System TL1 messages are based on Telcordia's TR-TSY-000833, Issue 3, Rev. 1.[†]

The X.25/TL1 interface supports two permanent virtual circuits (PVCs) and up to six switched virtual circuits (SVCs). Every virtual circuit may be used to issue TL1 commands to the FT-2000 OC-48 Lightwave System and obtain responses. The ability of virtual circuits to send autonomous TL1 messages varies with the FT-2000 OC-48 Lightwave System release used.

TL1 Autonomous Messaging in Release 4.1 and Earlier

For releases through Release 4.1, the FT-2000 OC-48 Lightwave System supports two PVCs (Table 10-18). Only PVC #1 can send autonomous messages. The FT-2000 OC-48 Lightwave System through Release 4.1 also supports up to six SVCs (Table).

Table 10-18. X.25/TL1 Interface – PVC Assignments (Through Release 4.1)

Logical Channel #	Use
1	Autonomous and Command/Response Messages
2	Command/Response Messages

* Trademark of Bell Communications Research, Inc.

† Where TR-TSY-000833, Issue 3, Rev. 1 was deficient, the FT-2000 OC-48 Lightwave System conforms to Telcordia's TR-NWT-000833, Issue 4.

Table 10-19. X.25/TL1 Interface – SVC Assignments (Through Release 4.1)

Logical Channel #	Use
3	Command/Response Messages
4	Command/Response Messages
5	Command/Response Messages
6	Command/Response Messages
7	Command/Response Messages
8	Command/Response Messages

Provisioning TL1 Autonomous Messages for Release 5 and Later Releases

In Release 5 and later releases, up to eight logical channels (two PVCs and up to six SVCs) are supported. All logical channels may be used for command/response messages. In addition, TL1 autonomous messages are optionally available on all of the logical channels via user provisioning. There are four OS types, that can be provisioned per PVC logical channel. An OS type is a filter that determines the types of TL1 autonomous messages that appear at a logical channel.

For SVCs, the OS type is provisioned indirectly, in conjunction with the X.25 calling address. The user can specify what OS type to associate with a given X.25 calling address. When an SVC is set up using one of the provisioned calling addresses, then the call is assigned the associated OS type. If the SVC does not supply an X.25 calling address or if the X.25 calling address does not match any of the provisioned X.25 calling addresses, then command/response only is assumed.

When an OS establishes an SVC call, an available SVC is selected by the packet network. The user does not control SVC selection.

DTE Configuration

At the packet layer, the FT-2000 OC-48 Lightwave System is configured as a passive data terminating equipment (DTE) with the parameters listed in Table 10-20.

Table 10-20. TL1/X.25 Interface — X.25 Packet Layer Parameters

Parameter	Value
Packet Size	256 bytes
Window Size	2 packets
D bit support	No
M bit support	Yes
Q bit support	No

At the link layer, the FT-2000 OC-48 Lightwave System uses the standard link access procedure B (LAPB) protocol with the parameters listed in Table 10-21.

Table 10-21. TL1/X.25 Interface — LAPB Link Layer Parameters

Parameter	Value
Maximum Frame Size	2104 bits
Modulo	8
Window Size	7 frames
n2	6 retries
T1	3 seconds
T3	not supported

The FT-2000 OC-48 Lightwave System uses synchronous, full duplex, continuous carrier communication. Data rates of 1200, 2400, 4800, 9600, and 19200 are supported.

The EIA-232D interface is configured as DTE, using the pins listed in Table 10-22.

Table 10-22. TL1/X.25 Interface — EIA-232D Pin Connections

Pin	Description
1	Protective Ground (Shield)
2	Transmitted Data
3	Received Data
4	Request to Send (RTS)*
5	Clear to Send (CTS)†
6	DCE Ready
7	Signal Ground
8	Received Line Signal Detector
15	Transmit Clock (TC)
17	Receive Clock (RC)
20	Data Terminal Ready (DTR)*

* DTR and RTS are always on when FT-2000 is powered.

† FT-2000 will only transmit data via X.25 when CTS is on.

External Synchronization Interface Reliability Specifications

DS3/EC-1 Channel Unavailability (hardware only)	
— FT-2000 OC-48 Add/Drop-Rings* Terminal for 2-Fiber Rings (equipped with DS3 and/or STS1E interfaces)	0.37 minutes/year, per DS3 channel
— 566 mile, 16-node ring circumference† (DS3/STS1E channel traversing 9 FT-2000 OC-48 Add/Drop-Rings Terminals)	102 minutes/year, per DS3/STS1E channel
OC-3/IS-3 Channel Unavailability (hardware only)	
— FT-2000 OC-48 Add/Drop-Rings* Terminal for 2-Fiber Rings (equipped with protected OC-3/IS-3 interfaces)	0.0002 minutes/year, per OC-3/IS-3 channel
— FT-2000 OC-48 Add/Drop-Rings* Terminal for 2-Fiber Rings (equipped with unprotected OC-3/IS-3 interfaces)	4.9 minutes/year, per OC-3/IS-3 channel
— 566 mile, 16-node ring circumference† (OC-3 channel traversing nine FT-2000 OC-48 Add/Drop-Rings Terminals with protected OC-3/IS-3 interfaces)	1.20 minutes/year, per OC-3/IS-3 channel
— 566 mile, 16-node ring circumference† (OC-3 channel traversing nine FT-2000 OC-48 Add/Drop-Rings Terminals with unprotected OC-3/IS-3 interfaces)	11.00 minutes/year, per OC-3/IS-3 channel
OC-12 Channel Unavailability (hardware only)	
— FT-2000 OC-48 Add/Drop-Rings* Terminal for 2-Fiber Rings (equipped with protected OC-12 interfaces)	0.003 minutes/year, per OC-12 channel
— FT-2000 OC-48 Add/Drop-Rings* Terminal for 2-Fiber Rings (equipped with unprotected OC-12 interfaces)	7.9 minutes/year, per OC-12 channel
— 566 mile, 16-node ring circumference† (OC-12 channel traversing nine FT-2000 OC-48 Add/Drop-Rings Terminals with protected OC-12 interfaces)	1.20 minutes/year, per OC-12 channel
— 566 mile, 16-node ring circumference† (OC-12 channel traversing nine FT-2000 OC-48 Add/Drop-Rings Terminals with unprotected OC-12 interfaces)	17.00 minutes/year, per OC-12 channel
Silent Failure Unavailability*	
— FT-2000 OC-48 Add/Drop-Rings	011 minutes/year

Terminal for 2-Fiber Rings (equipped with DS3 and STS1E interfaces)	per DS3 channel
— FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings (equipped with protected and/or unprotected OC-3/IS-3/OC-12 interfaces)	0
Operations System Interface Unavailability*	
— FT-2000 OC-48 Add/Drop-Rings	11.0 minutes/year
Terminal for 2-Fiber Rings FT-2000 with DS3/STS1E and FT-2000 with OC3/IS-3/OC-12	12.1 minutes/year 14.0 minutes/year
Mean Time[†] Between Maintenance Activities	
— FT-2000 OC-48 Add/Drop-Rings	26.6 months
Terminal for 2-Fiber Rings (equipped with DS3 and/or STS1E interfaces)	
— FT-2000 OC-48 Add/Drop-Rings	21.6 months
Terminal for 2-Fiber Rings (equipped with protected and/or unprotected OC-3/IS-3 interfaces)	
— FT-2000 OC-48 Add/Drop-Rings	31.5 months
Terminal for 2-Fiber Rings (equipped with protected and/or unprotected OC-12 interfaces)	
— FT-2000 OC-48 Repeater Bay	78.3 months
One Repeater Shelf - System Controller	
— FT-2000 OC-48 Repeater Bay	39.2 months
Two Repeater Shelf - System Controllers	
— FT-2000 OC-48 Repeater Bay	26.1 months
Three Repeater Shelf - System Controllers	
— FT-2000 OC-48 Repeater Shelf	78.3 months
Mean Time to Repair	≤ 4 hours (includes dispatch, diagnostics, and repair time)
Infant Mortality	≤ 1.4 times the steady state failures/10 ⁻⁹ hours (FIT) as defined in Telcordia TA-418, Issue 2
Product Design Life	25 years

* Based on Method 1 of the Telcordia, Reliability Prediction Procedure for Electronic Equipment, Issue 5, December 1995.

† Assuming one fiber failure event every 3 years, and an average fiber repair time of 3 hours.

‡ Based on the AT&T Reliability Information Notebook, 7th Edition, August 1995.

External Synchronization Cabling	
— to a DSX-1 cross-connect panel	655 feet maximum
— to other sources	1310 feet maximum
DS1 Synchronization Inputs	
— Source	Two references; must be derived from a stratum 3 or better timing source
— Line Coding	AMI (original value) or B8ZS
— Frame Format	SF (original value) or ESF
— Connector	DB9
— Protection Switching..... Between References	Revertive, Automatic on LOS, AIS, LOF, OOF, or BER
DS1 Synchronization Outputs	
— Source	Two references; derived directly from the OC-48 line
Point-to-Point	Reference 1 derived from Service Line; Reference 2 derived from Protection Line
Rings	Reference 1 derived from West Direction; Reference 2 derived from East Direction
— Frame Format	SF or ESF
— DS1	AIS inserted if reference becomes unsuitable
— Connector	DB9
— LBO	CIT provisionable

Circuit Pack FIT Rates

Circuit Pack	Failures/10 ⁹ hours* (FIT)
LAA2B DS3.....	1304
LAA4 STS1E.....	1311
LAA10 OC3.....	2278
LAA5 IS3.....	2000
T939A OC12.....	3000
LAA12 LSSW.....	2023
LAA12B LSSW.....	2023
LAA14 OW.....	1500
LAA18 TG3 (DS1).....	1305
LAA21 OHCTL (TERM).....	1084
LAA22 OHCTL (TERM).....	1075
LAA23B SYSCTL.....	1442
LAA23C SYSCTL.....	1811
LAA25 SYSMEM.....	1528
LAA26 TOHCTL.....	1100
LAA28 LNCTL (4 MEG).....	1104
39B2 OC48 REGENR (1.3 STD).....	3630
39C2 OC48 REGENR (1.3 HI PERF).....	3630
39J2 OC48 REGENR (1.5 STD).....	3630
39R2 OC48 REGENR (1.5 LBA W1).....	3630
39S2 OC48 REGENR (1.5 LBA W2).....	3630
739B2 OC48 TRMTR (A/D 1.3 STD).....	3904
739B4 OC48 TRMTR (A/D 1.3 STD).....	4000
739B5 OC48 TRMTR (A/D STS1 1.3 STD).....	5000
739C2 OC48 TRMTR (A/D 1.3 HI PERF).....	3904
739C5 OC48 TRMTR (A/D STS1 1.3 HI PERF).....	5000
739E1-E8 OC48 TRMTR (A/D STS1 1.5 360km DISP).....	7522
739J2 OC48 TRMTR (A/D 1.5 STD).....	3904

Circuit Pack	Failures/10 ⁹ hours* (FIT)
739J3 OC48 TRMTR (A/D 1.5 LBA).....	3904
739J4 OC48 TRMTR (A/D 1.5 STD)	4000
739J5 OC48 TRMTR (A/D STS1 1.5 STD)	5000
739P5 OC48 TRMTR (A/D STS1 1.5 LBA)	5000
739R2 OC48 TRMTR (A/D 1.5 LBA W1)	3904
739R5 OC48 TRMTR (A/D STS1 1.5 LBA W1)	5000
739S2 OC48 TRMTR (A/D 1.5 LBA W2).....	3904
739S5 OC48 TRMTR (A/D STS1 1.5 LBA W2).....	5000
839B2 OC48 RCVR (A/D)	2912
839B4B OC48 RCVR (A/D).....	3000
839B5 OC48 RCVR (A/D STS1)	3000

* Based on Method 1 of the Telcordia, *Reliability Prediction Procedure for Electronic Equipment*, Issue 5, December 1995.

Table of Contents

Overview	11-1
CPro-2000	11-1
FT-2000 CIT-PC Software Introduction	11-3
Inputs	11-3
Notice Frames, Confirmation Requests, and Aborting Commands	11-4
Context-Sensitive Help	11-5
CIT Function Keys	11-5
PC Screen Header Line	11-7
Outputs	11-37
Examples of Reports [Generated by Various Inputs (Commands)]	11-38
Example 1 - SECURITY-Retrieve-Active-User	11-38
Example 2 - FAULT-Retrieve-Alarm	11-40
Example 3 - CONFIGURATION-Retrieve-Assignment-DCC	11-42
Example 4 - CONFIGURATION-Retrieve-Attribute-Alarm	11-43
Example 5 - CONFIGURATION-Retrieve-Attribute-Control	11-44
Example 6 - CONFIGURATION-Retrieve-Attribute-Environment	11-45
Example 7 - CONFIGURATION-Retrieve-Circuit-STS1	11-46
Example 8 - CONFIGURATION-Retrieve-Circuit-STS3	11-49
Example 9 - CONFIGURATION-Retrieve-Crossconnection-STS1	11-51
Example 10 - CONFIGURATION-Retrieve-Crossconnection-STS3	11-53
Example 11 - CONFIGURATION-Retrieve-Crossconnection-All	11-58
Example 12 - CONFIGURATION-Retrieve-Date	11-61

Table of Contents

Example 13 - CONFIGURATION-Retrieve-EC1	11-62
Example 14 - CONFIGURATION-Retrieve-Equipment	11-63
Example 15 - SECURITY-Retrieve-Far End Communications	11-65
Example 16 - FAULT-Retrieve-History	11-67
Example 17 - SECURITY-Retrieve-Login	11-68
Example 18 - CONFIGURATION-Retrieve-Low Speed	11-70
Example 19 - CONFIGURATION-Retrieve-Map-Network	11-71
Example 20 - CONFIGURATION-Retrieve-Map-Ring	11-72
Example 21 - FAULT-Retrieve-Network Alarms	11-75
Example 22 - SECURITY-Retrieve-Network Element	11-78
Example 23 - CONFIGURATION-Retrieve-OC3	11-82
Example 24 - CONFIGURATION-Retrieve-OC48	11-84
Example 25 - CONFIGURATION-Retrieve-Path-Trace	11-87
Example 26 - PERFORMANCE-Retrieve-PM-EC1	11-90
Example 27 - PERFORMANCE-Retrieve-PM-Line	11-92
Example 28 - PERFORMANCE-Retrieve-PM Mode-T3	11-96
Example 29 - PERFORMANCE-Retrieve-PM-Optics	11-97
Example 30 - PERFORMANCE-Retrieve-PM-Section	11-99
Example 31 - PERFORMANCE-Retrieve-PM-Start Time	11-101
Example 32 - PERFORMANCE-Retrieve-PM-STS1	11-102
Example 33 - PERFORMANCE-Retrieve-PM-T3	11-106
Example 34 - PERFORMANCE-Retrieve-PM-TCA	11-109
Example 35 - PERFORMANCE-Retrieve-PM Threshold-EC1	11-111
Example 36 - PERFORMANCE-Retrieve-PM Threshold-Line	11-114
Example 37 - PERFORMANCE-Retrieve-PM Threshold-Optics	11-116
Example 38 - PERFORMANCE-Retrieve-PM Threshold-Section	11-118
Example 39 - PERFORMANCE-Retrieve-PM Threshold-STS1	11-121
Example 40 - PERFORMANCE-Retrieve-PM Threshold-T3	11-124
Example 41 - CONFIGURATION-Retrieve-Protection-Line	11-127
Example 42 - CONFIGURATION-Retrieve-Protection-Low Speed	11-130
Example 43 - SECURITY-Retrieve-Security-Network Element	11-131
Example 44 - SECURITY-Retrieve-Security-Port	11-132
Example 45 - CONFIGURATION-Retrieve-Squelch Map-STS1	11-134

Table of Contents

Example 46 - CONFIGURATION-Retrieve-Squelch Map-STS3	11-135
Example 47 - FAULT-Retrieve-State	11-136
Example 48 - CONFIGURATION-Retrieve-Synchronization	11-143
Example 49 - CONFIGURATION-Retrieve-T3	11-145
Example 50 - CONFIGURATION - Retrieve-Timeslot-STS1	11-147
Example 51 - CONFIGURATION-Retrieve-Timeslot-STS3	11-149
■ CIT Tutorial Using CIT-PC Software	11-151
Additional Training on an FT-2000 Add/Drop-Rings Terminal	11-168

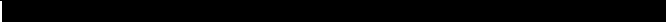


Table of Contents

Overview

This section covers the following topics:

- CPro-2000
- FT-2000 CIT-PC Software Introduction
- Inputs
- Notice Frames, Confirmation Requests, and Aborting Commands
- Context-Sensitive Help
- CIT Function Keys
- PC Screen Header Line
- Outputs
- Examples of Reports
- Craft Interface Terminal (CIT) Tutorial.

The user should be familiar with the information in this tab before using the Operation and Maintenance Task Oriented Practices (TOP) procedures in this manual.

CPro-2000

CPro-2000 is a *Microsoft Windows*^{*}-based application (software) that provides access to SONET network elements. The network elements are accessed via the software through a command-based ASCII User Interface (AUI) and/or a Graphical User Interface (GUI). Network elements currently include DDM-2000 OC-3, DDM-2000 OC-12, and FT-2000 OC-48.

* Microsoft is a registered trademark and Windows is a trademark of Microsoft Corporation.

CPro-2000 is typically used by centralized work center personnel (operations systems) or maintenance personnel to analyze, provision, cross-connect, and test the network. If trouble is found, it is localized to a network element, then repair personnel are dispatched to complete the trouble clearing activity.

CPro-2000 is documented in 190-523-101, *CPro-2000 User Manual*. The features are described in the manual along with procedures for using them. Specific procedures for network elements are documented in the appendixes. For example, Appendix C provides a cross-reference list of commands for the FT-2000 OC-48 Add/Drop-Rings Terminal. Also supported is a cross-reference table of Operation and Maintenance tasks (Table 11-1) which lists the procedures that have been identified where CPro-2000 could be used. And in Appendix D, Script Files are very helpful in setting up remote access to network elements in different configurations.

Table 11-1. CPro-2000 Cross-Reference Procedures

Data Element

Circuit Order: NTP-002	Add DS3 and/or EC1 Low Speed Port(s)
Circuit Order: NTP-003	Delete DS3 and/or EC1 Low Speed Port(s)
Circuit Order: NTP-010	Add OC3 Low Speed Slot(s)
Circuit Order: NTP-011	Delete OC3 Low Speed Slot(s)
Circuit Order: NTP-012	Add OC12 Low Speed Slot(s)
Circuit Order: NTP-013	Delete OC12 Low Speed Slot(s)
Operation: NTP-003	Establish Dual Ring Interworking (DRI) Connections
Operation: NTP-003	Disconnect Dual Ring Interworking (DRI) Connections
Detailed Level Procedure: DLP-517	Provision Low Speed Slot and Port Parameters
Detailed Level Procedure: DLP-522	Change, Delete, or Enter a User's Login
Detailed Level Procedure: DLP-536	Enter/Delete/Edit STS-3 Cross-Connections at Network Elements for 2-Fiber Ring Network
Detailed Level Procedure: DLP-537	Enter/Delete/Edit STS-1 Cross-Connections at Network Elements for 2-Fiber Ring Network

FT-2000 CIT-PC Software Introduction

FT-2000 CIT-PC software is the user-friendly, menu-driven interface used with the FT-2000 OC-48 Lightwave System. FT-2000 CIT-PC software consists of a series of menus with different security levels (cit or cit expert). The software package runs on an *MS-DOS** compatible personal computer (PC).

The PC and the FT-2000 CIT-PC software together make up the craft interface terminal (CIT). The CIT connects to an FT-2000 network element (also referred to as an FT-2000 terminal or an FT-2000 repeater bay/shelf in this manual).

Inputs

Inputs to FT-2000 CIT-PC consist of a series of selections from menus and parameter inputs followed by the pressing of function key **F9**, labeled **EXECUTE**. This sequence of selecting and executing can be called an "input" or a "command."

Table 11-2 (Release 7) lists the inputs (commands) menu for an **FT-2000 OC-48 Add/Drop-Rings Terminal**.

Table 11-3 (Release 6) lists the inputs (commands) menu for an **FT-2000 OC-48 Add/Drop-Rings Terminal**.

Table 11-4 (Release 5) lists the inputs (commands) menu for an **FT-2000 OC-48 Add/Drop-Rings Terminal**.

Table 11-5 (Release 4) lists the inputs (commands) menu for an **FT-2000 OC-48 Add/Drop-Rings Terminal**.

Table 11-6 (Release 3) lists the inputs (commands) menu for an **FT-2000 OC-48 Add/Drop-Rings Terminal**.

Table 11-7 (Releases 2, 4, and 6) lists the inputs (commands) menu for an **FT-2000 repeater bay/shelf**.

Table 11-8 provides a brief description of inputs (commands) which are arranged alphabetically by the verb and are applicable to both the FT-2000 add/drop-rings terminal and the FT-2000 repeater bay/shelf.

Table 11-9 provides a list of inputs (commands) that generate reports for the FT-2000 network elements.

* Registered trademark of Microsoft Corporation.

Notice Frames, Confirmation Requests, and Aborting Commands

Some inputs are potentially service-affecting and will cause a notice frame or confirmation request to appear on the screen. This alerts the user to the possible consequences of executing the selected inputs.

- A notice frame is acknowledged and cleared by pressing **ESC**.
- A confirmation request consists of a caution message followed by inputs entered and the prompt "Execute? (Yes/No)". Pressing **y** and **ENTER** causes inputs to be executed. Pressing **n** and **ENTER** causes the parameter value entry frame to be displayed again.
- Inputs can be aborted by pressing function key **F8**, when labeled **CANCEL**.

Context-Sensitive Help

Help for inputs is provided in three forms:

1. Message Line Help — This appears near the bottom of the screen immediately above the display of the function keys. This line provides procedural information, status of the FT-2000 OC-48 Lightwave System, or yes/no response information; for example, "Highlight the desired item and press return to select" or "Execution in progress".
2. Function key **F1** Help — When function key **F1** is labeled HELP, it can be pressed to display a context-sensitive description of the highlighted menu item, parameter, notice frame, etc.
3. Question mark (?) Help — When a parameter is highlighted in a parameter frame, a menu of allowable values (point-and-shoot) or the parameter range is displayed when a question mark (?) or a wrong value is entered.

CIT Function Keys

The function keys, **F1** through **F9**, displayed at the bottom of the screen provide a particular function when labeled. The nine function keys are available to expedite certain operations. All function keys are not active at all times. An active function key is shown by its label. Inactive function keys are not labeled. The function of a function key may vary with time and this is indicated by changes in the labeling.

- (F1) When labeled HELP, context-sensitive information is available for the highlighted menu item. By pressing the **F1** key, this additional and specific information is displayed.
- (F2) When labeled REMOTE in the local only state or session, this key initiates a connection to a remote FT-2000 network element. **F2** is labeled REMOTE in the local state and LOCAL in the remote state, thereby allowing local/remote toggling.
- (F3) When labeled OUTPUT, this key clears the screen of menus and parameter frames so the output frame can be viewed. By pressing **F3** again (now labeled INPUT), the screen is restored with the menus or parameter frames. This capability is particularly useful while entering parameters because the user can review information that was displayed just previously, and then resume the entry of parameters from the same point, without repeating what has already been done.

When labeled DIAL in the local connect state, this key allows the user to enter control data for a modem.

When labeled BREAK in the dial state, this key sends a break character to the communications port.

- (F4) When labeled LOGOUT, this key permits the user to terminate the connection to the FT-2000 network element.

When labeled CHECK, this key sends the autobaud character sequence (break, enter, enter) to an FT-2000 network element.
- (F5) When labeled EXIT, this key permits the user to terminate the connection and exit the FT-2000 CIT-PC software, returning the user back to the *MS-DOS* prompt.
- (F6) When labeled SETUP, this key permits the user to configure the operating parameters of the PC.
- (F7) When labeled PRINT, this key permits the user to send the FT-2000 network element output to a printer, to a file, or to both.
- (F8) When labeled CANCEL, this key permits the user to discard all user entries for the current command or cancels the dial request. If **F8** is pressed after executing a command, an attempt to cancel the command is sent to the FT-2000 network element.
- (F9) When labeled EXECUTE, this key permits the user to execute the completed command (valid parameter values).

When labeled REPEAT, this key permits the user to repeat the last command or display the last command and edit it.

When labeled ALARM, this key permits the user to execute a **Retrieve-Alarm:All** command with a single keystroke.
- (F10) Currently, this key is not being used.

PC Screen Header Line

The six fields displayed from left to right across the **top of the screen** are as follows:

- Network element status indicator (displays highest current system status. This field is blank when there are no alarms or any NE or FE activity present.)
- Lucent Technologies (fixed in program)
- FT-2000 (fixed in program)
- Software release and platform string (currently running in the SYSCTL)
- Terminal identification [target identifier (TID) of the network element]
- Session context (CIT connection state as follows):
 - LOCAL ONLY — CIT is only connected to local network element.
 - REMOTE (Flashing) — CIT is connected to remote network element from local network element.
 - LOCAL — CIT is connected to remote network element, but it is toggled back to local network element.

**Table 11-2. Release 7 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
FAULT	Retrieve	Alarm (and Status)		Report Only	2
		History		Report Only	15
		Network Alarms		Report Only	20
		State		Report Only	46
	Switch	Line		General	
		Low Speed		General	
		OC3		General	
		OC12*		General	
		Path	STS1	General	
			STS3	General	
		Synchronization	Hardware	General	
			Mode	General	
			Reference	General	
	Test	Alarm		Report Only	
		Auto Turnup	DSX	Privileged/Expert	
			Local	Privileged/Expert	
		LED		Report Only	
		Telemetry	Parallel	General	
			Serial	General	
	Operate	Alarm Cutoff		Report Only	
		Control	Point	General	
		Loopback	EC1	General	
			OC3	General	
			OC12*	General	
			T3	General	
	Release	Control	Point	General	
		Loopback	EC1	General	
			OC3	General	
			OC12*	General	
			T3	General	
	Allow	Alarm		General	
	Inhibit	Alarm		General	
	Reset			General	
CONFIGURATION	Retrieve	Assignment	DCC	Report Only	3
		Attribute	Alarm	Report Only	4
			Control	Report Only	5
			Environment	Report Only	6
		Circuit	STS1	Report Only	7

**Table 11-2. Release 7 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal (Contd)**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
CONFIGURATION (Cont'd)	Retrieve (Contd)	Cross-connection [†]	All	Report Only	9
		Cross-connect	STS1	Report Only	10
		Cross-connect	STS3	Report Only	11
		Date		Report Only	12
		EC1		Report Only	13
		Equipment		Report Only	14
		Low Speed		Report Only	18
		Map	Network	Report Only	19
			Ring	Report Only	20
			Neighbor*	Report Only	
		OC3		Report Only	23
		OC12*		Report Only	
		OC48		Report Only	24
		Path	Trace	Report Only	25
		Protection	Line	Report Only	41
			Low Speed	Report Only	42
			STS1	Report Only	
			STS3	Report Only	
		Synchronization		Report Only	48
		T3		Report Only	49
		Timeslot	STS1	Report Only	50
			STS3	Report Only	51
	Set	Attribute	Alarm	General	
			Control	General	
			Environment	General	
		Date		Privileged	
		EC1		General, Privileged/Expert	
		Low Speed		General	
		OC3		General, Privileged/Expert	
		OC12*		General, Privileged/Expert	
		OC48		General	
		Path	Trace	General	
		Protection	Line	General	
			Low Speed	General	

**Table 11-2. Release 7 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal (Contd)**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
CONFIGURATION (Cont'd)	Set (Contd)	State	EC1	General	
			OC12*	General	
			OC3*	General	
			T3	General	
		Synchronization		Privileged/Expert	
		T3		General, Privileged/Expert	
	Copy	EC1		General, Privileged/Expert	
		OC3		General, Privileged/Expert	
		OC12		General, Privileged/Expert	
		OC48		General	
		Path	Trace	General	
		Program		Privileged/Expert	
		T3		General, Privileged/Expert	
	Enter	Assignment	DCC	General	
		Crossconnection	STS1	General	
			STS3	General	
		Protection	STS1	General	
			STS3	General	
		Roll	STS1	General	
			STS3	General	
	Delete	Assignment	DCC	General	
		Crossconnection	STS1	General	
			STS3	General	
	Update			General	
	Initialize	System		Privileged/Expert	

**Table 11-2. Release 7 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal (Contd)**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
PERFORMANCE	Retrieve	PM	EC1	Report Only	26
			Line	Report Only	27
			Optics	Report Only	29
			Section	Report Only	30
			Start Time	Report Only	31
			STS1	Report Only	32
			T3	Report Only	33
			TCA	Report Only	34
		PM Mode	T3	Report Only	28
		PM Threshold	EC1	Report Only	35
			Line	Report Only	36
			Optics	Report Only	37
			Section	Report Only	38
			STS1	Report Only	39
			T3	Report Only	40
	Set	PM	Start Time	General	
		PM Mode	T3	General	
		PM Threshold	EC1	General	
			Line	General	
			Optics	General	
			Section	General	
			STS1	General	
			T3	General	
	Initialize	PM	All, Day, QH	General	
SECURITY	Retrieve	Far End Comm.		Report Only	15
		Login		Report Only	17
		Network Element		Report Only	22
		Security	Network Element	Report Only	43
			Port	Report Only	44
		Active	User	Report Only	1
	Set	Far End Comm.		Privileged	
		Login	Change	Privileged	
			Delete	Privileged	
			Enter	Privileged	
		Network Element		Privileged	
		Password		Report Only	
		Security	Network Element	Privileged	
			Port	Privileged	

* Available with Release 7.1 and later releases

† Available with Release 7.2.7 and later releases.

Table 11-3. Release 6 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
FAULT	Retrieve	Alarm (and Status)		Report Only	2
		History		Report Only	15
		Network Alarms		Report Only	20
		State		Report Only	46
	Switch	Line		General	
		Low Speed		General	
		OC3		General	
		Path	STS3	General	
		Synchronization	Hardware	General	
			Mode	General	
			Reference	General	
	Test	Alarm		General	
		Auto Turnup	DSX	Privileged/Expert	
			Local	Privileged/Expert	
		LED		Report Only	
		Telemetry	Parallel	General	
			Serial	General	
	Operate	Alarm Cutoff		Report Only	
		Loopback	EC1	General	
			OC3	General	
			T3	General	
	Release	Loopback	EC1	General	
			OC3	General	
			T3	General	
	Allow	Alarm		General	
	Inhibit	Alarm		General	
	Reset			General	
CONFIGURATION	Retrieve	Assignment	DCC	Report Only	3
		Attribute	Alarm	Report Only	4
			Control	Report Only	5
			Environment	Report Only	6
		Circuit	STS3	Report Only	8
		Crossconnection	STS3	Report Only	10
		Date		Report Only	11
		EC1		Report Only	12
		Equipment		Report Only	13
		Low Speed		Report Only	17
		Map	Network	Report Only	18
			Ring	Report Only	19

**Table 11-3. Release 6 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal (Contd)**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
CONFIGURATION (Cont'd)	Retrieve (Cont'd)	OC3		Report Only	22
		OC48		Report Only	23
		Path	Trace	Report Only	24
		Protection	Line	Report Only	40
			Low Speed	Report Only	41
		Squelch Map	STS1	Report Only	44
			STS3	Report Only	45
		Synchronization		Report Only	47
	Set	T3		Report Only	48
		Timeslot	STS3	Report Only	50
		Attribute	Alarm	General	
			Control	General	
			Environment	General	
		Date		Privileged	
		EC1		General, Privileged/Expert	
		Low Speed		General	
		OC3		General, Privileged/Expert	
		OC48		General	
		Path	Trace	General	
		Protection	Line	General	
			Low Speed	General	
		State	EC1	General	
			T3	General	
		Synchronization		Privileged/Expert	
		T3		General, Privileged/Expert	
	Copy	EC1		General, Privileged/Expert	
		OC3		General, Privileged/Expert	
		OC48		General	
		Path	Trace	General	
		Program		Privileged/Expert	
		T3		General, Privileged/Expert	
	Enter	Assignment	DCC	General	
		Crossconnection	STS3	General	
		Roll	STS3	General	
		Squelch Map	STS1	General	

**Table 11-3. Release 6 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal (Contd)**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
CONFIGURATION (Cont'd)	Delete	Assignment	DCC	General	
		Crossconnection	STS3	General	
		Squelch Map	STS1	General	
	Update			General	
	Initialize	System		Privileged/Expert	
PERFORMANCE	Retrieve	PM	EC1	Report Only	25
			Line	Report Only	26
			Optics	Report Only	28
			Section	Report Only	29
			Start Time	Report Only	30
			STS1	Report Only	31
			T3	Report Only	32
			TCA	Report Only	33
		PM Mode	T3	Report Only	27
		PM Threshold	EC1	Report Only	34
			Line	Report Only	35
			Optics	Report Only	36
			Section	Report Only	37
			STS1	Report Only	38
			T3	Report Only	39
	Set	PM	Start Time	General	
		PM Mode	T3	General	
		PM Threshold	EC1	General	
			Line	General	
			Optics	General	
			Section	General	
			STS1	General	
			T3	General	
	Initialize	PM	All, Day, QH	General	

**Table 11-3. Release 6 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal (Contd)**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
SECURITY	Retrieve	Far End Comm.		Report Only	14
		Login		Report Only	16
		Network Element		Report Only	21
		Security	Network Element	Report Only	42
			Port	Report Only	43
		Active	User	Report Only	1
	Set	Far End Comm.		Privileged	
		Login	Change	Privileged	
			Delete	Privileged	
			Enter	Privileged	
		Network Element		Privileged	
		Password		Report Only	
		Security	Network Element	Privileged	
			Port	Privileged	

**Table 11-4. Release 5 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
FAULT	Retrieve	Alarm (and Status)		Report Only	2
		History		Report Only	15
		Network Alarms		Report Only	20
		State		Report Only	46
	Switch	Line		General	
		Low Speed		General	
		OC3		General	
		Path	STS3	General	
		Synchronization	Hardware	General	
			Mode	General	
			Reference	General	
	Test	Alarm		General	
		Auto Turnup	DSX	Privileged/Expert	
			Local	Privileged/Expert	
		LED		Report Only	
		Telemetry	Parallel	General	
			Serial	General	
	Operate	Alarm Cutoff		Report Only	
		Loopback	EC1	General	
			OC3	General	
			T3	General	
	Release	Loopback	EC1	General	
			OC3	General	
			T3	General	
	Allow	Alarm		General	
	Inhibit	Alarm		General	
	Reset			General	
CONFIGURATION	Retrieve	Attribute	Alarm	Report Only	4
			Control	Report Only	5
			Environment	Report Only	6
		Circuit	STS3	Report Only	8
		Crossconnection	STS3	Report Only	10
		Date		Report Only	11
		EC1		Report Only	12
		Equipment		Report Only	13
		Low Speed		Report Only	17
		Map	Ring	Report Only	19

**Table 11-4. Release 5 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal (Contd)**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
CONFIGURATION (Cont'd)	Retrieve (Cont'd)	OC3		Report Only	22
		OC48		Report Only	23
		Path	Trace	Report Only	24
		Protection	Line	Report Only	40
			Low Speed	Report Only	41
		Squelch Map	STS1	Report Only	44
			STS3	Report Only	45
		Synchronization		Report Only	47
	Set	T3		Report Only	48
		Timeslot	STS3	Report Only	50
		Attribute	Alarm	General	
			Control	General	
			Environment	General	
		Date		General Privileged	
		EC1		General Privileged/Expert	
		Low Speed		General	
		OC-3		General, Privileged/Expert	
		OC48		General	
		Path	Trace	General	
		Protection	Line	General	
			Low Speed	General	
		State	EC1	General	
			T3	General	
		Synchronization		Privileged/Expert	
		T3		General,	
	Copy	EC1		General, Privileged/Expert	
		OC3			
		OC48		General	
		Path	Trace	General	
		Program		Privileged/Expert	
		T3		General, Privileged/Expert	
	Enter	Crossconnection	STS3	General	
		Roll	STS3	General	
		Squelch Map	STS1	General	
	Delete	Crossconnection	STS3	General	
		Squelch Map	STS1	General	
	Update			General	

**Table 11-4. Release 5 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal (Contd)**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
CONFIGURATION	Initialize	System		Privileged/Expert	
PERFORMANCE	Retrieve	PM	EC1	Report Only	25
			Line	Report Only	26
			Optics	Report Only	28
			Section	Report Only	29
			Start Time	Report Only	30
			STS1	Report Only	31
			T3	Report Only	32
			TCA	Report Only	33
		PM Mode	T3	Report Only	27
		PM Threshold	EC1	Report Only	34
			Line	Report Only	35
			Optics	Report Only	36
			Section	Report Only	37
			STS1	Report Only	38
			T3	Report Only	39
	Set	PM	Start Time	General	
		PM Mode	T3	General	
		PM Threshold	EC1	General	
			Line	General	
			Optics	General	
			Section	General	
			STS1	General	
			T3	General	
	Initialize	PM	All, Day, QH	General	
		Far End Comm.		Report Only	14
		Login		Report Only	16
SECURITY	Retrieve	Network Element		Report Only	21
		Security	Network Element	Report Only	42
			Port	Report Only	43
		Active	User	Report Only	1
	Set	Far End Comm.		Privileged	
		Login	Change	Privileged	
			Delete	Privileged	
			Enter	Privileged	
		Network Element		Privileged	

**Table 11-4. Release 5 Inputs (Commands) Menu for FT-2000
Add/Drop-Rings Terminal (Contd)**

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
		Password		Report Only	
		Security	Network Element	Privileged	
			Port	Privileged	

Table 11-5. Release 4 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
FAULT	Retrieve	Alarm (and Status)		Report Only	2
		History		Report Only	15
		Network Alarms		Report Only	20
		State		Report Only	46
	Switch	Line		General	
		Low Speed		General	
		Path	STS3	General	
		Synchronization	Hardware	General	
			Mode	General	
			Reference	General	
	Test	Alarm		General	
		Auto Turnup	DSX	Privileged/Expert	
			Local	Privileged/Expert	
		LED		Report Only	
		Telemetry	Parallel	General	
			Serial	General	
	Operate	Alarm Cutoff		Report Only	
		Loopback	EC1	General	
			T3	General	
	Release	Loopback	EC1	General	
			T3	General	
	Allow	Alarm		General	
	Inhibit	Alarm		General	
	Reset			General	
CONFIGURATION	Retrieve	Attribute	Alarm	Report Only	4
			Control	Report Only	5
			Environment	Report Only	6
		Crossconnection	STS3	Report Only	10
		Date		Report Only	11
		EC1		Report Only	12
		Equipment		Report Only	13
		Low Speed		Report Only	17
		Map	Ring	Report Only	19
		OC48		Report Only	23
		Path	Trace	Report Only	24
		Protection	Line	Report Only	40
			Low Speed	Report Only	41
		Synchronization		Report Only	47
		T3		Report Only	48

Table 11-5. Release 4 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal (Contd)

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
CONFIGURATION (Contd)	Set	Attribute	Alarm	General	
			Control	General	
			Environment	General	
		Date		Privileged(R4.1) General(R4.0)	
		EC1		General	
		Low Speed		General	
		OC48		General	
		Path	Trace	General	
		Protection	Line	General	
			Low Speed	General	
		State	EC1	General	
			T3	General	
		Synchronization		Privileged/Expert	
		T3		General	
	Copy	EC1		General	
		OC48		General	
		Path	Trace	General	
		T3		General	
	Enter	Crossconnection	STS3	General	
		Roll	STS3	General	
	Delete	Crossconnection	STS3	General	
	Update			General	
	Initialize	System		Privileged/Expert	
PERFORMANCE	Retrieve	PM	EC1	Report Only	25
			Line	Report Only	26
			Optics	Report Only	28
			Section	Report Only	29
			STS1	Report Only	31
			T3	Report Only	32
			TCA	Report Only	33
		PM Threshold	EC1	Report Only	32
			Line	Report Only	35
			Optics	Report Only	36
			Section	Report Only	37
			STS1	Report Only	38
			T3	Report Only	39
	Set	PM Threshold	EC1	General	
			Line	General	
			Optics	General	
			Section	General	

Table 11-5. Release 4 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal (Contd)

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
PERFORMANCE (Contd)	Set (Cont'd)	PM Threshold (Cont'd)	STS1	General	
			T3	General	
	Initialize	PM	All, Day, QH	General	
SECURITY	Retrieve	Far End Comm.		Report Only	14
		Network Element		Report Only	21
		Login		Report Only	16
		Security	Network Element	Report Only	42
			Port	Report Only	43
	Set	Far End Comm.		Privileged	
		Login	Change	Privileged	
			Delete	Privileged	
			Enter	Privileged	
		Network Element		Privileged	
		Password		Report Only	
		Security	Network Element	Privileged	
			Port	Privileged	

Table 11-6. Release 3 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
FAULT	Retrieve	Alarm and Status		Report Only	2
		History		Report Only	15
		Network Alarms		Report Only	20
		State		Report Only	46
	Switch	Line		General	
		Low Speed		General	
		Synchronization	Hardware	General	
			Mode	General	
			Reference	General	
	Test	Alarm		General	
		Auto Turnup	DSX	General	
			Local	General	
		LED		General	
		Telemetry	Parallel	General	
			Serial	General	
	Operate	Alarm Cutoff		Report Only	
		Loopback	T3	General	
	Release	Loopback	T3	General	
	Allow	Alarm		General	
	Inhibit	Alarm		General	
	Reset			General	
CONFIGURATION	Retrieve	Attribute	Alarm	Report Only	4
			Control	Report Only	5
			Environment	Report Only	6
		Crossconnection	STS3	Report Only	10
		Date		Report Only	11
		Equipment		Report Only	13
		Low Speed		Report Only	17
		Map	Ring	Report Only	19
		OC48		Report Only	23
		Protection	Line	Report Only	40
		Synchronization		Report Only	47
		T3		Report Only	48
	Set	Attribute	Alarm	General	
			Control	General	
			Environment	General	
		Date		General	
		LowSpeed		General	

Table 11-6. Release 3 Inputs (Commands) Menu for FT-2000 Add/Drop-Rings Terminal (Contd)

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
CONFIGURATION (Cont'd)	Set (Cont'd)	OC48		General	
		Protection	Line	General	
		State	T3	General	
		Synchronization		General	
		T3		General	
	Copy	OC48		General	
		T3		General	
	Enter	Crossconnection	STS3	General	
	Delete	Crossconnection	STS3	General	
	Update			General	
	Initialize	System		Privileged	
PERFORMANCE	Retrieve	PM	Line	Report Only	26
			Optics	Report Only	28
			Section	Report Only	29
			STS1	Report Only	31
			T3	Report Only	32
	Initialize	PM		General	
SECURITY	Retrieve	Far End Comm.		Report Only	14
		Network Element		Report Only	21
		Login		Report Only	16
		Security	Network Element	Report Only	42
			Port	Report Only	43
	Set	Far End Comm.		Privileged	
		Login	Change	Privileged	
			Delete	Privileged	
			Enter	Privileged	
		Network Element		Privileged	
		Password		Report Only	
		Security	Network Element	Privileged	
			Port	Privileged	

Table 11-7. Releases 2, 4, and 6 Inputs (Commands) Menu for FT-2000 Repeater Bay/Shelf

Management Category	Verb	First Modifier	Second Modifier	Security Level	Report Example
CONFIGURATION	Retrieve	Attribute	Environment	Report Only	6
		Date		Report Only	11
		Equipment		Report Only	13
		OC48		Report Only	23
	Set	Attribute	Environment	General	
		Date		General	
	Update			General	
	Initialize	System		Privileged	
FAULT	Retrieve	Alarm (and Status)		Report Only	2
		History		Report Only	15
	Test	Auto Turnup		General	
		LED		Report Only	
	Reset			General	
SECURITY	Retrieve	Network Element		Report Only	21
		Login		Report Only	16
		Security	Network Element	Report Only	42
			Port	Report Only	43
	Set	Login	Change	Privileged	
			Delete	Privileged	
			Enter	Privileged	
		Network Element		Privileged	
		Password		Report Only	
		Security	Network Element	Privileged	
			Port	Privileged	
PERFORMANCE*					

* These commands are not accessible at the FT-2000 repeater bay/shelf.

Table 11-8. Input (Command) Definitions

Selected Input	Purpose
FAULT- <i>Allow-Alarm</i>	Allows the user to enable the reporting of alarms at an alarm-reporting interface.
CONFIGURATION- <i>Copy-EC1</i>	Copies the software provisionable attributes of an EC1 port (source) to one or more EC1 ports (destinations).
CONFIGURATION- <i>Copy-OC3</i>	Copies the software provisionable attributes of an OC3 slot (source) to one or more OC3 slots (destinations).
CONFIGURATION- <i>Copy-OC12</i>	Copies the software provisionable attributes of an OC12 slot (source) to one or more OC12 slots (destinations).
CONFIGURATION- <i>Copy-OC48</i>	Copies the software provisionable attributes of an OC-48 line to one or more OC-48 lines.
CONFIGURATION- <i>Copy- Path-Trace</i>	Copies the transmit and receive path trace fields from an STS-1 signal (source) to one or more STS-1 signals (destinations).
CONFIGURATION- <i>Copy- Program</i>	Copies the software contained in one SYSMEM (source NE) to another SYSMEM (destination NE) via the DCC.
CONFIGURATION- <i>Copy-T3</i>	Copies the software provisionable attributes of a DS3 port to one or more DS3 ports.
CONFIGURATION- <i>Delete-Assignment-DCC</i>	Removes a DCC channel assignment from a particular LS INTFC slot or pair of slots, thereby disabling DCC communication on the supporting OC-3 as well as disabling DCC access to a particular remote network element.
CONFIGURATION- <i>Delete-Crossconnections-STs1</i>	Removes STS1 level cross-connections in both directions in the FT-2000 add/drop-rings terminal.
CONFIGURATION- <i>Delete-Crossconnections-STs3</i>	Removes STS3 level cross-connections in both directions in the FT-2000 add/drop-rings terminal.
CONFIGURATION- <i>Delete-Squelch Map-STs1</i>	Removes an STS1 squelch map entry.
CONFIGURATION- <i>Enter-Assignment-DCC</i>	Assigns a DCC channel to a particular LS INTFC slot or pair of slots. It also sets the orientation of the addressed channel to "User" or "Network."
CONFIGURATION- <i>Enter-Crossconnections-STs1</i>	Makes STS1 level cross-connections as add/drop or through cross-connection. Also, used to make a DRI cross-connection between a low speed port and an STS-1 line tributary.

Table 11-8. Input (Command) Definitions (Contd)

Selected Input	Purpose
CONFIGURATION- <i>Enter</i> -Crossconnections-STS3	Makes STS3 level cross-connections in both directions in the FT-2000 add/drop-rings terminal.
CONFIGURATION- <i>Enter</i> -Protection-STS1	This command controls values of the protection access state variables associated with the STS-1 protection tributaries of the local NE.
CONFIGURATION- <i>Enter</i> -Protection-STS3	This command controls values of the protection access state variables associated with the STS-3 protection tributaries of the local NE.
CONFIGURATION- <i>Enter</i> - Roll-STS1	This command rolls the source of a 1-way STS1 cross-connection to a new source and/or changes a 1-way cross-connection type to/from a 1-way-dri cross-connection type. This is accomplished by deleting the existing cross-connection between the source and destination and immediately establishing a new 1-way cross-connection between the new source and the (existing) destination.
CONFIGURATION- <i>Enter</i> - Roll-STS3	This command rolls the source of a 1-way STS3 cross-connection to a new source and/or changes a 1-way cross-connection type to/from a 1-way-dri cross-connection type.
CONFIGURATION- <i>Enter</i> -Squelch Map-STS1	This command provisions the Source Node TID and the Destination Node TID of an STS1 squelch map. For Release 7, this command assigns Source and Destination node TIDs to STS-1 cross-connections and sets STS-1 cross-connections' AUDIT parameter. This allows nodes which do not support STS-1 Time Slot Assignment to support circuits which terminate and originate at other nodes which do support STS-1 Time Slot Assignment. The AUDIT parameter allows the circuit connectivity audit to be disabled on a per STS-1 cross-connection basis. This is needed to suppress circuit audit alarms which would otherwise be generated at non-STS-1-Time- Slot-Assignment capable nodes in a ring where other nodes do support STS-1 Time Slot Assignment. For Releases 5 and 6, this command overrides the STS3 provisioning and must be done at every node through which the identified STS-1s pass.
FAULT- <i>Inhibit</i> -Alarm	Inhibits the reporting of alarms.

Table 11-8. Input (Command) Definitions (Contd)

Selected Input	Purpose
PERFORMANCE- <i>Initialize-PM</i>	Initializes the performance-monitoring storage registers (to zero).
CONFIGURATION- <i>Initialize- System</i>	Causes current values in the SYSMEM to be replaced by the original values in the SYSMEM and then restarts the software.
FAULT- <i>Operate-Alarm Cutoff</i>	Silences the office audible alarms.
FAULT- <i>Operate-Loopback-EC1</i>	Executes a loopback on an EC1 port in the direction towards the OC-48 line.
FAULT- <i>Operate-Loopback-OC3</i>	Executes a loopback on an OC3 slot in the multiplex direction (towards the OC-48 line) or demultiplex direction (towards the low speed cross-connect panel).
FAULT- <i>Operate-Loopback-OC12</i>	Executes a loopback on an OC12 slot in the multiplex direction (towards the OC-48 line) or demultiplex direction (towards the low speed cross-connect panel).
FAULT- <i>Operate-Loopback-T3</i>	Executes a loopback on a DS3 port in the direction towards the OC-48 line.
FAULT- <i>Release-Loopback-EC1</i>	Releases a loopback on an EC1 port.
FAULT- <i>Release-Loopback-OC3</i>	Releases a loopback on an OC3 slot.
FAULT- <i>Release-Loopback-OC12</i>	Releases a loopback on an OC12 slot.
FAULT- <i>Release-Loopback-T3</i>	Releases a loopback on a DS3 port.
FAULT- <i>Reset</i>	Restarts the FT-2000 network element software as follows: (1) Current memory values are copied from the SYSCTL to the SYSMEM, (2) executable code is copied from the SYSMEM to the SYSCTL, (3) the executable code in the SYSCTL is restarted, and (4) the current values in the SYSMEM are reformatted as necessary to work properly with the executable code running in the SYSCTL.
SECURITY- <i>Retrieve-Active User</i>	Allows a user to retrieve the current CIT and TL1 port provisioning and who is currently logged into each port.
FAULT- <i>Retrieve-Alarm (and Status)</i>	Generates an active alarm and status report on the FT-2000 System.
CONFIGURATION- <i>Retrieve-Assignment-DCC</i>	Retrieves the DCC channel assignment to the LS INTFC slot or pair of slots.
CONFIGURATION- <i>Retrieve-Attribute-Alarm</i>	Retrieves the alarm attributes for the FT-2000 network element.

Table 11-8. Input (Command) Definitions (Contd)

Selected Input	Purpose
CONFIGURATION- <i>Retrieve-Attribute-Control</i>	Retrieves the descriptive name associated with the user-settable miscellaneous discrete control output points.
CONFIGURATION- <i>Retrieve-Attribute-Environment</i>	Retrieves the alarm and descriptive name associated with the user-settable miscellaneous discrete environmental input points.
CONFIGURATION- <i>Retrieve-Circuit-STs1</i>	Retrieves all cross-connections that make up that particular circuit path.
CONFIGURATION- <i>Retrieve-Circuit-STs3</i>	Retrieves all cross-connections that make up that particular circuit path.
CONFIGURATION- <i>Retrieve-Crossconnection-STs1</i>	Retrieves provisioned information on the current cross-connections in the FT-2000 add/drop-rings terminal.
CONFIGURATION- <i>Retrieve-Crossconnection-STs3</i>	Retrieves provisioned information on the current cross-connections in the FT-2000 add/drop-rings terminal.
CONFIGURATION- <i>Retrieve-Date</i>	Retrieves provisioned information on current date and time, and start and stop dates for daylight-saving time.
CONFIGURATION- <i>Retrieve-EC1</i>	Retrieves provisioned information and attributes of an EC1 port. Also, information on alarm severity that is implied by failure of the EC1 port, signal degrade threshold, and the memory administrative state.
CONFIGURATION- <i>Retrieve-Equipment</i>	Retrieves the equipage and version information for the FT-2000 network element.
SECURITY- <i>Retrieve-Far End Communications</i>	Retrieves status information on the far-end communications access that uses the data communications channel.
FAULT- <i>Retrieve-History</i>	Retrieves the maintenance history information on about 500 events for the FT-2000 network element.
SECURITY- <i>Retrieve-Login</i>	Retrieves at the privilege user level a complete list containing all user logins and their assigned security level but not their password.
CONFIGURATION- <i>Retrieve-Low Speed</i>	Retrieves characteristics of a low speed slot.
CONFIGURATION- <i>Retrieve-Neighbor</i>	Retrieves a list of all Network Elements that are optically connected to this NE, including information on connectivity, communication status, and the product type of each of those NEs.

Table 11-8. Input (Command) Definitions (Contd)

Selected Input	Purpose
CONFIGURATION- <i>Retrieve-Map-Network</i>	Retrieves a current list of all network elements in the maintenance subnetwork.
CONFIGURATION- <i>Retrieve-Map-Ring</i>	Retrieves the current values in the addressing parameters of the nodes in a ring network.
FAULT- <i>Retrieve-Network Alarms</i>	Generates a report that summarizes the alarm and status conditions currently being reported for a specific FT-2000 terminal or FT-2000 repeater bay/shelf.
SECURITY- <i>Retrieve-Network Element</i>	Retrieves characteristics of the FT-2000 network element.
CONFIGURATION- <i>Retrieve-OC3</i>	Retrieves provisioning information and the attributes of an OC3 slot.
CONFIGURATION- <i>Retrieve-OC12</i>	Retrieves provisioning information and the attributes of an OC12 slot.
CONFIGURATION- <i>Retrieve-OC48</i>	Retrieves provisioning information and the attributes of an OC-48 line.
CONFIGURATION- <i>Retrieve-Path-Trace</i>	Retrieves the provisioned transmit and receive path traces.
PERFORMANCE- <i>Retrieve-PM-EC1</i>	Generates a status report containing EC1 section and line performance-monitoring data.
PERFORMANCE- <i>Retrieve-PM-Line</i>	Generates a status report containing OC-48 line performance-monitoring data.
PERFORMANCE- <i>Retrieve-PM-Optics</i>	Generates a status report containing OC-48 optics performance-monitoring data.
PERFORMANCE- <i>Retrieve-PM-Section</i>	Generates a status report containing OC-48 section performance-monitoring data.
PERFORMANCE- <i>Retrieve-PM-Start Time</i>	Generates a status report stating the start time for measuring the day bins.
PERFORMANCE- <i>Retrieve-PM-STS1</i>	Generates a status report containing STS-1 path performance-monitoring data.
PERFORMANCE- <i>Retrieve-PM-T3</i>	Generates a status report containing DS3 performance-monitoring data.
PERFORMANCE- <i>Retrieve-PM-TCA</i>	Reports a status summary of TCAs reported over the past 2 days.
PERFORMANCE- <i>Retrieve-PM Mode-T3</i>	Reports via the P-bit processing or the F and M bit adjusting processing the determined and accumulated DS3 path performance monitored data.

Table 11-8. Input (Command) Definitions (Contd)

Selected Input	Purpose
PERFORMANCE- <i>Retrieve</i> -PM Threshold-EC1	Reports the performance monitoring TCA threshold values for the EC1 performance monitoring data.
PERFORMANCE- <i>Retrieve</i> -PM Threshold-Line	Reports the performance monitoring TCA threshold values for the line performance monitoring data.
PERFORMANCE- <i>Retrieve</i> -PM Threshold-Optics	Reports the performance monitoring TCA threshold values for the optical performance monitoring data.
PERFORMANCE- <i>Retrieve</i> -PM Threshold-Section	Reports the performance monitoring TCA threshold values for the section performance monitoring data.
PERFORMANCE- <i>Retrieve</i> -PM Threshold-STs1	Reports the performance monitoring TCA threshold values for the STs1 performance monitoring data.
PERFORMANCE- <i>Retrieve</i> -PM Threshold-T3	Reports the line and path performance monitoring TCA threshold values for the T3 performance monitoring data.
CONFIGURATION- <i>Retrieve</i> -Protection-Line	Retrieves the line protection switching characteristics for the FT-2000 end terminal.
CONFIGURATION- <i>Retrieve</i> -Protection-Low Speed	Retrieves the low-speed protection switching characteristics of the network element.
CONFIGURATION- <i>Retrieve</i> -Protection-STs1	Retrieves the current values of the local protection access state variables.
SECURITY- <i>Retrieve</i> -Security-Network Element	Retrieves only at the privileged user level the security provisioning of login aging, password aging, and allowed logins of the FT-2000 network elements.
SECURITY- <i>Retrieve</i> -Security-Port	Retrieves only at the privileged user level the security provisioning of login aging, password aging, and allowed logins of the timeout information on the craft access ports.
CONFIGURATION- <i>Retrieve</i> -Squelch Map-STs1	Retrieves the STs-1 level squelch map of an FT-2000 Add/Drop-Rings Terminal.
CONFIGURATION- <i>Retrieve</i> -Squelch Map-STs3	Retrieves the STs-3 level squelch map of an FT-2000 Add/Drop-Rings Terminal.
FAULT- <i>Retrieve</i> -State	Retrieves the slot, port, and protection switching state information for the FT-2000 terminal.

Table 11-8. Input (Command) Definitions (Contd)

Selected Input	Purpose
CONFIGURATION- <i>Retrieve</i> -Synchroniz ation	Retrieves provisioning and operational information and the attributes of the synchronization characteristics of the FT-2000 terminal.
CONFIGURATION- <i>Retrieve</i> -T3	Retrieves provisioning information on the attributes if a DS3 port.
CONFIGURATION- <i>Retrieve</i> -Timeslot-STS1	Retrieves Tributary Map Report, organized by node, for all nodes in a ring. The report shows all cross-connections on a single user-specified time slot throughout the entire ring, indicating parts of the ring available for new circuits.
CONFIGURATION- <i>Retrieve</i> -Timeslot-S TS3	Retrieves configuration information on an FT-2000 Add/Drop-Rings Terminal.
CONFIGURATION- <i>Set</i> -Attribute-Alarm	Provisions the alarm attributes of the FT-2000 terminal.
CONFIGURATION- <i>Set</i> -Attribute-Control	Provisions the character strings associated with the miscellaneous discrete control output points.
CONFIGURATION- <i>Set</i> -Attribute- Environment	Provisions the character strings associated with the miscellaneous discrete environmental input points.
CONFIGURATION- <i>Set</i> -Date	Provisions the correct time, calendar date, and time zone.
CONFIGURATION- <i>Set</i> -EC1	Provisions the characteristics of an EC1 port.
SECURITY- <i>Set</i> -Far End Communications	Provisions the control of the far-end communications access to the FT-2000 terminal over the DCC.
SECURITY- <i>Set</i> -Login-Change	Only a privileged user can make changes to the provisioned login parameters (which are new login, password, verify password, user privilege, and status).
SECURITY- <i>Set</i> -Login-Delete	Only a privileged user can delete the provisioned nonprivileged user logins.
SECURITY- <i>Set</i> -Login-Enter	Only a privileged user can provision the login parameters (which are new login, password, verify password, and user privilege).
CONFIGURATION- <i>Set</i> -Low Speed	Provisions the protection switch priority and the equipage dependent reporting capability of a low-speed slot.
SECURITY- <i>Set</i> -Network Element	Provisions the FT-2000 network element identification (TID).

Table 11-8. Input (Command) Definitions (Contd)

Selected Input	Purpose
CONFIGURATION-Set-OC3	Provisions the "0x1"/"1+1" protection mode, severity of the alarm assigned to the OC3 failures, the signal degrade threshold, and the AU-4 S bit.
CONFIGURATION-Set-OC12	Provisions the "0x1"/"1+1" protection mode, severity of the alarm assigned to the OC12 failures, the signal degrade threshold, and the AU-4 S bit.
CONFIGURATION-Set-OC48	Provisions the signal degrade threshold and the protection switch priority parameters of an OC-48 line.
CONFIGURATION-Set-Path-Trace	Provisions the transmit and receive path traces.
PERFORMANCE-Set-PM Mode T3	Provisions one of two different methods (either (a) P-bit processing or (b) F and M-bit adjusted processing)) for accumulating DS3 path performance monitoring data.
PERFORMANCE-Set-PM Start Time	Provisions the start-time for the measurement of all twenty-four hour performance monitoring parameters.
PERFORMANCE-Set-PM Threshold-EC1	Provisions the performance monitoring TCA threshold values for the EC1 data.
PERFORMANCE-Set-PM Threshold-Line	Provisions the performance monitoring TCA threshold values for the line performance monitoring data.
PERFORMANCE-Set-PM Threshold-Optics	Provisions by enabling or disabling the automatic reporting of TCAs for the optical performance monitoring data.
PERFORMANCE-Set-PM Threshold-Section	Provisions the performance monitoring TCA threshold values for the section performance monitoring data.
PERFORMANCE-Set-PM Threshold-STS1	Provisions the performance monitoring TCA threshold values for the STS1 path performance monitoring data.
PERFORMANCE-Set-PMThreshold-T3	Provisions the performance monitoring TCA threshold values for the DS3 performance monitoring data.
CONFIGURATION-Set-Protection-Line	Provisions these parameters (configuration, directional, wait to restore, and restoration protection access) for an OC-48 line. Currently, only the restoration protection access parameter can be set which turns off the ACTY and ABN LEDs for a preemptible protection access line.

Table 11-8. Input (Command) Definitions (Contd)

Selected Input	Purpose
CONFIGURATION-Set-Protection-Low Speed	Provisions the low-speed protection characteristics for the network element.
SECURITY-Set-Security-Network Element	Provisions (only by the privilege user) the network element parameters.
SECURITY-Set-Security-Port	Provisions (only by the privilege user) the port access parameters.
CONFIGURATION-Set-State-EC1	Provisions the memory administrative state of a low-speed EC1 port.
CONFIGURATION-Set-State-OC3	Provisions the memory administrative state of low-speed OC3 port.
CONFIGURATION-Set-State-OC12	Provisions the memory administrative state of
CONFIGURATION-Set-State-T3	Provisions the memory administrative state of a low-speed DS3 port.
CONFIGURATION-Set-Synchronization	Provisions the synchronization characteristics of the FT-2000 network.
CONFIGURATION-Set-T3	Provisions the characteristics of a DS3 port.
FAULT-Switch-Line	Makes or breaks the protection switching of an OC-48 line.
FAULT-Switch-Low Speed	Makes or breaks the protection switching of the low-speed slots.
FAULT-Switch-OC3	Controls the "1+1" protection switching operation (OC3 equipped slot pair) according to priority (reset, lockout of protection, inhibit, forced switch, and manual switch).
FAULT-Switch-OC12 operation	Controls the "1+1" protection switching (OC12 equipped slot pair) according to priority (reset, lockout of protection, inhibit, forced switch, and manual switch).
FAULT-Switch-Path-STS1	Controls the DRI protection switching operation through external switch requests according to reset (first) and manual switch (second) priorities.
FAULT-Switch-Path-STS3	Controls the DRI protection switching operation through external switch requests according to reset (first) and manual switch (second) priorities.
FAULT-Switch-Synchronization-Hardware	Controls the protection switching operation of the synchronization hardware.
FAULT-Switch-Synchronization-Mode	Controls the protection switching operation of the FT-2000 network element timing mode.
FAULT-Switch-Synchronization-Reference	Controls the protection switching operation of the synchronization timing reference.

Table 11-8. Input (Command) Definitions (Contd)

Selected Input	Purpose
FAULT- <i>Test</i> -Alarm	Tests the audible and visible office alarms and their corresponding user panel LEDs.
FAULT- <i>Test</i> -Auto Turnup-DSX	Initiates an automatic turnup test of the low-speed cabling between the FT-2000 network element and the DSX (STSX).
FAULT- <i>Test</i> -Auto Turnup-Local	Initiates an automatic turnup test of the internal transmission health of the FT-2000 network element. Testing must be done when FT-2000 System is out of service.
FAULT- <i>Test</i> -LED	Tests the alarm and status LEDs on the user panel and circuit packs.
FAULT- <i>Test</i> -Telemetry-Parallel	Tests the parallel telemetry and miscellaneous discrete points.
FAULT- <i>Test</i> -Telemetry-Serial	Tests the serial telemetry and miscellaneous discrete points.
CONFIGURATION- <i>Update</i>	Updates the FT-2000 System data base for the FT-2000 network element to reflect the existing hardware and incoming signals. For example, an empty operational slot would change to the AUTO state and the in-service ports receiving bad signals would change to the AUTO state.

Table 11-9. List of Inputs (Commands) That Generate Reports

[Alphabetized by Qualifier(s)]		
Input (Command)	Report (Note)	Example
SECURITY-Retrieve-Active-User	Active User Report	1
FAULT-Retrieve-Alarm	Active Alarms and Status Report	2
CONFIGURATION-Retrieve-Assignment-DCC	DCC Assignment Report	3
CONFIGURATION-Retrieve-Attribute-Alarm	System Alarm Attributes Report	4
CONFIGURATION-Retrieve-Attribute-Control	Control Point Provisioning Report	5
CONFIGURATION-Retrieve-Attribute-Environment	Environmental Alarm Provisioning Report	6
CONFIGURATION-Retrieve-Circuit-STS1	STS-1 Circuit Map Report	7
CONFIGURATION-Retrieve-Circuit-STS3	STS-3 Circuit Map Report	8
CONFIGURATION-Retrieve-Crossconnection-STS1	STS-1 Cross-Connection Map Report	9
CONFIGURATION-Retrieve-Crossconnection-STS3	STS-3 Cross-Connection Map Report	10
CONFIGURATION-Retrieve-Crossconnection-ALL	Cross-Connection Map Report	11
CONFIGURATION-Retrieve-Date	Date and Time Report	12
CONFIGURATION-Retrieve-EC1	EC1 Port Provisioning Report	13
CONFIGURATION-Retrieve-Equipment	Equipage and Version Report	14
SECURITY-Retrieve-Far End Communications	Far End Communication Configuration Report	15
FAULT-Retrieve-History	Maintenance History Report	16
SECURITY-Retrieve-Login	Login Provisioning Report	17
CONFIGURATION-Retrieve-Low Speed	Low Speed Slot Report	18
CONFIGURATION-Retrieve-Map-Neighbor	Neighbor Map Report	
CONFIGURATION-Retrieve-Map-Network	Network Map Report	19
CONFIGURATION-Retrieve-Map-Ring	Ring Map Report	20
FAULT-Retrieve-Network Alarms	Network Alarms Report	21
SECURITY-Retrieve-Network Element	FT-2000 System Provisioning Report	22
CONFIGURATION-Retrieve-OC3	OC3 Provisioning Report	23
CONFIGURATION-Retrieve-OC12	OC12 Provisioning Report	
CONFIGURATION-Retrieve-OC48	OC-48 Line Provisioning Report	24
CONFIGURATION-Retrieve-Path-Trace	Path Trace Report	25
PERFORMANCE-Retrieve-PM-EC1	EC1 Performance Monitoring Status Report	26
PERFORMANCE-Retrieve-PM-Line	Line Performance Monitoring Status Report	27
PERFORMANCE-Retrieve-PM Mode-T3	DS-3 Performance Monitoring Mode Report	28
PERFORMANCE-Retrieve-PM-Optics	Optics Performance Monitoring Status Report	29
PERFORMANCE-Retrieve-PM-Section	STS-48 Section Performance Monitoring Status Report	30
PERFORMANCE-Retrieve-PM-Start Time	Performance Monitoring Day Bin Start Time Report	31
PERFORMANCE-Retrieve-PM-STS1	STS-1 Path Performance Monitoring Status Report	32
PERFORMANCE-Retrieve-PM-T3	DS3 Performance Monitoring Status Report	33
PERFORMANCE-Retrieve-PM-TCA	Performance Monitoring Threshold Crossing Alarm Status Report	34

Table 11-9. List of Inputs (Commands) That Generate Reports (Contd)

[Alphabetized by Qualifier(s)]		
Input (Command)	Report (Note)	Example
PERFORMANCE-Retrieve- <i>PM Threshold-EC1</i>	EC1 Performance Monitoring Threshold Value Report	35
PERFORMANCE-Retrieve- <i>PM Threshold-Line</i>	OC-48 Line Performance Monitoring Threshold Value Report	36
PERFORMANCE-Retrieve- <i>PM Threshold-Optics</i>	OC-48 Optics Performance Monitoring Threshold Value Report	37
PERFORMANCE-Retrieve- <i>PM Threshold-Section</i>	STS-48 Section Performance Monitoring Threshold Value Report	38
PERFORMANCE-Retrieve- <i>PM Threshold-STS1</i>	STS-1 Path Performance Monitoring Threshold Value Report	39
PERFORMANCE-Retrieve- <i>PM Threshold-T3</i>	DS-3 Performance Monitoring Threshold Value Report	40
CONFIGURATION-Retrieve- <i>Protection-Line</i>	Network Element Protection Switching Provisioning Report	41
CONFIGURATION-Retrieve- <i>Protection-Low Speed</i>	Low Speed Protection Switching Provisioning Report	42
SECURITY-Retrieve- <i>Security-Network Element</i>	Access Security Global Configuration Report	43
SECURITY-Retrieve- <i>Security-Port</i>	Access Port Security Configuration Report	44
CONFIGURATION-Retrieve- <i>Squelch Map-STS1</i>	STS-1 Squelch Map Report	45
CONFIGURATION-Retrieve- <i>Squelch Map-STS3</i>	STS-3 Squelch Map Report	46
FAULT-Retrieve- <i>State</i>	State Report	47
CONFIGURATION-Retrieve- <i>Synchronization</i>	Synchronization Report	48
CONFIGURATION-Retrieve- <i>T3</i>	T3 Port Provisioning Report	49
CONFIGURATION-Retrieve- <i>Timeslot-STS1</i>	STS-1 Tributary Map Circuit Report	50
CONFIGURATION-Retrieve- <i>Timeslot-STS3</i>	STS-3 Tributary Map Report	51
Note: These reports appear on the following pages.		

Outputs

Every FT-2000 CIT-PC input that is executed will result in an output on the CIT screen. The output response is in a two-line standard format and consists of the FT-2000 network element identification, date and time (line 1), and the command name (executed input is echoed back), followed by either COMPLD or DENY (line 2).

COMPLD indicates that the action requested by the input was completed. The output may include a report, if applicable.

DENY indicates that the input was denied. The output includes a four-letter Telcordia error code and an explanation of the denial.

Tables 11-2 through 11-8 have a column that shows the number of the report example. Each example is introduced by its heading that shows the FT-2000 CIT-PC input that produces the report. An example is shown for each report, along with an explanation of the column headings and parameters. Table 11-9 lists the FT-2000 CIT-PC inputs (commands) that produce reports.

Some reports are too long to fit on a single screen and the question Continue report page-by-page?(y/n)Yes is displayed on the message line (help). Pressing selects the default (Yes) and the next page of the report is then displayed. If No was selected, then the question Continue report without paging?(y/n)Yes is displayed and must be answered. Scrolling (↑, ↓ keys) allows the user to bring information back to the screen after it has scrolled off, especially the long reports.

Examples of Reports [Generated by Various Inputs (Commands)]

Example 1 - SECURITY-Retrieve-Active-User

(Releases 5 and Later)

```

Delta-1 97-06-15 23:15:15 EST
M Retrieve-Active-User:All: COMPLD
/* Active User Report
=====
Access      Port      Type/      TL1 Calling      Active
Port      Channel      OS Type      Address/GNE TID      User
=====
dce                      cit                      -
dte                      tl1                      -
-----
dcc      vc-0      cit                      luc02
dcc      vc-1      memry-admin          luc01
dcc      vc-2      cmd-resp             luc03
      :
dcc      vc-8      -                      -
-----
x25      pvc-1      maintenance          NMA01
x25      pvc-2      other                  TMAS
-----
x25      svc-1      other      1234          SNC2000
      :
x25      svc-6      -                      -
*/
;

      Information below is for Release 5.0

=====
Access      Port      TL1      Active
Port      Channel      Type      Calling Address      User
=====

```

The preceding report shows the current CIT and TL1 port provisioning and who is currently logged in on each port.

The fields of the report are described below:

Access Port	-	Shows a specific operations interface (physical port) for which the status is being displayed.
Channel	-	Shows a virtual channel (vc) associated with the Access Port. For dte and dce ports, a blank (" ") is always reported. For the dcc port, virtual channel 1 through 8 is reported. For the x.25 port, the permanent virtual channel (pvc) 1 or 2 and the switched virtual channels (svc) from 1 through 6.
Port Type/ OS Type	-	Shows how a given port is operating; for example, the dce and dte ports are either "cit" or "tl1." All virtual channels of the x.25 port are "maintenance," "memry-admin," "cmd-resp," or "other." The dcc port, virtual channel 0, is the "cit" and virtual channels 1 through 8 are either "maintenance," "memry-admin," "cmd-resp," or "other." A dash (—) means that no user is currently logged in to the port.
TL1 Calling Address/ GNE TID	-	Shows the calling address the user entered as part of the x.25 dial string and is reported for x.25 virtual channels only or for DCC virtual channels (vc 1 through 8) for the gateway network element (GNE) at the time of the report. The GNE TID on the report may not be current due to some provisioning after logging into the GNE. (The GNE routes TL1 alarm messages over the X.25 port to the OSs.) For all other ports (and associated virtual channels), this parameter is reported as a blank (" ").
Active User	-	Shows the current login of the user. A dash (—) indicates the user is not currently logged in on the port/(virtual channel).

Example 2 - FAULT-Retrieve-Alarm**Releases 3 and Later**

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Alarm:All: COMPLD
/* Active Alarms and Status Report
=====
Alarm      Source      Date      Time      Srv      Description
Level      Address     Detected
=====
NE ACTY    sts1-1w-6-3 01-19     23:15:42   nsa      path yellow
NE ACTY    sts1-1w-6-2 01-19     23:15:42   nsa      path yellow
NE ACTY    sts1-1w-6-1 01-19     23:15:42   nsa      path yellow

      Information below is a Release 2 or earlier example
-----
ABN        ls-1-1a-1    05-19     23:15:42   nsa      LS loopback
-----
NE ACTY    line-1       05-19     23:15:42   nsa      line auto switch
NE ACTY    line-p       05-19     23:15:40   nsa      path AIS
*/
;

```

The preceding report shows no active alarm conditions, but three status conditions are shown (near-end activities or an abnormal and two near-end activities) in the FT-2000 network element. The greatest severity condition appears first and the least severity condition appears last. Equal severity conditions are listed chronologically, starting with the most recent at the top of the group listing.

The fields of the report are described below:

Alarm Level - Shows the alarm and status level with the greatest severity to the least severity. Highest alarm level also corresponds to associated user panel LED. If no LEDs are lit at the user panel (no active alarms), then an output status message is shown.

Alarm level/status condition will be one or more of the following:

CR (critical alarm)

MJ (major alarm)

MN (minor alarm)

ABN (abnormal status)

NE ACTY (near-end activity status)

The FE ACTY LED on the user panel and the CIT only refers to activity at the far end and will not be reflected in this CIT report.

If a protected (non-service-affecting) failure becomes unprotected (service-affecting) because of a failure to the protecting circuit pack or line, then both failures are adjusted accordingly. Similarly, if an unprotected failure becomes protected, the alarm level of that failure is adjusted accordingly.

Source Address	- Shows the address for the condition within the FT-2000 network element. For Release 2, example 2 shows that an abnormal condition occurred in the low-speed shelf, line 1, slot 1a, at port 1 (Source Address of ls-1-1a-1). A dash (-) indicates no particular address for a condition (for example, a protected power failure). For Release 3, an abnormal condition is shown in the same but without the line indicator (1 or P). Some conditions give an address, for example "sts1-1w-6-3", meaning an incoming sts1 (DS3) signal on line 1w at tributary 6 on port 3 is receiving "path yellow."
Date Time Detected	- Shows the month, day, and time in hours, minutes, and seconds that the alarm condition was first detected. If an alarm level changes because of a second failure (higher alarm level), the time will change to correspond to the second failure.
Srv	- Shows when service is affected (SA), or not affected (nsa). SA could be a DS3 INTFC failure, and an nsa could be an APS channel failure.
Description	- Shows a brief description of the condition. Explanations of these conditions are provided in Table 9-2 in Volume I, Section 9, "Maintenance Description."

Releases 2 and Later (Use the FAULT category.)

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Alarm:All: COMPLD
/* Active Alarms and Status Report
=====
There are no active alarms or status conditions of the requested level.
*/
;

```

The report above shows that no active alarms or status conditions of the requested level are reported for the addressed FT-2000 network element.

Example 3 - CONFIGURATION-Retrieve-Assignment-DCC**Releases 6 and Later**

```

/* DCC Assignment Report
=====
Slot          DCC Channel      Orientation
=====
ls-1a         1               Network
ls-1b         1               Network
ls-2a         -               -
ls-2b         -               -
ls-3a         -               -
ls-3b         5               Network
ls-4a         -               -
ls-4b         -               -
ls-5a         3               User
ls-5b         2               Network
ls-6a         -               -
ls-6b         -               -
ls-7a         -               -
ls-7b         -               -
ls-8a         -               -
ls-8b         -               -
*/

```

This report shows the DCC channel assignment to an LS INTFC slot or pair of slots and the DCC channel orientation (network or user). In the report, slots 1A and 1B of Low Speed Shelf 1 are operating in the 1+1 mode and are assigned to DCC Channel 1. Slots 3B, 5A, and 5B are operating in the 0x1 mode and are individually assigned to DCC Channels 5, 3, and 2, respectively. The remaining 11 slots have no DCC Channel assignments and are indicated by dashes (—).

The fields of the report are described below:

- Slot - Shows a single service LS INTFC slot designation.
- DCC Channel - Shows the DCC channel assigned to the specified slot. Eight DCC channels are available.
- Orientation - Shows either "User" or "Network" parameter. Network is the default setting. User setting allows the customer to perform OAM&P functions from a single access point.

Example 4 - CONFIGURATION-Retrieve-Attribute-Alarm

Releases 2 and Later

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Attribute-Alarm: COMPLD
/* System Alarm Attributes Report
=====
Parameter                               Value
=====
Incoming Signal Alarm Delay             20
Alarm Clear Delay                       20
Protection Availability                 NO
Tributary Reporting                    NO
*/
;

```

These reports show the provisioned delay values of the alarm attributes at the FT-2000 OC-48 Lightwave System level.

Incoming Signal Alarm Delay	-	Shows how many seconds the FT-2000 end terminal will wait after declaration of an incoming signal defect before activating all indicators.
Alarm Clear Delay	-	Shows how many seconds the FT-2000 end terminal will wait after the alarm retires before reporting it to the external maintenance interfaces (for example, user panel, operations systems).
Protection Access Availability	-	The alarm level associated with the "protection access unavailable" condition.
Tributary Reporting	-	The alarm level associated with the "tributary failed" condition.

Example 5 - CONFIGURATION-Retrieve-Attribute-Control

```
Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Attribute-Control:All: COMPLD
/* Control Point Provisioning Report
=====
Address      Description
=====
cont-1      start generator
cont-2      stop pump
cont-3      control 3
cont-4      control 4
*/
;
```

The above report shows the descriptive name associated with the user-settable miscellaneous discrete control output points. These output points are wired to control/identify the output control points at a remote site.

The fields of the report are described below:

- Address - Shows number selected (up to four) output points for the report.
- Description - Shows the name (26-character string) associated with the output points, such as "start pump," "start generator," etc.

Example 6 - CONFIGURATION-Retrieve-Attribute-Environment

```
Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Attribute-Environment: COMPLD
/* Environmental Alarm Provisioning Report
=====
Address      Alarm      Description
Level
=====
env-1        CR        fire
env-2        NA        open door
env-3        MN        environment #3
. . .
. . .
env-16       MN        environment #16
*/
```

The above report shows the descriptive name associated with the user-settable miscellaneous discrete environmental input points. Usually, these input points are wired to monitor equipment at a remote site. The fields of the report are described below:

Address	-	Shows number selected (up to 16) input points.
Alarm Level	-	Shows the provisioned alarm level (severity) of the environmental input, such as: CR (critical alarm) MJ (major alarm) MN (minor alarm) NA (status not alarmed) NR (status not alarmed and not reported to the OS)
Description	-	Shows descriptive name the user specified for the alarm condition, such as "fire," "open door," etc. This information is also reported in the condition description of the Active Alarms and Status Report and the History Report.

Example 7 - CONFIGURATION-Retrieve-Circuit-STS1**Releases 7 and Later**

```

Delta-1 94-11-29 13:15:15 EST
M Retrieve-Circuit-STS1:1: COMPLD
/*
=====
STS-1 Circuit Map Report
=====
Status: good
Ring Node      Source AID  Destination AID  Ring Direction  Primary State  Audit
=====
                Worcester-MA  1e-1-3    ls-1a-1
                Salem-NH   1e-1-3    1w-1-3    East to West    y
                Concord-MA  1e-1-3    1w-1-3    East to West    y
                Chicopee-MA 1s-4a-2   1w-1-3    East to West    y
*/

```

For these reports, the ring topology is:

- West to East from Worcester-MA
- West-East between Worcester and Salem-NH
- West-East between Salem and Concord-MA
- West-East between Concord and Chicopee-MA.

The above report was requested from Worcester and lists the complete circuit from Chicopee to Worcester in the West to East direction.

The following report was requested from Worcester and lists the complete circuit from Chicopee to Worcester in the West to East direction:

```

/*
=====
STS-1 Circuit Map Report
=====
Status: audit disabled
Ring Node      Source AID  Destination AID  Ring Direction  Primary State  Audit
=====
12345678901234567890 123456789 123456789 12345678901 1234 12
                Worcester-MA  1e-1-3    ls-1a-1
                Salem-NH   1e-1-3    1w-1-3    East to West    y
                Concord-MA  1e-1-3    1w-1-3    East to West    y
                Chicopee-MA 1s-4a-2   1w-1-3    East to West    y
*/

```

The following report was requested from Worcester when the slot is not cross-connected in the drop direction:

```
/*                               STS-1 Circuit Map Report
=====
Status: Not Cross-Connected
       The specified slot is not cross-connected in
       the drop direction.
*/
```

The following report was requested from Worcester when the topology was not available at Worcester:

```
/*                               STS-1 Circuit Map Report
=====
Status: Unavailable
       The ring topology is incomplete.
*/
```


Only one status condition is reported in order of precedence as shown below:

Not Cross-Connected	- The selected slot is not cross-connected in the drop direction.
Unavailable	- The topology is not available at the local node (for example, the ring is incomplete/open), all or part of the cross-connection table is missing, or circuit audit is suspended.
Path Integrity Failure	- The circuit path is open.
Squelch Map Inconsistent	- The circuit audit algorithm has declared an STS1 Squelch Map Inconsistent condition for this circuit.
Circuit Provisioning Error	- The circuit audit algorithm has declared a Circuit Provisioning Error condition for this circuit.
audit disabled	- One or more of the cross-connections which make up the circuit has its AUDIT parameters set to "NO."
good	- No errors were detected.

Report parameters are:

Source AID	- The STS-1 tributary or port address identifier.
Destination AID	- The STS-1 tributary or port address identifier.
Ring Direction	- The circuit path direction as specified in the command.
Primary State	- The status of 1WAYDRI and 1+1 protected OC-3 interface cross-connections.
Audit	- The cross-connection AUDIT parameter.

Example 8 - CONFIGURATION-Retrieve-Circuit-STS3**Releases 5 and Later**

```

Delta-1 94-11-29 13:15:15 EST
M Retrieve-Circuit-STS3:1: COMPLD
/* STS-3 Circuit Map Report
=====
      Status: good      Cross-      Ring
                        At Node:  West  Connect  East  Direction
=====
      Worcester-MA      1a      <-----      1e-1
      Salem-NH        1w-1      <-----      1e-1      ^
      Concord-MA        1w-1      <-----      1e-1      ^
      Chicopee-MA       1w-1      <-----      4a      ^

      Information above is an East to West circuit report.
      Information below is a West to East circuit report.

/* STS-3 Circuit Map Report
=====
      Status: good      Cross-      Ring
                        At Node:  West  Connect  East  Direction
=====
      Worcester-MA      1w-1      ----->      1a
      Holyoke-MA        1w-1      ----->      1e-1      ^
      Chicopee-MA       3b      ----->      1e-1      ^
*/

```

The above reports were requested from Worcester in a five node FT-2000 ring. The ring topology is as follows:

~> Worcester-MA <-> Salem-NH <-> Concord-MA <-> Chicopee-MA <-> Holyoke-MA <-~

The first report lists the complete circuit in an East to West direction whereas the second report is in a West to East direction. Each report shows the complete circuit (path) of slot 1a and lists all cross-connections that make up the circuit.

The fields of the report are described below:

Status:	- Shows the circuit (path) status in order of precedence as shown below:
Not Cross-Connected	- The slot is not cross-connected in the drop direction.
Unavailable	- The ring topology is not available at the local node (for example, the ring is incomplete/open), all or part of the cross-connection table is missing, or circuit audit is suspended.
Path Integrity Failure	- The circuit path is open.
Squelch Map Inconsistent	- The circuit audit algorithm has declared an STS3 Squelch Map Inconsistent condition for this circuit.
Circuit Provisioning Error	- The circuit audit algorithm has declared a Circuit Provisioning Error condition for this circuit.
good	- No errors were detected.
At Node:	- Shows the target identifier (TID) of the node where the report is requested (topmost entry).
West	- Shows the selected line and tributary, or slot.
Cross-Connect	- Shows the active and/or standby cross-connection, direction arrow, and a question mark (?) for an open circuit (path).
East	- Shows the selected line and tributary, or slot.
Ring Direction	- Shown by the caret (^) for all nodes except for the local node.

Example 9 - CONFIGURATION-Retrieve-Crossconnection-STS1**Release 7.0.1**

```

/*          STS-1 Cross-Connection Map Report          *: = Invalid Flag
                                                    +: = AUDIT parameter Flag
=====
Source   Destination   Type      Source   Destination   Primary
AID      AID           Type      TID      TID           State
=====
1e-1-1    1w-1-1      2WAY      Chicopee-MA    Worcester-MA
ls-4a-3    1e-1-2      2WAY      Worcester-MA    Chicopee-MA
1w-1-2    ls-2b-1      1WAY
ls-5a-1    1w-2-1      1WAY      West-Ossipee-NH    ACT
ls-5b-1    lw-2-1      1WAY      West-Ossipee-NH    STBY
1w-2-1    ls-5a-1      1WAY      West-Ossipee-NH
1w-2-1    ls-5b-2      1WAY      West-Ossipee-NH
1e-3-2    1w-3-2      2WAY      Lenox-MA        Quabbin-MA
1e-4-1    1w-4-1      1WAYDRI    Keene-NH        Boscawen-NH    STBY
ls-6a-1    lw-4-1      1WAYDRI    Boscawen-NH    Boscawen-NH    ACT
1w-4-3    1e-4-3      1WAY      Boscawen-NH    Keene-NH
1w-4-1    ls-6a-2      1WAY      Boscawen-NH
1e-5-3    1w-5-3      2WAY      Suncook-NH      Jamaica-VT
1w-5-1    ls-4a-1      1WAY      Jamaica-VT
1e-6-2    1w-6-2      1WAY      Pittsfield-MA   Quabbin-MA
1w-7-1    ls-8b-2      2WAY      *Goffstown-NH
1e-7-1    ls-6a-3      2WAY      Alfred-ME
1e-7-1    ls-4b-1      1WAY      Suncook-NH
*/

```

The above report shows information on the add/drop/through configuration of an FT-2000 Add/Drop Rings Terminal. If no cross-connections are found, the heading of this report will print, followed by the message:

NOTICE: No cross-connections exist.

The fields of the report are described below:

Source AID	-	The STS-1 Tributary or Port Address identifier.
Destination AID	-	The STS-1 Tributary or Port Address identifier.
Type	-	The type of connection.
Source TID	-	The signal entry source destination (LOCA).
Destination TID	-	The signal exit destination (LOCZ).
Primary State	-	The status of 1WAYDRI and 1+1 protected OC-3 interface cross-connections. The values are ACT (active) or STBY (standby).
Invalid Flag	-	An asterisk displays (first character of the Source/Destination TID) if a provisioned cross-connection does not exist in the ring.
AUDIT parameter Flag	-	A "+" displays (last character of the Type field) if a provisioned cross-connection has its AUDIT parameter set to "No." If the AUDIT parameter is set to "Yes," the last character is blank.

Example 10 - CONFIGURATION-Retrieve-Crossconnection-STS3**Releases 5 and 6**

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Crossconnection-STS3: COMPLD
/* STS-3 Cross-Connection Map Report (State: ... = standby --- = active)
=====
Destination/Source      Cross-Connection      Source/Destination
=====
NOTICE! Please use the Retrieve-Squelch Map-STS1 command to
        check for possible additional information.

Worcester-MA  1w-1  ---> 8a  1b ---> 1e-1  *Chicopee-MA
               1w-1      1b ...> 1e-1
               1w-1      1a <--- 1e-1
               1w-1      1b <--- 1e-1  Springfield-MA

-             1w-2                      1e-2  -

West-Ossipee-NH 1w-3  -----> 1e-3  Rutland-VT
               1w-3  ---> 2a      1e-3
               1w-3  ---> 2b      1e-3
               1w-3  <... 2a      1e-3
               1w-3  <--- 2b      1e-3
West-Ossipee-NH 1w-3  <..... 1e-3  Rutland-VT

Boscawen-NH    1w-4  -----> 1e-4  Keene-NH
               1w-4      3a ...> 1e-4
               1w-4      3a <--- 1e-4
Boscawen-NH    1w-4  <----- 1e-4  Keene-NH

Jamaica-VT     1w-5  <-----> 1e-5  Suncook-NH
Jamaica-VT     1w-5  --> 4a  4b <--- 1e-5  Suncook-NH

Quabbin-MA     1w-6  -----> 1e-6  *?
Quabbin-MA     1w-6<----- 1e-6  Pittsfield-MA

Goffstown-NH* 1w-7  ---> 5b  6a <--> 1e-7  Alfred-ME
Goffstown-NH* 1w-7<---> 8b  7a <--- 1e-7
               1w-7      7b <--- 1e-7  Alfred-ME

-             1w-8                      1e-8  -
:
-             1w-16                     1e-16  -

*/
;

```

The above report shows an STS-3 cross-connection map from an FT-2000 OC-48 Add/Drop-Rings Terminal. The West (left of center) and East (right of center) tributaries are displayed concurrently down the page in ascending order. Additional information on cross-connections is in Volume I, Section 8, "Cross-Connections."

The fields of the report are described below:

Destination - /Source	-	Shows the target identifier (TID) and may be either the destination node or the source node. A dash is shown when a tributary is not cross-connected. A question mark is shown when a tributary An asterisk is shown for invalid TID/NID pairs.
Cross- Connection	-	Shows the line, direction (West is left, East is right), and tributary number where the STS3 signal is added/dropped/through connected in this network element. Active cross-connection is shown by dashes (---) whereas a standby cross-connection is shown by periods (...).
Source/Dest - ination	-	Shows the target identifier (TID) and may be either the source node or the destination node. A dash is shown when a tributary is not cross-connected. A question mark is shown when a tributary is cross-connected to an incorrect Source/Destination TID. An asterisk is shown for invalid TID/NID pairs.

The fields of the report are described below:

Release 4.1

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Crossconnection-STS3: COMPLD
/* STS-3 Cross-Connection Map Report
=====
Destination/Source  NID   DRI   Cross-Connection          DRI NID   Source/Destination
=====
Worcester-MA       2      1w-1  <-> 1a  1b-> 1e-1          1      *Chicopee-MA
                   -      1w-1          1b<-- 1e-1          15     *Springfield-MA
                   -      1w-2          1e-2          -
West-Ossipee-NH    4      1w-3  -----> 1e-3          3      Rutland-VT
                   4      1w-3  --> 2a      1e-3          3
                   4 A  1w-3  <-- 2a      1e-3          3
West-Ossipee-NH    4 S  1w-3  <..... 1e-3          3      Rutland-VT
Boscawen-NH        6      1w-4  -----> 1e-4          A  5      Keene-NH
                   6      1w-4          3a.,> 1e-4          S  5
                   6      1w-4          3a<-- 1e-4          5
Boscawen-NH        6      1w-4  <----- 1e-4          5      Keene-NH
Jamaica-VT         8      1w-5  <----- 1e-5          7      Suncook-NH
Jamaica-VT         8      1w-5  --> 4a  4b<-- 1e-5          7      Suncook-NH
Quabbin-MA         10     1w-6  -----> 1e-6          12     *?
Quabbin-MA         10     1w-6  <----- 1e-6          11     Pittsfield-MA
*Goffstown-NH     ?      1w-7  --> 5b  6a<--> 1e-7          13     Alfred-ME
*Goffstown-NH     ?      1w-7  <-> 8b  7a<-- 1e-7          13
                   ?      1w-7          7b<-- 1e-7          13     Alfred-ME
                   -      1w-8          1e-8          -
                   :
                   -      1w-16         1e-16         -
*/

```

The above report shows an STS-3 cross-connection map that exists in the FT-2000 OC-48 Add/Drop-Rings Terminal. Tributary 6 is the only 2-way pass-through cross-connection. Slot 1a to tributary 1 of line 1w is a 2-way add/drop cross-connection. Slot 1b to tributary 1 of line 1e is a 1-way add cross-connection. Slot 1b from tributary 1 of line 1e is a 1-way drop cross-connection. A 1-way dri cross-connection in its normal state is shown as an active (slot 2a to tributary 3 of line 1w) and a standby (1w-3 pass-through to 1e-3). A 1-way dri cross-connection in a switched state is shown as an active (1w-4 pass-through to 1e-4) and a standby (slot 3a to tributary 4 of line 1e). Examples of cross-connections and additional information are provided in Volume I, Section 8.

The fields of the report are described below:

Destination/ Source	- Shows the target identifier and may be either the destination node or the source node. Blank spaces are shown when a tributary is not cross-connected.
NID	- Shows the legal node identifier value (1 through 16) that originated/terminated from the west. A dash is shown when the tributary is not cross-connected. A question mark is shown when a tributary is cross-connected to an incorrect Source/Destination TID. An asterisk is shown for invalid TID/NID pairs.
DRI	- Shows the dual ring interworking (dri) cross-connection as A (active cross-connection) or S (standby cross-connection).
Cross-Connection	- Shows the line, direction, and tributary number where the STS3 signal is added/dropped/through connected in this network element. Active dri cross-connection is shown by dashes (--) whereas the standby dri cross-connection is shown by periods (..).
DRI	- Shows the dual ring interworking (dri) cross-connection as A (active cross-connection) or S (standby cross-connection).
NID	- Shows the legal node identifier value (1 through 16) that originated/terminated from the east. A dash is shown when the tributary is not cross-connected. A question mark is shown when a tributary is cross-connected to an incorrect Source/Destination TID. An asterisk is shown for invalid TID/NID pairs.
Source/ Destination	- Shows the target identifier and may be either the source node or the destination node. Blank spaces are shown when a tributary is not cross-connected.

Releases 3 and 4.0

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Crossconnection-ST3:  COMPLD
/* STS-3 Cross-Connection Map Report
=====
Source/      1W      Slot  Slot      1E      Source/
Destination  Trib      Slot  Slot      Trib      Destination
Node ID
=====
4            1      <----->      1            2
?            2      <----->      2            ?
1            3      <----->      3            2
3            4      <-----> 1a      4            ?
4            5      <----->      5            2
2            6      <----->      6            4
3            7      <-----> 1b  4b <-----> 7            3
1            8      <----->      8            1

2            9      <----->      9            4
4           10      <----->     10           2
:            :            :            :
2           16      <----->     16           4

*/
;

```

The above report shows an STS-3 cross-connection map that exists in the FT-2000 OC-48 Add/Drop-Rings Terminal. Tributaries 1, 3, 5, 6, and 8 through 16 are 2-way pass-through cross-connections. Slots 1a, 1b, and 4b are 2-way added/dropped cross-connections. Examples of cross-connections and additional information are provided in Volume I, Section 8.

The fields of the report are described below:

- Source/
Destination
Node ID - Shows the node identifier and may be either the source node or the destination node.
- 1W Trib - Shows the tributary (1 through 16) originating or terminating on line 1W.
- Slot - Shows the slot number where the STS3 signal is added/dropped in this network element.
- 1E Trib - Shows the tributary (1 through 16) terminating or originating on line 1E.
- Source/
Destination
Node ID - Shows the node identifier of either the destination node or the source node.

Example 11 - CONFIGURATION-Retrieve-Crossconnection-All **Releases 7.2 and Later**

```

DALLAS01 01-08-15 08:59:54
M Retrieve-Crossconnection-All: COMPLD
/* Cross-Connection Map Report
(..stby STS1,::stby STS3,--act STS1,==act STS3,*Invalid Flag,+AUDIT set to No)
=====
Destination/Source          Cross-Connection          Source/Destination
=====
*/
- 1w-1 1e-1 -
- 1w-2 1e-2 -
- 1w-3 1e-3 -
- 1w-4 1e-4 -
Boscawen-NH 1w-4-3-----> 1e-4-3 Keene-NH
Jamaica-VT*+ 1w-5-1 --> 7a-1 1e-5-1 -
- 1w-5-2 1e-5-2 -
Jamaica-VT*# 1w-5-3 <-----> 1e-5-3 #Suncook-NH
- 1w-6-1 1e-6-1 -
Pittsfield-MA 1w-6-2 <-----> 1e-6-2 Quabbin-MA
- 1w-6-3 1e-6-3 -
Goffstown-NH* 1w-7-1 <-> 8b-2 4a-2<-> 1e-7-1 #*Alfred-ME
- 1w-7-1 4a-3<-- 1e-7-1 +*Alfred-ME
- 1w-7-2 1e-7-2 -
- 1w-7-3 1e-7-3 -
Jamaica-VT*+# 1w-8 <===== 1e-8 #+*Suncook-NH
- 1w-9 1e-9 -
- 1w-10-1 1e-10-1 -
- 1w-10-2 1w-2-2--> 1e-10-2 Newburyport-MA
West-Ossipee-NH 1w-10-2 -->7a-3 1e-10-2 -
- 1w-10-2 1w-2-2<.. 1e-10-2 #Newburyport-MA
- 1w-10-3 1e-10-3 -
- 1w-11-1 1e-11-1 -
- 1w-11-2 1e-11-2 -
Milton-MA#@ 1w-11-3 -->1a-2 1e-11-3 -
Haverhill-MA# 1w-11-3 <--1a-2 1e-11-3 -
- 1w-12 1e-12 -
- 1w-13 1e-13 -
Quincy-MA 1w-14 ::::::::::::::> 1e-14 #West-Ossipee-NH
- 1w-14 3b==> 1e-14 #West-Ossipee-NH
- 1w-15 1e-15 -
- 1w-16 1e-16 -
- 1w-5 1e-5 -
- 1w-6 1e-6 -
- 1w-7 1e-7 -
- 1w-8 1e-8 -
- 1w-9 1e-9 -
- 1w-10 1e-10 -
- 1w-11 1e-11 -
- 1w-12 1e-12 -

*/

```

The previous report shows information on the add/drop/through configuration of an FT-2000 Add/Drop Rings Terminal. If no cross-connections are found, the heading of this report will print, followed by the message:

The output parameters are:

Source AID	-	The STS-1 Tributary or Port Address identifier
Destination AID	-	The STS-1 Tributary or Port Address identifier.
Type	-	The type of connection.
Source TID	-	The signal entry source destination (LOCA).
Destination TID	-	The signal exit destination (LOCZ).
Primary State	-	The status of 1WAYDRI and 1+1 protected OC-3 interface cross-connections. The values are ACT (active) or STBY (standby).
Invalid Flag	-	An asterisk displays (first character of the Source/Destination TID) if a provisioned cross-connection does not exist in the ring.
AUDIT Parameter Flag	-	A "+" displays (last character of the Type field) if a provisioned cross-connection has its AUDIT parameter set to "No." If the AUDIT parameter is set to "Yes," the last character is blank.

Release 7.2.10

```

BOSTON 02-05-02 13:41:54 EDT
M Retrieve-Crossconnection-All: COMPLD
/* Cross-Connection Map Report
(..stby STS1,::stby STS3,--act STS1,==act STS3,*Invalid Flag,+AUDIT set to No)
=====
Destination/Source      Cross-Connection      Source/Destination
=====
DALLAS                  1w-1 <=>3a            1e-1      -
                        1w-1-1                1e-1-1    -
                        1w-1-2                5a-2<->1e-1-2  ATLANTA
                        1w-1-3                5a-3<->1e-1-3  ATLANTA

DALLAS                  1w-2 <=>3b            7a<=> 1e-2  ATLANTA

DALLAS                  1w-3 <=>4a            7b<=> 1e-3  ATLANTA

DALLAS                  1w-4 <=>4b            1b<=> 1e-4  ATLANTA

DALLAS                  1w-5 <=>5b            1e-5      -
=====

```

Example 12 - CONFIGURATION-Retrieve-Date

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Date: COMPLD
/* Date and Time Report
=====
Parameter                               Value
=====
Current Date                           93-01-19
Current Time                           23:15:15
Standard Time Zone                     EST
Start Daylight Savings Time           04-15
Stop Daylight Savings Time            10-05
Daylight Savings Time Zone            EDT
*/
;

```

The above report shows the provisioned daylight savings time information. If the start and stop daylight savings time dates are the same, automatic switching to daylight savings time is disabled. The fields of the report are described below:

Current Date	-	Shows current year, month, and date.
Current Time	-	Shows current time in hours, minutes, and seconds.
Standard Time Zone	-	Shows the standard time zone that is outside of daylight savings time (for example, EST, CST, MST, and PST).
Start Daylight Savings Time*	-	Shows the month and day that daylight savings time is to start.
Stop Daylight Savings Time*	-	Shows the month and day that daylight savings time is to stop
Daylight Savings Time Zone	-	Shows the daylight savings time zone (for example EDT, CDT, MDT, and PDT).

* The Daylight Savings Time feature is "disabled" (disabled will be shown on the report) when the same date is entered for Start Daylight Savings Time and Stop Daylight Savings Time.

Example 13 - CONFIGURATION-Retrieve-EC1**Releases 4.1 and Later** (Use the **CONFIGURATION** category.)

```

Delta-1 94-02-15 09:15:15 EST
M Retrieve-EC1:All: COMPLD
/* EC1 Port Provisioning Report
=====
Port      Alarm   Signal   --Port State--
Address   Level  Degrade  from      from
          NE FE   Threshold STSX1     OC48
=====
ls-1a-1   MJ MN    -6       is        is
ls-1a-2   MJ MN    -6       auto      auto
ls-1a-3   CR MN    -6       is        is
.
.
.
ls-8b-3   MN MN    -3       is        is
;

```

The fields of the report are described below:

Port Address	-	Shows a specific slot and low-speed port (1, 2, or 3) of an STS1E circuit pack (and line only for 1x1 end terminal).
Alarm Level	-	Shows the highest severity user panel LED that is lit which is the alarm that is assigned to the incoming EC1 signal failure. If no LEDs are lit, then status is shown as NA (not alarmed) or NR (not reported) if provisioned as such.
Signal Degrade Threshold	-	Shows the provisioned signal degrade threshold, either -3 or -6. The example shows a -6, meaning the error rate 10^{-6} .
Port State	-	Shows the memory administrative state of the port from the STS1X or from the OC-48 line; such as, is for in service, auto for automatic, and nmon for not monitored.

Example 14 - CONFIGURATION-Retrieve-Equipment

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Equipment:All: COMPLD
/* Equipage and Version Report
=====
Slot      Circuit  Apparatus Series  CLEI    ECI    Serial
Address   Pack    Code    Number  Code    Code    Number
=====
sysctl    SYCTL    LAA23    S1:2    SNPQWAAAA 673254  91MV08616574
          FT-2000  RELEASE 2.1.0-TRM
sysmem    SYSMEM   LAA25    S1:2    SNPQWA1AAA 673230  91MV08616747
          FT-2000  RELEASE 2.1.0-TRM
-----
lnctl-1   LNCTL    LAA27    S1:1    SNPQAHWAAA 680009  91MV06581722
lnctl-p   LNCTL    LAA27    S1:1    SNPQAHWAAA 680009  91MV06581063
-----
ohctl-1   OHCTL    LAA27    S2:2    SNPQAHWAAA 679492  91MV08615555
ohctl-p   -        -        -        -        -        -
-----
tg-1-1    TG3      LAA17    S1:1    SNPQAHWAAA 679113  91MV08615527
Continue report page-by-page?(y/n)Yes

```

```

Lucent Technologies      FT-2000      LOCAL ONLY
=====
Slot      Circuit  Apparatus Series  CLEI    ECI    Serial
Address   Pack    Code    Number  Code    Code    Number
=====
ls-1-6a   DS3      LAA2     S4:4    SNCLG0XAAA 683010  92MV07632228
          .        .        .        .        .        .
          .        .        .        .        .        .
ls-p-p1   -        -        -        -        -        -
ls-p-p2   -        -        -        -        -        -
-----
lssw-1-in LSSW     LAA12    S1:1    SNCLFLOAAA 673248  91MV06581726
lssw-1-out LSSW     LAA12    S1:1    SNCLFLOAAA 673248  91MV08616846
lssw-p-in -        -        -        -        -        -
lssw-p-out -        -        -        -        -        -
-----
sw-1      -        -        -        -        -        -
sw-p      -        -        -        -        -        -
*/
;
Press F9 to repeat last command or F3 to return to input mode.

```


These reports show the equipment and version information for the FT-2000 end terminal (previous page) and for an FT-2000 repeater bay/shelf (below). If either the SYSCTL or SYSMEM is addressed, a software version is displayed. The currently running software release is displayed when SYSCTL is addressed, and the stored software release is displayed when SYSMEM is addressed (for example, after a download or before a Reset is entered). If the requested information is not available, a report is generated stating "Equipment Data Unavailable."

```

M  Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
   Retrieve-Equipment:All:  COMPLD
   /* Equipment and Version Report
   =====
   Slot      Circuit  Apparatus  Series  CLEI      ECI      Serial
   Address   Pack      Code       Number Code      Code      Number
   =====
   sysctl     SYSCTL   LAA23      S1:2    SNPQWAAAA 673255 91MV08616575
           FT-2000  RELEASE 2.1.0-RPTR
   sysmem     SYSMEM   LAA25      S1:2    SNPQAH1AAA 673231 91MV08616748
           FT-2000  RELEASE2.1.0-RPTR
   -----
   rohctl-1   -        -          -        -          -        -
   rohctl-p   -        -          -        -          -        -
   -----
   regenr-1ew REGENR   39B1      S1:1    SNPQAAAAJ 684567 93MV06876543
   regenr-1we REGENR   39B1      S1:1    SNPQAAAAJ 684568 93MV06876542
   regenr-pew REGENR   39B1      S1:1    SNPQAAAAJ 684569 93MV06876541
   regenr-pwe REGENR   39B1      S1:1    SNPQAAAAJ 684510 93MV06876549
   */
;

```

The fields of the report are described below:

Slot Address	-	Shows the address of the slot for the selected line.
Circuit Pack*	-	Shows the functional name of the circuit pack.
Apparatus Code*	-	Shows the assigned code of the circuit pack.
Series Number*	-	Shows the series number of the circuit pack.
CLEI [†] Code*		Shows the COMMON Language [†] of the circuit pack.
ECI Code*	-	Shows the equipment catalog item code of the circuit pack.
Serial Number*	-	Shows the serial number of the circuit pack.

* A dash ("-") is displayed when a field is not applicable; for example, a slot is not equipped. A question mark "?" is displayed when the field information is not available; for example, a failed circuit pack.

[†] COMMON Language is a registered trademark and CLEI, CLLI, CLCI, and CLFI are trademarks of Telcordia Technologies, Inc.

Example 15 - SECURITY-Retrieve-Far End Communications

Releases 3 and Later (Use the **SECURITY** category.)

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Far End Communications: COMPLD
/* Far End Communication Configuration Report
=====
Parameter                      Value
=====
Remote Activity Reporting      enabled
Remote Office Alarms          enabled

                        Information below is a Release 3 example

Far End Terminal              Lucent Technologies
Remote Activity Reporting      enabled
Remote Office Alarms          enabled
*/
;

```

The above report shows that access to a remote location over the data communications channel (DCC) is enabled (access is allowed). The fields of the report are described below:

Remote Activity Reporting	-	Shows that remote reporting is enabled or disabled. For example, when enabled, far-end activity is indicated by the FE ACTY LED being lit and reported in the Retrieve-Network Alarms Report.
Remote Office Alarms	-	Shows that the remote office alarms are allowed to be displayed locally (enabled) or not (disabled). For example, when disabled, only local office alarms are reported locally.
Far End Terminal	-	For Release 3 only. Shows the far-end terminal manufacturer, "Lucent Technologies" or "Other."

Release 2 (Use the **SECURITY** category.)

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Far End Communications:DCC: COMPLD
/* Far End Communication Configuration Report
=====
DCC      Far      Remote      Remote      Far
Address  End      Activity    Office      End
          Terminal Reporting    Alarms      TBOS
=====
dcc      Lucent Technologies enabled      disabled  enabled
*/

```

The above report shows that access to a remote location over the data communications channel (DCC) is enabled (access is allowed). This report is not available at a repeater site.

The fields of the report are described below:

- DCC Address - Shows the access identifier of the SONET data communications channel.
- Far End Terminal - Shows the far-end terminal manufacturer, "Lucent Technologies" or "Other."
- Remote Activity Reporting - Shows that remote reporting is enabled or disabled. For example, when enabled, far-end activity is indicated by the FE ACTY LED being lit and reported in the Retrieve-Network Alarms Report.
- Remote Office Alarms - Shows that the remote office alarms are allowed to be displayed locally (enabled) or not (disabled). For example, when disabled, only local office alarms are reported locally.
- Far End TBOS - Shows that the far-end parallel telemetry (TBOS) is allowed to be displayed locally (enabled) or not (disabled).

Example 16 - FAULT-Retrieve-History

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-History: COMPLD
/* Maintenance History Report
=====
Date Time      System  Source  Event
of Event      Alarm   Address Description
=====
01-31 21:18:47  -       cit-dce Set-Date
01-31 21:17:47  -       cit-dce Login
01-31 21:16:17  -       system  Reset in progress - cleared
01-31 21:14:07  MJ      system  Reset in progress
01-31 21:12:23  MN      Site-1-02 repeater bay env. input-cleared
.
.
.
01-30 19:49:32  MN      Site-1-02 repeater bay environment input
*/
;

```

The above report, normally a multiple page report, shows up to 500 of the most recent events that have occurred in the FT-2000 System. The events are shown with the most recent events at the top and include:

- The beginning and end of FT-2000 System alarm and status conditions
- Completed CIT inputs, except for Retrieve-modifier(s), Operate-Alarm-Cutoff, and Test - LED inputs
- Automatic provisioning state transitions
- Operations systems interface
- All login and logout attempts.

The fields of the report are described below:

Date of Event	-	Shows the month, day, hours, minutes, and seconds the event occurred.
System Alarm	-	Shows the active alarm level at the time and date shown. Possible alarms are CR, MJ, MN, ABN, NE ACTY, and dash (-) when no alarms exist in the system.
Source Address	-	Shows the address of where the event occurred.
Event Description	-	A brief description of the event.

Example 17 - SECURITY-Retrieve-Login**Releases 5 and Later**

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Login: COMPLD
/* Login Provisioning Report
=====
Login          Privilege      Visitor  Expiration    Last Logged In
=====
Priv1          privileged    -        -             12:20:09 91-12-25
Priv2          privileged    -        -             -
-----
crafta         general       no        -             01:01:09 92-10-05
craftb         general       yes       93-02-18      04:02:50 93-02-17
-----
clerk1         reports only  no        -             -
*/
;

```

The above report shows a user the authorization information on that FT-2000 network element. The report lists each user's login and their authorization privileges but not the user's password. Different groups of users are separated by a row of hyphens (---).

Login	-	Shows the user's login (up to 10 characters) as assigned by Set-Login-Enter.
Privilege	-	Shows the assigned privilege (reports only, general, and privileged).
Visitor	-	Shows the login to be a visitor (yes) or not (no).
Expiration	-	Shows the date that the visitor login expires. A dash (-) is shown for nonvisitor logins.
Last Logged In	-	Shows the time in hours, minutes, and seconds and the date by year, month, and day that the user last successfully logged into the FT-2000 network element. A dash "-" is shown if a user has never logged in.

Releases 4.1 and Earlier

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Login: COMPLD
/* Login Provisioning Report
=====
Login      Privilege    Status      Last Logged In
=====
luc01      privileged  enabled     12:20:09    91-12-05
luc02      privileged  enabled     -
-----
BOBD       general     enabled     01:01:09    91-10-05
tech1      general     expired     -
-----
tech2      reports only disabled     -
*/
;

```

The above report shows a privileged user the authorization information on that FT-2000 network element. The information is grouped by privilege and duplicates the information below. The user's password is not shown.

The fields of the report are described below:

- Login - Shows the user's login (up to 10 characters) as assigned by Set-Login-Enter.
- Privilege - Shows the assigned privilege (reports only, general, and privileged).
- Status - Shows the current status of the login, such as enabled or disabled.
- Last Logged In - Shows the time in hours, minutes, and seconds and the date by year, month, and day that the user last successfully logged into the FT-2000 network element. A dash "-" is shown if a user has never logged in.

Example 18 - CONFIGURATION-Retrieve-Low Speed

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Low Speed:All: COMPLD
/* Low Speed Slot Report
=====
Slot          PS
Address       Priority
=====
ls-1-1a      HIGH
ls-1-1b      HIGH
ls-1-2a      low
ls-1-2b      low
.            .
.            .
.            .
ls-1-8a      HIGH
ls-1-8b      HIGH
*/
;

```

The above report shows the protection switching priority of each low speed slot that is addressed.

The fields of the report are described below:

- Slot Address - Shows the line and slot address of the low speed interface.
- PS Priority - Shows the provisioned protection switching priority, either "HIGH" or "LOW," for an electrical low speed slot.

Example 19 - CONFIGURATION-Retrieve-Map-Network**Releases 6 and Later**

```

/* Network Map Report
=====
TID                DCC   Product
Status            Type                DSNE AGNE      Alarm
=====
LOCAL-TID-GOES-HERE    good   FT-2000      yes          251
ANOTHER-TID-HERE-0     good   FT-2000          251
TID-3-GOES-HERE        good   FT-2000          251
LAST-TID-LISTED        good   FT-2000          251
-----
ANOTHER-TID-HERE-1     good   FT-2000          251
ANOTHER-TID-HERE-2     good   FT-2000          251
ANOTHER-TID-HERE-3     good   FT-2000          251
ANOTHER-TID-HERE-4     good   DDM-OC3        yes          253
ANOTHER-TID-HERE-5     good   DDM-OC3          253
ANOTHER-TID-HERE-6     good   DDM-OC12       yes          253
ANOTHER-TID-HERE-7     good   DACS IV        yes          253
ANOTHER-TID-HERE-8     good   FbrRch          253
ANOTHER-TID-HERE-9     good   ?              ?
*/

```

This report lists all network elements (NEs) in the maintenance subnetwork (limit of 64 network elements). The NEs above the dashed line are in the local ring arranged in a West-to-East sequence. And below the dashed line, all other NEs are accessible via DCC channels (both OC-48 and OC3) and/or intra-office LANs (IAO-LANs). The report shows that the local ring network is complete (DCC continuity is good). An incomplete/open ring network is indicated by FAIL in the DCC Status column. The fields of the report are described below:

TID	-	Shows the target identifier assigned to the node (up to 20 characters).
DCC Status	-	Shows either good or FAIL for the data communications channel (DCC) between the local network element and the remote network element.
Product Type	-	Shows the listed network element as FT-2000, DDM-OC3 (DDM-2000 OC-3), DDM-OC12 (DDM-2000 OC-12), DACS IV (DACS IV-2000), SLC [®] -2000, FbrRch (FiberReach), and ? (unknown, foreign, and all others).
DSNE	-	Shows if this network element is serving as the directory service network element (DSNE) in the subnetwork.
ASNE	-	Shows if this network element is serving as an alarm server network element (ASNE) in the subnetwork.
Alarm Group	-	Shows a value from zero to 255 where the value represents a SONET maintenance sub-branch (SMSB) alarm group of network elements that exchange alarm information. A zero (0) means no remote alarm information is exchanged.

Example 20 - CONFIGURATION-Retrieve-Map-Ring**Releases 4.1 and Later**

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Map-Ring:  COMPLD
/* Ring Map Report
=====
TID                DSNE      DCC      Ring
                    Status    Continuity
=====
Delta-1            yes       good     good
Delta-2                        good     good
Delta-3                        good     good
Delta-4                        FAIL     good

      Delta-4 -->(w)      Delta-1 (E) -->      Delta-2
*/
;

      Information below is for Release 4.1

=====
NID  TID                System      TL-1    DCC    Ring
      Identifier      DSNE    Gateway Status Continuity
=====

```

This report shows the current ring topology (ring is complete) and reports on the communication status to each node (DCC communication failed at Delta-4). The topmost entry is the node where the report is requested (local node), followed by all remaining nodes in a west-to-east direction. The last text line shows Delta-1 as the local node (where the report is requested). The two other nodes connected to the local node include the West node (Delta-4) and the East node (Delta-2). When data is unavailable, a "?" appears in the appropriate column of the report.

The fields of the report are described below:

NID	- For Releases 4.1 and 5 only. Shows the self-healing ring address (node identifier), 0 through 15.
TID	- Shows the target identifier assigned to the node (up to 20 characters).
System Identifier	- Shows part of the factory-provisioned character string of the network services access point (NSAP). The NSAP resides in the SYSCTL circuit pack.
DSNE	- Shows if this network element is serving as the directory service network element (DSNE).

- TL-1 Gateway - Shows if this network element is serving as the TL-1 gateway network element (GNE).
- DCC Status - Shows either good or FAIL for the data communications channel (DCC) between the local network element and the remote network element.
- Ring Continuity - Shows if a node is isolated or segmented from the other nodes in the ring by either a good, isolated, or hyphen (-) indication. All hyphens (-) in this column indicate an "incomplete/open" ring.

Releases 3 and 4.0

```

Delta-1 93-01-07 08:30:45
M Retrieve-Map-Ring: COMPLD
/* Ring Map Report
=====
NID      TID      NSAP                                          Comm
Stat
=====
0      Delta-1  39.1a3b.00.911ff4.1020.2112.3456.2181954aa677.ff  good
1      Delta-2  39.1a3b.00.911ff4.1020.2112.3456.2181954aa678.ff  good
2      Delta-3  39.1a3b.00.911ff4.1020.2112.3456.2181954aa679.ff  good
*/
;

```

```

Delta-1 93-01-07 08:30:45
M Retrieve-Map-Ring: COMPLD
/* Ring Map Report
=====
NID      TID      NSAP                                          Comm
Stat
=====
Notice: Ring Incomplete/Open
0      Delta-1  39.1a3b.00.911ff4.1020.2112.3456.2181954aa677.ff  good
1      Delta-2  39.1a3b.00.911ff4.1020.2112.3456.2181954aa678.ff  good
2      Delta-3  39.1a3b.00.911ff4.1020.2112.3456.2181954aa679.ff  FAIL
*/
;

```

The above reports show the current values of the parameters used in addressing the ring nodes. The first report shows a **complete/close ring** and the second report shows an **incomplete/open ring**. The topmost entry is the node where the report is requested followed by all remaining nodes in a west-to-east direction. When data is unavailable, a "?" appears in the appropriate column of the report. The fields of the report are described below:

- NID - Shows the node identifier, 0 through 15.
- TID - Shows the target identifier assigned to a node.
- NSAP - Shows the factory-provisioned character string of the network services access point. The NSAP resides in the SYSCTL circuit pack.
- Comm Stat - Shows either good or FAIL for the data communications channel (DCC) between the local network element and a remote network element.

Example 21 - FAULT-Retrieve-Network Alarms

Releases 3 and Later (Release 2 shown in separate example.)

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Network Alarms: COMPLD
/* Network Alarms Report
=====
System   Date   Time      TID                Product   Local
Alarm                                Type      Ring
=====
MN       01-19  21:18:47  Delta-2            FT-2000   Yes
MN       01-19  21:18:27  Delta-3            FT-2000   Yes
MN       01-19  21:20:57  Echo-1             DDM-2000  no
*/

      Information below is for Releases 3, 4.0, 4.1 and 5
=====
System   Date   Time      TID
Alarm
=====

```

The above report shows the alarm and status conditions that are currently being reported in the maintenance subnetworks. If no alarms exist, one of the following reports will be generated: There are no active system alarms or There are no active alarms in the alarm group. If a warning statement is displayed, the information is incomplete due to one or more of the following conditions:

- The OC-48 DCC has been declared failed in both an East and West direction.
- The OC-3 DCC has been declared failed in the local network element.
- The **SECURITY-Set-FECOM** command has been disabled. The status is shown in the "Remote Activity Reporting" column of the "Far End Communication Configuration Report."
- If one or more nodes are "unreachable" because of a double ring failure.

The warning statement ("information incomplete" message) is appended to the report header and appears as follows for Releases 4.1 and 5.0. The warning statement for Release 3 and 4.0 does not include Retrieve-Map-Ring and is not shown.

Warning! The information displayed may be incomplete. Check Retrieve-Map-Network, Retrieve-Far End Communications Retrieve-Alarm for further details.

The information displayed may also be incomplete when an NE status comm failure condition exists; but, for Release 6, the above warning statement is not displayed for such a condition. Future releases will output the above warning statement when the NE status comm failure condition exists.

The fields of the report are described below:

- System Alarm - Shows the highest system alarm at each node in the ring network. The alarm corresponds to the topmost user panel LED that is lit at an FT-2000 network element or the alarm level reported by the FT-2000 repeater bay/shelf.
- Date - Shows the date in month and day for each event when it was first reported.
- Time - Shows the time in hours, minutes, and seconds for each event when it was first reported.
- TID - Shows the target identifier assigned to the network element.
- Product Type - Shows the category or type of network element.
- Local Ring - Shows either yes or no to indicate if the network element is in the local ring.

Release 2 (Use the **FAULT** category.)

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Network Alarms: COMPLD
/* Network Alarms Report
=====
System   Date   Time   Source   Event
Alarm    Detected                Address   Description
=====
CR       03-26  18:35:10 TID-of-near-end-NE
MJ       03-26  18:36:02 site-p-01      repeater bay equipment failure
MN       03-26  18:36:28 site-1-03      incoming OC48 B1 parity error
MN       03-26  18:36:51 TID-of-far-end-NE
*/
;

```

The above report shows the alarm and status conditions that are currently being reported within the FT-2000 network. If no alarms exist in the FT-2000 network, a report is generated that states, "There are no active alarms or status conditions."

- System Alarm - Shows the highest system alarm at each FT-2000 network element. The alarm corresponds to the topmost user panel LED that is lit at an FT-2000 network element or the alarm level reported by the FT-2000 repeater bay/shelf.
- Date Time Detected - Shows the date in month and day, and the time in hours, minutes, and seconds for each event when it was first reported.
- Source Address - Shows the remote FT-2000 end terminal identification (TID) or the FT-2000 repeater bay/shelf site address.
- Event Description - Shows an alarm condition summary for the repeater bay/shelf. No *Event Description* is provided for an FT-2000 end terminal.

Example 22 - SECURITY-Retrieve-Network Element**Releases 6 and Later** (Use the **SECURITY** category.)

```

/* FT-2000 System Provisioning Report
=====
Parameter                               Value
=====
TID                                     system_name234567892
Directory Service NE                   yes
X25 Packet Size                        256
Site Address (SADD)                   site-1e-0
                                         site-1w-4
Alarm Group                           247
Alarm Gateway NE                       yes
TSA Granularity                        STS1
*/

```

The above report shows the provisioned parameters and values for an individual network element (NE). The retrieved parameter values include target ID (TID), directory services NE (DSNE), the X.25 packet size, the site address (SADD), the alarm group, and the alarm gateway network element (AGNE).

TID	- Shows the unique identification of the node.
Directory Service NE	- Shows whether or not this network element is serving as the Directory Server node in the ring network. Only one node is provisioned as the DS-NE in the ring network.
X25 Packet Size	- Shows the X.25 packet size to be used for a TL1 session to this network element. The original value (size) is 256 but may be 128.
Site Address (SADD)	- Shows the confirmed F-byte address (site address) of the FT-2000 network element as reported on both the East and West lines for Release 4.0 and later. The site address is derived from the autoprovisioning function based on an OC-48 line section. If a question mark (?) appears as part of the value, then auto provisioning has not been completed or it failed (for example, a removed or failed LNCTL).
Alarm Group	- Shows a value from zero to 255 where the value represents a SONET maintenance sub-branch (SMSB) alarm group of network elements that exchange alarm information. A zero (0) means no remote alarm information is exchanged.
Alarm Gateway NE	- Shows whether or not the local NE will serve the other members of its alarm group as an alarm gateway network element (AGNE).
TSA Granularity	- Release 7 and later. Shows whether or not the local NE is operating in an STS3 or STS1 granular mode (dependent on TRMTR and RCVR).

Releases 5 and Earlier (Use the **SECURITY** category.)

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Network Element: COMPLD
/* FT-2000 System Provisioning Report
=====
      Parameter      Value
=====
      TID             Delta-1
      NID             13
      Directory Service NE  yes
      Site Address      site-1e-0
                       site-1w-4

Information below is a Release 4.1 example

      TID             Delta-1
      NID             13
      Directory Service NE  yes
      TL-1 Gateway      yes
      Site Address      site-1e-0
                       site-1w-4

Information below is a Release 3.0 example

      TID             Delta-1
      NID             13
      Directory Service NE  no

*/

```

The above reports for a node show the provisioned parameters and values from the **SECURITY-Retrieve-Network Element** command.

TID	-	Shows the unique identification of the node.
NID	-	Shows the node identifier, 0 through 15.
Directory Service NE	-	Shows whether or not this network element is serving as the Directory Server node in the ring network. Only one node is provisioned as the DS-NE in the ring network.
Site Address	-	Shows the confirmed F-byte address of the FT-2000 network element as reported on both the East and West lines for FT-2000 ADR Release 4.0 and later. The site address for 1W line of the network element is derived from the autoprovisioning function based on the number of sections in the OC-48 line on the West side of the network element. If a question mark (?) appears as part of the value, then autoprovisioning has not been completed or it failed (for example, a removed or failed LNCTL). The site address is calculated with respect to the East side of the adjacent FT-2000 ADR node. Each FT-2000 ADR node will first label its East side as 1E-0 (SECURITY-Retrieve-Network Element). The next Regenerator away from 1E-0 will become 1E-1, the next 1E-2 and so on. This is done on a section/line basis between each pair of FT-2000 ADR nodes. In the previous example, site-1W-4 indicates that there are 3 regenerator sites. The regenerators on the West side of an FT-2000 ADR terminal are identified using the same numbering scheme. Regenerator alarms being received on the East or West side of the FT-2000 ADR terminal will be reported as "site 1E or W-x..." where 'x' is the regenerator address.
TL-1 Gateway	-	Shows whether or not this network element is serving as the TL-1 gateway in a ring network for Release 4.1 and later. A ring network may have more than one TL-1 gateway network element.

Release 2 (Use the **SECURITY** category.)

```

TERMB 92-01-19 23:15:15 EST
M Retrieve-Network Element: COMPLD
/* FT-2000 System Provisioning Report
=====
Parameter          Value
=====
TID                TERMB
Site 0             No
Site Address       site-1-04
                  site-p-04
*/
;

```

The above report for an FT-2000 end terminal shows the provisioned parameters and values from the **SECURITY-Set-Network Element** command. Also shown are the results of the site address (SADD) autoprovisioning.

An FT-2000 repeater bay/shelf report is the same except it does not have the Site 0 parameter. An FT-2000 repeater bay/shelf site is automatically provisioned with a site address for each OC-48 line.

- TID - Shows the unique identification of an FT-2000 network element.
- Site 0 - Shows if an FT-2000 end terminal has been provisioned to be Site 0, either Yes or No.
- Site Address - Shows the confirmed F-byte address (site address) of the FT-2000 network element from the auto provisioning function. If questions marks (??) appear as part of the value, then auto provisioning has not been completed or it failed.

Example 23 - CONFIGURATION-Retrieve-OC3**(Releases 7 and Later)**

```

Delta-1 94-06-15 23:15:15 EST
M Retrieve-OC3:All: COMPLD
/* OC3 Provisioning Report
=====
Port   Alarm  SD   Output   Provisioned   Operating   Port
Addr   Level  Thr  AU4      Mode          Mode          state
      NE  FE  Map  Lngth  (from  (from  (from  (from  (from  (from
      NE  FE  Map  Lngth  OC48)  OC3)   OC48)  OC3)   OC48)  OC3)  OC3)
=====
ls-1a  CR  MN  -6  SONET  1310  1+1    1+1    1+1    1+1    -3     -3C    i
ls-1b  CR  MN  -6  SONET  1310  1+1    1+1    1+1    1+1    -3C    -3C    i
ls-2a  MJ  MN  -8  SONET  1310  0x1    0x1    0x1    0x1    -3     -3     i
ls-2b  MJ  MN  -8  SONET  1310  0x1    0x1    0x1    0x1    -3     -3     i
ls-3a  MN  MN  -6  SDH    1310  1+1    1+1    0x1    1+1    -3     -3     i
ls-3b  NA  MN  -6  SDH    1310  1+1    1+1    1+1    1+1    -3C    -3C    i
.
.
ls-8b  NA  MN  -8  SDH    1310  0x1    0x1    0x1    0x1    -3C    -3C    i
*/

```

The above report shows the provisioning information and attributes of an OC3 slot. It provides information on the severity of the alarm implied by the failure of the OC3 slot, the signal degrade threshold, and the memory administrative state.

Port Addr	-	Shows a specific low speed interface slot (physical) address.
Alarm Level Near End	-	Shows the provisioned alarm level for incoming OC-3 signal conditions. Possible values: (CR, MJ, MN, NA, NR, NO).
Alarm Level Far End	-	Shows the provisioned alarm level for incoming conditions from the NE. Possible values: (CR, MJ, MN, NA, NR).
SD Thr	-	Shows the provisioned signal degrade threshold value (-5, -6, -7, -8, -9) to base 10 of the bit error rate.
Output AU4 Map	-	Shows the provisioned AU4 mapping of either SONET or synchronous digital hierarchical (SDH).
Wave-Lngth	-	Shows the optical wavelength of "1310".
Provisioned-Mode	-	Shows the provisioned protection switching mode (1+1 or 0x1) in the "from OC48" direction and the "from OC3" direction.
Operating-Mode	-	Shows the actual operating protection switching mode (1+1 or 0x1) in the "from OC48" direction and the "from OC3" direction.
Payload-	-	Shows the signal type (-3 or -3C) being forwarded through the OC3 circuit pack in the "from OC48" direction and the "from OC3" direction.
Port State	-	Shows the memory administrative state in the "from OC3" direction.

Releases 5 and 6

```

Delta-1 94-06-15 23:15:15 EST
M Retrieve-OC3:All: COMPLD
/* OC3 Provisioning Report
=====
Port   Alrm  SD   Output   Line   Wave-   Provisioned   Operating
Addr   Lvl   Thrsh AU4      Code   Length   Mode        Mode
              Map                               (from   (from
              (from   (from   (from   (from   (from   (from
              OC48)  OC3)   OC48)  OC3)   OC48)  OC3)
=====
ls-1a  CR    -6   SONET    nrz     1310    1+1    1+1    1+1    1+1    -3    -3C
ls-1b  CR    -6   SONET    nrz     1310    1+1    1+1    1+1    1+1    -3C   -3C
ls-2a  MJ    -8   SONET    nrz     1310    0x1    0x1    0x1    0x1    -3    -3
ls-2b  MJ    -8   SONET    nrz     1310    0x1    0x1    0x1    0x1    -3    -3
ls-3a  MN    -6   SDH      nrz     1310    1+1    1+1    0x1    1+1    -3    -3
ls-3b  NA    -6   SDH      nrz     1310    1+1    1+1    1+1    1+1    -3C   -3C
.
.
ls-8b  NA    -8   SDH      nrz     1310    0x1    0x1    0x1    0x1    -3C   -3C
*/
;

```

The above report shows the provisioning information and attributes of an OC3 slot. It provides information on the severity of the alarm implied by the failure of the OC3 slot, the signal degrade threshold, and the memory administrative state.

Port Addr	- Shows a specific low speed interface slot (physical) address.
Alrm Lvl	- Shows the provisioned service affecting (SA) alarm level for the slot (CR, MJ, MN, NA, or NR).
SD Thrsh	- Shows the provisioned signal degrade threshold value (-5, -6, -7, -8, -9) to base 10 of the bit error rate.
Output AU4 Map	- Shows the provisioned AU4 mapping of either SONET or synchronous digital hierarchical (SDH).
Line Code	- Shows the optical line code of "nrz".
Wave- Lngth	- Shows the optical wavelength of "1310".
Provisioned-Mode	- Shows the provisioned protection switching mode (1+1 or 0x1) in the "from OC48" direction and the "from OC3" direction.
Operating-Mode	- Shows the actual operating protection switching mode (1+1 or 0x1) in the "from OC48" direction and the "from OC3" direction.
Payload	- Shows the signal type (-3 or -3C) being forwarded through the OC3 circuit pack in the "from OC48" direction and the "from OC3" direction.

Example 24 - CONFIGURATION-Retrieve-OC48

Releases 7.2 and Later

```
DALLAS01 01-08-15 09:00:22
M Retrieve-OC48:All: COMPLD
/* OC-48 Line Provisioning Report
=====
Line   Line   Signal Degrade   Sync
Address Code   Threshold   Messages
=====
line-1e nrz      -6          disabled
line-1w nrz      -6          disabled
*/
;
```

The above report shows provisioned information for each addressed high-speed line at an **FT-2000 add/drop-rings terminal**.

- Line Address - Shows the OC-48 line(s) selected for the report.
- Line Code - Shows the non-return-to-zero (nrz) line code.
- Signal Degrade Threshold - Shows the provisioned threshold value. This value determines when a signal degrade condition will be declared. The value is the line parity error rate for the specified line(s); for example, -5 is error rate 10^{-5} .
- Sync Messaging - Shows the provisioned **sync messaging state** in the "from OC48" direction.

⇒ NOTE:
B5 optics are required for this feature/option.

Release 3 and Later

```

Delta-1 94-06-15 23:15:15 EST
M Retrieve-OC48:All: COMPLD
/* OC-48 Line Provisioning Report
=====
Line      Line      Signal Degrade      Wave-
Address   Code      Threshold          Length
=====
line-1e   nrz        -5                 1310
line-1w   nrz        -5                 1310
*/
;

```

The above report shows provisioned information for each addressed high-speed line at an **FT-2000 add/drop-rings terminal**, whereas the report on the next page shows the information for an **FT-2000 repeater bay/shelf**.

- Line Address - Shows the OC-48 line(s) selected for the report.
- Line Code - Shows the non-return-to-zero (nrz) line code.
- Signal Degrade Threshold - Shows the provisioned threshold value. This value determines when a signal degrade condition will be declared. The value is the line parity error rate for the specified line(s); for example, -5 is error rate 10^{-5} .
- Wave-Length - Shows the provisioned optical line wavelength, either 1310 or 1550 nanometers. If a hyphen ("-") appears, it means the line is unequipped; for example, no TRMTR circuit pack inserted.
- PS* Priority - Only for Release 1.1.1 and Release 2.1.0. Shows the provisioned protection switch priority of the line, either "HIGH" or "LOW."

* This column only appears in an FT-2000 end terminal report.

```
Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-OC48:1: COMPLD
/* OC-48 Line Provisioning Report
=====
Slot          Line      Wave-
Address       Code      Length
=====
regenr-1ew    nrz        1310
regenr-1we    nrz        1310
regenr-pew    nrz        1310
regenr-pwe    nrz        1310
*/
;
```

The above report shows provisioned information for each **regenerator** that is addressed at an FT-2000 repeater bay/shelf.

- Slot Address - Shows the regenerator slot(s) selected for the report.
- Line Code - Shows the non-return-to-zero (nrz) optical line code.
- Wave-Length - Shows the provisioned optical line wavelength, either 1310 or 1550 nanometers.

Example 25 - CONFIGURATION-Retrieve-Path-Trace**Releases 4.0 and Later**

```

Delta-1 91-07-15 23:15:15 EST
M Retrieve-Path-Trace:All:Text:  COMPLD
/* Path Trace Report
=====
Address
Parameter  Value
=====
ls-1a-1
Status:    MISMATCH                Label: 01 equipped-non specific payload
ActRx:     Hello from secondary node=slot 1a-port1
PrvRx:     Hello from primary node=slot 1a-port1
PrvTx:     Hello from Atlanta node=slot 1a-port1
-----
ls-1a-2
Status:    good                    Label: 01 equipped-non specific payload
ActRx:     Hello from primary node=slot 1a-port2
PrvRx:     Hello from primary node=slot 1a-port2
PrvTx:     Hello from Atlanta node=slot 1a-port2
-----
ls-1a-3
Status:    MISMATCH                Label: 01 equipped-non specific payload
ActRx:     .....
PrvRx:     Hello from primary node=slot 1a-port3
PrvTx:     Hello from Atlanta node=slot 1a-port3
-----
ls-1b-1
Status:    NOT APPLICABLE          Label: -
ActRx:     -
PrvRx:     Hello from primary node=slot 1b-port1
PrvTx:     Hello from Atlanta node=slot 1b-port1
-----
ls-1b-2
Status:    UNAVAILABLE             Label: ?
ActRx:     ?
PrvRx:     Hello from primary node=slot 1b-port2
PrvTx:     Hello from Atlanta node=slot 1b-port2
:
-----
ls-8b-3    ...
*/

```

This command retrieves the provisioned transmit and receive path strings and the actual receive path trace string for a DS3 circuit pack only (not for STS1E or OC-3). The actual receive string at this node should be the provisioned transmit string from another node. If the strings match, then the circuit path is good. The provisioned receive string is what is expected at this node and may be different than the actual receive string. If they differ, then the circuit path is a mismatch.

The path trace is for one STS-1 signal that is reported in either a text format or a binary format. The first report (text format) displays only an ASCII text interpretation. The second report (binary format) displays raw hexadecimal values, followed by an ASCII text interpretation. Periods (...) indicate unprintable ASCII characters for both formats.

Additional information on path trace is in Volume I, Sections 8 and 9, "Administration and Provisioning," and "Maintenance Description," respectively.

Binary Format

```

Delta-1 91-07-15 23:15:15 EST
M Retrieve-Path-Trace:1a:1:Binary: COMPLD
/* Path Trace Report
=====
----- ASCII Character Code (HEX) ----- ---Text String---
=====
ls-1a-1

Status: MISMATCH          Label: 01 equipped-non specific payload

Actual Receive Path Trace:
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....
00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 00 .....

Provisioned Receive Path Trace:
00 12 04 76 69 73 69 6f 6e 65 64 52 65 63 65 69 ...visio nedRecei
76 65 54 72 61 63 65 78 79 7a 61 62 63 64 65 66 veTracex yzabcdef
67 68 69 6a 6b 6c 6d 6e 6f 70 71 72 73 74 75 76 ghijklmn opqrstuv
77 78 79 7a 31 32 33 34 35 36 37 38 39 31 00 00 wxyz1234 567891..

Provisioned Transmit Path Trace:
50 72 6f 76 69 73 69 6f 6e 65 64 54 72 61 6e 73 Provisio nedTrans
6d 69 74 54 72 61 63 65 79 7a 61 62 63 64 65 66 mitTrace yzabcdef
67 68 69 6a 6b 6c 6d 6e 6f 70 71 72 73 74 75 76 ghijklmn opqrstuv
77 78 79 7a 31 32 33 34 35 36 37 38 39 31 00 00 wxyz1234 567891..
*/

```

The fields of the report are described below:

Address	- Shows the OC-48 line, DS3 circuit pack slot, and port of the STS-1 path.
Parameter Value	- Shows the status, the actual received data string (ActRx), the provisioned received (PrvRx), and/or transmit (PrvTx) data strings.
ActRx	- Shows the actual incoming STS-1 data string for the specified port.
PrvRx	- Shows the expected incoming STS-1 data string for the specified port.
PrvTx	- Shows the provisioned outgoing STS-1 data string for the specified port.
Status	- Shows the ActRx (actual received) data string as follows: - good means ActRx path is the same as PrvRx path. - MISMATCH means ActRx path is different than PrvRx path. - NOT APPLICABLE means the addressed slot is in the AUTO state or is not equipped with a DS3 circuit pack. - UNAVAILABLE means that there is an incoming OC-48 line failure, a missing slot cross-connection, or an unprotected failure.
Label	- Shows the C2 byte value in hexadecimal code for the received path signal and a corresponding text description of the STS payload type.

Example 26 - PERFORMANCE-Retrieve-PM-EC1**Releases 4 and Later**

```

Delta-1 91-07-15 23:15:15 EST
M Retrieve-PM-EC1:All: COMPLD
/* EC1 Performance Monitoring Status Report
  Bin Start Time: quarter hour registers at yy-mm-dd hh:mm:ss
                    day registers at yy-mm-dd hh:mm:ss
=====
Address      Start    Section  ----- EC1 Line (B2) -----
             Time      SEFS-S    CV-L   ES-L   SES-L   UAS-L
=====
ls-1a-1      Today      5         1       0       0       34000>
             09:00      5         1       0       0       5
=====
ls-1a-2      Today      12345*    25       2       0       12345*
             09:00      5         1       0       0       5
=====
ls-1a-3      Today      12345?    25?      2?      0?      12345?
             09:00      -         -        -        -        -
=====
ls-2b-1      Today      5         1       0       0       5
             09:00      5         1       0       0       5
=====
ls-2b-2      Today      12345     25       2       0       12345
             09:00      5         1       0       0       5
=====
ls-2b-3      Today      12345     25       2       0       12345
             09:00      5         1       0       0       5
*/
;

```

Information below is for Release 4.0 and Release 4.1

```

/* EC1 Performance Monitoring Status Report
  Last initialized: quarter hour registers at yy-mm-dd hh:mm:ss
                    day registers at yy-mm-dd hh:mm:ss
=====
Address      Start    Section  ----- EC1 Line (B2) -----
             Time      SEFS     CV    ES    SES    UAS
=====

```

The above report, which can be a multiple page report, shows the EC1 section and line performance-monitoring data (day and quarter-hour). Data is collected from the signal coming from the OC-48 line (section) and from the cross-connections (EC1 line). When a greater than sign (>) is displayed, it means the violations are greater than the maximum value for the parameter. When a question mark (?) is displayed, it means corrupted data or an incomplete time interval for that parameter. When a hyphen (-) is displayed, it means the data is unavailable. An asterisk (*) is displayed whenever the threshold-crossing alert (TCA) is reached. This report is not available at the repeater site.

The fields of the report are described below:

Address	-	Shows the AID of the line, slot, and ports for point-to-point network or slot and ports for a ring network.
Start Time	-	Shows the time in hours and minutes (quarter-hour increments) that data collection started. Yesterday is shown by MM-DD.
Section SEFS-S	-	Shows the number of severely frame errored seconds reported in the bin during the specified time that will cause a TCA.
CV-L	-	Shows the number of EC1 line code violations reported in the bin during the specified time that will cause a TCA.
ES-L	-	Shows the number of EC1 line errored seconds reported in the bin during the specified time that will cause a TCA.
SES-L	-	Shows the number of EC1 line severely errored seconds reported in the bin during the specified time that will cause a TCA.
UAS-L	-	Shows the number of EC1 line unavailable seconds reported in the bin during the specified time that will cause a TCA.

Example 27 - PERFORMANCE-Retrieve-PM-Line**Releases 4 and Later** (Use the **PERFORMANCE** category.)

```

Delta-1 94-06-15 13:15:15 EST
M Retrieve-Performance Monitoring-Line-OC48:1e: COMPLD
/* OC-48 Line Performance Monitoring Status Report
   Bin Start Time: quarter hour registers at yy-mm-dd hh:mm:ss
                   day registers at yy-mm-dd hh:mm:ss
=====
Address      Start  ----- OC-48 Line (B2) -----
            Time    CV-L   ES-L   SES-L   UAS-L   PSC
=====
line-1e      Today   133456?  0?    0?    0?    0?
            03-20    1      3      1      1      0
            03-19    1      3      1      1      0
            03-18    0      0      1      0      1
-----
            09:00    1      0      0      0      0
            08:45    3      0      0      5      0
            08:30    8      8      3     900      0
            08:15   5315000> 115*    0      0      0
*/
;

      Information below is for Release 4.0 and Release 4.1

M Retrieve-Performance Monitoring-Line-OC48:All: COMPLD
/* OC-48 Line Performance Monitoring Status Report
   Last initialized: quarter hour registers at 93-01-15 13:15:00
                   day registers at yy-mm-dd hh:mm:ss
=====
Address      Start  ----- OC-48 Line (B2) -----
            Time    CV    ES    SES    UAS    PSC
=====

```

The above report shows the current performance-monitoring data for the OC-48 line(s). When a greater than sign (">") is displayed, it means the violations are greater than the maximum value for the parameter (overflow). When a question mark ("?") is displayed, it means corrupted data or incomplete time cycle for that parameter. An asterisk is displayed whenever the threshold-crossing alert (TCA) is reached. This report is not available at the repeater site.

The fields of the report are described below:

- Address - Shows the specified OC-48 line.
- Start Time - Shows the time by month, day, hour, and minutes.
- B2 CV-L - Shows the number of code violations reported in B2 during the specified time.
- B2 ES-L - Shows the number of errored seconds reported in B2 during the specified time.
- B2 SES-L - Shows the number of severely errored seconds reported in B2 during the specified time.
- B2 UAS-L - Shows the number of unavailable seconds reported in B2 during the specified time.
- PSC - Shows the number of protection switching counts during the specified time.

Release 3 (Use the **PERFORMANCE** category.)

```

Delta-1 93-01-15 13:15:15 EST
M Retrieve-Performance Monitoring-Line:All: COMPLD
/* STS-48 Line Performance Monitoring Status Report
   Last initialized: quarter hour registers at 93-01-15 13:15:00
                      day registers at 93-01-15 00:00:00
=====
Address      Start   B2    B2    B2    B2    PSC
              Time   CV     ES    SES   UAS
=====
line-1e      Today   1118?  1?    1?    4095>  3
-----
              13:15   0?    0?    0?    15     0
-----
line-1w      Today   0      0      0      0      0
-----
              13:15   0      0      0      0      0
*/
;

```

The above report shows the current performance-monitoring data for the OC-48 lines. When a greater than sign (">") is displayed, it means the violations are greater than the maximum value for the parameter. When a question mark ("?",) is displayed, it means corrupted data or incomplete time cycle for that parameter. This report is not available at the repeater site.

The fields of the report are described below:

Address	- Shows the specified OC-48 line.
Start Time	- Shows the time by month, day, hour, and minutes.
B2 CV	- Shows the number of code violations reported in B2 during the specified time.
B2 ES	- Shows the number of errored seconds reported in B2 during the specified time.
B2 SES	- Shows the number of severely errored seconds reported in B2 during the specified time.
B2 UAS	- Shows the number of unavailable seconds reported in B2 during the specified time.
PSC	- Shows the number of protection switching counts during the specified time.

Release 2 (Use the **PERFORMANCE** category.)

```

TERMB 92-07-15 13:15:15 EST
M Retrieve-Performance Monitoring-Line:1: COMPLD
/* STS-48 Line Performance Monitoring Status Report
   Last initialized: quarter hour registers at 92-07-15 13:15:00
                      day registers at 92-07-15 00:00:00
=====
Address   Start      B2      B2      B2      B2      PSC
          Time      CV      ES      SES     UAS
=====
line-1   Today      4?      2?      0?      820      1
          07-14      1735      5      3      0      0
-----
          13:15      1      1      0      0      0
          09:00      3      1      0      0      0
          08:45      0?      0?      0?      820      1
*/
;

```

The above report shows the current performance-monitoring data for all OC-48 lines. When a greater than sign (">") is displayed, it means the violations are greater than the maximum value for the parameter. When a question mark ("?") is displayed, it means corrupted data or an incomplete time cycle for that parameter. This report is not available at the repeater site.

The fields of the report are described below:

- Address - Shows the specified OC-48 line.
- Start Time - Shows the time by month, day, hour, and minutes.
- B2 CV - Shows the number of code violations reported in B2 during the specified time.
- B2 ES - Shows the number of errored seconds reported in B2 during the specified time.
- B2 SES - Shows the number of severely errored seconds reported in B2 during the specified time.
- B2 UAS - Shows the number of unavailable seconds reported in B2 during the specified time.
- PSC - Shows the number of protection switching counts during the specified time.

Example 28 - PERFORMANCE-Retrieve-PM Mode-T3

```

Delta-1 94-06-15 23:15:15 EST
M Retrieve-PM Mode-T3:All: COMPLD
/* DS-3 Performance Monitoring Mode Report
=====
Port      DS3
Address   Mode
=====
ls-1a-1   P-bit
ls-1a-2   P-bit
ls-1a-3   F&M-bit
.
.
.
ls-8b-3   P-bit
*/
;

```

The above report shows the algorithm used for determining and accumulating DS3 path performance monitoring data, either P-bit processing or F-bit and M-bit, adjusted processing.

Port	-	Shows the AID of low-speed port.
Address		
DS3 Mode	-	Shows the provisioned algorithm used to determine and accumulate the DS3 path performance data (P-bit or F&M-bit).

Example 29 - PERFORMANCE-Retrieve-PM-Optics**Releases 3 and Later** (Use the **PERFORMANCE** category.)

```

Delta-1 93-01-15 09:15:15 EST
M Retrieve-Performance Monitoring-Optics-OC48-All: COMPLD
/* OC48 Optics Performance Monitoring Status Report
   Bin Start Time: quarter hour registers at yy-mm-dd hh:mm:ss
                   day registers at yy-mm-dd hh:mm:ss
=====
Address      Start   Transmit   Receive   Laser Bias
              Time    Power      Power     Current
=====
line-1e      Today    in range   in range   1.04 in range
              09:15    in range   in range   1.03 in range
line-1w      Today    in range   in range   0.97 in range
              09:15    in range   in range   0.99 in range
*/
;

      Information below is for Releases 3.0, 4.0, and 4.1

/* Optics Performance Monitoring Status Report
   Last initialized: quarter hour registers at 93-01-15 09:15:00
                   day registers at 93-01-15 00:00:00
=====
Address      Start   Transmit   Receive   Laser Bias
              Time    Power      Power     Current
=====

```

The above report shows the current performance-monitoring data for the OC-48 transmitter and receiver optics for all lines at an FT-2000 terminal. This report is not available at the repeater site.

- Address - Shows the address of the transmitter or receiver in the FT-2000 terminal.
- Start Time - Shows the time in hours and minutes for today only, or the month and day are shown.
- Transmit Power - Shows if the transmit power is within the specifications.
- Receive Power - Shows if the receive power is within the specifications. A hyphen (-) indicates that the quarter-hour bin data is unavailable for the TRMTR circuit pack.
- Laser bias Current - Shows if the laser bias current is within the specifications.

Release 2 (Use the **PERFORMANCE** category.)

```

TERMB 92-07-15 09:15:15 EST
M Retrieve-Performance Monitoring-Optics:All: COMPLD
/* Optics Performance Monitoring Status Report
   Last initialized: quarter hour registers at 92-07-15 09:15:00
                      day registers at 92-07-15 00:00:00
=====
Address      Start      Transmit      Receive      Laser bias
              Time       Power        Power        Current
=====
line-1       Today       in range     in range     in range
              09:15       in range     in range     in range
=====
line-p       Today       in range     in range     in range
              09:15       in range     in range     in range
=====
*/
;

```

The above report shows the current performance-monitoring data for the transmitter and receiver optics for all lines at an FT-2000 end terminal. This report is not available at the repeater site.

- Address - Shows the address of the transmitter or receiver in the FT-2000 end terminal.
- Start Time - Shows the time in hours and minutes for today only, or the month and day are shown.
- Transmit Power - Shows if the transmit power is within the specifications.
- Receive Power - Shows if the receive power is within the specifications.
- Laser bias Current - Shows if the laser bias current is within the specifications.

Example 30 - PERFORMANCE-Retrieve-PM-Section**Releases 4 and Later** (Use the **PERFORMANCE** category.)

```

Lucent Technologies-FT-2000 96-07-15 13:15:15 EST
M Retrieve-Performance Monitoring-Section-OC48-All: COMPLD
/* STS-48 Section Performance Monitoring Status Report
   Bin Start Time: quarter hour registers at yy-mm-dd hh:mm:ss
                   day registers at yy-mm-dd hh:mm:ss
=====
Address      Start      --- STS-48 (B1) Section ---
              Time      CV-S    ES-S    SES-S  SEFS-S
=====
line-1e      Today      133456*    0      0      0
              09:00      1      0      0      0
=====
line-1w      Today      133456*    0      1238*    0
              09:00      1      1200*    0      0
              08:45      3      0      1200*    0
*/
;

      Information below is for Release 4.0 and Release 4.1

/* STS-48 Section Performance Monitoring Status Report
   Last initialized: quarter hour registers at 91-07-15 13:15:00
                   day registers at 91-07-15 00:00:00
=====
Address      Start      --- STS-48 (B1) Section ---
              Time      CV    ES    SES    SEFS
=====

```

The above report shows the current section performance-monitoring data for an OC-48 line section. When a greater than sign (">") is displayed, it means the violations are greater than the maximum value for the parameter. When a question mark ("?",) is displayed, it means corrupted data or an incomplete time cycle for that parameter. An asterisk (*) is displayed whenever the threshold-crossing alert (TCA) is reached. This report is not available at the repeater site.

Address	- Shows the line containing the section.
Start Time	- Shows the time in hours and minutes (quarter-hour increments) that data collection started. But if month and day are shown, the data is for yesterday.
CV-S	- Shows the number of code violations reported in B1 during the specified time.
ES-S	- Shows the number of errored seconds reported in B1 during the specified time.
SES-S	- Shows the number of severely errored seconds reported in B1 during the specified time.
SEFS-S	- Shows the number of severely errored frame seconds reported for the specified section during the specified time.

Release 3

```

Lucent Technologies-FT-2000 91607-15 13:15:15 EST
M Retrieve-Performance Monitoring-Section:1E: COMPLD
/* STS-48 Section Performance Monitoring Status Report
   Last initialized: quarter hour registers at 91-07-15 13:15:00
                      day registers at 91-07-15 00:00:00
=====
Address  Start      B1      B1      B1      SEFS
        Time      CV      ES      SES
=====
line-1e Today      224831    902    403    0
        07-14      393      5      0      0
-----
        13:15      0        0      0      0
        09:00      1        1      0      0
        08:45      3        1      0      0
        08:15      0        0      0      0
        08:00      224000> 900    403    0
*/
;

```

The above report shows the current section performance-monitoring data for an OC-48 line section. When a greater than sign (">") is displayed, it means the violations are greater than the maximum value for the parameter. When a question mark ("?") is displayed, it means corrupted data or an incomplete time cycle for that parameter. This report is not available at the repeater site.

- Address - Shows the line containing the section.
- Start Time - Shows the time in hours and minutes (quarter-hour increments) that data collection started. But if month and day are shown, the data is for yesterday.
- B1 CV - Shows the number of code violations reported in B1 during the specified time.
- B1 ES - Shows the number of errored seconds reported in B1 during the specified time.
- B1 SES - Shows the number of severely errored seconds reported in B1 during the specified time.
- SEFS - Shows the number of severely errored frame seconds reported for the specified section during the specified time.

Example 31 - PERFORMANCE-Retrieve-PM-Start Time

```
Delta-1 94-06-15 23:15:15 EST
M Retrieve-PM-Start Time:All: COMPLD
/* Performance Monitoring Day Bin Start Time Report
=====
Parameter      Value
=====
Start Time     13:00
*/
;
```

The above report shows the start time for measuring the day bins for the performance monitoring data.

Start Time - Shows the provisioned hours:minutes. Hours may be 00 to 23 and minutes is always "00".

Example 32 - PERFORMANCE-Retrieve-PM-STS1**Releases 4 and Later**

```

Delta-1 93-07-15 13:15:15 EST
M Retrieve-Performance Monitoring-STs1:1e-All: COMPLD
/* STS-1 Path Performance Monitoring Status Report
   Bin Start Time: quarter hour registers at yy-mm-dd hh:mm:ss
                   day registers at yy-mm-dd hh:mm:ss
=====
Address      Start  Time  Path (B3)  Far End (FEBE)
              CV-P  ES-P  SES-P  UAS-P      CV-F  ES-F  SES-F  UAS-F
=====
sts1-1e-1-1  Today  3449*   1    0    0         5    1    0    0
              09:00    5    1    0    0         5    1    0    0
=====
sts1-1e-1-2  Today  8912345> 25    2    0    12345  25    2    0
.
.
.
=====
sts1-1e-16-3 Today  12345? 25?   2?   0?   12345? 25?   2?   0?
              09:00    0?   0?   0?   0?     0?   0?   0?   0?
*/
;

      Information below is for Release 4.0 and Release 4.1

/* STS-1 Path Performance Monitoring Status Report
   Last initialized: quarter hour registers at yy-mm-dd hh:mm:ss
                   day registers at yy-mm-dd hh:mm:ss
=====
Address      Start  Time  Path (B3)  Far End (FEBE)
              CV    ES  SES  UAS      CV    ES  SES  UAS
=====

```

The above report (can be a multiple page report) shows the current STS-1 path performance-monitoring data (day and quarter-hour) for an electrical low-speed signal on line 1, slot 1, ports 1, 2, and 3 received from the OC-48 line. Data is collected at the near end (B3) and at the far end (FEBE). When a greater than sign (">") is displayed, it means the violations are greater than the maximum value for the parameter. When a question mark ("?") is displayed, it means corrupted data or an incomplete time cycle for that parameter. When a hyphen ("-") is displayed, it means the data is unavailable, such as any port in the NMON or AUTO state. An asterisk (*) is displayed whenever the threshold-crossing alert (TCA) is reached. This report is not available at the repeater site.

The fields of the report are described below:

- Address - Shows the path name including the line, slot or tributary, and port.
- Start Time - Shows the time in hours and minutes (quarter-hour increments) that data collection started. But if month and day are shown, the data is for yesterday.
- CV - P - Shows the number of code violations reported in the bin during the specified time that will cause a TCA.
- ES - P - Shows the number of errored seconds reported in the bin during the specified time that will cause a TCA.
- SES - P - Shows the number of severely errored seconds reported in the bin during the specified time that will cause a TCA.
- UAS - P - Shows the number of unavailable seconds reported in the bin during the specified time that will cause a TCA.

Release 3

```

TERMB 92-07-15 13:15:15 EST
M Retrieve-Performance Monitoring-STs1:1-1-All: COMPLD
/* STS-1 Path Performance Monitoring Status Report
   Last initialized: quarter hour registers at 92-07-15 13:15:00
                      day registers at 92-07-15 00:00:00
=====
Address      Start      B3      B3      B3      B3
              Time      CV      ES      SES      UAS
=====
sts1-1-1-1   Today      5        1        0        0
              13:15      0        0        0        0
              09:00      5        1        0        0
=====
sts1-1-1-2   Today      0        0        0        0
              13:15      0        0        0        0
=====
sts1-1-1-3   Today      0        0        0        0
              13:15      0        0        0        0
*/
;

```

The above report (can be a multiple page report) shows the current STS-1 path performance-monitoring data for an electrical low-speed signal on line 1, slot 1, ports 1, 2, and 3. When a greater than sign (">") is displayed, it means the violations are greater than the maximum value for the parameter. When a question mark ("?") is displayed, it means corrupted data or an incomplete time cycle for that parameter. When a hyphen ("-") is displayed, it means the data is unavailable, such as any port in the NMON or AUTO state. This report is not available at the repeater site.

The fields of the report are described below:

- Address - Shows the path name including the line, slot or tributary, and port.
- Start Time - Shows the time in hours and minutes (quarter-hour increments) that data collection started. But if month and day are shown, the data is for yesterday.
- B3 CV - Shows the number of code violations reported in B3 during the specified time.
- B3 ES - Shows the number of errored seconds reported in B3 during the specified time.
- B3 SES - Shows the number of severely errored seconds reported in B3 during the specified time.
- B3 UAS - Shows the number of unavailable seconds reported for path during the specified time.

Example 33 - PERFORMANCE-Retrieve-PM-T3**Releases 4 and Later**

```

Delta-1 91-07-15 23:15:15 EST
M Retrieve-Performance Monitoring-T3:1-1A-All: COMPLD
/* DS-3 Performance Monitoring Status Report
   Bin Start Time: quarter hour registers at yy-mm-dd hh:mm:ss
                   day registers at yy-mm-dd hh:mm:ss
=====
Address      Start   -DS3 Line-   ----- DS3 Path -----
              Time    ES-L         CV-P    ES-P    SES-P    SAS-P    UAS-P
=====
ls-1a-1      Today    1*           11      0       5       4       3
(F&M-bit)    09:00      0           0       3       3       3      19
=====
ls-1a-2      Today    25           2       0      45       3      19
(F&M-bit)    09:00      0           0       0       0       0       0
=====
ls-1a-3      Today    0            0       0       0       0       0
(P-bit)     09:00      0           0       0       0       0       0
=====
ls-1b-1      Today    111*         10      0       5       4       3
(P-bit)     09:00      0          80      3       3       3      19
=====
ls-1b-2      Today    25           2       0       5       3      19
(F&M-bit)    09:00      0           0       0       0       0       0
=====
ls-1b-3      Today    1            0       0       0       0       0
(F&M-bit)    09:00      0           0       0       0       0       0
*/
;

Information below is for Release 4.0 and Release 4.1

/* DS-3 Performance Monitoring Status Report
   Last initialized: quarter hour registers at yy-mm-dd hh:mm:ss
                   day registers at yy-mm-dd hh:mm:ss
=====
Address      Start   -DS3 Line-   ----- DS3 Path -----
              Time    ES         CV    ES    SES    SAS    UAS
=====

```

The above report, which can be a multiple page report, shows the current performance-monitoring data (day and quarter-hour) for a T3 (DS3 line and DS3 path). Data is collected from the signal coming from the cross-connections (DS3 line) and from the OC-48 line (DS3 path). When a greater than sign (>) is displayed, it means the violations are greater than the maximum value for the parameter. When a question mark ("?",) is displayed, it means corrupted data or an incomplete time cycle for that parameter. When a hyphen ("-") is displayed, it means the data is unavailable, such as any port in the NMON or AUTO state. An asterisk (*) is displayed whenever the threshold-crossing alert (TCA) is reached. This report is not available at the repeater site.

The fields of the report are described below:

Address	-	Shows the address within the FT-2000 end terminal; for example, low-speed shelf, line 1, slot 1b, and port 1. The address for an FT-2000 add/drop-rings terminal would show the low-speed shelf, slot, and port.
Start Time	-	Shows the time in hours and minutes (quarter-hour increments) that data collection started.
ES-L	-	Shows the number of errored seconds reported in the bin during the specified time that will cause a TCA.
CV-P	-	Shows the number of code violations reported in the bin during the specified time that will cause a TCA.
ES-P	-	Shows the number of errored seconds reported in the bin during the specified time that will cause a TCA.
SES-P	-	Shows the number of severely errored seconds reported in the bin during the specified time that will cause a TCA.
SAS-P	-	Shows the number of severely errored frame/AIS seconds reported in the bin during the specified time that will cause a TCA.
UAS-P	-	Shows the number of unavailable seconds reported in the bin during the specified time that will cause a TCA.

Release 3

```

TERMB 91-07-15 23:15:15 EST
M Retrieve-Performance Monitoring-T3:1-1B-1: COMPLD
/* DS-3 Performance Monitoring Status Report
   Last initialized: quarter hour registers at 91-07-15 23:15:00
                      day registers at 91-07-15 00:00:00
=====
Address      Start      P    CV
              Time
=====
ls-1-1b-1    Today      65535>
              07-14      20
-----
              23:15      0
              16:00      65535>
              15:45      3
              15:30      9
*/
;

```

The above report, which can be a multiple page report, shows the current performance-monitoring data for a T3 (address a DS3 port). This report is the same in a point-to-point network and a ring network except the address (after T3:) is different. When a greater than sign (">") is displayed, it means the violations are greater than the maximum value for the parameter. When a question mark ("?",) is displayed, it means corrupted data or an incomplete time cycle for that parameter. When a hyphen ("-") is displayed, it means the data is unavailable, such as any port in the NMON or AUTO state. This report is not available at the repeater site.

- Address - Shows the address within the FT-2000 end terminal; for example, low-speed shelf, line 1, slot 1b, and port 1. The address for an FT-2000 add/drop-rings terminal would show the low-speed shelf, slot, and port.
- Start Time - Shows the time in hours, minutes, and seconds.
- P CV - Shows the number of performance code violations.

Example 34 - PERFORMANCE-Retrieve-PM-TCA**Releases 4 and Later**

```

Delta-1 93-07-15 23:15:15 EST
M Retrieve-Performance Monitoring-TCA: COMPLD
/* Performance Monitoring Threshold Crossing Alarm Summary Report
=====
Retrieve-PM-* * = Optics Section Line STS1 T3 EC1
=====
line-1e      3      9      0      1      0      0
line-1w      0      3      0      1      0      0
low speed    0      3      3      0      1      9
-----

STS1 * = 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16
-----
sts1-1e-*    - 0 0 0 0 1 - - - - - - - -
sts1-1w-*    - - - - - - - 0 1 - - 0 - 0 0
-----

T3 * = 1a 1b 2a 2b 3a 3b 4a 4b 5a 5b 6a 6b 7a 7b 8a 8b
-----
ls-*         - 0 5 0 0 1 - - 0 1 - - 0 - 0 0
-----

EC1 * = 1a 1b 2a 2b 3a 3b 4a 4b 5a 5b 6a 6b 7a 7b 8a 8b
-----
ls-*         3 - - - - - 0 0 - - 0 0 - 0 - -
-----
line(OC3) 1a 1b 2a 2b 3a 3b 4a 4b 5a 5b 6a 6b 7a 7b 8a 8b
-----
ls-*         - - - - 3 0 - - - - - - - - -
-----
sect(OC3) 1a 1b 2a 2b 3a 3b 4a 4b 5a 5b 6a 6b 7a 7b 8a 8b
-----
ls-*         - - - - 3 0 - - - - - - - - -
-----
optic(OC3) 1a 1b 2a 2b 3a 3b 4a 4b 5a 5b 6a 6b 7a 7b 8a 8b
-----
ls-*         - - - - 0 0 - - - - - - - - -
*/
;

```

The above report, which can be a multiple page report, shows a summary of TCAs reported over the past 2 days. Detailed information on a specific TCA (optics, section, line, STS1, T3, and EC1) may be obtained by using the **PERFORMANCE-Retrieve-PM-TCA** command. When a hyphen ("-") is displayed, it means the data is unavailable.

The fields of the report are described below:

- Retrieve-PM - Shows the address for high-speed (line), low-speed (slot), or path (STS3:STS1 tributaries).
- Optics - Shows the total number of TCAs reported for PM-optics.
- Section - Shows the total number of TCAs reported PM-section.
- Line - Shows the total number of TCAs reported PM-line.
- STS1 - Shows the total number of TCAs reported PM-STs1.
- T3 - Shows the total number of TCAs reported PM-T3.
- EC1 - Shows the total number of TCAs reported PM-EC1.

Example 35 - PERFORMANCE-Retrieve-PM Threshold-EC1 (Release 7 and Later)

```

Delta-1 91-07-15 23:15:15 EST
M Retrieve-PM Threshold-EC1:1a:All: COMPLD
/* EC1 Performance Monitoring Threshold Value Report
=====
      Section  ----- EC1 Line (B2) -----
Address  Bin    SEFS-S  BER    CV-L    ES-L    SES-L    UAS-L
=====
1s-1a-1  QH      5      -9    900      25      10      50
          Day    10      -9    900      250     10      50
1s-1a-2  QH      5      -9    900      25      10      50
          Day    10      -9    900      250     10      50
1s-1a-3  QH      5      -9    900      25      10      50
          Day    10      -9    900      250     10      50

/* EC1 TCA Autonomous Reporting Status Report
=====
      Section  ----- EC1 Line (B2) -----
      TCA      TCA      TCA      TCA      TCA      TCA      TCA
Address  Bin    Report  Report  Report  Report  Report  Report
=====
1s-1a-1  QH  Enable  Enable  Enable  Disable  Disable  Disable
          Day  Enable  Enable  Enable  Disable  Disable  Disable
1s2-1a-2  QH  Enable  Enable  Enable  Disable  Disable  Disable
          Day  Enable  Enable  Enable  Disable  Disable  Disable
1s2-1a-3  QH  Enable  Enable  Enable  Disable  Disable  Disable
          Day  Enable  Enable  Enable  Disable  Disable  Disable
*/

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the EC1 low-speed line. This report is not available at the repeater site.

The fields of the report are described below:

Address	- Shows the specific slot and port for the EC-1 line.
Bin	- Shows the quarter-hour (QH) and day bins.
Section SEFS-S	- Shows the provisioned number of section severely errored frame seconds for the QH bin and the day bin that will cause a TCA.
BER	- Shows the provisioned bit error rate; for example, -9 is 10^{-9} , for the QH bin and day bin.
CV-L	- Shows the number of line code violations reported during the specified time that exceeded the BER setting.
ES-L	- Shows the provisioned number of line errored seconds for the QH bin and the day bin that will cause a TCA.
SES-L	- Shows the provisioned number of line severely errored seconds for the QH bin and the day bin that will cause a TCA.
UAS-L	- Shows the provisioned number of unavailable seconds for the QH bin and the day bin that will cause a TCA.
TCA Report	- Shows if the automatic reporting of TCAs is enabled or disabled.

Releases 4, 5, and 6 (Use the PERFORMANCE category.)

```

Delta-1 91-07-15 23:15:15 EST
M Retrieve-PM Threshold-EC1:1a:All: COMPLD
/* EC1 Performance Monitoring Threshold Value Report
=====
              TCA      Section  ----- EC1 Line (B2) -----
Address      Bin Report      SEFS-S   BER    CV-L ES-L  SES-L   UAS-L
=====
ls-1a-1      QH  Enable       5        -9    900   25   10    50
              Day Disable    10        -9    900  250   10    50
ls-1a-2      QH  Enable       5        -9    900   25   10    50
              Day Enable     10        -9    900  250   10    50
ls-1a-3      QH  Enable       5        -9    900   25   10    50
              Day Enable     10        -9    900  250   10    50

      Information below is for Release 4.0 and Release 4.1

/* EC1 Performance Monitoring Threshold Value Report
=====
              TCA      Section  ----- EC1 Line (B2) -----
Address      Bin Report      SEFS   BER    CV    ES    SES    UAS
=====
*/

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the EC1 low-speed line. This report is not available at the repeater site.

- Address - Shows the specific slot and port for the EC-1 line.
- Bin - Shows the quarter-hour (QH) and day bins.
- TCA Report - Shows if the automatic reporting of TCAs is enabled or disabled.
- Section SEFS-S - Shows the provisioned number of section severely errored frame seconds for the QH bin and the day bin that will cause a TCA.
- BER - Shows the provisioned bit error rate; for example, -9 is 10^{-9} , for the QH bin and day bin.
- CV-L - Shows the number of line code violations reported during the specified time that exceeded the BER setting.
- ES-L - Shows the provisioned number of line errored seconds for the QH bin and the day bin that will cause a TCA.
- SES-L - Shows the provisioned number of line severely errored seconds for the QH bin and the day bin that will cause a TCA.
- UAS-L - Shows the provisioned number of unavailable seconds for the QH bin and the day bin that will cause a TCA.

Example 36 - PERFORMANCE-Retrieve-PM Threshold-Line**Releases 7 and Later** (Use the **PERFORMANCE** category.)

```

Delta-1 93-01-15 13:15:15 EST
M Retrieve-PM Threshold-Line:OC48:All: COMPLD
/* OC-48 Line Performance Monitoring Threshold Value Report
=====
----- OC-48 Line (B2) -----
Address      Bin  BER      CV-L      ES-L      SES-L      UAS-L
=====
line-1e     QH   -9      9000      25        10         10
             Day  -9      9000      250       10         10
line-1w     QH   -9      9000      25        10         10
             Day  -9      9000      250       10         10
*/
;

/* OC-48 Line TCA Autonomous Reporting Status Report
=====
----- OC-48 Line (B2) -----
Address      Bin  BER      TCA Report CV      ES      SES      UAS      TCA Report
=====
line-1e     QH   Enable   Enable   Disable Disable Disable Disable
             Day  Enable   Enable   Disable Disable Disable Disable
line-1w     QH   Enable   Enable   Disable Disable Disable Disable
             Day  Enable   Enable   Disable Disable Disable Disable
*/

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the OC-48 lines. This report is not available at the repeater site.

Address	-	Shows the specific OC-48 line.
Bin	-	Shows the quarter-hour (QH) and day bins.
BER	-	Shows the provisioned bit error rate; for example, -9 is 10 ⁻⁹ , for the QH bin and Day bin.
CV-L	-	Shows the number of line code violations reported during the specified time that exceeded the BER setting.
ES-L	-	Shows the provisioned number of line errored seconds for the QH bin and the Day bin that will cause a TCA.
SES-L	-	Shows the provisioned number of line severely errored seconds for the QH bin and the Day bin that will cause a TCA.
UAS-L	-	Shows the provisioned number of unavailable seconds for the QH bin and the Day bin that will cause a TCA.
TCA Report	-	Shows if the automatic reporting of TCAs is enabled or disabled.

Releases 4, 5, and 6 (Use the **PERFORMANCE** category.)

```

Delta-1 93-01-15 13:15:15 EST
M Retrieve-PM Threshold-Line:OC48:All: COMPLD
/* OC-48 Line Performance Monitoring Threshold Value Report
=====
Address      Bin      TCA      ----- OC-48 Line (B2) -----
Report      BER      CV-L      ES-L      SES-L      UAS-L
=====
line-1e      QH      Disable  -9      9000      25      10      10
              Day      Enable  -9      9000      250     10      10
line-1w      QH      Enable  -9      9000      25      10      10
              Day      Disable -9      9000      250     10      10
*/
;

      Information below is for Release 4.0 and Release 4.1

/* OC-48 Line Performance Monitoring Threshold Value Report
=====
Address      Bin      TCA      ----- OC-48 Line (B2) -----
Report      BER      CV      ES      SES      UAS
=====

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the OC-48 lines. This report is not available at the repeater site.

- Address - Shows the specific OC-48 line.
- Bin - Shows the quarter-hour (QH) and day bins.
- TCA Report - Shows if the automatic reporting of TCAs is enabled or disabled.
- BER - Shows the provisioned bit error rate; for example, -9 is 10^{-9} , for the QH bin and Day bin.
- CV-L - Shows the number of line code violations reported during the specified time that exceeded the BER setting.
- ES-L - Shows the provisioned number of line errored seconds for the QH bin and the Day bin that will cause a TCA.
- SES-L - Shows the provisioned number of line severely errored seconds for the QH bin and the Day bin that will cause a TCA.
- UAS-L - Shows the provisioned number of unavailable seconds for the QH bin and the Day bin that will cause a TCA.

Example 37 - PERFORMANCE-Retrieve-PM Threshold-Optics

Releases 7 and Later (Use the **PERFORMANCE** category.)

```
Delta-1 91-07-15 23:15:15 EST
M Retrieve-PM Threshold-Optics:OC48:All: COMPLD
/* OC-48 Optics TCA Autonomous Reporting Status Report
=====
Address      Bin      TCA Report TX-Pwr  TCA Report Rcv-Pwr  TCA Report LBC
=====
line-1e      QH      Enable    Enable   Enable
              Day    Disable   Disable  Disable
line-1w      QH      Enable    Enable   Enable
              Day    Disable   Disable  Disable
* /
```

- Address - Shows the specific optical line.
- Bin - Shows the quarter-hour (QH) and day bins.
- TX-Pwr - Shows if the automatic reporting of TCAs is enabled or disabled for laser transmitted power.
- Rcv-Pwr - Shows if the automatic reporting of TCAs is enabled or disabled for laser received power.
- LBC - Shows if the automatic reporting of TCAs is enable or disabled for laser bias current.

Releases 4, 5, and 6 (Use the PERFORMANCE category.)

```

Delta-1 91-07-15 23:15:15 EST
M Retrieve-PM Threshold-Optics:OC48:All: COMPLD
/* OC-48 Optics Performance Monitoring Threshold Value Report
=====
                        TCA
Address      Bin  Report
=====
line-1e     QH   Enable
             Day  Disable
line-1w     QH   Enable
             Day  Disable
* /

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the transmitter and receiver optics. This report is not available at the repeater site.

- Address - Shows the specific optical line.
- Bin - Shows the quarter-hour (QH) and day bins.
- TCA Report - Shows if the automatic reporting of TCAs is enabled or disabled.

Example 38 - PERFORMANCE-Retrieve-PM Threshold-Section

(Releases 7 and Later)

```

Delta-1 93-01-15 13:15:15 EST
M Retrieve-PM Threshold-Section:All:  COMPLD
/* STS-48 Section Performance Monitoring Threshold Value Report
=====
Address      Bin      BER      CV-S      ES-S      SES-S      SEFS-S
=====
line-1e     QH      -9      1000      25      10      5
            Day      -9      1000      3      250      50
line-1w     QH      -9      1000      25      10      5
            Day      -9      1000      3      250      50
*/
;

/* STS-48 TCA Autonomous Reporting Status Report
=====
TCA      STS-48 Section (B1)
TCA Report TCA Report TCA Report TCA Report TCA Report
Address    Bin  BER  CV-S  ES-S  SES-S  SEFS-S
=====
line-1e   QH   Enable Disable Enable Disable Disable
            Day   Enable Disable Enable Disable Disable
line-1w   QH   Enable Disable Enable Disable Disable
            Day   Enable Disable Enable Disable Disable
*/

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the STS-48 section. This report is not available at the repeater site.

The fields of the report are described below:

Address	-	Shows the specific OC-48 line.
Bin	-	Shows the quarter-hour (QH) and the day bins.
TCA Report	-	Shows if the automatic reporting of TCAs is enabled or disabled.
BER	-	Shows the provisioned bit error rate for the QH and day bins that will cause a TCA.
CV-S	-	Shows the provisioned number of code violations for the QH and day bins that will cause a TCA.
ES-S	-	Shows the provisioned number of errored seconds for the QH and day bins that will cause a TCA.
SES-S	-	Shows the provisioned number of severely errored seconds for the QH and day bins that will cause a TCA.
SEFS-S	-	Shows the provisioned number of section errored frame seconds for the QH and day bins that will cause a TCA.
TCA Report	-	Shows if the automatic reporting of TCAs is enabled or disabled.

Releases 4, 5, and 6 (Use the **PERFORMANCE category.)**

```

Delta-1 93-01-15 13:15:15 EST
M Retrieve-PM Threshold-Section:All: COMPLD
/* STS-48 Section Performance Monitoring Threshold Value Report
=====
Address      Bin  TCA      Report  BER      CV-S      ES-S      SES-S      SEFS-S
=====
line-1e      QH  Enable   -9       1000      25        10         5
              Day  Enable   -9       1000      3         250        50
line-1w      QH  Disable  -9       1000      25        10         5
              Day  Disable  -9       1000      3         250        50
*/
;

      Information below is for Release 4.0 and Release 4.1

/* STS-48 Section Performance Monitoring Threshold Value Report
=====
Address      Bin  TCA      Report  BER      CV      ES      SES      SEFS
=====

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the STS-48 section. This report is not available at the repeater site.

Address	-	Shows the specific OC-48 line.
Bin	-	Shows the quarter-hour (QH) and the day bins.
TCA Report	-	Shows if the automatic reporting of TCAs is enabled or disabled.
BER	-	Shows the provisioned bit error rate for the QH and day bins that will cause a TCA.
CV-S	-	Shows the provisioned number of code violations for the QH and day bins that will cause a TCA.
ES-S	-	Shows the provisioned number of errored seconds for the QH and day bins that will cause a TCA.
SES-S	-	Shows the provisioned number of severely errored seconds for the QH and day bins that will cause a TCA.
SEFS-S	-	Shows the provisioned number of section errored frame seconds for the QH and day bins that will cause a TCA.

Example 39 - PERFORMANCE-Retrieve-PM Threshold-STs1**Releases 7 and Later** (User the **PERFORMANCE** category.)

```

/* STS-1 Path Performance Monitoring Threshold Value Report
=====
----- STS-1 Path (B3)/FEBE -----
Address      Bin  BER  CV-P/F  ES-P/F  SES-P/F  UAS-P/F
=====
sts1-1e-1-1  QH   -9   1000    25     10      10
              Day  -9   1000   250     10      10
sts1-1e-1-2  QH   -9   1000    25     10      10
              Day  -9   1000   250     10      10
sts1-1e-1-3  QH   -9   1000    25     10      10
              Day  -9   1000   250     10      10
sts1-1e-2-1  QH   -9   1000    25     10      10
              Day  -9   1000   250     10      10
sts1-1e-2-2  QH   -9   1000    25     10      10
              Day  -9   1000   250     10      10
              .
              .
              .
sts1-1e-16-3 QH   -9   1000    25     10      10
              Day  -9   1000   250     10      10
*/
:

/* STS-1 Path TCA Autonomous Reporting Status Report
=====
TCA Report TCA Report TCA Report TCA Report TCA Report
Address      Bin  BER      CV-P/F  ES-P/F  SES-P/F  UAS-P/F
=====
sts1-1e-1-1  QH  Enable   Enable   Disable  Disable   Disable
              Day  Enable   Enable   Disable  Disable   Disable
sts1-1e-1-2  QH  Enable   Enable   Disable  Disable   Disable
              Day  Enable   Enable   Disable  Disable   Disable
sts1-1e-1-3  QH  Enable   Enable   Disable  Disable   Disable
              Day  Enable   Enable   Disable  Disable   Disable
sts1-1e-2-1  QH  Enable   Enable   Disable  Disable   Disable
              Day  Enable   Enable   Disable  Disable   Disable
sts1-1e-2-2  QH  Enable   Enable   Disable  Disable   Disable
              Day  Enable   Enable   Disable  Disable   Disable
              .
              .
              .
sts1-1e-16-3 QH  Enable   Enable   Disable  Disable   Disable
              Day  Enable   Enable   Disable  Disable   Disable
*/

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the STS-1 path. This report is not available at the repeater site.

The fields of the report are described below:

Address	-	Shows the specific OC-48 line, STS-3 tributary, and STS-1 tributary for the STS-1 path.
Bin	-	Shows the quarter-hour (QH) and the day bins.
BER	-	Shows the provisioned bit error rate for the QH and day bins that will cause a TCA.
CV - P/F	-	Shows the provisioned number of code violations for the QH and day bins that will cause a TCA.
ES - P/F	-	Shows the provisioned number of errored seconds for the QH and day bins that will cause a TCA.
SES - P/F	-	Shows the provisioned number of severely errored seconds for the QH and day bins that will cause a TCA.
UAS - P/F	-	Shows the provisioned number of unavailable seconds for the QH and day bins that will cause a TCA.
TCA Report	-	Shows if the automatic reporting of TCAs is enabled or disabled.

Releases 4, 5, and 6 (User the **PERFORMANCE category.)**

```

/* STS-1 Path Performance Monitoring Threshold Value Report
=====
Address      TCA      STS-1 Path (B3)/FEBE
              Bin Report  BER  CV-P/F  ES-P/F  SES-P/F  UAS-P/F
=====
sts1-1e-1-1  QH  Enable  -9   1000   25     10     10
              Day  Enable  -9   1000   250    10     10
sts1-1e-1-2  QH  Enable  -9   1000   25     10     10
              Day  Enable  -9   1000   250    10     10
sts1-1e-1-3  QH  Enable  -9   1000   25     10     10
              Day  Enable  -9   1000   250    10     10
*/
:

      Information below is for Release 4.0 and Release 4.1

/* STS-1 Path Performance Monitoring Threshold Value Report
=====
Address      TCA      STS-1 Path (B3)/FEBE
              Bin Report  BER   CV   ES   SES   UAS
=====

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the STS-1 path. This report is not available at the repeater site.

The fields of the report are described below:

- Address - Shows the specific OC-48 line, STS-3 tributary, and STS-1 tributary for the STS-1 path.
- Bin - Shows the quarter-hour (QH) and the day bins.
- TCA Report - Shows if the automatic reporting of TCAs is enabled or disabled.
- BER - Shows the provisioned bit error rate for the QH and day bins that will cause a TCA.
- CV-P/F - Shows the provisioned number of code violations for the QH and day bins that will cause a TCA.
- ES-P/F - Shows the provisioned number of errored seconds for the QH and day bins that will cause a TCA.
- SES-P/F - Shows the provisioned number of severely errored seconds for the QH and day bins that will cause a TCA.
- UAS-P/F - Shows the provisioned number of unavailable seconds for the QH and day bins that will cause a TCA.

Example 40 - PERFORMANCE-Retrieve-PM Threshold-T3**Releases 7 and Later** (Use the **PERFORMANCE** category.)

```

/* DS-3 Performance Monitoring Threshold Value Report
=====
DS3 Line
Address  Bin ES-L  BER      CV-P  ES-P  SES-P  SAS-P  UAS-P
=====
ls-1a-1  QH   5    -9     500    25    10     50     5
          Day  3    -9     500   250    10     50     3
ls-1a-2  QH   5    -9     500    25    10     50     5
          Day  3    -9     500   250    10     50     3
ls-1a-3  QH   5    -9     500    25    10     50     5
          Day  3    -9     500   250    10     50     3
ls-1b-1  QH   5    -9     500    25    10     50     5
          Day  3    -9     500   250    10     50     3
.        .    .    .      .      .      .      .      .
ls-8a-3  QH   5    -9     500    25    10     50     5
          Day  3    -9     500   250    10     50     3
*/
;

TCA RptTCA Rpt TCA Rpt TCA Rpt TCA Rps TCA Rpt TCA Rpt
1s-1a-1  QH   Enable  Enable  Enable  Enable  Enable  Enable  Enable  Enable
          Day  Disable Disable Disable Disable Disable Disable Disable Disable
1s-1a-2  QH   Enable  Enable  Enable  Enable  Enable  Enable  Enable  Enable
          Day  Disable Disable Disable Disable Disable Disable Disable Disable
1s-1a-3  QH   Enable  Enable  Enable  Enable  Enable  Enable  Enable  Enable
          Day  Disable Disable Disable Disable Disable Disable Disable Disable
1s-1b-1  QH   Enable  Enable  Enable  Enable  Enable  Enable  Enable  Enable
          Day  Disable Disable Disable Disable Disable Disable Disable Disable
1s-1b-2  QH   Enable  Enable  Enable  Enable  Enable  Enable  Enable  Enable
          Day  Disable Disable Disable Disable Disable Disable Disable Disable
.        .    .    .      .      .      .      .      .
1s-8b-3  QH   Enable  Enable  Enable  Enable  Enable  Enable  Enable  Enable
          Day  Disable Disable Disable Disable Disable Disable Disable Disable
*/

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the DS3 line and path. This report is not available at the repeater site.

The fields of the report are described below:

Address	- Shows the specific DS3 low-speed slot and port.
Bin	- Shows the quarter-hour (QH) and the day bins.
DS3 Line ES-L	- Shows the provisioned number of line errored seconds for the QH and day bins that will cause a TCA.
BER	- Shows the provisioned bit error rate for the QH and day bins that will cause a TCA.
CV-P	- Shows the provisioned number of code violations for the QH and day bins that will cause a TCA.
ES-P	- Shows the provisioned number of errored seconds for the QH and day bins that will cause a TCA.
SES-P	- Shows the provisioned number of severely errored seconds for the QH and day bins that will cause a TCA
SAS-P	- Shows the provisioned number of severely errored frame/AIS seconds for the QH and day bins that will cause a TCA.
UAS-P	- Shows the provisioned number of unavailable seconds for the QH and day bins that will cause a TCA.
TCA Report	- Shows if the automatic reporting of TCAs is enabled or disabled.

Releases 4, 5, and 6 (Use the PERFORMANCE category.)

```

/* DS-3 Performance Monitoring Threshold Value Report
=====
Address      Bin  TCA  DS3 Line  BER  CV-P  ES-P  SES-P  SAS-P  UAS-P
=====
ls-1a-1      QH  Enable  5      -9    500   25    10     50     5
              Day Enable  3      -9    500   250   10     50     3
ls-1a-2      QH  Enable  5      -9    500   25    10     50     5
              Day Disable 3      -9    500   250   10     50     3
ls-1a-3      QH  Disable 5      -9    500   25    10     50     5
              Day Disable 3      -9    500   250   10     50     3
*/
;

```

The report shows the current performance-monitoring threshold crossing alert (TCA) values for the DS3 line and path. This report is not available at the repeater site.

The fields of the report are described below:

Address	-	Shows the specific DS3 low-speed slot and port.
Bin	-	Shows the quarter-hour (QH) and the day bins.
TCA Report	-	Shows if the automatic reporting of TCAs is enabled or disabled.
DS3 Line ES-L	-	Shows the provisioned number of line errored seconds for the QH and day bins that will cause a TCA.
BER	-	Shows the provisioned bit error rate for the QH and day bins that will cause a TCA.
CV-P	-	Shows the provisioned number of code violations for the QH and day bins that will cause a TCA.
ES-P	-	Shows the provisioned number of errored seconds for the QH and day bins that will cause a TCA.
SES-P	-	Shows the provisioned number of severely errored seconds for the QH and day bins that will cause a TCA.
SAS-P	-	Shows the provisioned number of severely errored frame/AIS seconds for the QH and day bins that will cause a TCA.
UAS-P	-	Shows the provisioned number of unavailable seconds for the QH and day bins that will cause a TCA.

Example 41 - CONFIGURATION-Retrieve-Protection-Line**Releases 4 and Later** (Use the **CONFIGURATION** category)

```

Delta-1  93-01-19 23:15:15 EST
M Retrieve-Protection-Line:  COMPLD
/* Network Element Protection Switching Provisioning Report
=====
Parameter                                Value
=====
Operating Mode                          2 fiber ring    bi-directional
Wait To Restore                         0 (disabled)
*/

      Information below is a Release 4.0 1x1 end terminal example
Operating Mode                          1x1 bi-directional
Provisional Mode                        1x1 bi-directional
Wait To Restore                         3 minutes
Restoration Protection Access           no
*/
;

```

The first report shows the provisioned protection switching information of the FT-2000 add/drop-rings terminal. The second report shows a Release 4.0 1x1 end terminal report.

Operating Mode	-	Shows the add/drop-rings terminal configuration and whether protection switching is unidirectional or bidirectional.
Wait To Restore	-	Shows the time (up to 12 minutes after a trouble clears) before breaking automatic high-speed, low-speed, and synchronization reference protection switches. A value of zero minutes will disable this parameter.
Provisioned Mode	-	For 1x1 end terminal only. Shows the current provisioned protection switching configuration and whether protection switching is unidirectional or bidirectional.
Restoration Protection Access	-	For 1x1 end terminal only. Shows the parameter value (either yes or no) if the FT-2000 terminal is provisioned for restoration protection access.

Release 3 (Use the **CONFIGURATION** category.)

```
Delta-1 93-01-19 23:15:15 EST
M Retrieve-Protection-Line: COMPLD
/* Network Element Protection Switching Provisioning Report
=====
Parameter                               Value
=====
Operating Mode                          2 fiber ring bi-directional
Wait To Restore                         0 (disabled)
*/
;
```

The above report shows the provisioned protection switching information of the FT-2000 add/drop-rings terminal.

Operating Mode	-	Shows the add/drop-rings terminal configuration and whether protection switching is unidirectional or bidirectional.
Wait To Restore	-	Shows the time (up to 12 minutes after a trouble clears) before breaking automatic high-speed, low-speed, and synchronization reference protection switches.

Release 2 (Use the **CONFIGURATION** category.)

```

TERMB 92-01-19 23:15:15 EST
M Retrieve-Protection-Line: COMPLD
/* Network Element Protection Switching Provisioning Report
=====
Parameter                               Value
=====
Operating Mode                          1x1 bi-directional
Wait To Restore                         5 minutes
Restoration Protection Access           no
*/
;

```

The above report shows the provisioned protection switching information for the near-end FT-2000 terminal.

- Operating Mode - Shows the current operating protection switching configuration (1x1 or 1+1) and the direction (bidirectional or unidirectional).
- Wait To Restore - Shows the time in minutes to wait after a trouble clears before dropping the automatic protection switches (both low-speed and high-speed).
- Restoration Protection Access - Shows the parameter value (either yes or no) if the FT-2000 terminal is provisioned for restoration protection access.

Example 42 - CONFIGURATION-Retrieve-Protection-Low Speed

Releases 4 and Later (Use the **CONFIGURATION** category.)

```
Delta-1 93-01-19 23:15:15 EST
M Retrieve-Protection-Low Speed: COMPLD
/* Low Speed Protection Switching Provisioning Report
=====
Parameter                               Value
=====
Wait To Restore                         3 minutes
*/
;
```

The report shows the provisioned low-speed protection switching information of the FT-2000 network element.

Wait To Restore - Shows the time (up to 12 minutes after a trouble clears) before breaking the automatic low-speed switch. A value of zero minutes will disable this parameter.

Example 43 - SECURITY-Retrieve-Security-Network Element**Releases 4.1 and Later** (Use the **SECURITY** category.)

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Security-Network Element: COMPLD
/* Access Security Global Configuration Report
=====
Parameter                      Value
=====
Logins Allowed                  yes
Password Aging                  60 days
Login Aging                     60 days
*/
;

```

The above report shows global security information for the local FT-2000 network element.

- Logins Allowed - Shows if nonprivileged users are allowed to log in or not. Privileged users are always allowed to log in.
- Password Aging - For Release 4.1 and later. Shows the lifetime interval in days before the user must select a new password. A zero interval will disable this parameter.
- Login Aging - For Release 5.0 and later. Shows the lifetime interval in days before the user must select a new login. A zero interval will disable this parameter.

Releases 2, 3, and 4.0 (Use the **SECURITY** category.)

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Security-Network Element: COMPLD
/* Access Security Global Configuration Report
=====
Parameter                      Value
=====
Logins Allowed                  yes
*/
;

```

The above report shows global security information for the local FT-2000 network element.

- Logins Allowed - Shows if nonprivileged users are allowed to log in or not. Privileged users are always allowed to log in.

Example 44 - SECURITY-Retrieve-Security-Port**Releases 5.0 and Later**

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Security-Port: COMPLD
/* Access Port Security Configuration Report
=====
Access      Port      Port Type/   Baud Rate/   Inactive
Port        Channel  Status      OS Type      Calling Address  Timer
=====
dce          enabled   cit          auto          10
dte          enabled   tl1          9600          15
dcc  vc-0    disabled   cit          10
-----
x25  pvc-1   memry-admin
x25  pvc-2   maintenance
-----
x25  tbl-1   cmd-resp    19089492810
x25  tbl-2   cmd-resp    12125551212
x25  tbl-3   cmd-resp    19142348217
x25  tbl-4   cmd-resp    -
x25  tbl-5   cmd-resp    -
x25  tbl-6   cmd-resp    -
*/
;

```

The above report shows the current CIT and TL1 port provisioning and who is currently logged in on each port.

Access Port	- Shows a specific operations interface (physical port) for which the status is being displayed.
Channel	- Shows the TL1 calling address table entry and may be a virtual channel (vc)-0, a permanent virtual channel (pvc) 1 or 2, or table entry 1 through 6. A blank (" ") is always reported for the dte and dce ports. A pvc -1 or -2 or a table entry 1 through 6 is reported for an x.25 port.
Port Status	- Shows if the port will or will not accept nonprivileged user logins (enabled or disabled).
Port Type OS Type	- Shows the protocol used (cit or tl1) on the port and displays the autonomous message set reported on the port as "maintenance," "memry-admin," "cmd-resp," or "other."
Baud Rate	- Shows the provisioned baud rate as blank, auto, 9600, 4800, 2400, or 1200.
Calling Address	- Shows the value of the x.25 address key used to associate the port type with the user.
Inactive Timer	- Shows the inactivity timeout in minutes (0 to 999).

Releases 4.1 and Earlier

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Security-Port: COMPLD
/* Access Port Security Configuration Report
=====
Access      Port      Current
Port        Status    Login
=====
cit-dce     enabled   luc01
cit-dte     enabled   luc02
cit-dcc     enabled   -
*/
;

```

This report shows access and timeout information for different ports. The top two entries are enabled for access and they are accessed by luc01 and luc02, respectively. The hyphen ("-") indicates that no current access over the DCC is being made to the network element. A report for an FT-2000 repeater bay/shelf is the same except the "Access Port" entry (cit-dcc) does not exist.

- Access Port - Shows the identity of the port (operations interface).
- Port Status - Shows if the port will or will not accept nonprivileged user logins (enabled or disabled).
- Current Login - Shows the login of the user that is currently logged on the port.

Example 45 - CONFIGURATION-Retrieve-Squelch Map-STS1**(Release 6 and earlier)**

```

Delta-1 94-06-15 23:15:15 EST
M Retrieve-Squelch Map:STS1:All: COMPLD
/* STS-1 Squelch Map Report
=====
Source/Destination      NID      West      East      NID      Destination/Source
=====
Worcester-MA           2  -->    1w-1-1    1e-1-1    -->    1      *Chicopee-MA
Worcester-MA           2  <--    1w-1-1    1e-1-1    <--   15      Springfield-MA
Worcester-MA           2  -->    1w-1-2    1e-1-2    -->    1      *Chicopee-MA
Worcester-MA           2  <--    1w-1-2    1e-1-2    <--   15      Springfield-MA
Worcester-MA           2  -->    1w-1-3    1e-1-3    -->    1      *Chicopee-MA
Worcester-MA           2  <--    1w-1-3    1e-1-3    <--    1      *Chicopee-MA
-                       -          1w-2      1e-2      -
West-Ossipee-NH        4  -->    1w-3-1    1e-3-1    -->    3      Rutland-VT
West-Ossipee-NH        4  <--    1w-3-1    1e-3-1    <--    3      Rutland-VT
West-Ossipee-NH        4  -->    1w-3-2    1e-3-2    -->    3      Rutland-VT
West-Ossipee-NH        4  <--    1w-3-2    1e-3-2    <--    3      Rutland-VT
West-Ossipee-NH        4  -->    1w-3-3    1e-3-3    -->    3      Rutland-VT
West-Ossipee-NH        4  <--    1w-3-3    1e-3-3    <--    3      Rutland-VT
Boscawen-NH*           ?  -->    1w-4-1    1e-4-1    -->    5      Keene-NH
Boscawen-NH*           ?  <--    1w-4-1    1e-4-1    <--    5      Keene-NH
-                       -  -->    1w-4-2    1e-4-2    -->    7      Suncook-NH
-                       -  <--    1w-4-2    1e-4-2    <--    7      Suncook-NH
-                       -  <---    1w-4-3    1e-4-3    -->    -
-                       -  -->    1w-4-3    1e-4-3    <--    -
-                       -          1w-5      1e-5      -
-                       -          .          .          .
-                       -          .          .          .
-                       -          1w-16    1e-16    -
*/
;

```

The above report shows the STS-1 level squelch map that exists in the FT-2000 add/drop-rings terminal. Examples of cross-connections and additional information are provided in Volume I, Section 8.

Source/ Destination	-	Shows the target identifier (TID) and may be either the source node or the destination node. A flag (* or a blank) is added to the TID to indicate a legal combination (TID/NID) in the DIB.
NID	-	Shows the node ID from 0 to 15 of the squelch map.
West	-	Shows the STS-1 tributary address for a valid entry and shows the STS-3 tributary address for a null (no) entry.
East	-	Shows the STS-1 tributary address for a valid entry and shows the STS-3 tributary address for a null (no) entry.

Example 46 - CONFIGURATION-Retrieve-Squelch Map-STS3**(Release 6 and earlier)**

```

Delta-1 94-06-15 23:15:15 EST
M Retrieve-Squelch Map:STS3:All: COMPLD
/* STS-3 Squelch Map Report
=====
Source/Destination      NID      West      East      NID      Destination/Source
=====
      Worcester-MA      2  -->    1w-1      1e-1  -->    1 *Chicopee-MA
      Worcester-MA      2  <--    1w-1      1e-1  <--   15 Springfield-MA
                                     -          1w-2      1e-2      -
      West-Ossipee-NH    4  <--    1w-3      1e-3  <--    3 Rutland-VT
      West-Ossipee-NH    4  -->    1w-3      1e-3  -->    3 Rutland-VT
      Boscawen-NH*      ?  -->    1w-4      1e-4  -->    5 Keene-NH
      Boscawen-NH*      -  <--    1w-4      1e-4  <--    5 Keene-NH
                                     -          1w-5      1e-5      -
                                     -          1w-6      1e-6      -
                                     -          1w-7      1e-7      -
                                     -          1w-8      1e-8      -
                                     -          1w-9      1e-9      -
                                     -          1w-10     1e-10     -
                                     -          1w-11     1e-11     -
                                     -          1w-12     1e-12     -
                                     -          1w-13     1e-13     -
                                     -          1w-14     1e-14     -
                                     -          1w-15     1e-15     -
                                     -          1w-16     1e-16     -
      */
;

```

The above report shows the STS-3 level squelch map that exists in the FT-2000 add/drop-rings terminal. Examples of cross-connections and additional information are provided in Volume I, Section 8. The fields of the report are described below:

Source/ Destination	- Shows the target identifier (TID) and may be either the source node or the destination node. A dash is shown when a tributary is not cross-connected. A question mark (?) is shown when a tributary is cross-connected to a null (no) source/destination TID. An asterisk is shown for invalid TID/NID pairs.
NID	- Shows the node ID from 0 to 15 of the squelch map. A dash is shown when the tributary is not cross-connected. A question mark is shown when a tributary is cross-connected to an incorrect source or destination TID.
West	- Shows the STS-3 tributary address (line direction and tributary).
East	- Shows the STS-3 tributary address (line direction and tributary).

Example 47 - FAULT-Retrieve-State**Releases 4.1 and Later**

```
Delta-1 93-01-19 23:15:15 EST
M Retrieve-State:All: COMPLD
/* State Report
=====
Address          Circuit      Port State   Switch  -Switch  Priority-
                  Pack        DSX  OC48    State   Active   Deferred
=====
DS1-in-pri              i         active
DS1-in-sec              i         standby
-----
tg-1          TG3          active
tg-2          TG3          active
-----
trmtr-1e      TRMTR          active
trmtr-1w      TRMTR          active
-----
rcvr-1e       RCVR          active
rcvr-1w       RCVR          active
-----
Continue report page-by-page?(y/n)Yes
```

The State Report shows a normal state at an FT-2000 Add/Drop-Rings Terminal, meaning that there are no active protection switches, inhibits, lockouts, or other abnormal conditions.

The fields of the report are described below:

Address	- Shows the address by slot function, high-speed line designation, and physical slot location.
Circuit Pack	- Shows the functional name of the circuit pack.
PortState DSX OC48	- DSX and OC48 Port States show the state of the port in the direction from the DSX and the OC-48 line, respectively. The state of multiple ports (1, 2, and 3) is shown as an i for the IS state, an a for the AUTO state, an n for the NMON state, a dash ("-") for not applicable (when the slot is not equipped), and a blank for the timing packs and the high-speed interfaces. The port state is listed left to right in ascending order.
Switch State	- Shows whether the circuit pack or DS1 reference input is in an active state (by carrying traffic or by providing timing) or in a standby state (by not carrying traffic or by not providing timing). For OC3 circuit packs, the parameter indicates the state for the transmit and receive OC3 lines.
Switch Priority	- Active switch priority shows the current protection switching priority of a circuit pack or external DS1 reference. The "-ring" suffix for a high-speed switch priority means that the circuit pack is signaling a switch request for a switch at the other side of the node or at another node. An external high-speed switch request results in an "-ne" (near end) suffix at the node where the request was entered, and an "-fe" (far end) suffix at the other end of the line. If the same priority request was entered at both ends of a line, both ends show an "-ne" suffix. A dash (-) indicates that the condition causing the switch failure has been cleared. The queued or deferred switch priorities can only be a "manual-ne," "APS(SD)," "APS(SF)," and "manual" for a low-speed interface.

The following example shows how a manual switch condition is displayed at the node where the manual switch is initiated.

```
-----
trmtr-1e   TRMTR      active    manual-ring
trmtr-1w   TRMTR      standby    manual-ne
-----
rcvr-1e    RCVR       active    manual-ring
rcvr-1w    RCVR       standby    manual-ne
```

The following example shows what is displayed at the node located at the opposite end of the line being switched.

```
-----
trmtr-1e   TRMTR      standby    manual-fe
trmtr-1w   TRMTR      active     manual-ring
-----
rcvr-1e    RCVR       standby    manual-fe
rcvr-1w    RCVR       active     manual-ring
```

The following example shows what is displayed at the remaining node(s) on the ring network.

```
-----
trmtr-1e   TRMTR      active     manual-ring
trmtr-1w   TRMTR      active     manual-ring
-----
rcvr-1e    RCVR       active     manual-ring
rcvr-1w    RCVR       active     manual-ring
```

Releases 3 and 4.0

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-State:All: COMPLD
/* State Report
=====
  Address      Circuit      Port      Switch -----Switch Priority -----
                Pack        State      State      Active   Deferred
=====
DS1-in-pri          i      active      -
DS1-in-sec          i      standby     -
-----
tg-1      TG3      active      -
tg-2      TG3      active      -
-----
trmtr-1e
trmtr-1w
-----
rcvr-1e      RCVR      active      -   -
rcvr-1w      RCVR      active      -   -
-----
Continue report page-by-page?(y/n)Yes

```

A dash (-) indicates that the data is either corrupted or not available.

The State Report shows a normal state at an FT-2000 Add/Drop-Rings Terminal, meaning that there are no active protection switches, inhibits, lockouts, or other abnormal conditions.

The fields of the report are described below:

- Address - Shows the address by slot function, high-speed line designation, and physical slot location.
- Circuit Pack - Shows the functional name of the circuit pack.
- Port State - Shows the state of multiple ports (1, 2, and 3) with an i for the IS state, an a for the AUTO state, an n for the NMON state, a dash ("-") for not applicable (when the slot is not equipped), and a blank for the timing packs and the high-speed interfaces. The port state is listed left to right in ascending order.
- Switch State - Shows whether the circuit pack or DS1 reference input is in an active state (by carrying traffic or by providing timing) or in a standby state (by not carrying traffic or by not providing timing).
- Switch Priority - **Active switch priority** shows the current protection switching priority of a circuit pack or external DS1 reference. The "-ring" suffix for a high-speed switch priority means that the circuit pack is signaling a switch request for a switch at the other side of the node or at another node. An external high-speed switch request results in an "-ne" (near end) suffix at the node where the request was entered, and an "-fe" (far end) suffix at the other end of the line. If the same priority request was entered at both ends of a line, both ends show an "-ne" suffix.

A dash ("-") indicates that the condition causing the switch failure has been cleared.

The queued or **deferred switch priorities** can only be a "manual-ne," "APS(SD)," "APS(SF)," and "manual" for a low-speed interface.

The following example shows how a manual switch condition is displayed at the node where the manual switch is initiated.

```

-----
      trmtr-1e  TRMTR      active      manual-ring
      trmtr-1w  TRMTR      standby     manual-ne
-----
      rcvr-1e   RCVR       active      manual-ring
      rcvr-1w   RCVR       standby     manual-ne

```

The following example shows what is displayed at the node located at the opposite end of the line being switched.

```

-----
      trmtr-1e  TRMTR      standby     manual-fe
      trmtr-1w  TRMTR      active      manual-ring
-----
      rcvr-1e   RCVR       standby     manual-fe
      rcvr-1w   RCVR       active      manual-ring

```

The following example shows what is displayed at the remaining node(s) on the ring network.

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-State:All: COMPLD
/* State Report
=====
Address          Circuit   Port      Switch      Switch
                  Pack      State     State        Priority
=====
ls-1-1a          DS3      aaa       active       -
ls-1-1b          DS3      iii       active       -
. .             .        .         .            .
. .             .        .         .            .
. .             .        .         .            .
ls-p-8b          -        -         -            -
ls-p-p1          DS3      -         standby      -
ls-p-p2          -        -         -            -
Press F3 to return to input mode.
*/
;

```

A dash ("-") indicates that information is not available for that entry (for example, the slot is not equipped).

The State Report on the previous page shows a normal state at the FT-2000 end terminal, meaning that there are no active protection switches, inhibits, lockouts, or other abnormal conditions. Technically, the report shows the administrative state of slots that have protection switching capability or of slots that have multiple port capability. The administrative state is obtained from the SYSMEM circuit pack in the FT-2000 end terminal. The report groups and separates the slots by their address with protection slots being last.

- Address - Shows the address by slot function, high-speed line designation, and physical slot location.
- Circuit Pack - Shows the functional name of the circuit pack.
- Port State - Shows the state of multiple ports (1, 2, and 3) with an i for the IS state, an a for the AUTO state, an n for the NMON state, a dash ("-") for not applicable (when the slot is not equipped), and a blank for the timing packs and the high-speed interfaces. The port state is listed left to right in ascending order.
- Switch State - Shows whether the circuit pack is in an active (carrying traffic or providing timing) or in a standby state (not in use).
- Switch Priority - Shows the priority of an automatic protection switch, manual switch, an inhibit switch, a lockout switch, or a dash ("-") when no switch request is active.

The following examples show how a 1x1 bidirectional protection switch condition is displayed. Assume that an automatic bidirectional line switch occurred because of a TRMTR circuit pack failure on line 1 at the near end terminal. The State Report at the near end is as follows:

```
-----
trmtr-1      TRMTR      standby auto switch (SF)-HIGH
trmtr-p      TRMTR      active
-----
rcvr-1      RCVR       standby auto switch (bi-directional)-HIGH
rcvr-p      RCVR       active
```

The State Report at the far end terminal is as follows:

```
-----
trmtr-1      TRMTR      standby auto switch (bi-directional)-HIGH
trmtr-1      TRMTR      active
-----
rcvr-1      RCVR       standby auto switch (SF)-HIGH
rcvr-p      RCVR       active
```

Example 48 - CONFIGURATION-Retrieve-Synchronization

Releases 3 and Later (Use the **CONFIGURATION** category.)

```

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Synchronization: COMPLD
/* Synchronization Report
=====
Parameter                               Value
=====
Provisioned Mode                        Free Running
Operational Mode (East)                 Free Running
Operational Mode (West)                 Free Running
TG1 Clock Range Status                  0%
TG2 Clock Range Status                   -2%
Primary DS1 In/Out Format                SF
Secondary DS1 In/Out Format              SF
Primary DS1 In Line Code                 AMI
Secondary DS1 In Line Code               AMI
DS1 Out 1 Equalization                   20%
DS1 Out 2 Equalization                   20%
*/
;

```

The above report shows the provisioned synchronization parameters of the FT-2000 ring network. Additional synchronization information is contained in Volume I, Section 5, "Control, Transmission, and Synchronization Interfaces."

Parameter - *Provisioned Mode* shows the synchronization mode that was provisioned at the FT-2000 add/drop-rings terminal.

Operating Mode (East/West) shows the current synchronization mode being used in the FT-2000 network.

TG1/TG2 Clock Range Status shows the current percentage that the frequency is off of the center frequency. The lower the percentage, the closer the frequency is to the center frequency. The deviation limits in ppm are ± 100 but the allowable range is ± 110 .

Primary/Secondary DS1 In/Out Format shows the value that was provisioned, either D4 super frame format (SF) or extended super frame (ESF).

Primary/Secondary DS1 In Line Code shows the provision value, either alternate mark inversion (AMI) or bipolar with 8 zeros substitution (B8ZS).

DS1 Out 1/Out 2 Equalizations shows the equalizer setting of the DS1 external reference output 1/output 2. The value is a percentage of the maximum cable length; for example, 100 (%) equates to a maximum of 660 feet of 600-type cable.

Value - The values are discussed with the parameter.

Release 2 (Use the **CONFIGURATION** category.)

```

Lucent Technologies-FT-2000 96-07-19 23:15:15 EST
M Retrieve-Synchronization:  COMPLD
/* Synchronization Report
=====
Parameter                               Value
=====
Provisioned Mode                        Phase Locked
Operational Mode                       Phase Locked - Secondary DS1
TG1 Clock Range Status                 10%
TG2 Clock Range Status                 -5%
Primary DS1 In Format                  ESF
Secondary DS1 In Format                 SF
Primary DS1 In Line Code               B8ZS
Secondary DS1 In Line Code             AMI
DS1 Out 1 Equalization                 20%
DS1 Out 2 Equalization                 20%
*/
;

```

The above report shows the provisioned synchronization parameters of the FT-2000 end terminal.

Parameter - *Provisioned Mode* shows the synchronization mode that was provisioned for the FT-2000 end terminal.

Operational Mode shows the current synchronization mode being used in the FT-2000 System.

TG1/TG2 Clock Range Status shows the current percentage that the frequency is off the center frequency. The lower the percentage, the closer the frequency is to the center frequency. The deviation limits in ppm are ± 100 but the allowable range is ± 110 .

Primary/Secondary DS1 In Format shows the value that was provisioned, either super frame format (D4) or extended super frame.

Primary/Secondary DS1 In Line Code shows the provision value, either alternate mark inversion or bipolar with 8 zeros substitution.

DS1 Out 1/Out 2 Equalizations shows the equalizer setting of the DS1 external reference output 1/output 2. The value is a percentage of the maximum cable length; for example, 100 (%) equates to a maximum of 660 feet of 600-type cable.

Value - The values are discussed with the parameter.

Example 49 - CONFIGURATION-Retrieve-T3

```

TERMB 94-06-15 23:15:15 EST
M Retrieve-T3:All: COMPLD
/* T3 Port Provisioning Report
=====
Port Address      VMR Mode      Signal Alarm Level  --Port State--
                        Degrade Threshold  from DSX      from OC48
=====
ls-1a-1          VMR          MJ          -6          is          is
ls-1a-2          VM          MJ          -6          auto        auto
ls-1a-3          CC w/ AIS   CR          -6          is          is
.
ls-8b-3          CC w/o AIS  MN          -3          is          is
*/
;
      Information below is for Release 4.0 and Earlier

/* T3 Port Provisioning Report
=====
Port Address      VMR Mode      Signal Alarm Level  Degrade Threshold  Port State
=====

```

The above report shows how the T3 (DS3) port(s) was provisioned and the port attributes. The report provides information on the violation monitor and removal mode, the severity of the alarm implied by the failure of the DS3 port, the signal degrade threshold, and the memory administrative state.

The fields of the report are described below:

Port Address	-	Shows a specific line (only in Releases 1.1.1 and 2), slot, and low-speed port (1, 2, or 3) of a DS3 circuit pack.
VMR Mode	-	Shows the violation monitor removal mode of the DS3 signal, such as VMR (for monitor and removal of DS3 signal format violations), VM (for monitor only but do not remove DS3 signal format violations), and CC (for clear channel) with or without alarm indication signal (AIS). When CC w/AIS is shown, this means an AIS signal will be inserted in the DS3 signal that is going in the opposite direction of the failure.
Alarm Level	-	Shows the highest severity user panel LED that is lit, which is the alarm that is assigned to the incoming DS3 signal failure. If no LEDs are lit, then status is shown as NA (not alarmed) or NR (not reported) if provisioned as such.
Signal Degrade Threshold	-	Shows the provisioned signal degrade threshold, either -3 or -6. The example shows a -6, meaning the error rate 10^{-6} .
Port State	-	Shows the administrative state (from memory) of the port address; (from DSX or OC48) such as, is for in service, auto for automatic, and nmon for not monitored.

```

TERMB 94-06-15 23:15:15 EST
M Retrieve-T3:All: COMPLD
/* T3 Port Provisioning Report
=====
Port      VMR      Alarm      Signal      Port      State      Path
Address   Mode      Level      Degrade     from      from      RFI
          Mode      Level      Threshold  DSX       OC48      Alarm
=====
ls-1a-1   VMR        MJ         -6          is        is        MN
ls-1a-2   VM         MJ         -6          auto      auto      MN
ls-1a-3   CC w/ AIS  CR         -6          is        is        MN
.
ls-8b-3   CC w/o AIS MN         -3          is        is        MN
*/
;

```

The above report shows how the T3 (DS3) port(s) was provisioned and the port attributes. The report provides information on the violation monitor and removal mode, the severity of the alarm implied by the failure of the DS3 port, the signal degrade threshold, and the memory administrative state.

- Port Address - Shows a specific line (only in Releases 1.1.1 and 2), slot, and low-speed port (1, 2, or 3) of a DS3 circuit pack.
- VMR Mode - Shows the violation monitor removal mode of the DS3 signal, such as VMR (for monitor and removal of DS3 signal format violations), VM (for monitor only but do not remove DS3 signal format violations), and CC (for clear channel) with or without alarm indication signal (AIS). When CC w/AIS is shown, this means an AIS signal will be inserted in the DS3 signal that is going in the opposite direction of the failure.
- Alarm Level - Shows the highest severity user panel LED that is lit, which is the alarm that is assigned to the incoming DS3 signal failure. If no LEDs are lit, then status is shown as NA (not alarmed) or NR (not reported) if provisioned as such.
- Signal Degrade Threshold - Shows the provisioned signal degrade threshold, either -3 or -6. The example shows a -6, meaning the error rate 10^{-6} .
- Port State - Shows the administrative state (from memory) of the port address; (from DSX or OC48) such as is for in service, auto for automatic, and nmon for not monitored.
- Path RFI Alarm - Shows the provisioned alarm level for near-end indications of far end path related conditions in the drop (from OC-48) direction. Values are CR, MJ, MN, NA, NR, NO.

Example 50 - CONFIGURATION - Retrieve-Timeslot-STS1**Releases 7 and Later**

/* STS-1 Tributary Map Circuit Report						
Ring	Node	Source TID	Source AID	Destination AID	Destination TID	Primary State

Worcester-MA						
	Quabbin-MA		1w-1-2	ls-1a-3	Worcester-MA	
	Worcester-MA		ls-1a-1	1e-1-2	Salem-NH	
	Chicopee-MA		1e-1-2	ls-1b-3	Worcester-MA	
	Worcester-MA			1w-1-2	Worcester-MA	

Salem-NH						
	Worcester-MA		1w-1-2	ls-7a-2	Salem-NH	
	Salem-NH		ls-2a-2	1e-1-2	Chicopee-MA	ACT
	Salem-NH		ls-2b-2	1e-1-2	Salem-NH	STBY
	Chicopee-MA		1e-1-2	1w-1-2	Worcester-MA	

Concord-MA						
	Salem-NH		1w-1-2	1e-1-2	Chicopee-MA	
	Chicopee-MA		1e-1-2	1w-1-2	Worcester-MA	

Chicopee-MA						
	Salem-NH		1w-1-2	ls-4a-2	Chicopee-MA	
	Chicopee-MA		1e-1-2		Chicopee-MA	
	Chicopee-MA		ls-4a-1	1w-1-2	Worcester-MA	
	Chicopee-MA		1e-1-2			
	Chicopee-MA					

Holyoke-MA						
	Holyoke-MA		1w-1-2		Holyoke-MA	
	Holyoke-MA		1e-1-2		Holyoke-MA	
	Holyoke-MA			1w-1-2	Holyoke-MA	
	Holyoke-MA			1e-1-2	Holyoke-MA	

Quabbin-MA						
	Quabbin-MA		ls-6a-1	1e-1-2	Worcester-MA	
	Quabbin-MA			1w-1-2	Quabbin-MA	
	Quabbin-MA		1w-1-2		Quabbin-MA	
	Quabbin-MA		1e-1-2		Quabbin-MA	

*/						

This command retrieves a Tributary Map report, organized by node, for all nodes in a ring. The report shows all cross-connections on a single user-specified time slot throughout the entire ring, indicating parts of the ring available for new circuits.

The fields of the report are described below:

Ring Node	- Lists the nodes in the ring. The topmost node is the node where the report request was issued. The remaining nodes in the ring are arranged in West to East sequence.
TID	- TID lists the target identifiers involved in the squelch map cross-connection entries associated with the time slot specified in the RETRIEVE - TIMESLOT command.
Source/Destination AID	- The STS-1 Tributary or Slot Address identifiers (AIDs). They are derived from local copies of the squelch maps of all nodes in the ring.
Primary State	- A parameter used to report the status of 1WAYDRI and 1+1 protected OC-3 interface cross-connections. It accepts two values: ACT for an active DRI or OC-3 protected cross-connection, STBY for one in a standby state.

For the above example, assume the ring topology to be:

- West to East from Worcester-MA
- West-East between Worcester and Salem-NH
- West-East between Salem and Concord-MA
- West-East between Concord and Chicopee-MA
- West-East between Chicopee and Holyoke-MA
- West-East between Chicopee and Quabbin-MA
- West to East from Quabbin to Worcester-MA

Example 51 - CONFIGURATION-Retrieve-Timeslot-STS3 (Releases 5 and Later)

```

Delta-1 94-06-15 23:15:15 EST
M Retrieve-Timeslot:STS3:1: COMPLD
/* STS-3 Tributary Map Report
=====
At Node          TID    West  Cross-connection    East    TID
=====
Worcester-MA
    Quabbin-MA      1w-1  ---> 1a    1a ---> 1e-1    Salem-NH
                                1b <--- 1e-1    Chicopee-MA
-----
Salem-NH
    Worcester-MA     1w-1 ---> 2b    4a ---> 1e-1    Chicopee-MA
                                1w-1 <--- 2b    1e-1
    Worcester-MA     1w-1 <..... 1e-1    Chicopee-MA
-----
Concord-MA
    Salem-NH        1w-1 -----> 1e-1    Chicopee-MA
    Worcester-MA     1w-1 <----- 1e-1    Chicopee-MA
-----
Chicopee-MA
    Salem-NH        1w-1 ---> 4a          1e-1
    Worcester-MA     1w-1 <--- 4a
-----
Holyoke-MA
                                1w-1          1e-1
-----
Quabbin-MA
                                1w-1          6a ---> 1e-1 Worcester-MA
*/

```

The above report shows the STS-1 level cross-connections making up the specified STS-3 tributary that exists in the FT-2000 ring (West-to-East sequence). Examples of cross-connections and additional information are provided in Volume I, Section 8.

The fields of the report are described below:

At Node/	- Shows the target identifier (TID) of the node where the report is requested (topmost entry).
TID	- Shows the TIDs in a West-to-East ring direction (top to bottom). The second entry from the top is the node immediately East of the node requesting the report. A question mark (?) indicates the cross-connection table is not available at that particular node.
West	- Shows the direction of the line and tributary.
Cross-connection	- Shows the direction and slot number where the STS3 signal is connected for an add and/or drop connection. Only direction is shown for a through tributary cross-connection. Blank spaces are shown when a tributary is not cross-connected.
East	- Shows the direction of the line and tributary.
TID	- Shows the TID of the node immediately East of the node requesting the report. A question mark (?) indicates the cross-connection table is not available at that particular node.

CIT Tutorial Using CIT-PC Software

General: This tutorial illustrates, through a series of exercises, the features of the FT-2000 CIT-PC software. This software which supports the FT-2000 user interface is menu-driven and user-friendly. *It is strongly recommended that this tutorial only be used with an FT-2000 network element that is **out of service**.*

It is assumed that the craft interface terminal (CIT) is conditioned and connected to the FT-2000 network element. If it is not, refer to **DLP-501** in the Detailed Level Procedures section of this manual for assistance in getting to that point. Press **F5**, labeled EXIT, if it is shown on the screen.

1. Change directory to where the FT-2000 CIT-PC software is installed, for example "cd cit".
2. Enter "**cit**" and press **(ENTER)**. This enters the user into the standard mode and not into the expert mode.

```

                Lucent Technologies
                CIT-PC Release 7.2.0

        Support for FT-2000 and OLS Releases:

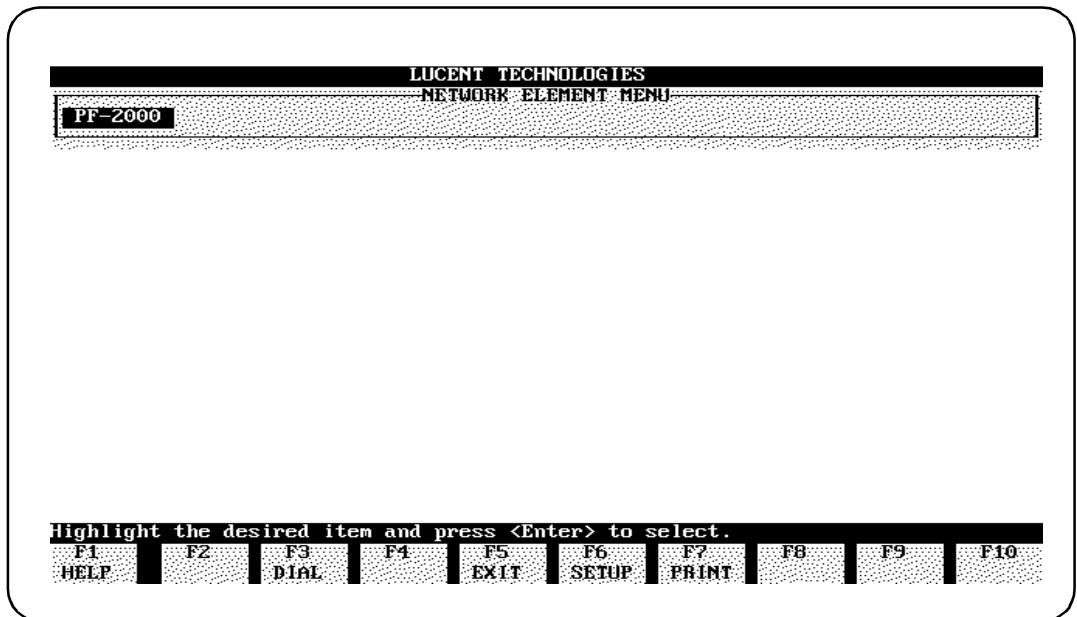
2.1.1-TRM  2.1.0-RPTR  3.0.1-ADR  6.0.1-ADR
            4.0.0-RPTR  4.0.0-ADR  6.0.2-ADR
            6.0.0-RPTR  4.1.0-ADR  7.0.0-ADR
            6.0.1-RPTR  5.0.1-ADR  7.0.1-ADR
                        5.0.2-ADR  7.0.3-ADR
                        5.0.3-ADR  7.1.0-ADR
                        6.0.0-ADR  7.2.0-ADR

```

PRESS ANY KEY TO CONTINUE.

Copyright (c) 1996 Lucent Technologies
All Rights Reserved.

3. Press "space bar" or any other key to display next screen.

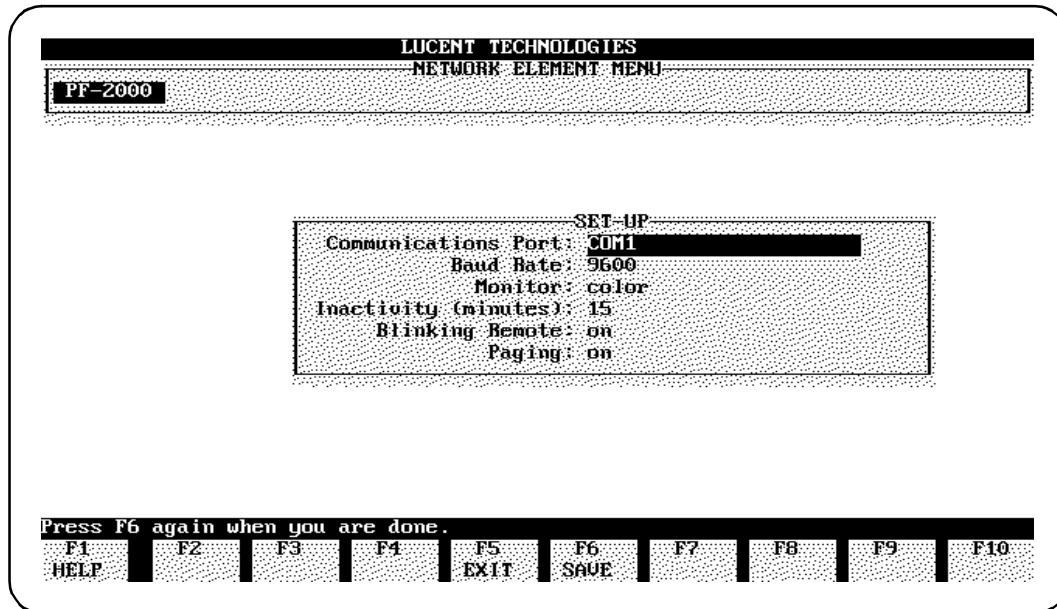


4. Press function key **F6** for **SETUP** menu.

5. Compare displayed screen with information below and note any differences:

Communications: com-1 (as appropriate)
Baud Rate: 9600
Monitor: (appropriate value for type of monitor)
Inactivity (minutes): (value between 0* and 9999)
Blinking Remote: on
Paging: on

* This value (zero) disables the inactivity timer.



6. As an exercise, the baud rate will be set to 9600, even if it is already set to 9600.

Use up (↑) and down (↓) arrow keys to move highlighted area to **Baud Rate** parameter value.

7. Type a "?" when **Baud Rate** is highlighted.

The screenshot shows a terminal window with the following content:

```
LUCENT TECHNOLOGIES
NETWORK ELEMENT MENU
PF-2000

SET-UP
Communications Port: COM1
Baud Rate: 9600
Monitor: color
Inactivity (minutes): 15
Blinking Remote: on
Paging: on

1200
2400
4800
9600

Select from the list or press ESCape to return to parameter form.
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP EXIT
```

8. Use ↑ and ↓ arrow keys to select desired value "9600".
9. Press .
10. Use arrow keys and "?"; set any other parameter values (if necessary) to values given in Step 5.

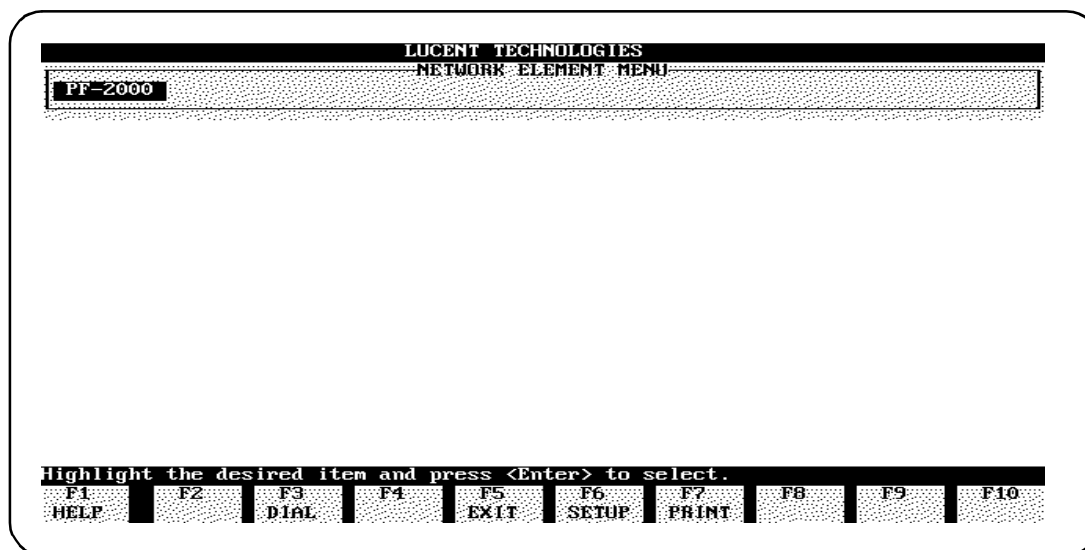
11.



NOTE:

The **message line** immediately over the display of the function keys will always show what is to be done next. For example, it now says "Press F6 again when you are done;" PT-2000 is highlighted at this time.

Press again when parameters are set.



12. Press **ENTER**.

LUCENT TECHNOLOGIES PF2000LOCAL ONLY

Communications Established.
Please Login.
Login:

Enter your login.

F1
HELP

F2

F3

F4

F5
EXIT

F6
SETUP

F7
PRINT

F8
CANCEL

F9

F10

13. Enter a valid login (obtained from the system administrator) and press **ENTER**.

The screenshot shows a terminal window with a black header bar containing "LUCENT TECHNOLOGIES PF2000" on the left and "LOCAL ONLY" on the right. In the center, a shaded rectangular box displays the following text: "Login Failed.", "Please try again.", "Login: LUC01", and "Password:". Below this box, a black bar contains the text "Enter your password.". At the bottom of the terminal window, a row of ten function keys is displayed: F1 (HELP), F2, F3, F4, F5 (EXIT), F6 (SETUP), F7 (PRINT), F8 (CANCEL), F9, and F10.

14. Enter the appropriate password (obtained from the system administrator) and press **ENTER**.

```

=====
LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY
=====
COMMAND MENU
=====
FAULT CONFIGURATION PERFORMANCE SECURITY
=====
NOTICE: This is a private computer system.
Unauthorized access or use may lead to prosecution.

*/
;

DALLAS 70-03-06 02:46:38
A Retrieve-Alarm:All: COMPLD
/* Active Alarms and Status Report
=====
Alarm Source Date Time Srv Description
Level Address Detected
=====
MJ system 03-02 01:12:38 nsa ring incomplete/open
*/
;
Highlight the desired item and press <Enter> to select.
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP REMOTE OUTPUT LOGOUT EXIT SETUP PRINT ALARMS

```

15. Notice that after a successful login, an Active Alarms and Status Report is displayed. An example is shown above. The top line shows the actual target identifier (TID) of the network element.
16. Use ← and → arrow keys to highlight **CONFIGURATION** but **do not press ENTER** at this time.

17. Press ↓ key once to display the **CONFIGURATION** menu. The figure shows the report for an Add/Drop-Rings Terminal. The menu for other platforms may differ slightly. Also, the menu is slightly different when the user is in the expert mode.

```

=====
LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY
=====
COMMAND MENU
=====
FAULT CONFIGURATION PERFORMANCE SECURITY
=====
NOTI Retrieve a private computer system.
Unauthor Set or use may lead to prosecution.
Enter
Delete
Copy
Update

*/
:

DALLAS 70-03-06 02:46:38
^ Retrieve Alarm:All: COMPLD
/* Active Alarms and Status Report
=====
Alarm Source Date Time Srv Description
Level Address Detected
=====
MJ system 03-02 01:12:38 nsa ring incomplete/open
*/
:
Highlight the desired item and press <Enter> to select.
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP OUTPUT LOGOUT EXIT SETUP PRINT CANCEL

```

18. Notice that the following tables list all of the inputs available under each of the four management categories (**FAULT**, **CONFIGURATION**, **PERFORMANCE**, and **SECURITY**).

Table 11-2 Release 7 Inputs (Commands) Menu for Add/Drop-Rings Terminal

Table 11-3 Release 6 Inputs (Commands) Menu for Add/Drop-Rings Terminal

Table 11-4 Release 5 Inputs (Commands) Menu for Add/Drop-Rings Terminal

Table 11-5 Release 4 Inputs (Commands) Menu for Add/Drop-Rings Terminal and

Table 11-6 Release 3 Inputs (Commands) Menu for Add/Drop-Rings Terminal

Table 11-7 Releases 2, 4, and 6 Inputs (Commands) Menu for FT-2000 Repeater Bay/Shelf

Table 11-8 [Inputs (Commands) Definitions] of this section provides a brief description of each of these inputs.

19.

**NOTE:**

You are now going to use the **Set-Date** command to set the hour of the day. (After completing this tutorial, you may want to set the time back to the original value.)

Use the ↑ and ↓ arrow keys to highlight the verb **Set** and press **ENTER**. The figure shows the menu for an Add/Drop-Rings Terminal. The menu for other platforms may differ slightly.

```

PF1000 LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY
COMMAND MENU
FAULT CONFIGURATION PERFORMANCE SECURITY
NOTI Retrieve a private computer system.
Unauthor Set Set ad to prosecution.
Enter
Delete
Copy
Update
Attribute
Date
EC1
Low Speed
OC3
OC12
OC48
Path
Protection
State
T3
=====
Alarm Source Date Description
Level Address Detec
=====
MJ system 03-02 01:12:38 nsa ring incomplete/open
*/
;
DALLAS 70-03-06 02:46:38
A Retrieve-Alarm:All: COMPLD
/* Active Alarms and Status
=====
Alarm Source Date Description
Level Address Detec
=====
MJ system 03-02 01:12:38 nsa ring incomplete/open
*/
;
Highlight the desired item and press <Enter> to select.
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP OUTPUT LOGOUT EXIT SETUP PRINT CANCEL

```

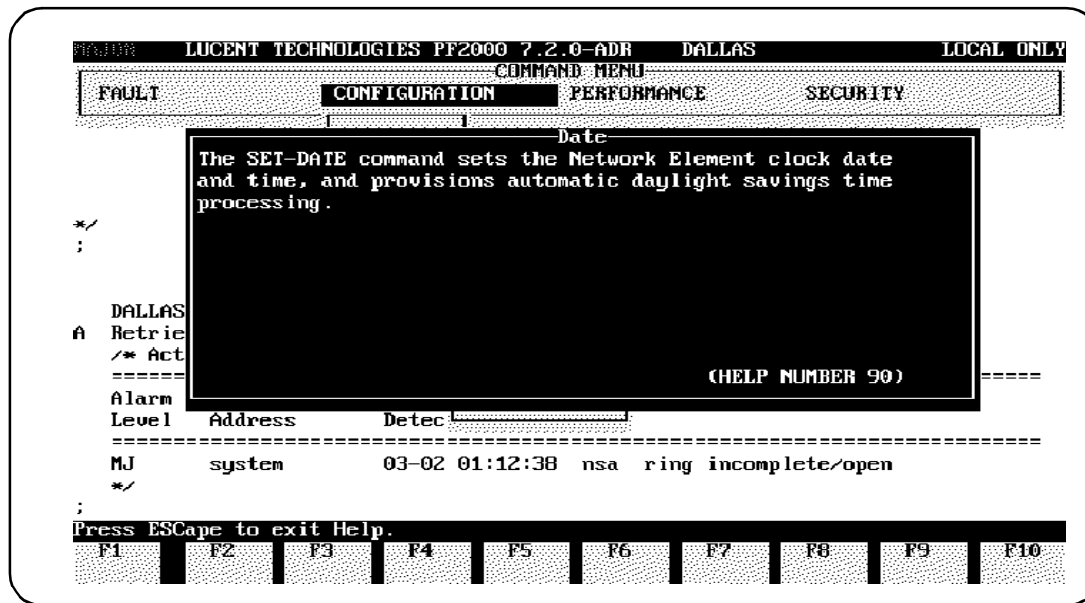
20. Use ↓ and ↑ keys to highlight **Date**, but **do not press ENTER** at this time.

21.

**NOTE:**

When function key **F1** is labeled **HELP**, it can be pressed to obtain information about the item highlighted on the screen.

Press function key **F1** labeled **HELP** to display the explanation of the highlighted item.



22. After reading the information, press **ESC** (escape) key, then press **ENTER** to select **Date** (highlighted).

23. Use the ↑ and ↓ keys to highlight **Hours**, but **do not press** **ENTER** at this time.

```

=====
LUCENT TECHNOLOGIES PF2000 7.2.0-ADR    DALLAS    LOCAL ONLY
=====
COMMAND MENU
=====
FAULT      CONFIGURATION    PERFORMANCE    SECURITY
=====
                                     Set Date
                                     Date: 97-05-13
                                     Hours: 15
                                     Minutes: 14
                                     Seconds: 20
*/
:
                                     Standard Time Zone:
D      Start Daylight Savings Date: 04-20
A      Stop Daylight Savings Date: 10-22
R      Daylight Savings Time Zone: EST
/
=====
Alarm Source   Date | T3 | Description
Level Address  Detec
=====
MJ      system   03-02 01:12:38 nsa ring incomplete/open
*/
:
Enter value & press <Enter> or press ? for choices & restore prev value if appl.
F1      F2      F3      F4      F5      F6      F7      F8      F9      F10
HELP    OUTPUT  LOGOUT  EXIT    CANCEL

```

24.

⇒ **NOTE:**
You will now deliberately mistype a value.

Enter "99" (without the quotes) for **Hours**, but **do not press the** **at this time.**

25. Use the "BACK SPACE" key to erase your entry.

26.

⇒ **NOTE:**
You will now deliberately enter an invalid value.

Enter "26" for the hour followed by .

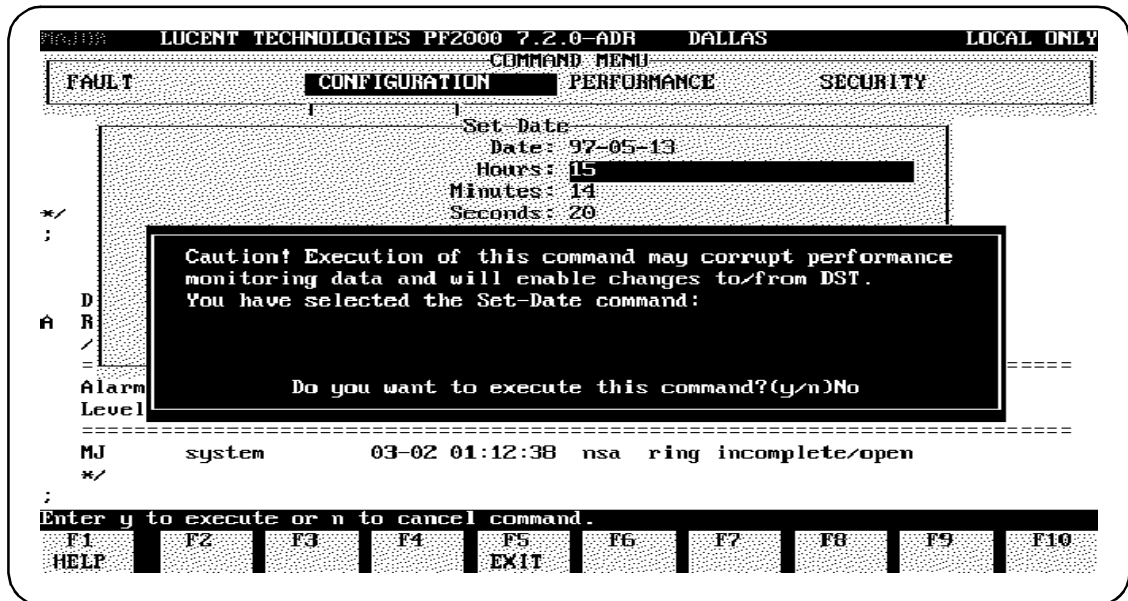
The screenshot shows a terminal window with the title bar "LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY". The main menu has four options: FAULT, CONFIGURATION (highlighted), PERFORMANCE, and SECURITY. The CONFIGURATION menu is open, showing "Set Date" with fields for Date (97-05-13), Hours (26), Minutes (14), and Seconds (20). Below this, it shows "Standard Time Zone" and "Daylight Savings" settings. A message box at the bottom of the CONFIGURATION menu states: "Hours must be a number between 0 and 23 in steps of 1. Press ESCape to continue." At the bottom of the terminal window, there is a row of function keys: F1 (HELP), F2, F3, F4, F5 (EXIT), F6, F7, F8, F9, and F10. A prompt at the bottom left says "Press ESCape to return or F1 for additional help".

27.

⇒ **NOTE:**
A screen appears and displays the allowable values for **Hours**.

Press the key.

28. Enter a different or correct value (hour) than the one displayed, followed by .
29. Press function key to execute the input.
30. Notice that a confirmation frame appears on the screen with a Caution statement.



31. Enter **y** for the "yes" selection and press **ENTER**.

```

PF9:PF10  LUCENT TECHNOLOGIES PF2000 7.2.0-ADR  DALLAS  LOCAL ONLY
*/
;

DALLAS 97-05-13 16:39:37 EST
A Retrieve-Alarm:All: COMPLD
/* Active Alarms and Status Report
=====
Alarm   Source      Date Time      Srv  Description
Level   Address      Detected
=====
MJ      system      03-02 01:12:38  nsa  ring incomplete/open
=====
MN      system      05-13 15:58:59  nsa  STS1 squelch map conflict
*/
;

DALLAS 97-05-13 16:43:21 EST
M Set-Date:97-05-13,16:43:21,EST,04-20,10-22,EST: COMPLD
;

Press F9 to repeat last command or F3 to return to input mode.
F1  F2  F3  F4  F5  F6  F7  F8  F9  F10
HELP REMOTE INPUT LOGOUT EXIT          REPEAT

```

32. Notice that the **COMPLD** output echoes the command. Function key **F3** is now labeled **INPUT** because the display is now in the output mode. (**F3** toggles between **INPUT** and **OUTPUT** modes. You may experiment with the F3 key now.)
33. Notice that when the CIT is in the output mode, a **scroll bar** appears on the right side of the screen. You can scroll the screen either forward or backward depending on whether the darker-shaded portion of the bar is at top or bottom. Use the **↑** and **↓** keys to scroll the screen.
- 34.

⇒ NOTE:
You just used **CONFIGURATION-Set-Date** to set the hour. You will now use **Retrieve-Date** to look at the value of that parameter.

Press **F3** to return to OUTPUT mode.

35. Under the **CONFIGURATION** category, select **Retrieve** and then press **ENTER**.

36.



NOTE:

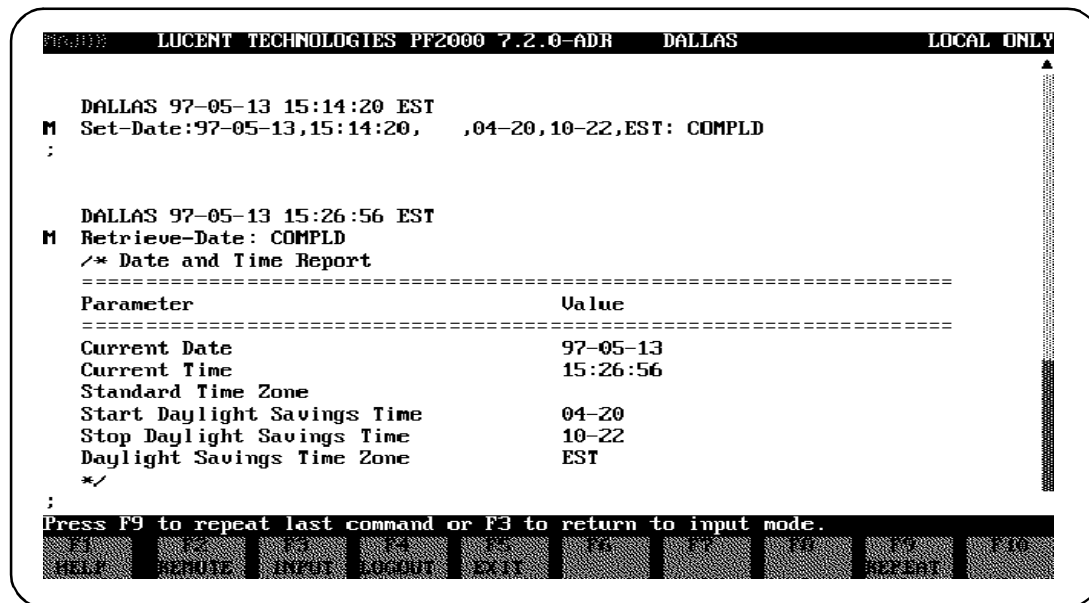
You will now deliberately make an incorrect selection of **Retrieve-Attribute** instead of **Retrieve-Date**.

Under **Retrieve**, highlight **Attribute** and press **ENTER**.

37. To correct, press **ESC** to back up to the preceding screen. Any time you are in the INPUT mode, you can back up one screen at a time by pressing the **ESC** key.
38. Under **Retrieve**, highlight **Date** (by using ↓ and ↑ arrow keys) and press **ENTER**.

LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY									
COMMAND MENU									
FAULT		CONFIGURATION		PERFORMANCE		SECURITY			
		Retrieve		Retrieve					
		Set		Assignment					
		Enter		Attribute					
				Circuit					
DALLAS									
A	Retri	Retrieve-Date							
/* Ac									
=====1=====									
Alarm	Source	Date	Equipment	ription					
Level	Address	Detec	Low Speed						
=====			Map	=====					
MJ	system	03-02	OC3	incomplete/open					
*/			OC12						
			OC48						
			Path						
			Protection						
			Synchronization						
			T3						
			Timeslot	COMPLD					
DALLAS 97-05-13 15:14:20 ES									
M	Set-Date:97-05-13,15:14:20,								
;									
Press F9 to execute the command.									
F1	F2	F3	F4	F5	F6	F7	F8	F9	F10
HELP		OUTPUT	LOGOUT	EXIT			CANCEL	EXECUTE	

39. Press function key to execute.



```

LUCENT TECHNOLOGIES PF2000 7.2.0-ADR  DALLAS  LOCAL ONLY
DALLAS 97-05-13 15:14:20 EST
M Set-Date:97-05-13,15:14:20,  ,04-20,10-22,EST: COMPLD
;

DALLAS 97-05-13 15:26:56 EST
M Retrieve-Date: COMPLD
/* Date and Time Report
=====
Parameter                                Value
=====
Current Date                             97-05-13
Current Time                             15:26:56
Standard Time Zone
Start Daylight Savings Time              04-20
Stop Daylight Savings Time               10-22
Daylight Savings Time Zone               EST
*/
;
Press F9 to repeat last command or F3 to return to input mode.
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
EXIT RETRIEVE INPUT LOGOUT DATE
  
```

40. Refer to Table 11-7 [List of Inputs (Commands) That Generate Reports for FT-2000 Network Elements] in this section to determine the report generated by **Retrieve-Date**. Then refer to the indicated example and read the definition of the parameters.
41. Refer to the report on the CIT screen and note that the **Hours** have been changed.
42. If appropriate, use **CONFIGURATION-Set-Date** to set the **Hours** back to the original value. Otherwise, continue with the next step.
43. At this point in the tutorial, you must decide to either stop or continue with the CIT-PC training. If you continue, the additional training pertains to an add/drop-rings terminal in a ring network.
 If **exiting the tutorial**, then continue with Step 44.
 If **continuing the tutorial**, then continue with Step 45.
44. Log off the CIT by pressing (**LOGOUT** from the FT-2000 network element) or exit the FT-2000 CIT-PC software by pressing (**EXIT**) as appropriate.

Additional Training on an FT-2000

Add/Drop-Rings Terminal

45.

**NOTE:**

You are now going to use **CONFIGURATION-Set-State-T3** to set the administrative state of port 1 of slot 1A to **NMON** for a DS3 circuit pack. If an STS1E or OC-3 circuit pack is in slot 1A, then select another slot that contains a DS3 circuit pack (use this new slot reference throughout the procedure). (After completing this portion of the tutorial, you may want to set the port state to **AUTO**.)

Depress function key **F3** to return to the input mode and select **CONFIGURATION-Set-State-T3** by using the F3, ←, →, ↑, and ↓ arrow keys to highlight the command and press **ENTER**.

```

LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY
COMMAND MENU
FAULT CONFIGURATION PERFORMANCE SECURITY
Current Date Retrieve Set 97-05-13
Current Time Set 6:56
Standard Time Zone Enter Attribute
Start Daylight Sav Delete Date 0
Stop Daylight Sav Copy EC1 2
Daylight Savings Update Low Speed
*/ OC3
: OC12
: OC48
: Path
: Protection
M DALLAS 97-05-13 15:29:37 ES State Set-State,PLD
M Set-Date:97-05-13,15:29:37, EC1
: EC3
: OC12
: T3
:
DALLAS 97-05-13 15:29:37 EST
M Set-Date:97-05-13,15:29:37,EST,04-20,10- PLD
:
Highlight the desired item and press <Enter> to select.
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP OUTPUT LOGOUT EXIT SETUP PRINT CANCEL

```

46. Enter a "3a" for slot followed by . If slot 3A does not contain a DS3 circuit pack, select another slot that does contain a DS3 circuit pack.
47. Enter a "1" for port followed by .
48. Select **From DSX** for the **Direction** (Releases 4.1 and later).

```

LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY
COMMAND MENU
FAULT CONFIGURATION PERFORMANCE SECURITY

Set-State-T3
Slot: 3a
Port: 1
Direction: From DSX

DAL
M Set
:

DAL
M Ret

/* Ring Map Report
=====
TID          DSNE  State
=====
Notice: Ring Incomplete/Ope

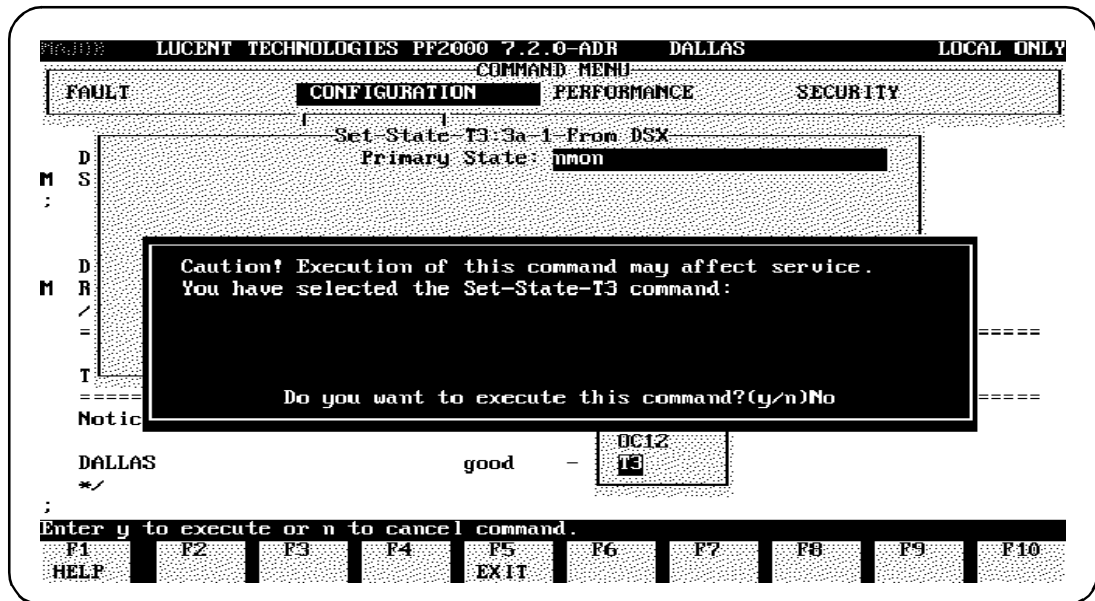
DALLAS          good  -
*/

;
Enter a value or press ? to list choices.
F1  F2  F3  F4  F5  F6  F7  F8  F9  F10
HELP OUTPUT LOGOUT EXIT  CANCEL

```

49. Press .

50. Enter "nmon" for the primary state followed by .
51. Press function key to execute the inputs.
52. Notice that a confirmation frame appears on the screen with a Caution statement.



53. Enter **y** for the "yes" selection and press **ENTER**.

```

LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY
M Set-Date:97-05-13,15:29:37,EST,04-20,10-22,EST: COMPLD
;

DALLAS 97-05-13 15:39:03 EST
M Retrieve-Map-Ring: COMPLD
/* Ring Map Report
=====
TID          DSNE      DCC      Ring
              Status    Continuity
=====
Notice: Ring Incomplete/Open

DALLAS              good      -
*/
;

DALLAS 97-05-13 15:45:11 EST
M Set-State-T3:3A-1,From DSX:NMON: COMPLD
;
Press F9 to repeat last command or F3 to return to input mode.
F1  F2  F3  F4  F5  F6  F7  F8  F9  F10  F11  F12
HELP REPORT INPUT LOGOUT EXIT  F6  F7  F8  REPEAT F10

```

- 54.



NOTE:

You just used **CONFIGURATION-Set-State-T3** to set the administrative state of port 1 of slot 1A to **NMON**. You will now use **CONFIGURATION-Retrieve-T3** to look at the value of that parameter.

Press **F3** to return to the INPUT mode.

- ```

=====
DALLAS 97-05-13 15:47:43 EST
M Retrieve-T3:All: COMPLD
/* T3 Port Provisioning Report
=====
Port UMR Alarm Signal
Address Mode Level Degrade
--Port State--
 from from
 DSX OC48
=====
ls-1a-1 UMR CR -6 auto auto
ls-1a-2 UMR CR -6 auto auto
ls-1a-3 UMR CR -6 auto auto
ls-1b-1 UMR CR -6 auto auto
ls-1b-2 UMR CR -6 auto auto
ls-1b-3 UMR CR -6 auto auto
ls-2a-1 UMR CR -6 auto auto
ls-2a-2 UMR CR -6 auto auto
ls-2a-3 UMR CR -6 auto auto
ls-2b-1 UMR CR -6 auto auto
ls-2b-2 UMR CR -6 auto auto
=====
Continue report page-by-page?(y/n)Yes
=====
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP EXIT CANCEL

```

61. Press function key **F8** to cancel the remaining portion of the report.

| LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY |          |             |                          |                         |                    |
|--------------------------------------------------------|----------|-------------|--------------------------|-------------------------|--------------------|
| Port Address                                           | VMR Mode | Alarm Level | Signal Degrade Threshold | --Port State-- from DSX | from OC48          |
| 1s-2b-3                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-3a-1                                                | VMR      | CR          | -6                       | nmom                    | auto               |
| 1s-3a-2                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-3a-3                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-3b-1                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-3b-2                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-3b-3                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-4a-1                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-4a-2                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-4a-3                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-4b-1                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-4b-2                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-4b-3                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-5a-1                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-5a-2                                                | VMR      | CR          | -6                       | auto                    | auto               |
| 1s-5a-3                                                | VMR      | CR          | -6                       | auto                    | auto               |
| Continue report page-by-page?(y/n)Yes                  |          |             |                          |                         |                    |
| F1<br>HELP                                             | F2       | F3          | F4                       | F5<br>EXIT              | F6<br>F7<br>CANCEL |

62. Scrolling is now possible by using the ↑ and ↓ arrow keys.
63. If appropriate, use **CONFIGURATION-Set-State-T3** to set port 1 of slot 1A to AUTO. Otherwise, continue with the next step.

64.



**NOTE:**

You are now going to enter and delete a cross-connection. You will retrieve a cross-connection report and select an appropriate address (unused slot and tributary). Then, you will make an add cross-connection and verify it. And finally, you will remove the cross-connection and verify its removal.



**NOTE:**

Other types of cross-connections are explained in detail in Volume I, Section 8, "Administration and Provisioning."

Depress function key **F3** to return to the input mode and select the **CONFIGURATION-Retrieve-Crossconnection-All** command.

65. Press the **ENTER** key to obtain an STS-1 Cross-Connection Map Report.

```

DALLAS LUCENT TECHNOLOGIES PF2000 7.2.0-ADR DALLAS LOCAL ONLY
;

DALLAS 97-05-13 15:52:58 EST
M Retrieve-Crossconnection-All: COMPLD
/* Cross-Connection Map Report
(.stby STS1,:stby STS3,--act STS1,==act STS3,*Invalid Flag,+AUDIT set to No)
=====
Destination/Source Cross-Connection Source/Destination
=====
- 1w-1 1e-1 -
BOSTON 1w-2 <=>2a 1e-2 -
- 1w-3 1e-3 -
- 1w-4 4b<=> 1e-4 ATLANTA
- 1w-5 1e-5 -
- 1w-6 6b<== 1e-6 ATLANTA
Continue report page-by-page?(y/n)Yes
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP EXIT CANCEL

```

66. From the report, select an unused slot (3a through 5b) and an unused tributary that is identified by a line number (1E or 1W) and a tributary number (3 through 5). An unused slot and tributary is indicated with a dash (-) in the Destination/Source columns.
67. Select the **CONFIGURATION-Enter-Crossconnection-STs1** command to add (slot to tributary) cross-connection. For example, **CONFIGURATION- Enter-Crossconnection-STs1:2-way,3a,1w-5** is a 2-way add/drop from slot 5b to tributary 5 in the West direction.

```

***** LUCENT TECHNOLOGIES PF2000 7.1.0-ADR CHICAGO LOCAL
ls-7b DS3 LAA2 S4:4 SNCLG0XAAA 683010 92MU08609552
ls-8a DS3 LAA2 S4:4 SNCLG0XAAA 683010 92MU09631447
ls-8b DS3 LAA2 S4:4 SNCLG0XAAA 683010 92MU10564725
ls-p1 DS3 LAA2 S4:4 SNCLG0XAAA 683010 92MU01571265
ls-p2 STS1E LAA4 S1:1 SNCLZJ0AAA 682303 93MU04560870

lssw-in LSSW LAA12 S1:3 SNCLFL0AAC 682943 92MU08628239
lssw-out LSSW LAA12 S1:3 SNCLFL0AAC 682943 93MU01598698
*/
;

CHICAGO 97-01-17 14:26:15 CST
M Enter-Crossconnection-STs1: COMPLD
/*
 Type: 2-way
 Source: ls-3a-1 CHICAGO
 Destination: sts1-1w-5-1 BOSTON
 Audit: Yes
*/
;

Press F9 to repeat last command or F3 to return to input mode.
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP REMOTE INPUT LOGOUT EXIT

```



68. Select the **CONFIGURATION-Retrieve-Crossconnection-All** command to obtain an updated STS-1 Cross-Connection Map Report and verify the add cross-connection (slot 5b to tributary 5).

```

=====
LUCENT TECHNOLOGIES PF2000 7.1.0-ADR CHICAGO LOCAL
=====

CHICAGO 97-01-17 14:36:13 CST
M Retrieve-Crossconnection-STs1: COMPLD
/*STs-1 Cross-Connection Map Report
(standby ..STs1,::STs3,active --STs1,==STs3,*:Invalid Flag,+:AUDIT set to No)
=====
Destination/Source Cross-Connection Source/Destination
=====
- 1w-1 1e-1 -
- 1w-2 1e-2 -
- 1w-3 1e-3 -
- 1w-4 1e-4 -
 BOSTON 1w-5 <=>3a 1e-5 -
- 1w-6 1e-6 -
- 1w-7 1e-7 -
- 1w-8 1e-8 -
- 1w-9 1e-9 -
- 1w-10 1e-10 -
Continue report page-by-page?(y/n)Yes
=====
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP EXIT CANCEL
=====

```

69. Select the **CONFIGURATION-Delete-Crossconnection-STS3** command to remove the cross-connection established in Step 67. Using the same example, select the **CONFIGURATION-Delete-Crossconnection-STS3: 2-way,5b,1w-5** command.
70. Select the **CONFIGURATION-Retrieve-Crossconnection-All** command to obtain an updated STS-1 Cross-Connection Map Report and verify the cross-connection has been removed.

```

FE ACTY LUCENT TECHNOLOGIES PF2000 7.1.0-ADR CHICAGO LOCAL ONLY

CHICAGO 97-01-17 14:59:40 CST
M Retrieve-Crossconnection-STS1: COMPLD
/*STS-1 Cross-Connection Map Report
(standby ..STS1,::STS3,active --STS1,==STS3,*:Invalid Flag,+:AUDIT set to No)
=====
Destination/Source Cross-Connection Source/Destination
=====
- 1w-1 1e-1 -
- 1w-2 1e-2 -
- 1w-3 1e-3 -
- 1w-4 1e-4 -
- 1w-5 1e-5 -
- 1w-6 1e-6 -
- 1w-7 1e-7 -
- 1w-8 1e-8 -
- 1w-9 1e-9 -
- 1w-10 1e-10 -
- 1w-11 1e-11 -
- 1w-12 1e-12 -
Continue report page-by-page?(y/n)Yes
F1 F2 F3 F4 F5 F6 F7 F8 F9 F10
HELP EXIT CANCEL

```

71. Log off the CIT by pressing **F4** (**LOGOUT**) or exit FT-2000 CIT-PC by pressing **F5** (**EXIT**) as appropriate.
72. **STOP! YOU HAVE COMPLETED THE ADD/DROP-RINGS TERMINAL TUTORIAL.**

---

# Glossary

---

## Numerics

### **0x1 Line Operation**

A 0x1 protection system has one service line and no protection line.

### **1+1 Line Protection**

A 1+1 protection system has two bidirectional lines. The transmitting terminal transmits the same signal on two lines. The receiving terminal monitors two lines independently and chooses one line as the active line and the other line as the standby line. When a protection switch occurs, the receiving terminal selects the signal from the standby line which causes the standby line to be the new active line. The original active line becomes the standby line. This status remains the same (nonrevertive) after the fault clears.

### **1x1 Line Protection**

A 1x1 protection system has two OC-48 high speed lines. One line is designated as the service line, and the other line is designated as the protection line. The service line normally carries traffic. When a protection switch occurs, the protection line is selected to carry traffic. When the fault clears, the original service line is selected (revertive). Also see Revertive Switching.

### **1xM Electrical Low Speed Interface Protection**

1xM electrical low speed interface protection refers to a system with one protection electrical low speed interface circuit pack (DS3 or STS1E) and up to 16 service electrical low speed interface circuit packs (DS3 or STS1E). When a low speed protection switch occurs, the electrical low speed signals are routed from the service low speed interface circuit pack to the protection low speed interface circuit pack. When the fault clears, the low speed signals are routed back to the service low speed interface circuit pack (revertive). Also see Revertive Switching.

---

## A

### **ABN**

Abnormal — An LED on the user panel that is lighted when a temporary condition potentially affecting transmission exists; for example, a CIT-initiated protection switch or lockout, or loopback.

### **ACO**

Alarm Cutoff — A pushbutton switch on the user panel that can be used to retire the active audible office alarm. If another alarmable condition occurs while the ACO is active, the highest level audible alarm is activated. The alarm cutoff function is also available from the CIT and from an operations system.

### **Active**

Active refers to a 1+1 protected OC-3 line that is currently selected by the receiving terminal at either end as the payload carrying signal. (Also refer to Standby.)

### **ADM**

Add/Drop Multiplexer

**Add/Drop-Rings Terminal Platform**

The SONET standards that are implemented in the FT-2000 OC-48 Lightwave System. More specifically, it is where one or more low speed shelves are interconnected to either one Enhanced high speed shelf (2-fiber ring) or two Enhanced high speed shelves (4-fiber ring). And, where any slot on the low speed shelf may be provisioned to have its traffic added/dropped from either the East or West OC-48 high speed links.

**AGNE**

Alarm Gateway Network Element — A network element that receives remote network element status information from other network elements in an alarm group and rebroadcasts the information to all network elements in the alarm group. Using AGNEs makes it unnecessary for each network element in the alarm group to communicate directly with all other network elements in the alarm group.

**AID**

Access Identifier — A unique identifier used to address circuit pack slots, ports, and tributaries.

**AIS**

Alarm Indication Signal — A maintenance signal transmitted downstream in a digital network that shows that an upstream failure has been detected and alarmed by some upstream equipment, if the upstream alarm has not been suppressed.

**Alarm**

A contact closure to the office alarm grid.

**Alarm Group**

A group of network elements that share remote alarm information through an alarm gateway network element (AGNE). Also refer to AGNE. The maximum size of an alarm group is 32 network elements.

**AMI**

Alternate Mark Inversion — A line code that employs a ternary signal to convey binary digits, in which successive binary ones are represented by signal elements that are normally of alternating, positive, and negative polarity but equal in amplitude, and in which binary zeros are represented by signal elements that have zero amplitude.

**ANSI**

American National Standards Institute — An organization consisting of producer, consumer, and general interest groups that establishes the procedures by which accredited organizations create and maintain voluntary industry standards in the United States.

**APD**

Avalanche Photodiode — A highly sensitive optical detector.

**APS**

Automatic Protection Switch — a protection switch that occurs in response to an automatically detected trouble condition.

**AS&C**

Alarm, Status, and Control.

**ASCII**

American Standard Code for Information Interchange — A standard 8-bit code used for exchanging information among data processing systems and associated equipment.

**ASN.1**

Abstract Syntax Notation 1.

**Asynchronous**

Refers to network elements that are not timed from references traceable to a single timing source.

**ATM**

Asynchronous Transfer Mode — A new technology based on a simple, fixed-length packet (or cell) format that allows integrated networking of voice, data, and video traffic. The packet length is set at 53 bytes. The first 5 bytes are reserved for address and control fields, and the remaining 48 bytes carry data.

**AUTO**

Automatic — One possible state of a low speed port or slot. When a port is in the automatic state and the presence of a good signal is detected, the port is automatically placed in the IS (in service) state. When a slot is in the automatic state and the presence of a circuit pack is detected, the slot is automatically placed in the EQ (equipped) state.

**Autonomous Indicators**

Autonomous indicators are those indicators that are subject to incoming signal alarm delay. These include office alarm outputs, parallel telemetry outputs, TL1 autonomous messages, the alarm LEDs (CR, MJ, and MN) on the user panel, and the network element status field in the upper left-hand corner of the CIT.

---

**B**

**B3ZS**

Bipolar 3-Zero Substitution — A line coding method that replaces a string of three zeros with a sequence of symbols having some special characteristic.

**B8ZS**

Bipolar 8-Zero Substitution — A line coding method that replaces a string of eight zeros with a sequence of symbols having some special characteristic.

**BCLAN**

Board Controller Local Area Network — The internal local area network that provides communications between the Line Controller circuit pack (terminals only) and board controllers on the circuit packs associated with an OC-48 high speed line. In the ^FT-2000 OC-48 Repeater Bay, the BCLAN provides communications between the System Controller circuit pack and board controllers on the circuit packs.

**BDFB**

Battery Distribution and Fuse Bay.

**BER**

Bit Error Rate — The ratio of bits received in error to bits sent.

**BIP**

Bit Interleaved Parity — A method of error monitoring over a specified number of bits (BIP-3 or BIP-8).

**BIP-N**

Bit Interleaved Parity - N — A method of error monitoring. With even parity, an N-bit code is generated by the transmitting equipment over a specified portion of the signal in such a way that the first bit of the code provides even parity over the first bit of all N-bit sequences in the covered portion of the signal. The second bit provides even parity over the second bits of all the N-bit sequences

within the specified portion, etc. Even parity is generated by setting the BIP-N bits so that there are an even number of ones in each of all the N-bit sequences including the BIP-N.

**BITS**

Building Integrated Timing Supply — A single clock that provides all the DS1 and DS0 synchronization references required by clocks in a building.

**BOC**

Bell Operating Company.

**Broadband Communications**

Voice, data, and/or video communications at rates greater than DS1 rates (1.544 Mb/s).

**Broadband Service Transport**

STS-3c and/or STS-12c transport over FT-2000 OC-48 links for ATM applications.

**BRT**

Business Remote Terminal.

---

**C**

**CC w/AIS**

Clear Channel with Alarm Indication Signal Insertion — A provisionable mode for the DS3 output in which the outgoing DS3 signals are passed through without any violation monitoring. If an incoming loss of signal is detected, a DS3 AIS is inserted.

**CC w/oAIS**

Clear Channel without Alarm Indication Signal Insertion — A provisionable mode for the DS3 output in which the outgoing DS3 signals are passed through without any violation monitoring. If an incoming loss of signal is detected, no DS3 AIS is inserted.

**CEV**

Controlled Environment Vault.

**CIT**

Craft Interface Terminal — A personal computer that meets FT-2000 minimum requirements loaded with the FT-2000 CIT-PC software or CPro-2000 software (Release 5 and later). In Release 5 and later releases, an ASCII-based (dumb) terminal that meets FT-2000 minimum requirements can also be used as a craft interface terminal.

**CCITT**

International Telephone and Telegraph Consultative Committee — An international advisory committee under United Nations sponsorship that has composed and recommended for adoption worldwide standards for international communications. Recently changed to the International Telecommunications Union (ITU) Telecommunication Standardization Sector (TSS).

**CO**

Central Office — A telephone company building where switching and/or transmission system equipment is located.

**Concatenated Switching**

Concatenated switching provides protection for simultaneous failures in adjacent sections of the same service line or different service lines. In concatenated switching the protection line is divided into sections. Each section of the protection line can be used to protect any service line in the section. Consequently, simultaneous failures in more than one section can be protected.

**Condition**

Conditions persist in time and indicate that there is something not normal about the system. Conditions appear in the Retrieve-Alarm Report.

**CR**

Critical (alarm).

**Cross-Connection**

A reconfigurable transmission path interconnection.

**Cross Connection Map**

The cross-connection map for each FT-2000 ring node contains information about how signals are connected between the high speed time slots and low speed interface packs. (See also Squelch Map).

**CTS**

Customer Technical Support — A Lucent Technologies organization that helps customers maintain installed systems (formerly known as the Technical Support Organization).

**Current Value**

The value currently assigned to a provisionable parameter.

**CV**

Coding Violation — A performance-monitoring parameter. The coding violations are monitored for the OC-48 line, OC-3 line, STS-48 section, STS-3 section, STS-1 path, EC-1 line, and DS3 path.

---

**D**

**DACS**

Digital Access and Cross-Connect System.

**DACS III-2000**

One of Lucent Technologies SONET-compatible Digital Access and Cross-Connect Systems.

**DACS IV-2000**

One of Lucent Technologies SONET-compatible Digital Access and Cross-Connect Systems.

**Data**

A collection of system parameters and their associated values.

**DCC**

Data Communications Channel — The embedded overhead communications channel in the SONET line. This is used for end-to-end communications and maintenance. It carries alarm, control, and status information between network elements in a SONET network.

**DCC Link**

Data Communications Channel Link — A bidirectional point-to-point fiber connection at the OC-3 (Release 6 or later) or OC-48 rate that has the DCC bytes terminated at both ends.

**DCE**

Data Circuit Equipment — The equipment that provides the signal conversion and coding between the data terminating equipment and the line. The DCE may be separate equipment or a part of the data terminating equipment.

**DCMS**

Design Change Management System — The Design Change Management System is used to issue product change notifications (PCNs) to customers.

**DDM-2000**

Lucent Technologies SONET-ready network multiplexer that can function as a lightwave terminal. It is designed primarily for loop feeder and interoffice applications that will work in existing asynchronous as well as the emerging SONET networks.

**DDM-FiberReach**

The DDM-2000 FiberReach Multiplexer is an OC-1 low density access product offering DS0 and DS1 services for business carrier access, fiber-in-the-loop, or private network applications. DDM-2000 FiberReach is hosted by the DDM-2000 OC-3 or OC-12 Multiplexer or the SLC-2000 Access System with a single- or dual-homed ring or star topology.

**DWDM**

Dense Wave Division Multiplexing — Transmitting multiple OC-48 optical signals (simultaneously) over a single fiber. The DWDM signal is interface compatible with the OLS product.

**Demultiplexing**

A process applied to a multiplexed signal for recovering signals combined within it and for restoring the distinct individual channels of these signals.

**Destination Node**

The destination node is the node in a ring where the low speed signals on an STS-3 tributary terminate. If an STS-3 tributary is bridged at one or more nodes in a ring, the last node where the low speed signals are terminated is the destination node. The destination node and source node of an STS-3 tributary are always in the same ring (also see Source Node).

**DFB**

Distributed Feedback.

**DM**

Degraded Minutes — A performance-monitoring parameter.

**DMA**

Direct Memory Access — A data transfer technique that uses a control circuit to move data between a device and memory.

**DPLL**

Digital Phase-Locked Loop.



**DRAM**

Dynamic Random Access Memory — Semiconductor random-access memory that requires short interval refreshing to retain its contents.

**DS3**

Digital Signal Level 3 (44.736 M/bs).

**DS3 Interface Circuit Pack**

The FT-2000 OC-48 Lightwave System DS3 Interface circuit pack interfaces with up to three bidirectional DS3 signals.

**DS-NE**

Directory Service Network Element — The single reference point in a ring network. The Directory Service Network Element stores the IDs of the other network elements in the ring and updates the other network elements after configuration changes.

**DSX**

Digital Cross-Connect Panel — A panel designed to interconnect equipment that operates at a designated rate. For example, a DSX-3 interconnects equipment operating at the DS3 rate.

**DTE**

Data Terminating Equipment — The equipment that originates data for transmission and accepts transmitted data.

**Dual Ring Interworking**

Dual Ring Interworking (DRI) is a configuration of two ring networks that share two common nodes. DRI allows a circuit with one termination in one ring and one termination in another ring to survive a loss-of-signal failure of the shared node that is currently carrying service for the circuit.

---

**E**

**EC, EC-n**

Electrical Carrier — The basic logical building block signal with a rate of 51.840 Mb/s for an EC-1 signal and a rate of n times 51.840 Mb/s for an EC-n signal.

**EC-1**

An STS-1 signal that has been shaped and encoded for transmission over electrical media. This refers to the actual physical representation of an electrical signal with an EC-1 format at a manual or electronic cross-connect interface. This signal is typically carried by coaxial cables from one equipment location to another. The term EC-1 not only refers to the organization and data rate of a signal but also to the voltage template the signal must conform to and the impedances for which the voltage template is valid.

**ECI**

Equipment Catalog Item

**EEPROM**

Electrically Erasable Programmable Read-Only Memory — Readable memory that is nonvolatile in nature, erased electrically, and programmed externally from the processor that uses it.

**EIA**

Electronic Industries Association — A trade association of the electronics industry that establishes electrical and functional standards.

**Electrical Line Buildout**

An equalizer network between the FT-2000 OC-48 Add/Drop-Rings Terminal and the DSX or STSX1 panel (or equivalent). It guarantees the proper signal level and shape at the DSX panel (or equivalent).

**EMC**

Electromagnetic Compatibility.

**EMI**

Electromagnetic Interference — High-energy, electrically induced magnetic fields that cause data corruption in cables passing through the fields.

**EPROM**

Erasable Programmable Read-Only Memory — Readable memory that is nonvolatile in nature, erased by exposure to intense ultraviolet light, and programmed externally from the processor that uses it.

**EQ**

Equipped — A memory administrative state for slots. EQ refers to a slot that is fully monitored and alarmed.

**Equal Level Cross-Connect**

A cross-connect panel where the signal strength (power) is the same for all the inputs and outputs so that any output can be connected to any input. The DSX-3 is an example of an equal level cross-connect panel.

**ES**

Errored Seconds — A second in which one or more coding violations are detected. Errored seconds are monitored for the OC-48 line, OC-3 line, STS-48 section, STS-3 section, STS-1 path, EC-1 line, and the DS3 path/line.

**ESD**

Electrostatic Discharge — The discharge of static electricity into equipment that potentially causes component damage and logic errors.

**ESF**

Extended Super Frame — A DS1 format option.

**Event**

Events happen at a particular time (do not persist in time) and appear in the Retrieve-History Report.

**Executable Code**

The "program" that controls the operation of the system.

**Externally Timed**

An operating condition of a network element in which it is locked to an external reference. (Also referred to as Phase Locked in releases prior to Release 5.)

**Extra Traffic**

Extra traffic is a capability of the FT-2000 OC-48 Add/Drop-Rings terminal that allows access to the protection STS-3 tributaries and STS-1 tributaries (Release 7 and later) of an OC-48 high speed line for low priority traffic or for restoration purposes.

---

## **F**

### **FDA**

Food and Drug Administration.

### **FDDI**

Fiber Distributed Data Interface.

### **FE ACTY**

Far-End Activity — An LED on the user panel that is lighted when an alarm or status condition exists at a remote FT-2000 OC-48 Repeater Bay or FT-2000 OC-48 Add/Drop-Rings Terminal.

### **FEBE**

Far-End-Block Error — An indication returned to the transmitting terminal that an errored block has been detected at the receiving terminal. A block is a specified grouping of bits.

### **FERF**

Far-End-Receive Failure — An indication returned to a transmitting terminal that the receiving terminal has detected an incoming failure. Also refer to the line remote defect indicator (RDI-L) signal.

### **FIT**

Failures in Time — Circuit pack failure rates per 10<sup>4</sup> hours as calculated using the method described in TR-TSY-000332, Issue 3, "*Reliability Prediction Procedure for Electronic Equipment (RPP)*".

### **Flash EPROM**

A new technology that combines the nonvolatility of EPROM with the in-circuit reprogrammability of EEPROM (electrically-erasable PROM).

### **FLS**

Frame Loss Seconds.

### **Folded Rings**

Folded (collapsed) rings are ring networks without fiber diversity. The terminology derives from the image of folding a ring into a linear segment.

### **Free Running**

An operating condition of a network element in which its local oscillator is not locked to any synchronization reference and is using no storage techniques to sustain its accuracy.

### **FT-LBA**

FT-Lightwave Booster Amplifier

### **FT-RDS**

FT-Ring Diversity Switch

---

## **G**

### **Gb/s**

Gigabits (10<sup>9</sup> bits) per Second.

### **GCNS-2000**

Lucent Technologies' Gigabit Cell Network Switch.

**GNE**

Gateway Network Element — A network element that serves as a single interface to the local X.25 message-based operations system for all the FT-2000 OC-48 Add/Drop-Rings Terminals in a ring network or all the network elements in a SONET subnetwork (for example, DDM-2000 Multiplexer).

**Grooming**

The rearrangement of STS-1 tributaries within an STS-3 tributary. DS3 and EC-1 signals may be rearranged within an STS-3 tributary using time slot assignment with STS-1 granularity.

---

**H****Hardware Ready**

The shelf, connector, and backplane have been designed to accept hardware (circuit packs) that is not available yet. Additional cables may be required.

**Headend**

Refers to the transmitting end where a signal originates.

**Holdover**

An operating condition of a network element in which its local oscillator is not locked to any synchronization reference but is using storage techniques to maintain its accuracy with respect to the last known frequency comparison with a synchronization reference.

---

**I****IMTV**

Interactive Multimedia Television.

**I/O**

Input/Output.

**IR**

Intermediate Reach.

**IS**

In Service — A memory administrative state for ports. IS refers to a port that is fully monitored and alarmed.

**IS-3 Interface Circuit Pack**

The FT-2000 OC-48 low-cost low speed (STS-3 rate) intraoffice optical interface.

**ITU**

International Telecommunications Union — An international advisory committee under United Nations sponsorship that has composed and recommended for adoption worldwide standards for international communications (also refer to CCITT).

---

## J

### **Jitter**

Timing jitter is defined as short-term variations of the significant instants of a digital signal from their ideal positions in time.

---

## L

### **LAN**

Local Area Network.

### **LBA**

Lightwave Booster Amplifier.

### **LBO**

Line Buildout — An equalizer network that guarantees the proper signal level. Also see electrical line buildout and optical line buildout.

### **LCLAN**

Line Controller Local Area Network — The internal local area network that provides communications between the System Controller circuit pack and the Line Controller circuit packs.

### **LED**

Light-Emitting Diode.

### **Line**

A bidirectional optical transmission line. In T1/Bellcore terminology, "line" refers to a transmission medium, together with the associated high speed equipment, required to provide the means of transporting information between two consecutive Network Elements, one of which originates the line signal and the other terminates the line signal.

### **LNCTL**

The FT-2000 OC-48 Lightwave System Line Controller circuit pack performs line specific computations.

### **LOF**

Loss of Frame — A failure to synchronize to an incoming signal.

### **LOHCTL**

The FT-2000 OC-48 Lightwave System Overhead Controller - LAN circuit pack provides user access to the LAN SONET overhead channels.

### **LOP**

Loss of Pointer — A failure to extract a good pointer from an STS-1 signal.

### **LOS**

Loss of Signal — The absence of an adequate incoming signal.

### **LSSW**

The FT-2000 OC-48 Lightwave System Low Speed Switch circuit pack performs low speed interface protection switching.

---

## M

### **μm**

Micrometer —  $10^{-6}$  meters

### **Menu**

A set of possible values for a parameter.

### **Midspan Meet**

The capability to interface between two lightwave terminals of different vendors. This applies to high speed optical interfaces.

### **MJ**

Major (alarm).

### **MML**

Human-Machine Language.

### **MN**

Minor (alarm).

### **Multiplexing**

The process of combining several distinct digital signals into a single composite digital signal.

---

## N

### **NCDRH**

National Center for Devices and Radiological Health.

### **NE**

Network Element — Refers to an FT-2000 OC-48 Repeater Bay or FT-2000 OC-48 Add/Drop-Rings Terminal in a network.

### **NE ACTY**

Near-End Activity — An LED on the user panel that is lighted when an alarm or status condition exists at the local FT-2000 OC-48 Repeater Bay or FT-2000 OC-48 Add/Drop-Rings Terminal.

### **NEBS**

Network Equipment-Building System.

### **NID**

Node Identifier — A unique number from 0 to 15 used to identify an FT-2000 OC-48 Add/Drop-Rings Terminal in a bidirectional ring application. In Release 6 and later releases, the NID is automatically provisioned.

### **nm**

Nanometer —  $10^{-9}$  meters.

### **NMA**

Network Monitoring and Analysis System — Bellcore's alarm surveillance operations system.

**NMON**

Not Monitored — A memory administrative state for ports. This refers to a port that is not monitored and will not transition to the IS state if a good signal is detected.

**Node ID**

Node Identifier — See NID.

**Nonautonomous Indicators**

The nonautonomous indicators are those indicators that are not subject to incoming signal alarm delay. These indicators include the circuit pack FAULT LEDs, all CIT reports, TBOS alarm and status points, TL1 command response messages, and the LEDs on the user panel (except CR, MJ, and MN).

**Non-Preemptible Protection Access**

Available on a per STS-3 tributary basis. This feature increases a nodes "service traffic" capacity at the expense of reducing bandwidth on the protection fiber. Tributaries that are used this way are themselves "non-protected" and are not preempted in the advent of a switch from the service fiber to the protection fiber.

**Nonrevertive Switching**

In nonrevertive switching, there is an active and standby line, circuit pack, etc. When a protection switch occurs, the standby line, circuit pack, etc., is selected causing the old standby line, circuit pack, etc., to become the new active line, circuit pack, etc. The original active line, circuit pack, etc., becomes the standby line, circuit pack, etc. This status remains in effect when the fault clears. Therefore, this protection scheme is "nonrevertive" in that there is no switch back to the original status in effect before the fault occurred.

**NRZ**

Nonreturn to Zero.

**NSA**

Non-Service Affecting.

**NSAP**

AddressNetwork Services Access Point Address — An automatically assigned number that uniquely identifies a Network Element for the purpose of routing DCC messages.

**Null Modem**

A null modem cable allows a CIT local access to the FT-2000 OC-48 Lightwave System via the DTE port on the interconnection panel of the Low Speed Shelf - System Controller or Repeater Shelf - System Controller.

---

**O**

**O&M**

Operation and Maintenance.

**OALAN**

Overhead Access Local Area Network — The internal local area network that provides communications between the System Controller circuit pack and the Overhead Controller circuit pack.

**OAM&P**

Operations, Administration, Maintenance, and Provisioning.

**OC, OC-n**

Optical Carrier — The optical signal that results from an optical conversion of an STS signal; that is, OC-1 from STS-1 and OC-n from STS-n.

**OC3 Optical Interface Circuit Pack**

The FT-2000 OC-48 Lightwave System OC3 Optical Interface circuit pack interfaces with one bidirectional OC-3 signal.

**OC12 Optical Interface Circuit Pack**

The FT-2000 OC-48 Lightwave System OC12 Optical Interface circuit pack interfaces with one bidirectional OC-12 signal.

**OC48 RCVR**

The FT-2000 OC-48 Lightwave System OC48 Receiver circuit pack interfaces with the incoming OC-48 high speed line (receive direction).

**OC48 REGENR**

The FT-2000 OC-48 Lightwave System OC48 Regenerator circuit pack regenerates one OC-48 signal.

**OC48 TRMTR**

The FT-2000 OC-48 Lightwave System OC48 Transmitter circuit pack interfaces with an outgoing OC-48 high speed line (transmit direction).

**OHCTL (TERM)**

The FT-2000 OC-48 Lightwave System Overhead Controller - Terminal circuit pack provides user access to the OC-48 SONET overhead channels.

**OLS**

OLS is a standalone optical multiplexing (up to 20 Gb/s per fiber) system. The OLS has its own telemetry, fault location, and performance monitoring capability.

**Operations Interface**

Any interface that provides information on the system behavior or control. These include the equipment LEDs, user panel, CIT, office alarms, and all telemetry interfaces.

**Operations Interworking**

The capability to access, operate, provision, and administer remote systems through CIT access from any site in a SONET network or from a centralized operations system.

**Optical Line Buildout**

An equalizer network between the FT-2000 OC-48 Add/Drop-Rings Terminal and the lightguide cross-connect panel (or equivalent). It guarantees the proper received signal level at OC48 RCVR circuit pack.

**Original Value**

The value given to a CIT provisionable parameter at the factory.

**Original Value Provisioning**

The original values are the values given to CIT provisionable parameters at the factory. At system startup, the original values are copied and become the current values. These current values can be changed using local or remote provisioning. The original values can never be changed.

**OS**

Operations System — A central computer-based system used to provide operations, administration, and maintenance functions.



**OSI**

Open Systems Interconnection — Referring to the OSI reference model, a logical structure for network operations standardized by the International Standards Organization (ISO).

**OTDR**

Optical Time Domain Reflectometer.

---

**P**

**Parameter**

A characteristic of the system that affects its operation.

**Path**

A path at a given rate is a logical connection between the point at which a standard frame format for a signal at the given rate is assembled and the point at which the standard frame format for the signal is disassembled.

**Path AIS**

Path Alarm Indication Signal — A path level code that is sent downstream in a digital network as an indication that an upstream failure has been detected and alarmed by some upstream equipment, if the upstream alarm has not been suppressed.

**Path Terminating Equipment**

Network elements in which the path overhead is terminated.

**Path Trace Signal**

The path trace signal is provided in the STS-1 path trace byte (J1) of the path overhead. The path trace signal is transmitted repetitively to verify the continuity of the STS-1 path. The DS3 circuit pack is the only low speed interface circuit pack that generates or terminates the STS-1 path overhead.

**Path Unequipped Signal**

The path unequipped signal is provided in the STS-1 path signal label byte (C2) of the path overhead. The path unequipped signal is transmitted to notify downstream equipment that the STS-1 path is incomplete (for example, a low speed interface slot in the AUTO state or the absence of a valid cross-connection). The DS3 circuit pack is the only low speed interface circuit pack that terminates the STS-1 path overhead.

**Path Yellow Signal**

A maintenance signal used to indicate the presence of an alarm, detected at the remote terminal, in the path originating locally. Also, refer to the path remote defect indicator <sup>^</sup>(RDI-P) signal.

**Payload Pointer**

The offset in bytes from the Pointer Action Byte (H3) to the beginning of the synchronous payload envelope.

**PC**

Personal Computer.

**PCN**

Product Change Notification — Product change notifications are issued to notify customers that in-service product changes are required to correct an existing or potential problem. Product change notifications are issued through the Design Change Management System.

**Phase Locked**

An operating condition of a network element in which it is locked to an external reference. (Also referred to as Externally Timed in Release 5 and later releases.)

**Platform**

In the FT-2000 OC-48 Lightwave System, a platform is a family of equipment and software configurations designed to support a particular set of applications.

**Plesiochronous Network**

A network that contains multiple subnetworks, each internally synchronous and all operating at the same nominal frequency, but whose timing may be slightly different at any particular instant.

**PLL**

Phase Locked Loop.

**PM**

Performance Monitoring — Measures the quality of service and identifies degrading or marginally operating systems (before an alarm would be generated).

**POH**

Path Overhead — Overhead assigned to and transported with the payload until the payload is demultiplexed. It is used for functions that are necessary to transport the payload.

**Preemptible Traffic**

Also called "Extra Traffic"/ Traffic, which has a lower priority than that carried on the service fiber. Low priority (unprotected) traffic that is carried on a protection fiber and is subject to being dropped — if the service fiber should fail and a protection switch occurs.

**Preprovisioning**

The capability to provision a slot before installing a circuit pack.

**Primary Node**

The primary node is the interconnecting node in the Dual Ring Interworking application where a 1-way-dri cross-connection has been entered. When a fault occurs, the through cross-connection at the primary node switches to the active state, and the add cross-connection switches to the standby state.

**Proactive Maintenance**

Refers to the process of detecting degraded conditions not severe enough to initiate protection switching or alarming, but indicative of an impending signal fail or signal degrade defect.

**Provisioning**

Assigning a value to a parameter in memory.

**PSC**

Protection Switch Counts — The number of times that a protection switch has been performed. The protection switch counts are monitored for the OC-48 line and OC-3 line.

**PVC**

Permanent Virtual Circuit.

**PWR**

Power.

---

## **R**

### **RBP**

Receive Byte Processor — A device on the OC48 RCVR and OC48 REGENR circuit packs.

### **RDI-L Signal**

Line Remote Defect Indication Signal— An indication returned to a transmitting terminal that the receiving terminal has detected an incoming failure. This is new terminology adopted by the T1M1 committee for the SONET environment, which replaces the far-end-receive-failure (FERF) signal. If the RDI-L signal is integrated for a specified period of time, it becomes a remote failure indication (RFI) signal (a defect becomes a failure). Also refer to RFI.

### **RDI-P Signal**

Path Remote Defect Indicator — A maintenance signal inserted toward the OC-48 line by the DS3 circuit pack to show the presence of an alarm, detected at the remote terminal, in the path originating locally. This is new terminology adopted by the T1M1 committee for the SONET environment, which replaces the path yellow signal. If the RDI-P signal is integrated for a specified period of time, it becomes a remote failure indication (RFI) signal (a defect becomes a failure). Also refer to RFI.

### **RDS**

Ring Diversity Switch.

### **Reactive Maintenance**

Refers to detecting defects/failures and clearing them.

### **Revertive Switching**

In revertive switching, there is a service and protection high speed line, circuit pack, etc. When a protection switch occurs, the protection line, circuit pack, etc., is selected. When the fault clears, service "reverts" back to the original service line. Also see 1x1 Line Protection.

### **RF**

Raio Frequency.

### **RFI**

Remote Failure Indication — A signal that alerts upstream STS-1 path terminating equipment that a downstream failure has been alarmed along the STS-1 path. This action prevents multiple alarms from being activated for the same failure.

### **RMS**

Root Mean Square.

### **RPP**

Reliability Prediction Procedure.

### **RTAC**

Regional Technical Assistance Center — A Lucent Technologies organization that helps customers maintain installed systems.

### **RZ**

Return to Zero.

---

## S

### SA

Service Affecting.

### SAS

Severely Errored Frame/AIS Seconds — A second in which there is an out of frame or AIS defect detected in the DS3 signal received from OC-48 line (DS3 path).

### SD

Signal Degrade — A provisionable parameter for the incoming OC-48 high speed signal and DS3 signal (from cross-connect panel) that determines the threshold at which a signal degrade condition is declared.

### SDH

Synchronous Digital Hierarchy.

### Secondary Node

The secondary node is the interconnecting node in the Dual Ring Interworking application where only a 2-way add/drop (or a 1-way add and a 1-way drop) cross-connection has been entered. The secondary node is adjacent to the primary node (also see Primary Node).

### SEFS

Severely Errored Frame Seconds — The number of seconds during which an out-of-frame event occurred. The severely errored frame seconds are monitored for the ^STS-48 section, STS-3 section, and EC-1 section.

### SES

Severely Errored Seconds — A second in which the coding violations detected reach a fixed threshold. The severely errored seconds are monitored for the OC-48 line, OC-3 line, STS-48 section, STS-3 section, STS-1 path, EC-1 line, and DS3 path.

### SF

Signal Fail — Refers to a condition when an incoming signal's bit error rate exceeds a fixed value.

### Single-Ended Operations

The capability to perform operations, administration, maintenance, and provisioning of remote network elements on a centralized basis.

### Site Address

The confirmed F-byte address of the network element on OC-48 high speed lines. The site address consists of the line address and site number (for example, site-1e-0/site-1w-4).

### SLM

Single Longitudinal Mode.

### Software Ready

The software that runs on the FT-2000 OC-48 Lightwave System will accept, run, boot, and operate normally when future hardware (circuit packs) is installed.

### SONET

Synchronous Optical NETWORK — The American National Standards Institute's standard for broadband optical fiber communications.

**Source Node**

The source node is the node in a ring where the low speed signals on an STS-3 tributary originate. The source node and destination node of an STS-3 tributary are always in the same ring. Also see Destination Node.

**SPE**

Synchronous Payload Envelope — A 125-microsecond frame structure composed of STS path overhead and bandwidth for the payload.

**Squelch Map**

A map (table) containing information for each cross connection in the ring and indicates the source and destination nodes for the low speed circuit that define the cross connection. This information is used to prevent traffic misconnection in rings with isolated nodes or arguments. (This term is sometimes referred to as "Ring Squelch Map").

**Squelching**

To avoid misconnected traffic when performing a 2-fiber ring protection switch, the FT-2000 OC-48 Add/Drop-Rings Terminal inserts STS-1 path AIS on protection tributaries for traffic that cannot be terminated because of node failures or multiple failures in the ring. Squelching is only performed by nodes that have an active protection switch.

**Standby**

Standby refers to a 1+1 protected OC-3 line that is not currently selected by the receiving terminal at either end as the payload carrying signal. (Also refer to Active.)

**Status**

Status is a condition that does not raise an alarm.

**STS, STS-n**

Synchronous Transport Signal — The basic logical building block signal with a rate of 51.840 Mb/s for an STS-1 signal and a rate of n times 51.840 Mb/s for an STS-n signal.

**STS-1**

This refers to the FT-2000 OC-48 Add/Drop-Rings Terminal internal electrical signal with an EC-1 format.

**STS-1E**

The FT-2000 OC-48 Lightwave System STS1 Electrical Interface circuit pack interfaces with up to three bidirectional EC-1 signals.

**STS-1 Granularity**

In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal has an STS-1 time slot assignment capability that allows any service STS-1 tributary to be cross-connected to any service low speed port or to the same-numbered service STS-1 tributary in the opposite direction.

**STS-1 Group**

Three STS-1 signals in the same STS-3 signal.

**Subnetwork**

SONET networks with data communications channel connectivity. When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 or Release 9 of the DDM-2000 OC-3 Multiplexer, the recommended maximum subnetwork size is 24 network elements. In Release 7 and later releases, the recommended maximum subnetwork size is 32 network elements.

**Sync Messaging**

A method whereby nodes within a FT2000 ring communicate their synchronization status with other nodes in the ring.

**Synchronous**

Refers to network elements that are timed from references traceable to a single timing source.

**Synchronous Network**

The synchronization of synchronous transmission systems with synchronous payloads to a master (network clock that can be traced to a single reference clock).

**SYSCTL**

The System Controller and System Memory circuit packs provide the highest level of system control for the FT-2000 OC-48 Lightwave System. The System Controller circuit pack provides overall administrative control of the system.

**SYSTEMEM**

The System Memory and System Controller circuit packs provide the highest level of system control for the FT-2000 OC-48 Lightwave System. The System Memory circuit pack provides memory support for the SYSCTL circuit pack.

---

**T**

**T1X1 and T1M1**

The *ANSI* committees responsible for telecommunications standards.

**TA**

Technical Advisory.

**TBOS**

Telemetry Byte-Oriented Serial (Protocol) — Defines one physical interface for direct connection between the telemetry remote and the monitored equipment. An EIA-422 port is used to provide the operations system with enough alarm and status information to localize a problem and determine the severity.

**TBP**

Transmit Byte Processor — A device on the OC48 TRMTR and OC48 REGENR circuit packs.

**TCA**

Threshold-Crossing Alert — A condition set when a counter exceeds a user-selected high or low threshold. A TCA does not generate an alarm but is available on demand through the CIT.

**TEC**

Thermo-Electric Cooler.

**TG3 (DS1)**

The FT-2000 OC-48 Lightwave System Timing Generator, Stratum 3 - DS1 circuit pack generates clock signals for distribution to the transmit circuits. It operates in the free running, loop timed, through timed, externally timed, and holdover modes.

**Through Timed**

Refers to a network element that derives its transmit timing in the east direction (Line 1E) from a received line signal in the west direction (Line 1W) and the transmit timing in the west direction (Line 1W) from a received line signal in the east direction (Line 1E).

**TID**

Target Identifier — A provisionable parameter used to identify the FT-2000 OC-48 Lightwave System.

**TIE**

Time Interval Error — The variation of time delay of a given timing signal with respect to an ideal timing signal over a particular period of time.

**TL1**

Transaction Language 1 — A machine-to-machine communications language that is a subset of CCITT's human-machine language.

**TOHCTL**

The FT-2000 OC-48 Lightwave System Overhead Controller - Tributary circuit pack provides user access to the OC-3 data communications channel (DCC). The TOHCTL terminates up to eight OC-3 DCCs.

**TOP**

Task Oriented Practice.

**TR**

Technical Reference.

**Tributary**

A 51.84 Mb/s signal (STS-1 level) or 155 Mb/s signal (STS-3 level) within the FT-2000 OC-48 Lightwave System. The maximum capacity of an OC-48 signal is forty-eight 51.84 Mb/s tributaries (or sixteen 155 Mb/s tributaries).

**TSA**

Time Slot Assignment — This is a capability of the FT-2000 OC-48 Add/Drop-Rings Terminal that allows any service STS-3 tributary to be cross-connected to any service low speed interface slot or to the same-numbered service STS-3 tributary in the opposite direction. In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal has an STS-1 TSA capability that allows any service STS-1 tributary to be cross-connected to any service low speed port or to the same-numbered service STS-1 tributary in the opposite direction.

**TSO**

Technical Support Organization — Refer to as Customer Technical Support (CTS).

---

**U**

**UAS**

Unavailable Seconds — A second during which the monitored facility is unavailable. The unavailable seconds are monitored for the OC-48 line, OC-3 line, STS-1 path, EC-1 line, and DS3 path.

**Upgrade**

An upgrade is the addition of new capabilities (features) to an existing platform. This requires new software and may require new hardware.

---

## V

### **Value**

A number, text string, or other menu selection associated with a parameter.

### **VF Circuit**

Voice Frequency Circuit — A 64-kb/s digitized signal.

### **VM**

Violation Monitor — A provisionable mode for the DS3 output that causes parity violations to be monitored but not corrected before the DS3 signal is B3ZS encoded.

### **VMR**

Violation, Monitor, and Removal — A provisionable mode for the DS3 output that causes parity violations to be monitored and corrected before the DS3 signal is B3ZS encoded.

### **VT**

Virtual Tributary — A structure designed for transport and switching of sub-STs-1 payloads. There are currently four sizes: VT1.5 (1.728 Mb/s), VT2 (2.304 Mb/s), VT3 (3.456 Mb/s), and VT6 (6.912 Mb/s).

### **VT-G**

Virtual Tributary Group — A 9-row by 12-column structure (108 bytes) that carries one or more VTs of the same size. Seven VT groups (756 bytes) are byte interleaved with the VT-organized synchronous payload envelope.

---

## W

### **Wander**

The wander of a digital signal is defined as the *long term* variations of the significant instants of a digital signal from their ideal positions in time.

### **WDM**

Wavelength Division Multiplexing.

### **Wideband Communications**

Voice, data, and/or video communications from the DS0 rate (64 kb/s) to the DS1 rate (1.544 Mb/s).

---

## Z

### **Zero Code Suppression**

A technique used to reduce the number of consecutive zeros in a line-codes signal (B3ZS for DS3 signals).









# **FT-2000 OC-48 Lightwave System Releases 1 Through 7.2**

User/Service Manual (TOP)

Volume II

365-575-102  
Issue 8.0  
July 2002

**Copyright© 2002 Lucent Technologies**  
**All Rights Reserved**  
**Printed in U.S.A.**

This material is protected by the copyright and trade secret laws of the United States and other countries. It may not be reproduced, distributed or altered in any fashion by any entity (either internal or external to Lucent Technologies), except in accordance with applicable agreements, contracts, or licensing, without the express written consent of the Customer Training and Information Products organization and the business management owner of the material.

For permission to reproduce or distribute this document please contact your Account Executive.

**Notice**

The information in this document is subject to change without notice. Although every effort has been made to make this document as accurate, complete, and clear as possible, Lucent Technologies Inc. and its predecessors assume no responsibility for any errors that may appear in this document.

**Federal Communications Commission (FCC) Notification and Repair Information<sup>1</sup>**

This equipment is designed to comply with the limits for a Class A digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions manual, may cause interference to radio communications. Operation of this equipment in a residence is likely to cause harmful interference, in which case, the user will be required to correct the interference at his own expense.

**Security**

In rare instances, unauthorized individuals make connections to the telecommunications network. In such an event, applicable tariffs require that the customer pay all network charges for traffic. Lucent Technologies Inc. and its predecessors cannot be responsible for such charges and will not make any allowance or give any credit for charges that result from unauthorized access.

**Trademarks**

5ESS, DACScan, LGX, SLC, and ST and are registered trademarks of Lucent Technologies.

ANSI is a registered trademark of American National Standards Institute, Inc.

Common Language is a registered trademark and CLEI, CLLI, CLCI, and CLFI are trademarks of Bell Communications Research, Inc.

CSA is a registered trademark of Canadian Standards Association.

DANTEL is a registered trademark of DANTEL Incorporated.

ESS is a trademark of Lucent Technologies.

Gateway 2000 is a registered trademark of Gateway 2000, Inc.

IBM is a registered trademark of Microsoft Corporation.

MS-DOS is a registered trademark of Microsoft Corporation.

NCR is a trademark of NCR Corporation.

NEC is a registered of Nippon Denki Kabushiki Kaisha.

Netscape is a registered trademark of Netscape Communications Corporation.

PROCOMM PLUS is a registered trademark of Datastorm Technologies Inc.

SPARC is a registered trademark of SPARC International, Inc., licensed exclusively to Sun Microsystems Inc.

Sun is a registered trademark of SUN Microsystems Inc.

Titan is a registered trademark of Tellabs, Inc.

WaveStar is a registered trademark of Lucent Technologies.

Windows is registered trademark of Microsoft Corporation.

**Warranty**

The terms and conditions of sale will include a 1-year warranty on hardware and applicable software.

1. Language of statute.

**Customer Assistance and Technical Support**

The Lucent Technologies Regional Technical Assistance Center (RTAC) provides a technical assistance telephone number that is monitored 24 hours a day. For technical assistance, call 1-800-225-RTAC. You can also call this telephone number to provide comments on FT-2000 OC-48 Lightwave System or to suggest enhancements.

**Documentation**

This document was developed by the Lucent Technologies Optical Networking Group, Lucent Learning Services (formerly Customer Training and Information Products [CTIP]).



# Lucent Technologies values your comments!

FT-2000 OC-48 User/Service Manual, Releases 1.0 Through 7.2

365-575-102 Issue 8.0 Date: July 2002

Lucent Technologies welcomes your comments on this information product. Your opinion is of great value and helps us to improve.

**1. Was the information product:**

|                                             | Yes                      | No                       | Not applicable           |
|---------------------------------------------|--------------------------|--------------------------|--------------------------|
| In the language of your choice?             | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| In the desired media (paper, CD-ROM, etc.)? | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| Available when you needed it?               | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |

Please provide any additional comments:

---

---

**2. Please rate the effectiveness of this information product:**

|                         | Excellent                | More than satisfactory   | Satisfactory             | Less than satisfactory   | Unsatisfactory           | Not applicable           |
|-------------------------|--------------------------|--------------------------|--------------------------|--------------------------|--------------------------|--------------------------|
| Ease of use             | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| Level of detail         | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| Readability and clarity | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| Organization            | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| Completeness            | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| Technical accuracy      | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| Quality of translation  | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |
| Appearance              | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> | <input type="checkbox"/> |

If your response to any of the above questions is "Less than satisfactory" or "Unsatisfactory," please explain your rating.

---

---

**3. If you could change one thing about this information product, what would it be?**

---

---

**4. Please write any other comments about this information product:**

---

---

**Please complete the following if we may contact you for clarification or to address your concerns:**

Name: \_\_\_\_\_ Date: \_\_\_\_\_  
Company/organization: \_\_\_\_\_ Telephone number: \_\_\_\_\_  
Address: \_\_\_\_\_  
Email address: \_\_\_\_\_ Job function: \_\_\_\_\_

If you choose to complete this form online, go to <http://www.lucent-info.com/comments>  
Otherwise fax to 407 767 2760 (U.S.) or +1 407 767 2760 (outside the U.S.) or email comments to [ctiphotline@lucent.com](mailto:ctiphotline@lucent.com)







---

# **Operation and Maintenance (TOP)**

---

## **Overview**

---

This section supports the operation and maintenance of the FT-2000 OC-48 Lightwave System product via task oriented practices (TOP) procedures. Use of these procedures should be preceded by a general knowledge and understanding of the software for the FT-2000 network. Refer to Volume I, Section 11, "Craft Interface Terminal Usage." The TOP procedures are included in the following five separate tabs:

- Acceptance — Yellow tab
- Circuit Order — Orange tab
- Operation — Green tab
- Trouble Clearing — Red tab
- Detailed Level Procedures — Blue tab

## **Reason for Reissue**

---

This document has been reissued for the following reasons:

- To make modifications to the following procedures:
  - TAP-113
  - DLP-550
- To add the following procedure:
  - DLP-551
- To correct inconsistencies and/or errors that occurred during the processing of previous issues

## **TOP Section Organization**

---

The **Acceptance** tab covers procedures that are used to accept the hardware AFTER installation by someone else.

The **Circuit Order** tab covers procedures that are used when adding or deleting work order items like a DS3 path, a DS3 circuit pack, or an OC-48 line, or when entering/deleting cross-connections in a ring network.

The **Operation** tab covers procedures that can be used in day-to-day operations. Such procedures are to enter/change/delete a user's login, to establish a remote login using the CIT, to establish remote access to a network element by using a dial-up modem, etc. A complete listing is in the "Operation Task Index" (Operation: IXL-001).

The **Trouble Clearing** tab covers procedures on clearing the "condition" that caused the alarm and clearing trouble reports. These procedures are consistent with the two-tier maintenance philosophy.

The **Detailed Level Procedures (DLP)** tab contains detailed "how to" instructions, beginning with DLP-500.

Some procedures have an introductory "**Overview**" section that explains the purpose of the procedure. The other procedures are self-explanatory from the title.

## How To Use This TOP Section

---

A tutorial is provided for you in Section 11 of this manual to familiarize yourself with the interactions of the CIT-PC software and the network element. However, *it is strongly recommended that this tutorial be used only with an FT-2000 network element that is **out of service**.*

To find the instructions for performing acceptance, circuit order, operation, or trouble-clearing tasks, proceed as follows:

1. Find your job in the Task Index under the appropriate colored tab and go to the referenced procedure; for example, **Acceptance IXL-001** (this is the index for the acceptance tasks). Or, you may use the Master Task Index (**Operation and Maintenance: IXL-001**) to find your job.
2. Go to the referenced procedure. This is a director level procedure and includes one of the following types:
  - a. Non-Trouble-Clearing Procedure (**NTP**) - a director level procedure that lists normal work items to perform other than trouble clearing.
  - b. Trouble Analysis Procedure (**TAP**) - a director level procedure that contains step-by-step trouble-clearing instructions to locate and/or fix troubles. The maintenance philosophy is contained in trouble analysis data (**TAD**)-100.
  - c. Some DLPs are director level procedures. These procedures combine the "what to do" with the "how to do" information; for example, replacing a fan filter in DLP-526.
3. All procedures are listed in numerical order but are not used in numerical order. A "Numerical List of Procedures" is provided as a **reference only** and should not be used alone in finding instructions for performing tasks.
4. Perform all the items in the director level procedure in the suggested order unless it sends you to another director level procedure. After you have completed a director level procedure, you have finished the task. Where more detailed information is required, you will be sent to a Detailed Level Procedure **DLP-( )**. Or, you may be sent to a Detailed Level Procedure by another Detailed Level Procedure.
5. **IMPORTANT:** When you complete a Detailed Level Procedure, you **MUST** return to the procedure (NTP, TAP, or DLP) that sent you there unless the DLP is a director level procedure; for example, replacing a fan filter (DLP-526).
6. **IMPORTANT:** Unless otherwise instructed, if one director level procedure sends you to another director level procedure, you should not return to the first director level procedure after you complete the second.
7. Sometimes you will be asked to verify that things have occurred. This may take the form of a formal statement of the expected response. At other times, the instructions will merely state *verify that ...*.

8. If the expected response is not observed and a specific trouble-clearing reference is not made, you should reference the "Trouble Clearing Task Index" to start trouble clearing.
9. If you need help in clearing a trouble after completing all the applicable trouble-clearing procedures in this section, contact your local or regional maintenance assistance group. Then, the group may contact the Regional Technical Assistance Center (RTAC) at **1-800-225-RTAC** in accordance with local procedures.

Faulty equipment being returned for repair should be sent to the following address:

Lucent Technologies  
RS&R Department  
1600 Osgood Street  
North Andover, Massachusetts 01845

## **Screen Display and Equipment Designation Fonts**

---

The procedures in this manual use a MONOSPACE font to identify text on a screen or a response displayed from the network. A **BOLD** font identifies the lettering designations on panels, shelves, and circuit packs. Also, the bold font is used throughout this manual for emphasis.

## **FT-2000 Installation**

---

The 365-575-115, *FT-2000 OC-48 Lightwave System, Releases 3 through 8.1, Installation Manual* is available for customers planning to install the equipment themselves. To acquire this manual, refer to "How to Order Documents" in the About This Document tab.

## **FT-2000 Software Release Description**

---

Different releases have different comcode numbers and/or document numbers but all documents contain information such as known problems, problems that have been fixed from an earlier release, and upgrade/download procedures. For the specific release and document, refer to the **Operation: NTP-002** or to the "Related Documentation/Training" part of the "About This Document" section at the beginning of this manual.

## Safety Instructions

---

### Admonishments

---

This section may contain admonishments as **DANGERS**, **WARNINGS**, and **CAUTIONS**. These admonishments have the following definitions:

**DANGER** shows the presence of a hazard that *will* cause death or severe personal injury if the hazard is not avoided.

**WARNING** shows the presence of a hazard that *can* cause death or severe personal injury if the hazard is not avoided.

**CAUTION** shows the presence of a hazard that *will* or *can* cause minor personal injury or property damage if the hazard is not avoided. The caution is also used for property-damage-only accidents. This includes equipment damage, loss of software, or service interruption.

### Lightwave Safety

---

A Lucent Technologies lightwave digital transmission system and associated optical test sets use semiconductor laser transmitters. The lasers emit lightwaves, at or near infrared wavelengths, into lightguide cables. This light is at the red end of the visible spectrum. Direct exposure at close distances should be avoided.

Never view any unterminated optical connector with optical instruments other than indirect image-converting devices such as the FIND-R-SCOPE, since viewing optics tends to collimate the energy from an optical connector and, hence, increases the potential risk for injury.

For Warning Label and Compliance Label information, refer to the "About This Document" Section.

## **Electrostatic Discharge (ESD) Considerations**

---



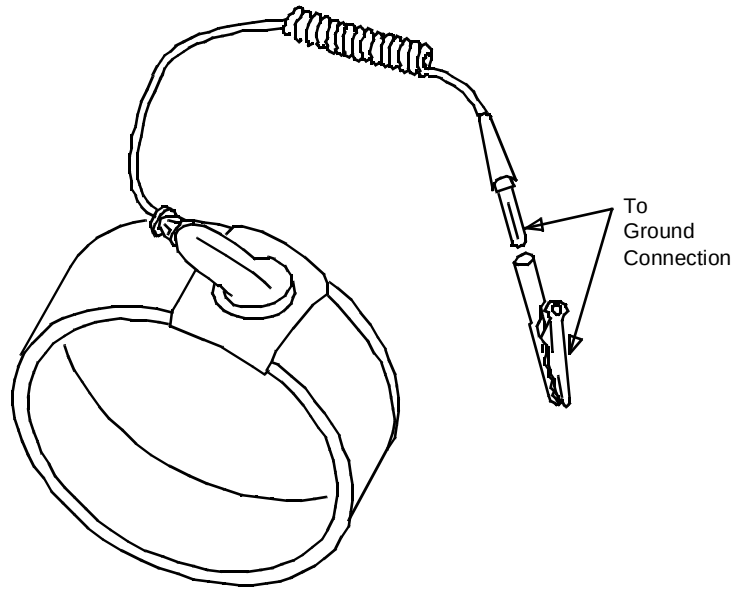
### **CAUTION:**

*Industry experience has shown that all integrated circuit packs can be damaged by static electricity that builds up on work surfaces and personnel. The static charges are produced by various charging effects of movement and contact with other objects. Dry air allows greater static charges to accumulate. Higher potentials are measured in areas with low relative humidity, but potentials high enough to cause damage can occur anywhere.*

Observe the following precautions when handling circuit packs to prevent damage by electrostatic discharge:

- Assume all circuit packs contain solid state electronic components that can be damaged by ESD.
- When handling circuit packs (storing, inserting, removing, etc.) or when working on the backplane, always wear a grounded wrist strap or wear a heel strap and stand on a grounded, static-dissipating floor mat.
- Handle all circuit packs by the faceplate or latch and by the top and bottom outermost edges. Never touch the components, conductors, or connector pins.
- Observe warning labels on bags and cartons. Whenever possible, do not remove circuit packs from antistatic packaging until ready to insert them into slots.
- If possible, open all circuit packs at a static-safe work position, using properly grounded wrist straps and static-dissipating table mats.
- Always store and transport circuit packs in static-safe packaging. Shielding is not required unless specified.
- Keep all static-generating materials such as food wrappers, plastics, and Styrofoam containers away from all circuit packs. On removal from bay, immediately put circuit packs into static-safe packages.
- Whenever possible, maintain relative humidity above 20 percent.
- Keep the electromagnetic interference (EMI)/ESD protective front covers on the shelves at all times except during an upgrade or maintenance procedure. Once a circuit pack is replaced in the shelf, close the front cover immediately.

To reduce the possibility of ESD damage, shelves are equipped with grounding jacks to enable personnel to ground themselves using wrist straps (Figure 1) while handling circuit packs or working on a shelf. The jacks for connection of wrist straps are located at the lower right-hand corner of each user panel and filter panel and at the rear of the bay. These jacks are labeled. The wrist straps should be checked periodically with a wrist strap tester to ensure that they are working properly.



---

**Figure 0-1. Static Control Wrist Strap**

### **IMPORTANT SAFETY INSTRUCTIONS**

READ AND UNDERSTAND ALL INSTRUCTIONS.

When using this telecommunication equipment, basic safety precautions should always be followed to reduce the risk of fire, electric shock, and injury to persons, including the following:

- Follow all warnings and instructions marked on the product.
- Slots and openings in this product and the back or bottom are provided for ventilation. To protect it from overheating, these openings must not be blocked or covered.
- Opening or removing rear covers or sheet-metal parts may present exposure to high current or electrical energy levels, or to other risks. Refer all servicing in those areas to qualified service personnel.

- Never push objects of any kind into this product through slots as they may touch dangerous voltage points or short out parts that could result in a risk of fire or electrical shock. Never spill liquid of any kind on the product.
- Refer servicing to qualified service personnel.



## Master Task Index

### Operation and Maintenance

---

#### FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO

---

|                                                                                                                 |                           |
|-----------------------------------------------------------------------------------------------------------------|---------------------------|
| Accept Added Electrical Low-Speed Cables to an In-Service FT-2000 Network Element                               | Acceptance: NTP-008       |
| Accept Added Optical Low-Speed Fibers to an In-Service FT-2000 Network Element                                  | Acceptance: NTP-010       |
| Accept Dual Ring Interworking (DRI) Connections To/From an FT-2000 Ring                                         | Acceptance: NTP-009       |
| Accept FT-2000 2-Fiber Ring Network                                                                             | Acceptance: NTP-007       |
| Accept FT-2000 OC-48 1x1 End Terminal                                                                           | Acceptance: NTP-002       |
| Accept FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Ring                                                   | Acceptance: NTP-006       |
| Accept FT-2000 OC-48 Add/Drop-Rings Terminal for Operation With an FT-2000 Lightwave Booster Amplifier (FT-LBA) | Acceptance: NTP-011       |
| Accept FT-2000 OC-48 Repeater Bay/Shelf                                                                         | Acceptance: NTP-005       |
| Accept FT-2000 Point-to-Point Network                                                                           | Acceptance: NTP-003       |
| Accept Protection Access Upgrade in Point-to-Point Network                                                      | Acceptance: NTP-004       |
| Add a Node to an In-Service 2-Fiber Ring Network                                                                | Circuit Order: NTP-008    |
| Add DS3 and/or EC1 Low-Speed Port(s)                                                                            | Circuit Order: NTP-002    |
| Add OC-12 Low-Speed Slot(s)                                                                                     | Circuit Order: NTP-012    |
| Add OC-3/IS-3 Low-Speed Slot(s)                                                                                 | Circuit Order: NTP-010    |
| Add OC-48 Protection Line to Point-to-Point Network                                                             | Circuit Order: NTP-005    |
| Address 'CP Reset in Progress'                                                                                  | Trouble Clearing: TAP-151 |
| Address 'Extra Traffic Preempted'                                                                               | Trouble Clearing: TAP-143 |

---

**FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                                                                                                                                                                         |                                   |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------|
| Address 'Inc. (from OC3) DCC Failure' or<br>Address 'Inc. (from OC12) DCC Failure'                                                                                                                                      | Trouble Clearing: TAP-169         |
| Address 'Incoming (From OC-48) DS3 AIS'                                                                                                                                                                                 | Trouble Clearing: TAP-109         |
| Address 'Line FERF' or 'Incoming OC-48 Line<br>RFI/Yellow'                                                                                                                                                              | Trouble Clearing: TAP-121         |
| Address 'Path AIS', 'Path Yellow', 'Incoming (from<br>OC-48) DS3 LOF', 'Inc(from STSX) Line<br>RFI/Yellow', 'Inc(from OC48) Path RFI/Yellow',<br>'Incoming OC3 Line RFI/Yellow', and 'Incoming<br>OC12 Line RFI/Yellow' | Trouble Clearing: TAP-122         |
| Address 'Reset in Progress'                                                                                                                                                                                             | Trouble Clearing: TAP-124         |
| Address 'TG Warming Up'                                                                                                                                                                                                 | Trouble Clearing: TAP-154         |
| Address 'TID Address Map Full'                                                                                                                                                                                          | Trouble Clearing: TAP-170         |
| Address 'TL-1 Link Failure'                                                                                                                                                                                             | Trouble Clearing: TAP-106         |
| Address 'Traffic Squelched'                                                                                                                                                                                             | Trouble Clearing: TAP-146         |
| Address an Unexplained Loss of Service                                                                                                                                                                                  | Trouble Clearing: TAP-166         |
| Address Condition at Remote Repeater<br>Bay/Shelf                                                                                                                                                                       | Trouble Clearing: TAP-147         |
| Address Environmental Input Condition                                                                                                                                                                                   | Trouble Clearing: TAP-107         |
| Address Incoming Signal Failure                                                                                                                                                                                         | Trouble Clearing: TAP-110         |
| Address Missing or Incorrect Response                                                                                                                                                                                   | Trouble Clearing: TAP-108         |
| Change an OC-12 Link From 0x1 to 1+1<br>Protection Mode                                                                                                                                                                 | Circuit Order: NTP-015            |
| Change an OC-12 Link From 1+1 to 0x1<br>Protection Mode                                                                                                                                                                 | Circuit Order: NTP-017            |
| Change an OC-3/IS-3 Link From 0x1 to 1+1<br>Protection Mode                                                                                                                                                             | Circuit Order: NTP-014            |
| Change an OC-3/IS-3 Link From 1+1 to 0x1<br>Protection Mode                                                                                                                                                             | Circuit Order: NTP-016            |
| Change, Delete, or Enter a User's Login                                                                                                                                                                                 | Detailed Level Procedure: DLP-522 |
| Clean Optical Fibers and Connectors                                                                                                                                                                                     | Detailed Level Procedure: DLP-510 |
| Clear (Optical Parameter) 'Out-of-Range'                                                                                                                                                                                | Trouble Clearing: TAP-135         |
| Clear 'APS Channel Failure'                                                                                                                                                                                             | Trouble Clearing: TAP-105         |
| Clear 'APS Provisioning Conflict'                                                                                                                                                                                       | Trouble Clearing: TAP-150         |

---

**FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                     |                           |
|---------------------------------------------------------------------|---------------------------|
| Clear 'Circuit Audit Suspended'                                     | Trouble Clearing: TAP-161 |
| Clear 'Circuit Pack Failure'                                        | Trouble Clearing: TAP-111 |
| Clear 'Circuit Pack Removed'                                        | Trouble Clearing: TAP-112 |
| Clear 'Circuit Provisioning Error'                                  | Trouble Clearing: TAP-162 |
| Clear 'Clock Out of Range'                                          | Trouble Clearing: TAP-134 |
| Clear 'CP (Unknown Type) Failure'                                   | Trouble Clearing: TAP-123 |
| Clear 'CP (Unknown Type) Removed'                                   | Trouble Clearing: TAP-148 |
| Clear 'CPYPGM:IP (tid)'                                             | Trouble Clearing: TAP-159 |
| Clear 'DCC Startup in Progress'                                     | Trouble Clearing: TAP-129 |
| Clear 'DS3 Path Unequipped'                                         | Trouble Clearing: TAP-156 |
| Clear 'DS-NE Not Reachable'                                         | Trouble Clearing: TAP-137 |
| Clear 'Duplicate Node ID Defined' or 'Duplicate TID Defined'        | Trouble Clearing: TAP-138 |
| Clear 'Excessive Holdover'                                          | Trouble Clearing: TAP-117 |
| Clear 'Fan Failure'                                                 | Trouble Clearing: TAP-118 |
| Clear 'FT-LBA (Condition)'                                          | Trouble Clearing: TAP-157 |
| Clear 'Fuse/Power Failure'                                          | Trouble Clearing: TAP-119 |
| Clear 'Inc. (From OC48) Sync Ref Fail'                              | Trouble Clearing: TAP-152 |
| Clear 'Incoming (From OC-48) STS LOP'                               | Trouble Clearing: TAP-174 |
| Clear 'Incoming DCC Failure' or 'Inc. (From OC48) DCC Failure'      | Trouble Clearing: TAP-120 |
| Clear 'Incoming OC3 APS Byte Fail' or 'Incoming OC12 APS Byte Fail' | Trouble Clearing: TAP-160 |
| Clear 'Incoming OC-48 APS Byte Fail'                                | Trouble Clearing: TAP-132 |
| Clear 'Incoming OC48 APS Default Byte'                              | Trouble Clearing: TAP-168 |
| Clear 'Incoming OC-48 Invalid F Byte'                               | Trouble Clearing: TAP-145 |
| Clear 'LCLAN Failure'                                               | Trouble Clearing: TAP-175 |
| Clear 'Multiple DS-NEs Defined'                                     | Trouble Clearing: TAP-139 |
| Clear 'NE Status Comm Failure'                                      | Trouble Clearing: TAP-172 |
| Clear 'Node ID Conflict'                                            | Trouble Clearing: TAP-140 |
| Clear 'OC3/OC12 Mode Conflict (Equipment, Slot Pair, X-Connect)'    | Trouble Clearing: TAP-158 |
| Clear 'Path Integrity Failure'                                      | Trouble Clearing: TAP-163 |

---

**FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                                                               |                           |
|---------------------------------------------------------------------------------------------------------------|---------------------------|
| Clear 'Path TCA', 'Section TCA', or 'Line TCA'                                                                | Trouble Clearing: TAP-144 |
| Clear 'Ring Incomplete/Open'                                                                                  | Trouble Clearing: TAP-141 |
| Clear 'Ring Startup in Progress'                                                                              | Trouble Clearing: TAP-142 |
| Clear 'Site Provisioning Conflict'                                                                            | Trouble Clearing: TAP-130 |
| Clear 'Software Download in Progress'                                                                         | Trouble Clearing: TAP-131 |
| Clear 'STS1 Squelch Map Conflict'                                                                             | Trouble Clearing: TAP-164 |
| Clear 'STS3 Squelch Map Conflict', 'STS1<br>Squelch Map Conflict', or 'Squelch Map Conflict'                  | Trouble Clearing: TAP-153 |
| Clear 'STS3 Squelch Map Inconsistent' or 'STS1<br>Squelch Map Inconsistent'                                   | Trouble Clearing: TAP-165 |
| Clear 'SYSMEM Failure' or 'SYSCTL Failure'                                                                    | Trouble Clearing: TAP-113 |
| Clear 'SYSMEM Removed'                                                                                        | Trouble Clearing: TAP-133 |
| Clear 'SYSMEM Unrecognizable Code'                                                                            | Trouble Clearing: TAP-125 |
| Clear 'SYSMEM/SYSCTL Code Mismatch'                                                                           | Trouble Clearing: TAP-126 |
| Clear 'Unexpected CP Type'                                                                                    | Trouble Clearing: TAP-127 |
| Clear 'X-Conn Provisioned But Not Est'                                                                        | Trouble Clearing: TAP-173 |
| Clear Alarm When 'NE ACTY' and 'FE ACTY'<br>LEDs Are Off (Cannot Log in to a Network<br>Element)              | Trouble Clearing: TAP-136 |
| Clear Automatic Switch Condition                                                                              | Trouble Clearing: TAP-115 |
| Clear DRI Switch                                                                                              | Trouble Clearing: TAP-149 |
| Clear Local Trouble When 'USE CIT' LED is<br>Lighted                                                          | Trouble Clearing: TAP-102 |
| Clear Local Trouble When 'USE CIT' LED is Off                                                                 | Trouble Clearing: TAP-101 |
| Clear NE ACTY and/or FE ACTY (Regardless of<br>Alarm Level)                                                   | Trouble Clearing: TAP-103 |
| Clear Potential Loss of Service Due to Double<br>Failure                                                      | Trouble Clearing: TAP-167 |
| Clear Suspected Failure in FT-2000 Network<br>Element(s) Via Remote Modem (or Datakit®<br>Network) Connection | Trouble Clearing: TAP-155 |
| Clear Trouble in CIT (CIT Does Not Respond to<br>Commands)                                                    | Trouble Clearing: TAP-128 |
| Clear Trouble Report                                                                                          | Trouble Clearing: TAP-104 |

---

**FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                                                                                     |                                   |
|-------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------|
| Clear Trouble When 'ABN' LED Is Lighted                                                                                             | Trouble Clearing: TAP-114         |
| Clear Unsuccessful Circuit Pack Replacement                                                                                         | Trouble Clearing: TAP-116         |
| Connect and Condition Craft Interface Terminal (CIT)                                                                                | Detailed Level Procedure: DLP-501 |
| Connect Two FT-2000 Rings Via an Optical Link                                                                                       | Operations: NTP-011               |
| Copy Software From One Network Element to Another Network Element                                                                   | Operations: NTP-005               |
| Delete a Node From an In-Service 2-Fiber Ring Network                                                                               | Circuit Order: NTP-009            |
| Delete DS3 and/or EC1 Low-Speed Port(s)                                                                                             | Circuit Order: NTP-003            |
| Delete OC-12 Low-Speed Slot(s)                                                                                                      | Circuit Order: NTP-013            |
| Delete OC-48 Protection Line From Point-to-Point Network                                                                            | Circuit Order: NTP-004            |
| Disconnect Dual Ring Interworking (DRI) STS-3 (or STS-1) Cross-Connections                                                          | Operations: NTP-004               |
| Discontinue Restoration Protection Access From Point-to-Point Network                                                               | Circuit Order: NTP-006            |
| Enter or Change TID String of FT-2000 Network Element                                                                               | Detailed Level Procedure: DLP-544 |
| Enter/Delete DCC Assignment on OC-3/OC-12 Line                                                                                      | Detailed Level Procedure: DLP-542 |
| Enter/Delete/Edit Limited Granularity STS-1 Cross-Connections in Network Elements for 2-Fiber Ring Network (for Rel. 6 and Earlier) | Detailed Level Procedure: DLP-537 |
| Enter/Delete/Edit STS-3 (or STS-1) Cross-Connections at Network Elements for 2-Fiber Ring Network                                   | Detailed Level Procedure: DLP-536 |
| Establish Dual Ring Interworking (DRI) STS-3 (or STS-1) Cross-Connections                                                           | Operations: NTP-003               |
| Establish Modem or Datakit <sup>®</sup> Network Access                                                                              | Detailed Level Procedure: DLP-521 |
| Establish OC-3 Optical Span Extension Between DDM-2000 OC-12 Ring and DDM-2000 OC-3 Ring                                            | Circuit Order: NTP-018            |
| Establish or Disconnect Remote Login Using CIT                                                                                      | Detailed Level Procedure: DLP-524 |

---

**FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                                             |                                   |
|---------------------------------------------------------------------------------------------|-----------------------------------|
| Establish Restoration Protection Access in Point-to-Point Network                           | Circuit Order: NTP-007            |
| Identify Primary/Secondary and Source/Destination Nodes                                     | Operations: NTP-006               |
| Identify Source of Incoming Signal                                                          | Detailed Level Procedure: DLP-535 |
| Inspect (Repair) Optical and/or Electrical Cable(s)                                         | Detailed Level Procedure: DLP-528 |
| Install and/or Remove Optical Loopback at OC48 REGENR Circuit Pack                          | Detailed Level Procedure: DLP-519 |
| Install Software (Initial Installation/Upgrade/Change) into CIT and FT-2000 Network Element | Operations: NTP-002               |
| Install/Remove Apparatus (Circuit Pack) Blank                                               | Detailed Level Procedure: DLP-533 |
| Install/Remove Lightguide Buildout                                                          | Detailed Level Procedure: DLP-512 |
| Introduction to the PC                                                                      | Detailed Level Procedure: DLP-529 |
| Loop Back DS3 and/or EC1 Circuit(s) for Testing                                             | Detailed Level Procedure: DLP-525 |
| Loop Back OC-12 Line(s) for Testing                                                         | Detailed Level Procedure: DLP-549 |
| Loop Back OC-3 Line(s) for Testing                                                          | Detailed Level Procedure: DLP-538 |
| Maintenance Philosophy                                                                      | Trouble Clearing: TAD-100         |
| Make Manual Optical Loopback at FT-2000 Network Element                                     | Detailed Level Procedure: DLP-500 |
| Merge Two OC-48 Rings                                                                       | Operations: NTP-009               |
| Obtain State Report                                                                         | Detailed Level Procedure: DLP-516 |
| Optical Upgrade From STS-3 to STS-1 Granularity                                             | Operations: NTP-010               |
| Perform Automatic Low-Speed Cabling (Fiber) Test                                            | Detailed Level Procedure: DLP-503 |
| Perform Local Automatic Turnup Test                                                         | Detailed Level Procedure: DLP-502 |
| Perform Provisioning                                                                        | Detailed Level Procedure: DLP-505 |
| Perform Provisioning Test Office Alarm Operation                                            | Detailed Level Procedure: DLP-504 |
| Power Down/Power Up an Optical Network Element                                              | Detailed Level Procedure: DLP-551 |
| Properly Terminate DS3 and/or EC1 Outputs                                                   | Detailed Level Procedure: DLP-520 |

---

**FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                              |                                   |
|------------------------------------------------------------------------------|-----------------------------------|
| Provision 1-Way-DRI Video Distribution Circuit for Entire Ring               | Operations: NTP-007               |
| Provision Low-Speed Slot and Port Parameters                                 | Detailed Level Procedure: DLP-517 |
| Provision OC12 Slot for 1-Way Drop Service                                   | Detailed Level Procedure: DLP-547 |
| Provision OC3 Slot for 1-Way Drop Service                                    | Detailed Level Procedure: DLP-540 |
| Provision STS-1 Path Trace                                                   | Detailed Level Procedure: DLP-531 |
| Provision STS-1/STS-3 Non-Preemptible Protection Access (NPPA)               | Operations: NTP-008               |
| Release Looped Back DS3 and/or EC1 Circuit(s)                                | Detailed Level Procedure: DLP-523 |
| Release Looped Back OC-12 Line(s)                                            | Detailed Level Procedure: DLP-548 |
| Release Looped Back OC-3 Line(s)                                             | Detailed Level Procedure: DLP-539 |
| Remove and/or Install Circuit Pack                                           | Detailed Level Procedure: DLP-514 |
| Remove OC-3/IS-3 Low-Speed Circuit Pack(s) From the Shelf                    | Circuit Order: NTP-011            |
| Remove/Replace Shelf Cover                                                   | Detailed Level Procedure: DLP-511 |
| Replace a Defective Power-Line Filter                                        | Detailed Level Procedure: DLP-550 |
| Replace Circuit Pack With Different Circuit Pack                             | Detailed Level Procedure: DLP-527 |
| Replace Defective Fan Unit in D-Bay                                          | Detailed Level Procedure: DLP-546 |
| Replace Fan Filter (in E-Bay)                                                | Detailed Level Procedure: DLP-526 |
| Replace Fan Filter Every Six Months (in D-Bay)                               | Detailed Level Procedure: DLP-545 |
| Replace Fuse in Power Distribution and Fuse Panel or User Panel              | Detailed Level Procedure: DLP-515 |
| Restore Network Element Operation After Power Loss                           | Trouble Clearing: TAP-171         |
| Ring Reset                                                                   | Operations: NTP-012               |
| STS3 (or STS1) Path Trace Monitor for Non-DS3 Signals                        | Detailed Level Procedure: DLP-543 |
| Temporarily Remove High-Speed Optical Loopback(s) at FT-2000 Network Element | Detailed Level Procedure: DLP-518 |
| Test DRI Cross-Connections for Path Integrity in FT-2000 Ring                | Detailed Level Procedure: DLP-541 |
| Test Fan Assembly (ED-6G992-30)                                              | Detailed Level Procedure: DLP-513 |
| Test Miscellaneous Discrete Telemetry                                        | Detailed Level Procedure: DLP-530 |

---

**FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                             |                                   |
|---------------------------------------------|-----------------------------------|
| Test Parallel Telemetry                     | Detailed Level Procedure: DLP-508 |
| Test Serial Telemetry                       | Detailed Level Procedure: DLP-509 |
| Turnup Test - Point-to-Point System         | Detailed Level Procedure: DLP-507 |
| Verify FT-2000 Network Element Is Connected | Detailed Level Procedure: DLP-506 |
| Verify STS-1 Path Trace                     | Detailed Level Procedure: DLP-532 |
| Verify That Low-Speed Ports Are Not NMON    | Detailed Level Procedure: DLP-534 |



## **Numerical List of TOP Procedures**

---

**Table A – Numerical List of TOP Procedures (Note)\***

---

### **Acceptance Tab**

|         |                                                                                                                 |
|---------|-----------------------------------------------------------------------------------------------------------------|
| IXL-001 | Task Index — Acceptance                                                                                         |
| NTP-002 | Accept FT-2000 OC-48 1x1 End Terminal                                                                           |
| NTP-003 | Accept FT-2000 Point-to-Point Network                                                                           |
| NTP-004 | Accept Protection Access Upgrade in Point-to-Point Network                                                      |
| NTP-005 | Accept FT-2000 OC-48 Repeater Bay/Shelf                                                                         |
| NTP-006 | Accept FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Ring                                                   |
| NTP-007 | Accept FT-2000 2-Fiber Ring Network                                                                             |
| NTP-008 | Accept Added Electrical Low-Speed Cables to an In-Service FT-2000 Network Element                               |
| NTP-009 | Accept Dual Ring Interworking (DRI) Connections To/From an FT-2000 Ring                                         |
| NTP-010 | Accept Added Optical Low-Speed Fibers to an In-Service FT-2000 Network Element                                  |
| NTP-011 | Accept FT-2000 OC-48 Add/Drop-Rings Terminal for Operation With an FT-2000 Lightwave Booster Amplifier (FT-LBA) |

### **System Turnup/Circuit Order Tab**

|         |                                                                       |
|---------|-----------------------------------------------------------------------|
| IXL-001 | Task Index — System Turnup/Circuit Order                              |
| NTP-002 | Add DS3 and/or EC1 Low-Speed Port(s)                                  |
| NTP-003 | Delete DS3 and/or EC1 Low-Speed Port(s)                               |
| NTP-004 | Delete OC-48 Protection Line From Point-to-Point Network              |
| NTP-005 | Add OC-48 Protection Line to Point-to-Point Network                   |
| NTP-006 | Discontinue Restoration Protection Access From Point-to-Point Network |
| NTP-007 | Establish Restoration Protection Access in Point-to-Point Network     |
| NTP-008 | Add a Node to an In-Service 2-Fiber Ring Network                      |
| NTP-009 | Delete a Node From an In-Service 2-Fiber Ring Network                 |
| NTP-010 | Add OC-3/IS-3 Low-Speed Slot(s)                                       |
| NTP-011 | Remove OC-3/IS-3 Low-Speed Circuit Pack(s) From the Shelf             |
| NTP-012 | Add OC-12 Low-Speed Slot(s)                                           |
| NTP-013 | Delete OC-12 Low-Speed Slot(s)                                        |
| NTP-014 | Change an OC-3/IS-3 Link From 0x1 to 1+1 Protection Mode              |
| NTP-015 | Change an OC-12 Link From 0x1 to 1+1 Protection Mode                  |

---

**Table A – Numerical List of TOP Procedures (Note)\* (Contd)**

---

---

|         |                                                                                          |
|---------|------------------------------------------------------------------------------------------|
| NTP-016 | Change an OC-3/IS-3 Link From 1+1 to 0x1 Protection Mode                                 |
| NTP-017 | Change an OC-12 Link From 1+1 to 0x1 Protection Mode                                     |
| NTP-018 | Establish OC-3 Optical Span Extension Between DDM-2000 OC-12 Ring and DDM-2000 OC-3 Ring |
| NTP-019 |                                                                                          |
| NTP-020 |                                                                                          |
| NTP-021 |                                                                                          |

**Operations Tab**

|         |                                                                                             |
|---------|---------------------------------------------------------------------------------------------|
| IXL-001 | Task Index — Operations                                                                     |
| NTP-002 | Install Software (Initial Installation/Upgrade/Change) into CIT and FT-2000 Network Element |
| NTP-003 | Establish Dual Ring Interworking (DRI) STS-3 (or STS-1) Cross-Connections                   |
| NTP-004 | Disconnect Dual Ring Interworking (DRI) STS-3 (or STS-1) Cross-Connections                  |
| NTP-005 | Copy Software From One Network Element to Another Network Element                           |
| NTP-006 | Identify Primary/Secondary and Source/Destination Nodes                                     |
| NTP-007 | Provision 1-Way-DRI Video Distribution Circuit for Entire Ring                              |
| NTP-008 | Provision STS-1/STS-3 Non-Preemptible Protection Access (NPPA)                              |
| NTP-009 | Merge Two OC-48 Rings                                                                       |
| NTP-010 | Optical Upgrade From STS-3 to STS-1 Granularity                                             |
| NTP-011 | Connect Two FT-2000 Rings Via an Optical Link                                               |
| NTP-012 | Ring Reset                                                                                  |

**Trouble Clearing Tab**

|         |                                                          |
|---------|----------------------------------------------------------|
| IXL-001 | Task Index — Trouble Clearing                            |
| TAD-100 | Maintenance Philosophy                                   |
| TAP-101 | Clear Local Trouble When 'USE CIT' LED is Off            |
| TAP-102 | Clear Local Trouble When 'USE CIT' LED is Lighted        |
| TAP-103 | Clear NE ACTY and/or FE ACTY (Regardless of Alarm Level) |
| TAP-104 | Clear Trouble Report                                     |
| TAP-105 | Clear 'APS Channel Failure'                              |
| TAP-106 | Address 'TL-1 Link Failure'                              |

---

**Table A – Numerical List of TOP Procedures (Note)\* (Contd)**

|         |                                                                                                                                                                                                             |
|---------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| TAP-107 | Address Environmental Input Condition                                                                                                                                                                       |
| TAP-108 | Address Missing or Incorrect Response                                                                                                                                                                       |
| TAP-109 | Address 'Incoming (From OC-48) DS3 AIS'                                                                                                                                                                     |
| TAP-110 | Address Incoming Signal Failure                                                                                                                                                                             |
| TAP-111 | Clear 'Circuit Pack Failure'                                                                                                                                                                                |
| TAP-112 | Clear 'Circuit Pack Removed'                                                                                                                                                                                |
| TAP-113 | Clear 'SYSMEM Failure' or 'SYSCTL Failure'                                                                                                                                                                  |
| TAP-114 | Clear Trouble When 'ABN' LED Is Lighted                                                                                                                                                                     |
| TAP-115 | Clear Automatic Switch Condition                                                                                                                                                                            |
| TAP-116 | Clear Unsuccessful Circuit Pack Replacement                                                                                                                                                                 |
| TAP-117 | Clear 'Excessive Holdover'                                                                                                                                                                                  |
| TAP-118 | Clear 'Fan Failure'                                                                                                                                                                                         |
| TAP-119 | Clear 'Fuse/Power Failure'                                                                                                                                                                                  |
| TAP-120 | Clear 'Incoming DCC Failure' or 'Inc. (From OC48) DCC Failure'                                                                                                                                              |
| TAP-121 | Address 'Line FERF' or 'Incoming OC-48 Line RFI/Yellow'                                                                                                                                                     |
| TAP-122 | Address 'Path AIS', 'Path Yellow', 'Incoming (from OC-48) DS3 LOF', 'Inc(from STSX) Line RFI/Yellow', 'Inc(from OC48) Path RFI/Yellow', 'Incoming OC3 Line RFI/Yellow', and 'Incoming OC12 Line RFI/Yellow' |
| TAP-123 | Clear 'CP (Unknown Type) Failure'                                                                                                                                                                           |
| TAP-124 | Address 'Reset in Progress'                                                                                                                                                                                 |
| TAP-125 | Clear 'SYSMEM Unrecognizable Code'                                                                                                                                                                          |
| TAP-126 | Clear 'SYSMEM/SYSCTL Code Mismatch'                                                                                                                                                                         |
| TAP-127 | Clear 'Unexpected CP Type'                                                                                                                                                                                  |
| TAP-128 | Clear Trouble in CIT (CIT Does Not Respond to Commands)                                                                                                                                                     |
| TAP-129 | Clear 'DCC Startup in Progress'                                                                                                                                                                             |
| TAP-130 | Clear 'Site Provisioning Conflict'                                                                                                                                                                          |
| TAP-131 | Clear 'Software Download in Progress'                                                                                                                                                                       |
| TAP-132 | Clear 'Incoming OC-48 APS Byte Fail'                                                                                                                                                                        |
| TAP-133 | Clear 'SYSMEM Removed'                                                                                                                                                                                      |
| TAP-134 | Clear 'Clock Out of Range'                                                                                                                                                                                  |
| TAP-135 | Clear (Optical Parameter) 'Out-of-Range'                                                                                                                                                                    |
| TAP-136 | Clear Alarm When 'NE ACTY' and 'FE ACTY' LEDs Are Off (Cannot Log in to a Network Element)                                                                                                                  |
| TAP-137 | Clear 'DS-NE Not Reachable'                                                                                                                                                                                 |
| TAP-138 | Clear 'Duplicate Node ID Defined' or 'Duplicate TID Defined'                                                                                                                                                |
| TAP-139 | Clear 'Multiple DS-NEs Defined'                                                                                                                                                                             |
| TAP-140 | Clear 'Node ID Conflict'                                                                                                                                                                                    |

**Table A – Numerical List of TOP Procedures (Note)\* (Contd)**

---



---

|         |                                                                                                         |
|---------|---------------------------------------------------------------------------------------------------------|
| TAP-141 | Clear 'Ring Incomplete/Open'                                                                            |
| TAP-142 | Clear 'Ring Startup in Progress'                                                                        |
| TAP-143 | Address 'Extra Traffic Preempted'                                                                       |
| TAP-144 | Clear 'Path TCA', 'Section TCA', or 'Line TCA'                                                          |
| TAP-145 | Clear 'Incoming OC-48 Invalid F Byte'                                                                   |
| TAP-146 | Address 'Traffic Squelched'                                                                             |
| TAP-147 | Address Condition at Remote Repeater Bay/Shelf                                                          |
| TAP-148 | Clear 'CP (Unknown Type) Removed'                                                                       |
| TAP-149 | Clear DRI Switch                                                                                        |
| TAP-150 | Clear 'APS Provisioning Conflict'                                                                       |
| TAP-151 | Address 'CP Reset in Progress'                                                                          |
| TAP-152 | Clear 'Inc. (From OC48) Sync Ref Fail'                                                                  |
| TAP-153 | Clear 'STS3 Squelch Map Conflict', 'STS1 Squelch Map Conflict', or 'Squelch Map Conflict'               |
| TAP-154 | Address 'TG Warming Up'                                                                                 |
| TAP-155 | Clear Suspected Failure in FT-2000 Network Element(s) Via Remote Modem (or Datakit® Network) Connection |
| TAP-156 | Clear 'DS3 Path Unequipped'                                                                             |
| TAP-157 | Clear 'FT-LBA (Condition)'                                                                              |
| TAP-158 | Clear 'OC3/OC12 Mode Conflict (Equipment, Slot Pair, X-Connect)'                                        |
| TAP-159 | Clear 'CPYPGM:IP (tid)'                                                                                 |
| TAP-160 | Clear 'Incoming OC3 APS Byte Fail' or 'Incoming OC12 APS Byte Fail'                                     |
| TAP-161 | Clear 'Circuit Audit Suspended'                                                                         |
| TAP-162 | Clear 'Circuit Provisioning Error'                                                                      |
| TAP-163 | Clear 'Path Integrity Failure'                                                                          |
| TAP-164 | Clear 'STS1 Squelch Map Conflict'                                                                       |
| TAP-165 | Clear 'STS3 Squelch Map Inconsistent' or 'STS1 Squelch Map Inconsistent'                                |
| TAP-166 | Address an Unexplained Loss of Service                                                                  |
| TAP-167 | Clear Potential Loss of Service Due to Double Failure                                                   |
| TAP-168 | Clear 'Incoming OC48 APS Default Byte'                                                                  |
| TAP-169 | Address 'Inc. (from OC3) DCC Failure' or Address 'Inc. (from OC12) DCC Failure'                         |
| TAP-170 | Address 'TID Address Map Full'                                                                          |
| TAP-171 | Restore Network Element Operation After Power Loss                                                      |
| TAP-172 | Clear 'NE Status Comm Failure'                                                                          |
| TAP-173 | Clear 'X-Conn Provisioned But Not Est'                                                                  |

---

**Table A – Numerical List of TOP Procedures (Note)\* (Contd)**

---

---

|         |                                       |
|---------|---------------------------------------|
| TAP-174 | Clear 'Incoming (From OC-48) STS LOP' |
| TAP-175 | Clear 'LCLAN Failure'                 |

**Detailed Level Procedures (DLP) Tab**

|         |                                                                              |
|---------|------------------------------------------------------------------------------|
| IXL-001 | Task Index — Detailed Level Procedures                                       |
| DLP-500 | Make Manual Optical Loopback at FT-2000 Network Element                      |
| DLP-501 | Connect and Condition Craft Interface Terminal (CIT)                         |
| DLP-502 | Perform Local Automatic Turnup Test                                          |
| DLP-503 | Perform Automatic Low-Speed Cabling (Fiber) Test                             |
| DLP-504 | Perform Provisioning Test Office Alarm Operation                             |
| DLP-505 | Perform Provisioning                                                         |
| DLP-506 | Verify FT-2000 Network Element Is Connected                                  |
| DLP-507 | Turnup Test - Point-to-Point System                                          |
| DLP-508 | Test Parallel Telemetry                                                      |
| DLP-509 | Test Serial Telemetry                                                        |
| DLP-510 | Clean Optical Fibers and Connectors                                          |
| DLP-511 | Remove/Replace Shelf Cover                                                   |
| DLP-512 | Install/Remove Lightguide Buildout                                           |
| DLP-513 | Test Fan Assembly (ED-6G992-30)                                              |
| DLP-514 | Remove and/or Install Circuit Pack                                           |
| DLP-515 | Replace Fuse in Power Distribution and Fuse Panel or User Panel              |
| DLP-516 | Obtain State Report                                                          |
| DLP-517 | Provision Low-Speed Slot and Port Parameters                                 |
| DLP-518 | Temporarily Remove High-Speed Optical Loopback(s) at FT-2000 Network Element |
| DLP-519 | Install and/or Remove Optical Loopback at OC48 REGENR Circuit Pack           |
| DLP-520 | Properly Terminate DS3 and/or EC1 Outputs                                    |
| DLP-521 | Establish Modem or Datakit® Network Access                                   |
| DLP-522 | Change, Delete, or Enter a User's Login                                      |
| DLP-523 | Release Looped Back DS3 and/or EC1 Circuit(s)                                |
| DLP-524 | Establish or Disconnect Remote Login Using CIT                               |
| DLP-525 | Loop Back DS3 and/or EC1 Circuit(s) for Testing                              |
| DLP-526 | Replace Fan Filter (in E-Bay)                                                |
| DLP-527 | Replace Circuit Pack With Different Circuit Pack                             |
| DLP-528 | Inspect (Repair) Optical and/or Electrical Cable(s)                          |

---

**Table A – Numerical List of TOP Procedures (Note)\* (Contd)**

|         |                                                                                                                                     |
|---------|-------------------------------------------------------------------------------------------------------------------------------------|
| DLP-529 | Introduction to the PC                                                                                                              |
| DLP-530 | Test Miscellaneous Discrete Telemetry                                                                                               |
| DLP-531 | Provision STS-1 Path Trace                                                                                                          |
| DLP-532 | Verify STS-1 Path Trace                                                                                                             |
| DLP-533 | Install/Remove Apparatus (Circuit Pack) Blank                                                                                       |
| DLP-534 | Verify That Low-Speed Ports Are Not NMON                                                                                            |
| DLP-535 | Identify Source of Incoming Signal                                                                                                  |
| DLP-536 | Enter/Delete/Edit STS-3 (or STS-1) Cross-Connections at Network Elements for 2-Fiber Ring Network                                   |
| DLP-537 | Enter/Delete/Edit Limited Granularity STS-1 Cross-Connections in Network Elements for 2-Fiber Ring Network (for Rel. 6 and Earlier) |
| DLP-538 | Loop Back OC-3 Line(s) for Testing                                                                                                  |
| DLP-539 | Release Looped Back OC-3 Line(s)                                                                                                    |
| DLP-540 | Provision OC3 Slot for 1-Way Drop Service                                                                                           |
| DLP-541 | Test DRI Cross-Connections for Path Integrity in FT-2000 Ring                                                                       |
| DLP-542 | Enter/Delete DCC Assignment on OC-3/OC-12 Line                                                                                      |
| DLP-543 | STS3 (or STS1) Path Trace Monitor for Non-DS3 Signals                                                                               |
| DLP-544 | Enter or Change TID String of FT-2000 Network Element                                                                               |
| DLP-545 | Replace Fan Filter Every Six Months (in D-Bay)                                                                                      |
| DLP-546 | Replace Defective Fan Unit in D-Bay                                                                                                 |
| DLP-547 | Provision OC12 Slot for 1-Way Drop Service                                                                                          |
| DLP-548 | Release Looped Back OC-12 Line(s)                                                                                                   |
| DLP-549 | Loop Back OC-12 Line(s) for Testing                                                                                                 |
| DLP-550 | Replace a Defective Power-Line Filter                                                                                               |
| DLP-551 | Power Down/Power Up an Optical Network Element                                                                                      |

\* Table A is provided as a reference only and should not be used alone in finding instructions for performing tasks.

## Acceptance Task Index

---

### FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO

---

|                                                                                                                              |         |
|------------------------------------------------------------------------------------------------------------------------------|---------|
| Accept Added Electrical Low-Speed Cables to an In-Service<br>FT-2000 Network Element . . . . .                               | NTP-008 |
| Accept Added Optical Low-Speed Fibers to an In-Service<br>FT-2000 Network Element . . . . .                                  | NTP-010 |
| Accept Dual Ring Interworking (DRI) Connections To/From an<br>FT-2000 Ring . . . . .                                         | NTP-009 |
| Accept FT-2000 2-Fiber Ring Network . . . . .                                                                                | NTP-007 |
| Accept FT-2000 OC-48 1x1 End Terminal . . . . .                                                                              | NTP-002 |
| Accept FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Ring . . . . .                                                      | NTP-006 |
| Accept FT-2000 OC-48 Add/Drop-Rings Terminal for Operation<br>With an FT-2000 Lightwave Booster Amplifier (FT-LBA) . . . . . | NTP-011 |
| Accept FT-2000 OC-48 Repeater Bay/Shelf . . . . .                                                                            | NTP-005 |
| Accept FT-2000 Point-to-Point Network . . . . .                                                                              | NTP-003 |
| Accept Protection Access Upgrade in Point-to-Point Network . . . . .                                                         | NTP-004 |





## Accept FT-2000 OC-48 1x1 End Terminal

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This acceptance procedure tests an **out-of-service** FT-2000 1x1 End Terminal in a stand-alone state. Each FT-2000 end terminal in the FT-2000 point-to-point network should be tested individually.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Optical Loopback Jumpers with 20-dB buildout  
Wrist Strap



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 end terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**NOTE:**

This procedure is only used if the FT-2000 end terminal was installed by someone else. If the installation has not been completed (including circuit pack installation), notify the installation personnel or refer to 365-575-104 to complete the installation. If you completed the installation following the procedures in 365-575-104, do not perform this procedure.



**NOTE:**

It is assumed that installation of the FT-2000 end terminal has been completed (including circuit pack installation) for the engineered configuration. It is further assumed that the end terminal is powered and running. *If at any point in this procedure, the FT-2000 end terminal fails to respond in the indicated way, refer the trouble to installation personnel.*

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---



**NOTE:**

Ignore the LEDs at the user panel during this test.

|    |                                                                                                                                                                                                                                                    |                |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
|    | Inspect the FT-2000 end terminal for obvious damage.                                                                                                                                                                                               | —              |
| 2. | At the DSX-3 cross-connect panel or equivalent, disconnect all DS3 connections to the FT-2000 end terminal.                                                                                                                                        | —              |
| 3. | Loop back all high-speed OC-48 lines.                                                                                                                                                                                                              | <b>DLP-500</b> |
| 4. | If required, connect the Craft Interface Terminal (CIT) to the local FT-2000 network element and condition it.                                                                                                                                     | <b>DLP-501</b> |
| 5. | At the CIT, select SECURITY-Set-Network Element and Site 0 with a Yes, then execute by using <b>F9</b> .                                                                                                                                           | —              |
| 6. | At the CIT, select CONFIGURATION-Update and execute.                                                                                                                                                                                               | —              |
| 7. | Obtain an Active Alarm and Status Report (use Retrieve-Alarm) and verify that it shows only the two conditions below (no other conditions should be listed):<br><br>line-1     site provisioning conflict<br>line-p     site provisioning conflict | —              |
| 8. | Perform local automatic turnup test for all lines.                                                                                                                                                                                                 | <b>DLP-502</b> |

| <b>DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO</b>                                                                                                                                                    |                            |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------|
| 9. Obtain an Active Alarm and Status Report (use Retrieve-Alarm) and verify that it shows only the two conditions below (no other conditions should be listed):                                                       | —                          |
| line-1 site provisioning conflict                                                                                                                                                                                     |                            |
| line-p site provisioning conflict                                                                                                                                                                                     |                            |
| 10. Perform automatic low-speed cabling test ( to cross-connection) for all low-speed ports on each line.                                                                                                             | <b>DLP-503</b>             |
| 11. If required, perform provisioning at this time.                                                                                                                                                                   | <b>DLP-505</b>             |
| 12. If so equipped, test the fan assembly for proper operation.                                                                                                                                                       | <b>DLP-513</b>             |
| 13. The FT-2000 end terminal has passed the acceptance tests. After all FT-2000 network elements have been tested individually, the network (system) is ready for the FT-2000 point-to-point network acceptance test. | <b>Acceptance: NTP-003</b> |

```

Acceptance: NTP-002---+---DLP-500---+---DLP-511
| |---DLP-512
| |---DLP-510-----DLP-512
|---DLP-501-----Operation: NTP-002
|---DLP-502
|---DLP-503---+---DLP-518-----DLP-511
| |---DLP-500---+---DLP-511
| |---DLP-512
|---DLP-505 |---DLP-510-----DLP-512
|---DLP-513
|---Acceptance: NTP-003

```



## Accept FT-2000 Point-to-Point Network

---

### DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Overview:** This acceptance procedure is made on the FT-2000 point-to-point network after all FT-2000 network elements (including any repeaters) have been individually acceptance tested.

**Required Test Equipment:**

Craft Interface Terminal (CIT)

Wrist Strap

Optical Power Meter and/or Optical Time Domain Reflectometer  
(OTDR) (Used in DLP-506)



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**NOTE:**

This procedure is only used if the FT-2000 point-to-point network was installed by someone else. If the installation has not been completed, notify the installation personnel or refer to 365-575-104 to complete the installation. If you completed the installation following the procedures in 365-575-104, do not perform this procedure.

Each FT-2000 network element should have been individually acceptance tested. If this has not been done, refer to **Acceptance: NTP-002** for the FT-2000 end terminal and/or refer to **Acceptance: NTP-005** for the FT-2000 repeater bay/shelf. If at any point in this procedure, an FT-2000 network element fails to respond in the indicated way, refer the trouble to the group that installed it.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---



**NOTE:**

Ensure that all slots, including the low-speed slots, are properly equipped before initiating this test.

|     |                                                                                                                                                                                                                                                            |                |
|-----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
|     | Verify the FT-2000 network elements are connected in an FT-2000 point-to-point network and are not looped back.                                                                                                                                            | <b>DLP-506</b> |
| 2.  | At the local FT-2000 end terminal, connect and condition the craft interface terminal (CIT).                                                                                                                                                               | <b>DLP-501</b> |
| 3.  | At the CIT, select SECURITY-Retrieve-Network Element and execute to obtain a Retrieve-Network Element Report to determine the state of the Site 0 parameter. The site parameter is used to differentiate the network elements in a point-to-point network. | —              |
| 4.  | At the DSX-3 cross-connect panel or equivalent, disconnect all DS3 inputs and outputs at the local FT-2000 end terminal.                                                                                                                                   | —              |
| 5.  | At the CIT, select CONFIGURATION-Update and execute.                                                                                                                                                                                                       | —              |
| 6.  | Establish a remote login to the remote FT-2000 end terminal and repeat Steps 3 through 5. An assistant is required when Step 4 is performed at the remote FT-2000 end terminal.                                                                            | <b>DLP-524</b> |
| 7.  | If required, and at the CIT, set SECURITY-Site 0 parameter to Yes at one end terminal and to No at the other end terminal.                                                                                                                                 | —              |
| 8.  | Verify the <b>NE ACTY</b> and the <b>FE ACTY</b> LEDs are off at the local user panel.                                                                                                                                                                     | —              |
| 9.  | Perform a system automatic turnup test on all lines.                                                                                                                                                                                                       | <b>DLP-507</b> |
| 10. | If required, test office alarm operation.                                                                                                                                                                                                                  | <b>DLP-504</b> |

|     |                                                                                                                                                                                               |                                                                   |
|-----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------|
| 11. | If required, check telemetry interfaces.                                                                                                                                                      |                                                                   |
|     | <ul style="list-style-type: none"> <li>a. Parallel telemetry</li> <li>b. Serial telemetry</li> <li>c. Miscellaneous discrete telemetry</li> </ul>                                             | <p><b>DLP-508</b></p> <p><b>DLP-509</b></p> <p><b>DLP-530</b></p> |
| 12. | If required, notify personnel at the remote maintenance site (OS Center) that the FT-2000 point-to-point network is ready for any operational tests they wish to perform using the X.25 link. | —                                                                 |
| 13. | The FT-2000 point-to-point network (including any repeaters) has passed this acceptance test and is now ready to provide service.                                                             | —                                                                 |

```

Acceptance: NTP-003---+---DLP-506---+---DLP-511
 | |---DLP-510-----DLP-512
 |---DLP-501-----Operation: NTP-002
 |---DLP-524-----DLP-501-----Operation: NTP-002
 |---DLP-507
 |---DLP-504
 |---DLP-508
 |---DLP-509
 |---DLP-530

```





## Accept Protection Access Upgrade in Point-to-Point Network

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | FOR DETAILS, GO TO |
|------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 1.                                       | <p><b>Overview:</b> This acceptance procedure tests a Protection Access Upgrade in an FT-2000 point-to-point network. Circuit pack and software compatibility are described in Section 7, "Circuit Pack Descriptions."</p> <p><b>Required Test Equipment:</b><br/>Craft Interface Terminal (CIT)<br/>Wrist Strap</p> <p><i>Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 end terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in Trouble Clearing: TAD-100.</i></p> <p>This procedure is only used if the FT-2000 protection access upgrade was installed by someone else. If the installation has not been completed (including circuit pack installation), notify the installation personnel or refer to 365-575-104 to complete the installation. If you completed the installation following the procedures in 365-575-104, do not perform this procedure.</p> <p>It is assumed that installation of the FT-2000 protection access upgrade has been completed (including circuit pack installation) for the engineered configuration. It is further assumed that the new low-speed shelf is powered and running. If at any point in this procedure, the FT-2000 end terminal fails to respond in the indicated way, refer the trouble to installation personnel.</p> <p>At the local FT-2000 network element, inspect the shelf (Low Speed Shelf - Complementary) for obvious damage.</p> | —                  |
| 2.                                       | If required at the local FT-2000 network element, connect and condition the craft interface terminal (CIT).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | <b>DLP-501</b>     |
| 3.                                       | At the local FT-2000 network element and at the CIT, select CONFIGURATION-Retrieve-Protection-Line to obtain the Network Element Protection Switching Provisioning Report.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | —                  |

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                     | FOR DETAILS, GO TO |
|------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 4.                                       | At the local FT-2000 network element, does the report indicate that Restoration Protection Access has been provisioned to "yes"?<br><br>If <b>YES</b> , then continue with Step 5.<br><br>If <b>NO</b> , then continue with Step 6. |                    |
| 5.                                       | At the local FT-2000 network element and at the CIT, select CONFIGURATION-Set-Protection-Line and set Restoration Protection Access to no.                                                                                          | —                  |
| 6.                                       | At the local FT-2000 network element, remove all DS3 inputs and outputs at the DSX-3 cross-connect panel or equivalent for the protection access shelf.                                                                             | —                  |
| 7.                                       | At the local FT-2000 network element and at the CIT, select CONFIGURATION-Update and execute.                                                                                                                                       | —                  |
| 8.                                       | At the remote FT-2000 end terminal, have someone repeat Steps 1 through 7.                                                                                                                                                          | —                  |
| 9.                                       | At the local FT-2000 end terminal and at the CIT, select FAULT-Switch-Line for line P with priority set to Reset.                                                                                                                   | —                  |
| 10.                                      | At the local FT-2000 end terminal CIT, select FAULT (CONFIGURATION for Release 1) and perform the low-speed cabling test.                                                                                                           | <b>DLP-503</b>     |
| 11.                                      | At the remote FT-2000 end terminal CIT, select FAULT (CONFIGURATION for Release 1) and perform the low-speed cabling test.                                                                                                          | <b>DLP-503</b>     |
| 12.                                      | At the local FT-2000 end terminal CIT, select FAULT and perform the system automatic turnup test.                                                                                                                                   | <b>DLP-507</b>     |

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                           | FOR DETAILS, GO TO |
|------------------------------------------|-----------------------------------------------------------------------------------------------------------|--------------------|
| 13.                                      | At both FT-2000 end terminals, verify the <b>NE ACTY</b> LED at the user panel is off.                    | —                  |
| 14.                                      | If required, perform provisioning at this time.                                                           | <b>DLP-505</b>     |
| 15.                                      | The Protection Access Upgrade has passed the acceptance tests and is now ready for protection or traffic. | —                  |

Acceptance: NTP-004---+---DLP-501-----Operation: NTP-002  
| ---DLP-503---+---DLP-518-----DLP-511  
| | ---DLP-500---+---DLP-511  
| | | ---DLP-512  
| | | ---DLP-510-----DLP-512  
| ---DLP-507  
| ---DLP-505



## Accept FT-2000 OC-48 Repeater Bay/Shelf

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This acceptance procedure tests an out-of-service FT-2000 repeater bay/shelf. Each FT-2000 repeater bay/shelf in the FT-2000 network should be tested individually.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Optical Loopback Jumpers with 20-dB buildout  
Wrist Strap



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 repeater bay/shelf to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**NOTE:**

This procedure is only used if the FT-2000 repeater bay/shelf was installed by someone else. If the installation has not been completed (including circuit pack installation), notify the installation personnel or refer to 365-575-104 to complete the installation. If you completed the installation following the procedures in 365-575-104, do not perform this procedure.



**NOTE:**

It is assumed that installation of the FT-2000 repeater bay/shelf has been completed (including circuit pack installation) for the engineered configuration. It is further assumed that the repeater bay/shelf is powered and running. **If at any point in this procedure the FT-2000 repeater bay/shelf fails to respond in the indicated way, refer the trouble to installation personnel.**

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                          | FOR DETAILS, GO TO                                        |
|------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------|
|                                          | Inspect the FT-2000 repeater bay/shelf for obvious damage.                                                                                                                                                                               | —                                                         |
| 2.                                       | Install an optical loopback with line buildout between the <b>IN</b> and the <b>OUT</b> connectors of the same OC48 REGENR circuit pack. Repeat this step for any remaining OC48 REGENR circuit packs that are to be tested at one time. | <b>DLP-519</b>                                            |
| 3.                                       | Verify the <b>MN, NE ACTY, USE CIT</b> , and <b>PWR ON</b> LEDs at the user panel are lighted.                                                                                                                                           | —                                                         |
| 4.                                       | If required, connect and condition the craft interface terminal (CIT).                                                                                                                                                                   | <b>DLP-501</b>                                            |
| 5.                                       | Perform a local automatic turnup test on each OC48 REGENR circuit pack that is looped back.                                                                                                                                              | <b>DLP-502</b>                                            |
| 6.                                       | Remove the optical loopback with the line buildout from each OC48 REGENR circuit pack that is looped back and reconnect the OC-48 line(s).                                                                                               | <b>DLP-519</b>                                            |
| 7.                                       | If required, perform provisioning at this time.                                                                                                                                                                                          | <b>DLP-505</b>                                            |
| 8.                                       | This FT-2000 repeater bay/shelf has passed the acceptance test and is now ready for the FT-2000 point-to-point network or the FT-2000 2-fiber ring network (system) acceptance test.                                                     | <b>Acceptance: NTP-003<br/>or<br/>Acceptance: NTP-007</b> |

Acceptance: NTP-005---+---DLP-501  
 |---DLP-502  
 |---DLP-505  
 |---DLP-519  
 |---Acceptance: NTP-003 or  
 |---Acceptance: NTP-007

## Accept FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Ring

---

|                                          |                    |
|------------------------------------------|--------------------|
| DO ITEMS BELOW IN ORDER LISTED . . . . . | FOR DETAILS, GO TO |
|------------------------------------------|--------------------|

---

1. **Overview:** This acceptance procedure tests an **out-of-service** FT-2000 OC-48 Add/Drop-Rings Terminal for a 2-fiber ring in a stand-alone state. Each FT-2000 terminal (node) in the FT-2000 ring network should be tested individually.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap  
Optical Loopback Jumpers with 20-dB buildout



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**NOTE:**

This procedure is only used if the FT-2000 add/drop-rings terminal was installed by someone else. If the installation has not been completed (including circuit pack installation), notify the installation personnel or refer to 365-575-115 to complete the installation. **If you completed the installation following the procedures in 365-575-115, do not perform this procedure.**

---

|                                          |                    |
|------------------------------------------|--------------------|
| DO ITEMS BELOW IN ORDER LISTED . . . . . | FOR DETAILS, GO TO |
|------------------------------------------|--------------------|

---

⇒ **NOTE:**  
It is assumed that installation of the FT-2000 add/drop-rings terminal has been completed (including circuit pack installation) for the engineered configuration. It is further assumed that the add/drop-rings terminal is powered and running. **If at any point in this procedure the FT-2000 add/drop-rings terminal fails to respond in the indicated way, refer the trouble to installation personnel.**

⇒ **NOTE:**  
Ignore the LEDs at the user panel during this test.

Inspect the FT-2000 add/drop-rings terminal for  
obvious damage

—

---

2. ⇒ **NOTE:**  
All connections to other equipment must be removed.

At the DSX-3, STSX-1, and/or LGX<sup>®</sup>  
cross-connect panel or equivalent, remove all  
DS3, EC1, OC3, IS3 and/or OC12 connections  
to the FT-2000 terminal. All electrical output  
ports associated with slots equipped with  
DS3/EC1 packs must be terminated with  
75-ohms. Make a record of these  
disconnections so they can be replaced after  
the test.

—

---

3. Loop back all high-speed OC-48 lines (so that the  
OUT jack of TRMTR 1E is connected to the IN jack of  
RCVR 1E and the OUT jack of TRMTR 1W is  
connected to the IN jack of RCVR 1W).

**DLP-500**

---

4. If required, connect the craft interface terminal (CIT)  
to the local FT-2000 network element and condition  
it.

**DLP-501**

---

5. Log in to the CIT by entering "**cit expert**". This is  
required when using the  
**CONFIGURATION-Set-Synchronization**  
command in Steps 7 and 15.

—

---



| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | FOR DETAILS, GO TO |
|------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 6.                                       | At the CIT, select <b>SECURITY-Retrieve-Network Element</b> to obtain the FT-2000 System Provisioning Report (example shown in Section 11) and verify (1) the value of the TID (Target ID), (2) the value of the NID (Node ID) for Releases 5 and lower, and (3) the value of Directory Service NE (yes/no) agree with the values from the engineered configuration work order. At this time, you may want to change a parameter that does not agree with the work order. Use the <b>SECURITY-Set-Network Element</b> command. | —                  |
| 7.                                       | At the CIT, select <b>CONFIGURATION-Set-Synchronization</b> with the <b>Provisioned Mode</b> set to <b>Free Running</b> , but record the original parameter value before executing. Verification can be obtained by selecting <b>CONFIGURATION-Retrieve-Synchronization</b> (an example is shown in Section 11).                                                                                                                                                                                                               | —                  |
| 8.                                       | At the CIT, select <b>FAULT-Reset</b> and execute (reset takes about 15 minutes).                                                                                                                                                                                                                                                                                                                                                                                                                                              | —                  |
| 9.                                       |  <b>NOTE:</b><br>Reset disconnects the CIT from the network element.<br><br>At the CIT, log out and log in again by entering "cit expert". If you log in before reset is completed, the Active Alarms and Status Report will show a <b>reset in progress</b> condition.                                                                                                                                                                     | —                  |
| 10.                                      | At the CIT, select <b>CONFIGURATION-Update</b> and execute (update takes about 1 minute).                                                                                                                                                                                                                                                                                                                                                                                                                                      | —                  |

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | FOR DETAILS, GO TO |
|------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 11.                                      | Use <b>FAULT-Retrieve-Alarm</b> to obtain an Active Alarm and Status Report. Make a note of any conditions other than those listed below that appear in the report. (Your report may not include all of the conditions listed.) Report these conditions to installation personnel if any failures occur during the following test.<br><br>system      ring incomplete/open<br>system      ring startup in progress<br>system      DS-NE not reachable<br>system      squelch map conflict<br>dcc-1e      incoming DCC failure<br>dcc-1w      incoming DCC failure<br>line-1e      site provisioning conflict<br>line-1w      site provisioning conflict<br><br> <b>NOTE:</b><br>This test may be denied if the terminal is exhibiting conditions or alarms other than those listed here. The disallowed condition must be cleared before continuing. | —                  |
| 12.                                      | Perform local automatic turnup test for all lines.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | <b>DLP-502</b>     |
| 13.                                      | Use <b>FAULT-Retrieve-Alarm</b> to obtain an Active Alarm and Status Report. Make a note of any conditions, other than those listed in Step 11, that appear in the report. (Your report may not include all of the conditions listed.) Report these conditions to installation personnel if any failures occur during the following test.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | —                  |
| 14.                                      |  <b>NOTE:</b><br>Only 2-way cables (input and output) can be tested.<br><br>Perform automatic low-speed cabling test ( to cross-connect panel) for all low-speed ports.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | <b>DLP-503</b>     |

| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                                                          | FOR DETAILS, GO TO         |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------|
| 15. Restore the synchronization parameter value back to its original value as recorded in Step 7. (At the CIT, select <b>CONFIGURATION-Set-Synchronization</b> and set <b>Provisioned Mode</b> to the original parameter value.)                                                                  | —                          |
| 16. Reconnect all OC-48 lines (removed in Step 3).                                                                                                                                                                                                                                                | —                          |
| 17. Reconnect all DS3, EC1, OC3, IS3 and/or OC12 connections (recorded and removed in Step 2) at the DSX-3, STSX-1, and/or LGX cross-connect panel or equivalent.                                                                                                                                 | —                          |
| 18. If required, perform additional provisioning at this time (date, time, etc.).                                                                                                                                                                                                                 | <b>DLP-505</b>             |
| Helpful provisioning sources for the experienced technician are 365-575-122, <i>FT-2000 OC-48 Add/Drop-Rings Terminal Provisioning Job Aid (Releases 3, 4, 5, and 6)</i> , and 365-575-123, <i>FT-2000 Threshold-Crossing Alert (TCA) Parameters Provisioning Job Aid (Release 4 and later)</i> . |                            |
| 19. The FT-2000 terminal has passed the acceptance tests. After testing all of the FT-2000 terminals individually, the FT-2000 ring network will be ready for the acceptance test.                                                                                                                | <b>Acceptance: NTP-007</b> |

Acceptance: NTP-006---+---TAD-100  
 |---DLP-500  
 |---DLP-501  
 |---DLP-502  
 |---DLP-503  
 |---DLP-505  
 |---Job Aid 365-575-122  
 |---Job Aid 365-575-123  
 |---Acceptance: NTP-007



## Accept FT-2000 2-Fiber Ring Network

---

|                                                 |                           |
|-------------------------------------------------|---------------------------|
| <b>DO ITEMS BELOW IN ORDER LISTED . . . . .</b> | <b>FOR DETAILS, GO TO</b> |
|-------------------------------------------------|---------------------------|

---

1. **Overview:** This acceptance procedure for the FT-2000 2-fiber ring network can be performed from any FT-2000 add/drop-rings terminal in the network. This acceptance procedure is performed after all of the FT-2000 add/drop-rings terminals and FT-2000 repeater bay/shelves or FT-Lightwave Booster Amplifiers (FT-LBA) in the network have been individually acceptance tested.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**NOTE:**

This procedure is only used if the FT-2000 2-fiber ring network was installed by someone else. If the installation has not been completed, notify the installation personnel or refer to 365-575-115 to complete the installation. If ***you completed the installation following the procedures in 365-575-115, do not perform this procedure.***



**NOTE:**

Each FT-2000 network element should have been individually acceptance tested. If this has not been done, refer to **Acceptance: NTP-006** for the FT-2000 add/drop-rings terminal, or **Acceptance: NTP-011** for the FT-2000 add/drop-rings terminal for operation with an FT-LBA, and/or to **Acceptance: NTP-005** for the FT-2000 repeater bay/shelf. ***If at any point in this procedure any FT-2000 network element fails to respond in the indicated way, refer the trouble to installation personnel.***

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---



**NOTE:**

If the ring network is equipped with FT-LBAs, it is **required** that they be in place (connected in the ring) during ring acceptance testing. The FT-LBAs should be transparent during ring acceptance.

Verify the FT-2000 network elements are connected in an FT-2000 2-fiber bidirectional ring.

**DLP-506**

- 
- |    |                                                                                                                               |   |
|----|-------------------------------------------------------------------------------------------------------------------------------|---|
| 2. | Verify the <b>NE ACTY</b> and the <b>FE ACTY</b> LEDs are off at the user panel on the local FT-2000 add/drop-rings terminal. | — |
|----|-------------------------------------------------------------------------------------------------------------------------------|---|
- 
- |    |                                                                                                                |                |
|----|----------------------------------------------------------------------------------------------------------------|----------------|
| 3. | If required, connect the craft interface terminal (CIT) to the local FT-2000 network element and condition it. | <b>DLP-501</b> |
|----|----------------------------------------------------------------------------------------------------------------|----------------|
- 
- |    |                                                                                                                       |   |
|----|-----------------------------------------------------------------------------------------------------------------------|---|
| 4. | At the CIT, select <b>CONFIGURATION-Retrieve-Map-Ring</b> and execute to obtain a Ring Map Report for the local node. | — |
|----|-----------------------------------------------------------------------------------------------------------------------|---|
- 
- |    |                                                                                                                                                                                                                                                                                                                                                                                      |   |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 5. | The Ring Map Report lists the ring nodes from west to east starting at the local node. If the nodes are incorrectly connected, the order of the nodes in the report will also be incorrect.                                                                                                                                                                                          |   |
|    | Verify the Ring Map Report agrees with the engineered data, particularly the NID (node identification for Release 5 and lower) and TID (target identifier). For Releases 3 and 4.0, also verify that <b>Comm. Status</b> is "good". For Releases 4.1 and later, also verify that <b>DCC Status</b> and <b>Ring Continuity</b> are "good". Section 11 shows an example of the report. | — |
- 
- |    |                                                                                                                                                                        |   |
|----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 6. | At the CIT, select <b>SECURITY-Retrieve-Network Element</b> and execute to obtain an FT-2000 System Provisioning Report for the local FT-2000 add/drop-rings terminal. | — |
|----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
- 
- |    |                                                                                                                                                                                |   |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 7. | Verify the report agrees with the engineered data, particularly the Directory Service NE value. Only one DSNE is allowed per ring network. Section 11 shows example of report. | — |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
-

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | FOR DETAILS, GO TO                                 |
|------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------------------------------------------|
| 8.                                       | At the CIT, select<br><b>CONFIGURATION-Retrieve-Synchronization</b><br>and execute to obtain a Synchronization Report for<br>the local node.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | —                                                  |
| 9.                                       | Verify the report agrees with the engineered data,<br>particularly the Provisioned Mode. Section 11 shows<br>example of report.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | —                                                  |
| 10.                                      |  <b>NOTE:</b><br>If you are remotely logged in, you will require assistance at the distant node in<br>order to perform this step.<br><br>If required, test office alarm operation.                                                                                                                                                                                                                                                                                                                                                                                                         | <b>DLP-504</b>                                     |
| 11.                                      |  <b>NOTE:</b><br>An FT-LBA provides four miscellaneous discrete (environmental) inputs to the<br>FT-2000 terminal. If the network is so equipped, these inputs are tested<br>separately in <b>Acceptance: NTP-011</b> .<br><br> <b>NOTE:</b><br>If you are remotely logged in, you will require assistance at the distant node in<br>order to perform this step.<br><br>If required, check telemetry interfaces.<br>a. Parallel telemetry<br>b. Serial telemetry<br>c. Miscellaneous discrete telemetry | <b>DLP-508</b><br><b>DLP-509</b><br><b>DLP-530</b> |
| 12.                                      | Establish a remote login to one of the other FT-2000<br>add/drop-rings terminals on the FT-2000 ring network.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | <b>DLP-524</b>                                     |
| 13.                                      | Repeat Steps 6 through 11 for that FT-2000<br>add/drop-rings terminal.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      | —                                                  |
| 14.                                      | Discontinue the remote login.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | <b>DLP-524</b>                                     |
| 15.                                      | Repeat Steps 12 through 14 for each of the remaining<br>FT-2000 add/drop-rings terminals in the FT-2000 ring<br>network.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | —                                                  |

|     |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  |
|-----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 16. | <p> <b>NOTE:</b><br/>The <b>FE ACTY</b> LED will only light if the Remote Activity Report parameter of the <b>SECURITY-Set-Far End Communications</b> command has been enabled.</p> <p>Are both the <b>NE ACTY</b> and the <b>FE ACTY</b> LEDs off at the local FT-2000 add/drop-rings terminal? —</p> <p>If <b>YES</b>, then continue with Step 17.</p> <p>If <b>NO</b>, then the trouble must be referred to the group that installed it.</p> |
| 17. | <p>If required, notify personnel at the remote maintenance site (OS center) that the FT-2000 ring network is ready for any operational tests they wish to perform using the X.25 link. —</p>                                                                                                                                                                                                                                                                                                                                     |
| 18. | <p>The FT-2000 ring network has passed this acceptance test and is now ready to provide service. —</p>                                                                                                                                                                                                                                                                                                                                                                                                                           |

Acceptance: NTP-007---+---TAD-100  
|---DLP-501  
|---DLP-504  
|---DLP-506  
|---DLP-508  
|---DLP-509  
|---DLP-524  
|---DLP-530



## Accept Added Electrical Low-Speed Cables to an In-Service FT-2000 Network Element

---

|                                          |                    |
|------------------------------------------|--------------------|
| DO ITEMS BELOW IN ORDER LISTED . . . . . | FOR DETAILS, GO TO |
|------------------------------------------|--------------------|

---

1. **Overview:** This acceptance procedure is used to test added electrical low-speed cables between an in-service FT-2000 network element and the corresponding cross-connect panel or other terminating equipment.

**Required Test Equipment:** Craft Interface Terminal (CIT) Wrist Strap

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100.***

This procedure is only used if the added electrical low-speed cables were installed by someone else. If the installation has not been completed (including associated circuit pack installation), notify the installation personnel, or refer to the installation manual 365-575-115, to complete the installation. If you completed the installation following the procedures in the installation manual, do not perform this procedure.

It is assumed that installation of the added electrical low-speed cables has been completed (including associated circuit pack installation) for the engineered configuration and that the FT-2000 network element is in service. If at any point in this procedure the FT-2000 add/drop-rings terminal fails to respond in the indicated way, refer the trouble to installation personnel.

If required, connect the craft interface terminal (CIT) to the local FT-2000 network element and condition it.

**DLP-501**

- 
2. At the CIT, select CONFIGURATION-Retrieve-Alarm to obtain the Active Alarm and Status Report and verify that there are no active alarms or status conditions. Section 11 shows example of report.
-

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                               | FOR DETAILS, GO TO |
|------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| <hr/>                                    |                                                                                                                                                                                                                                                                                               |                    |
| 3.                                       | <i>If the automatic low-speed cabling test is accidentally performed on an in-service low-speed cable, service will be interrupted.</i><br><br>Only 2-way cables (input and output) can be tested.<br><br>Perform automatic low-speed cabling test for all added electrical low-speed cables. | <b>DLP-503</b>     |
| <hr/>                                    |                                                                                                                                                                                                                                                                                               |                    |
| 4.                                       | At the CIT, select CONFIGURATION-Retrieve-Alarm to obtain the Active Alarm and Status Report and verify that there are no active alarms or status conditions. Section 11 shows example of report.                                                                                             |                    |
| <hr/>                                    |                                                                                                                                                                                                                                                                                               |                    |
| 5.                                       | The added electrical low-speed cables have passed the acceptance tests and are ready for service.                                                                                                                                                                                             | —                  |
| <hr/>                                    |                                                                                                                                                                                                                                                                                               |                    |

Acceptance: NTP-008---+---TAD-100  
|---DLP-501  
|---DLP-503

## Accept Dual Ring Interworking (DRI) Connections to/from an FT-2000 Ring

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This procedure verifies path integrity as the path enters and/or leaves the local FT-2000 ring. Figures 1 through 5 show different path possibilities for the local FT-2000 ring. Every external network may be different in the way it protects path integrity; therefore, it is imperative that the active path and standby path(s) be known.

### **Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap  
Microwave Logic's ST-112 SONETest Transmission Analyzer, or  
Tektronix CTS-710 SONET Transmission Analyzer, or  
Tberd 310 SONET Transmission Analyzer, or  
Cerjac 156 SONET Transmission Analyzer, or equivalent.



### **WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



### **CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 add/drop-rings terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



### **NOTE:**

This procedure is used to test an individual path through a local FT-2000 ring, but keep in mind that a typical FT-2000 ring will have many more paths and DRI cross-connections than what is shown in Figures 1 through 5.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

⇒ **NOTE:**  
These tests pertain to a single path through a local FT-2000 ring that can have up to 16 nodes. The path being tested can encounter either one or two DRI cross-connections as it enters and/or leaves a ring per the examples in Figures 1 through 5.

⇒ **NOTE:**  
Assistance is required at the remote source/destination access points in the external network or at the test sets when connected directly to the local FT-2000 ring.

⇒ **NOTE:**  
Path testing should be accomplished by using an end-to-end arrangement as shown in Figure 6. However, due to equipment availability, another possibility includes a loopback arrangement (Figure 7).

⇒ **NOTE:**  
It is assumed that acceptance testing of the local FT-2000 ring has passed. ***If at any point in this procedure the local FT-2000 ring fails to respond in the indicated way, refer the trouble to installation personnel.***

Referring to Figures 1 through 5, which figure —  
most closely represents your configuration?

If **Figure 1**, then continue with Step 2.

If **Figure 2**, then continue with Step 11.

If **Figure 3**, then continue with Step 20.

If **Figure 4**, then continue with Step 33.

If **Figure 5**, then continue with Step 47.

- 
2. The local FT-2000 ring has one DRI cross-connection to the outside. Both DRI cross-connections and local source/destination cross-connections terminate directly to test sets.

Verify that there are no alarm or status conditions in —  
the ring (Figure 1).

---

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                   | FOR DETAILS, GO TO |
|------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 3.                                       | At the transmit test set, insert a verifiable signal for each path being tested.                                                                                                                                                                                                                                  | —                  |
| 4.                                       | At the receive test set, verify the detection of the inserted signal on each path being tested.                                                                                                                                                                                                                   | —                  |
| 5.                                       | To test path integrity for 2-way paths, repeat Steps 3 and 4 from opposite ends of each path.                                                                                                                                                                                                                     | —                  |
| 6.                                       | At the FT-2000 primary node P, execute a manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1)-Manual</b> command for the appropriate line(s) and tributary(s).                                                                                                                | —                  |
| 7.                                       | Repeat Steps 3 through 5 for each path being tested.                                                                                                                                                                                                                                                              | —                  |
| 8.                                       | At the FT-2000 primary node P, reset the manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1)-Reset</b> command for the appropriate line(s) and tributary(s).                                                                                                                 | —                  |
| 9.                                       | Verify that there are no alarm or status conditions in the ring.                                                                                                                                                                                                                                                  | —                  |
| 10.                                      | This completes testing path integrity (Figure 1).                                                                                                                                                                                                                                                                 | —                  |
| 11.                                      | The local FT-2000 ring has one DRI cross-connection to the outside. The DRI cross-connections terminate on an external network and the local source/destination cross-connections terminate directly to a test set.<br><br>Verify that there are no alarm or status conditions in either ring/network (Figure 2). | —                  |
| 12.                                      | At the transmit end in the external network, insert a verifiable signal into each path being tested.                                                                                                                                                                                                              | —                  |
| 13.                                      | At the receive test set, verify the detection of the inserted signal on each path being tested.                                                                                                                                                                                                                   | —                  |
| 14.                                      | To test path integrity for 2-way paths, repeat Steps 12 and 13 from opposite ends of each path.                                                                                                                                                                                                                   | —                  |

| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | FOR DETAILS, GO TO |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 15. At the FT-2000 primary node P, execute a manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-ST3(or ST1)-Manual</b> command for the appropriate line(s) and tributary(s).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | —                  |
| 16. Repeat Steps 12 through 14 for each path being tested.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | —                  |
| 17. At the FT-2000 primary node P, reset the manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-ST3(or ST1)-Reset</b> command for the appropriate line(s) and tributary(s).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | —                  |
| 18. Verify that there are no alarm or status conditions in the ring.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | —                  |
| 19. This completes testing path integrity (Figure 2) in the local FT-2000 ring. If the external network is non-Lucent (another vendor), refer to local maintenance procedures to test path integrity in that network. If the network is a DDM-2000 ring, refer to the appropriate manual as follows:<br><br><div data-bbox="337 1182 1312 1423" data-label="List-Group"> <ul style="list-style-type: none"> <li><b>63-206-202 for DDM-2000 OC3</b> for Releases 2 through 7.2</li> <li><b>63-206-280 for DDM-2000 OC3</b> for Release 8 and Higher</li> <li><b>63-206-207 for DDM-2000 OC12/OC12 Regenerator</b> for Releases 1 through 3.1</li> <li><b>63-206-290 for DDM-2000 OC12</b> for Release 5 and Higher</li> </ul> </div> | —                  |
| 20. The local FT-2000 ring has two DRI cross-connections to the outside. Both DRI cross-connections terminate directly to test sets.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |                    |
| Verify that there are no alarm or status conditions in the ring (Figure 3).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | —                  |
| 21. At the transmit test set, insert a verifiable signal for each path being tested.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | —                  |
| 22. At the receive test set, verify the detection of the inserted signal on each path being tested.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | —                  |

| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                           | FOR DETAILS, GO TO |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 23. To test path integrity for 2-way paths, repeat Steps 21 and 22 from opposite ends of each path.                                                                                                                                                                | —                  |
| 24. At the FT-2000 primary node P, execute a manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1) -Manual</b> command for the appropriate line(s) and tributary(s).                                                            | —                  |
| 25. Repeat Steps 21 through 23 for each path being tested.                                                                                                                                                                                                         | —                  |
| 26. DO NOT RESET the DRI switch at node P at this time. At FT-2000 primary node P' (P prime), execute a manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1) -Manual</b> command for the appropriate line(s) and tributary(s). | —                  |
| 27. Repeat Steps 21 through 23 for each path being tested.                                                                                                                                                                                                         | —                  |
| 28. At the FT-2000 primary node P, reset the manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1) -Reset</b> command for the appropriate line(s) and tributary(s).                                                             | —                  |
| 29. Repeat Steps 21 through 23 for each path being tested.                                                                                                                                                                                                         | —                  |
| 30. At the FT-2000 primary node P', reset the manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1) -Reset</b> command for the appropriate line(s) and tributary(s).                                                            | —                  |

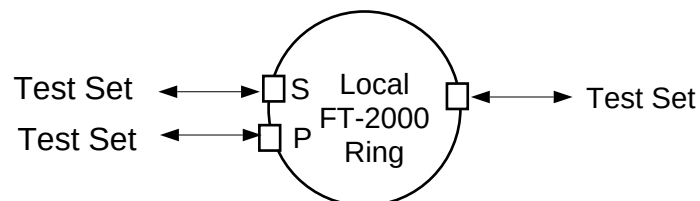
| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                          | FOR DETAILS, GO TO |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 31. Verify that there are no alarm or status conditions in the ring.                                                                                                                                                                                              | —                  |
| 32. This completes testing path integrity (Figure 3).                                                                                                                                                                                                             | —                  |
| 33. The local FT-2000 ring has two DRI cross-connections to the outside. One DRI cross-connection terminates in an external network and the other DRI cross-connection terminates directly to test sets.                                                          |                    |
| Verify that there are no alarm or status conditions in the ring (Figure 4).                                                                                                                                                                                       | —                  |
| 34. At the transmit end in the external network, insert a verifiable signal into each path being tested.                                                                                                                                                          | —                  |
| 35. At the receive test set, verify the detection of the inserted signal on each path being tested.                                                                                                                                                               | —                  |
| 36. To test path integrity for 2-way paths, repeat Steps 34 and 35 from opposite ends of each path.                                                                                                                                                               | —                  |
| 37. At the FT-2000 primary node P, execute a manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1)-Manual</b> command for the appropriate line(s) and tributary(s).                                                            | —                  |
| 38. Repeat Steps 34 through 36 for each path being tested.                                                                                                                                                                                                        | —                  |
| 39. DO NOT RESET the DRI switch at node P at this time. At FT-2000 primary node P' (P prime), execute a manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1)-Manual</b> command for the appropriate line(s) and tributary(s). | —                  |
| 40. Repeat Steps 34 through 36 for each path being tested.                                                                                                                                                                                                        | —                  |
| 41. At the FT-2000 primary node P, reset the manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3-Reset</b> command for the appropriate line(s) and tributary(s).                                                                      | —                  |



| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | FOR DETAILS, GO TO |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 42. Repeat Steps 34 through 36 for each path being tested.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | —                  |
| 43. At the FT-2000 primary node P', reset the manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3-Reset</b> command for the appropriate line(s) and tributary(s).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | —                  |
| 44. Verify that there are no alarm or status conditions in the ring.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | —                  |
| 45. This completes testing path integrity (Figure 4).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | —                  |
| 46. This completes testing path integrity (Figure 4) in the local FT-2000 ring. If the external network is non-Lucent (another vendor), refer to local maintenance procedures to test path integrity in that ring. If the network is a DDM-2000 ring, refer to the appropriate manual as follows:<br><br><div data-bbox="383 1073 1357 1314" data-label="List-Group"> <ul style="list-style-type: none"> <li><b>63-206-202 for DDM-2000 OC3</b> for Releases 2 through 7.2</li> <li><b>63-206-280 for DDM-2000 OC3</b> for Release 8 and Higher</li> <li><b>63-206-207 for DDM-2000 OC12/OC12 Regenerator</b> for Releases 1 through 3.1</li> <li><b>63-206-290 for DDM-2000 OC12</b> for Release 5 and Higher</li> </ul> </div> | —                  |
| 47. The local FT-2000 ring has two DRI cross-connections to the outside. Both DRI cross-connections terminate to external networks.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                    |
| Verify that there are no alarm or status conditions in either ring (Figure 5).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | —                  |
| 48. At the transmit end in the external network (left side), insert a verifiable signal into each path being tested.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             | —                  |
| 49. At the receive end in the external network (right side), verify the detection of the inserted signal on each path being tested.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | —                  |
| 50. To test path integrity for 2-way paths, repeat Steps 48 and 49 from opposite ends of each path.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | —                  |

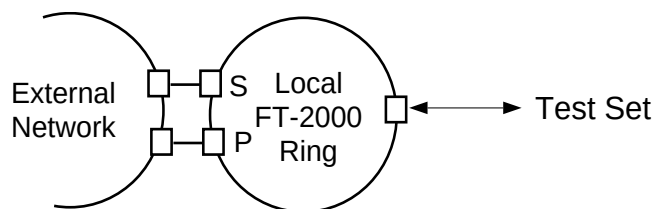
| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                           | FOR DETAILS, GO TO |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 51. At the FT-2000 primary node P, execute a manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1) -Manual</b> command for the appropriate line(s) and tributary(s).                                                            | —                  |
| 52. Repeat Steps 48 through 51 for each path being tested.                                                                                                                                                                                                         | —                  |
| 53. DO NOT RESET the DRI switch at node P at this time. At FT-2000 primary node P' (P prime), execute a manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1) -Manual</b> command for the appropriate line(s) and tributary(s). | —                  |
| 54. Repeat Steps 48 through 50 for each path being tested.                                                                                                                                                                                                         | —                  |
| 55. At the FT-2000 primary node P, reset the manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1) -Reset</b> command for the appropriate line(s) and tributary(s).                                                             | —                  |
| 56. Repeat Steps 48 through 50 for each path being tested.                                                                                                                                                                                                         | —                  |
| 57. At the FT-2000 primary node P'(P prime), reset the manual DRI switch for each path being tested. Use the <b>FAULT-Switch-Path-STS3(or STS1) -Reset</b> command for the appropriate line(s) and tributary(s).                                                   | —                  |
| 58. Verify that there are no alarm or status conditions in the ring.                                                                                                                                                                                               | —                  |

| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                                                                             | FOR DETAILS, GO TO |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 59. This completes testing path integrity (Figure 5).                                                                                                                                                                                                                                                                | —                  |
| 60. This completes testing path integrity (Figure 5) in the local FT-2000 ring. If one of the external networks is non-Lucent (another vendor), refer to local maintenance procedures to test path integrity in that network. If an external network is a DDM-2000 ring, refer to the appropriate manual as follows: | —                  |
| 63-206-202 for DDM-2000 OC3 for Releases 2 through 7.2                                                                                                                                                                                                                                                               |                    |
| 63-206-280 for DDM-2000 OC3 for Release 8 and Higher                                                                                                                                                                                                                                                                 |                    |
| 63-206-207 for DDM-2000 OC12/OC12 Regenerator for Releases 1 through 3.1                                                                                                                                                                                                                                             |                    |
| 63-206-290 for DDM-2000 OC12 for Release 5 and Higher                                                                                                                                                                                                                                                                |                    |



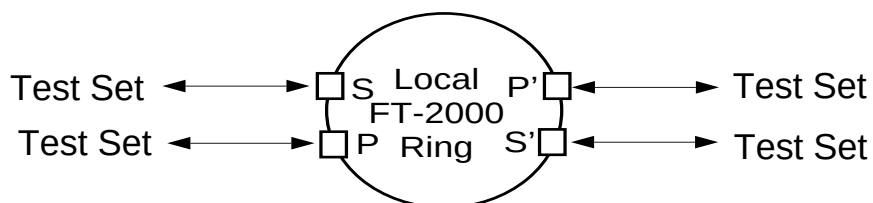
Note: One DRI cross-connection to the outside. Both DRI cross-connections and local source/destination cross-connections terminate directly to test sets.

**Figure 1 – Testing Local FT-2000 Ring (Three Nodes)**



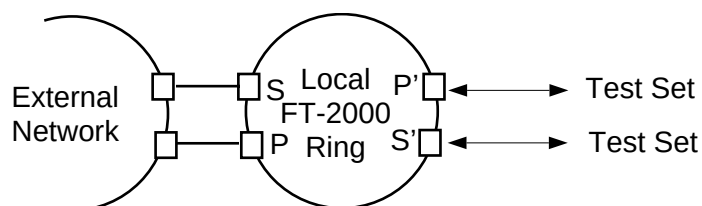
Note: One DRI cross-connection to the outside. The DRI cross-connections terminate on an external network and the local source/destination cross-connections terminate directly to a test set.

**Figure 2 – Testing Local FT-2000 Ring (Three Nodes) and External Network**



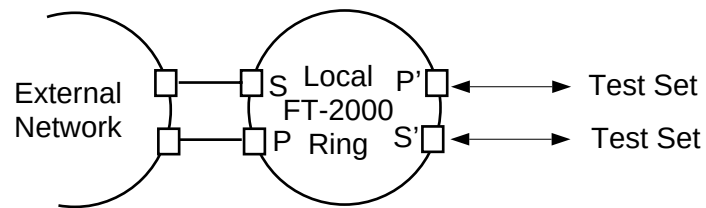
Note: Two DRI cross-connections to the outside. Both DRI cross-connections terminate directly to test sets.

**Figure 3 – Testing Local FT-2000 Ring (Four Nodes)**



Note: Two DRI cross-connections to the outside. One DRI cross-connection terminates in an external network and the other DRI cross-connection terminates directly to test sets.

**Figure 4 – Testing Local FT-2000 Ring (Four Nodes) and External Network**



Note: Two DRI cross-connections to the outside. One DRI cross-connection terminates in an external network and the other DRI cross-connection terminates directly to test sets.

**Figure 5 – Testing Local FT-2000 Ring (Four Nodes) and Two External Network**

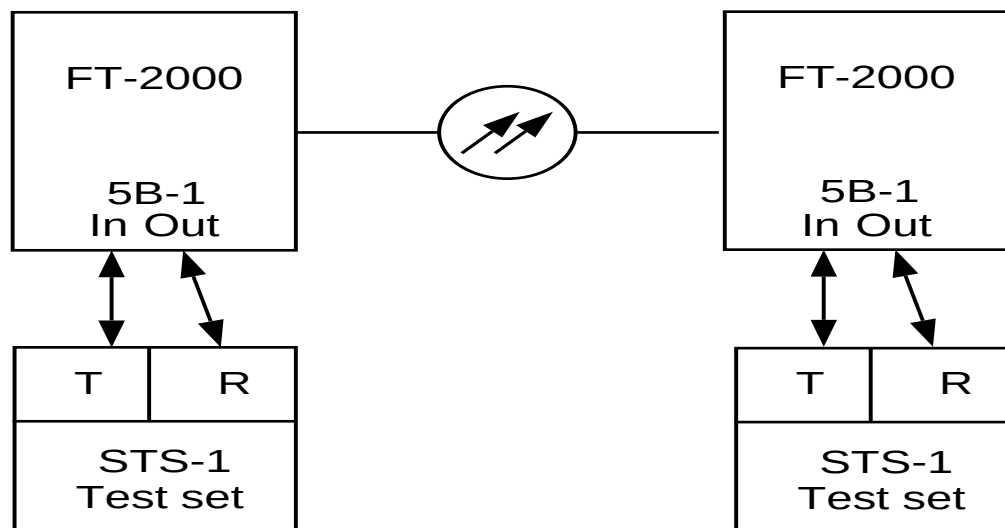


**Figure 6 – End-to-End Test Arrangement (for Slot 5B)**

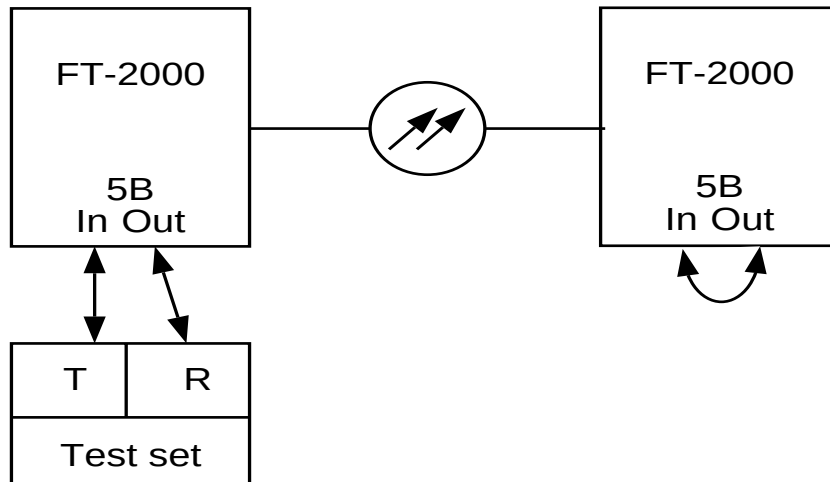


**Figure 7 – Loopback Test Arrangement (for Slot 5B/Port 1)**

## Accept Added Optical Low-Speed Fibers to an In-Service FT-2000 Network Element

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This acceptance procedure is used to test new optical low-speed fibers between an **in-service FT-2000 network element** and the corresponding lightguide cross-connect panel or other terminating equipment.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**NOTE:**

This procedure is only used if the new optical low-speed fibers were installed by someone else. If the installation has not been completed (including associated circuit pack installation), notify the installation personnel or refer to 365-575-115 to complete the installation. ***If you completed the installation following the procedures in the installation manual, do not perform this procedure.***

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

 **NOTE:**  
It is assumed that installation of the new optical low-speed fibers has been completed (including associated circuit pack installation) for the engineered configuration and that the FT-2000 network element is in service. ***If at any point in this procedure the FT-2000 add/drop-rings terminal fails to respond in the indicated way, refer the trouble to installation personnel.***

If required, connect the craft interface terminal (CIT) to the local FT-2000 network element and condition it.

**DLP-501**

- 
2. At the CIT, select FAULT-Retrieve-Alarm to obtain the Active Alarm and Status Report (example is shown in Section 11) and verify that there are no active alarms or status conditions.

—

- 
3.  **CAUTION:**  
*If the automatic low-speed cabling (fiber) test is accidentally performed on an in-service low-speed fiber, service will be interrupted.*

 **NOTE:**  
Only 2-way fibers (input and output) can be tested.

Perform automatic low-speed cabling (fiber) test for all new optical low-speed fibers.

**DLP-503**

- 
4. At the CIT, select FAULT-Retrieve-Alarm to obtain the Active Alarm and Status Report (example is shown in Section 11) and verify that there are no active alarms or status conditions.

—

- 
5. The new optical low-speed fibers have passed the acceptance tests and are ready for service.

—



## Accept FT-2000 OC-48 Add/Drop-Rings Terminal for Operation With an FT-2000 Lightwave Booster Amplifier (FT-LBA)

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This acceptance procedure tests the four miscellaneous discrete telemetry connections from an FT-Lightwave Booster Amplifier (FT-LBA) to the FT-2000 terminal. It also verifies that the appropriate text strings have been provisioned for these inputs at the FT-2000 terminal. Additional descriptive information is in Section 6, "User-Settable Miscellaneous Discrete Interface."

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---



**NOTE:**

This procedure is only used if the FT-2000 add/drop-rings terminal operating with an FT-LBA was installed by someone else. If the installation has not been completed (including circuit pack installation), notify the installation personnel or refer to 365-575-115 and/or 365-565-500 to complete the installation. If you completed the terminal installation and/or the FT-LBA installation following the procedures in 365-575-115 or 365-565-500, respectively, do not perform this procedure.

It is assumed that installation of the FT-2000 add/drop-rings terminal operating with an FT-LBA has been completed (including circuit pack installation) for the engineered configuration. It is further assumed that the add/drop-rings terminal and the FT-LBA are powered and running. If at any point in this procedure the FT-2000 add/drop-rings terminal or the FT-LBA fails to respond in the indicated way, refer the trouble to installation personnel.

It is assumed that all of the FT-LBAs were in place (connected in the ring) during the ring acceptance procedure (**Acceptance: NTP-007**).

Inspect the FT-2000 add/drop-rings terminal and the FT-LBA for obvious damage.

—

- 
2. Do you wish to test the FT-LBA environmental discretes (inputs to the FT-2000 add/drop-rings terminal) for proper operation?

—

If **YES**, then continue with Step 3.

If **NO**, then continue with Step 9.

- 
3. If required, connect the craft interface terminal (CIT) to the local FT-2000 network element and condition it.

**DLP-501**

- 
4. Log in to the CIT by entering "cit".

—

- 
- 5.



**NOTE:**

Additional test information on the FT-LBA is contained in 365-565-500.

---

| <b>DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO</b>                                                                                                                                                                                  |   |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| At the FT-LBA, remove the 10A fuse from the -48A fuse socket on the user panel.                                                                                                                                                                     | — |
| 6. At the CIT, select FAULT-Retrieve-Alarm command to obtain an Active Alarm and Status Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.                                     | — |
| 7. From the report, verify the correct condition is listed in the <b>Description</b> column of the report. (Your report may include only one condition at a time.) If the condition does not appear, notify the installation group of the problem.  | — |
| FT-LBA fuse/power failure<br>FT-LBA 1WE failure<br>FT-LBA 1EW failure<br>FT-LBA fan failure                                                                                                                                                         |   |
| 8. Repeat Steps 5 through 7 for each of the miscellaneous discretes listed below.<br><br>Remove the 10A fuse from the -48B fuse socket at the FT-LBA,<br><br>Disconnect the 1EW fiber at the FT-LBA,<br><br>Disconnect the 1WE fiber at the FT-LBA. | — |
| 9. <b>STOP! YOU HAVE COMPLETED THIS PROCEDURE.</b>                                                                                                                                                                                                  | — |

Acceptance: NTP-011---+---TAD-100  
 |---365-575-115  
 |---365-565-500  
 |---Acceptance: NTP-007  
 |---DLP-501



## Circuit Order Task Index

---

### **FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                                                       |         |
|-------------------------------------------------------------------------------------------------------|---------|
| Add a Node to an In-Service 2-Fiber Ring Network . . . . .                                            | NTP-008 |
| Add DS3 and/or EC1 Low-Speed Port(s) . . . . .                                                        | NTP-002 |
| Add OC-12 Low-Speed Slot(s) . . . . .                                                                 | NTP-012 |
| Add OC-3/IS-3 Low-Speed Slot(s) . . . . .                                                             | NTP-010 |
| Add OC-48 Protection Line to Point-to-Point Network . . . . .                                         | NTP-005 |
| Change an OC-12 Link From 0x1 to 1+1 Protection Mode . . . . .                                        | NTP-015 |
| Change an OC-12 Link From 1+1 to 0x1 Protection Mode . . . . .                                        | NTP-017 |
| Change an OC-3/IS-3 Link From 0x1 to 1+1 Protection Mode . . . . .                                    | NTP-014 |
| Change an OC-3/IS-3 Link From 1+1 to 0x1 Protection Mode . . . . .                                    | NTP-016 |
| Delete a Node From an In-Service 2-Fiber Ring Network . . . . .                                       | NTP-009 |
| Delete DS3 and/or EC1 Low-Speed Port(s) . . . . .                                                     | NTP-003 |
| Delete OC-12 Low-Speed Slot(s) . . . . .                                                              | NTP-013 |
| Delete OC-48 Protection Line From Point-to-Point Network . . . . .                                    | NTP-004 |
| Discontinue Restoration Protection Access From Point-to-Point Network . .                             | NTP-006 |
| Establish OC-3 Optical Span Extension Between DDM-2000<br>OC-12 Ring and DDM-2000 OC-3 Ring . . . . . | NTP-018 |
| Establish Restoration Protection Access in Point-to-Point Network . . . . .                           | NTP-007 |
| Remove OC-3/IS-3 Low-Speed Circuit Pack(s) From the Shelf . . . . .                                   | NTP-011 |



## Add DS3 and/or EC1 Low-Speed Port(s)

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This procedure is used to add a low-speed port to a network element and not to make cross-connections. Refer to **DLP-536** for cross-connect information for an FT-2000 OC-48 Add/Drop-Rings Terminal.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**NOTE:**

Shelves are keyed to prevent circuit packs from being accidentally installed into the wrong slots.



**NOTE:**

This procedure may be performed after all installation wiring and testing of the Low Speed Shelf - System Controller and/or the Low Speed Shelf - Complementary have been completed or on an in-service basis. Additional installation information is contained in 365-575-104, *FT-2000 OC-48 Lightwave System, Releases 1 and 2, Installation Manual*, and in 365-575-115, *FT-2000 OC-48 Lightwave System, Releases 3, 4, and 5, Installation Manual*.



**NOTE:**

A DS3 (LAA2/LAA2B) or STS1E (LAA4) circuit pack has three ports.

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       | FOR DETAILS, GO TO |
|------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
|                                          | Obtain the circuit order instructions to add a DS3 or EC1 port(s) and note the assigned address. A point-to-point network address requires a line, slot, and port(s) assignment. A ring network address requires a slot and port(s) assignment.                                                                                                                                                                                                                                                                                       | —                  |
| 2.                                       | If required, connect and condition the craft interface terminal (CIT).                                                                                                                                                                                                                                                                                                                                                                                                                                                                | <b>DLP-501</b>     |
| 3.                                       | At the CIT, select CONFIGURATION-Retrieve-Equipment, then LS INTFC and execute to obtain an Equipage and Version Report. Section 11 shows an example of the report.                                                                                                                                                                                                                                                                                                                                                                   | —                  |
| 4.                                       | Determine from the report if the DS3 or STS1E circuit pack is already present in each slot associated with a DS3 or EC1 port to be added. A circuit pack is in the slot if the type of circuit pack is indicated in the <b>Circuit Pack</b> column of the report.<br><br>If <b>circuit pack(s) is present in the slot(s)</b> , then continue with Step 10.<br>If <b>circuit pack(s) is not present in the slot(s)</b> , then continue with Step 5.                                                                                    |                    |
| 5.                                       | Remove the appropriate low-speed shelf cover.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | <b>DLP-511</b>     |
| 6.                                       | If necessary, remove the apparatus (circuit pack) blank(s) from the slot(s) requiring new circuit pack(s).                                                                                                                                                                                                                                                                                                                                                                                                                            | <b>DLP-533</b>     |
| 7.                                       |  <b>NOTE:</b><br>For circuit pack and software compatibility, refer to Table 7-1 in Section 7, "Circuit Pack Description."<br><br> <b>NOTE:</b><br>A protection circuit pack of the same type (DS3 or STS1E) as a new service electrical low-speed circuit pack must be installed in one of the protection slots (P1 or P2) if low-speed protection is desired. |                    |



| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                      |                                                                                                                                                                                                                                                                                                                                   | FOR DETAILS, GO TO |
|---------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| Install DS3 or STS1E circuit pack(s) into the specified slot(s) of the low-speed shelf per the circuit order. |                                                                                                                                                                                                                                                                                                                                   | <b>DLP-514</b>     |
| 8.                                                                                                            | Verify the <b>FAULT</b> LED on each DS3 or STS1E circuit pack(s) goes off within 1 minute after the circuit pack is installed.                                                                                                                                                                                                    | —                  |
| 9.                                                                                                            | Replace the shelf cover.                                                                                                                                                                                                                                                                                                          | <b>DLP-511</b>     |
| 10.                                                                                                           | Make the appropriate external cross-connections at the DSX-3 or equivalent.                                                                                                                                                                                                                                                       | —                  |
| 11.                                                                                                           |  <b>NOTE:</b><br>Signal degrade provisioning for the STS1E circuit pack is done for all three ports at the same time. For the DS3 circuit pack, it is done for one port at a time.<br><br>If required, provision the DS3 or EC1 port parameters. | <b>DLP-517</b>     |
| 12.                                                                                                           |                                                                                                                                                                                                                                                                                                                                   | <b>DLP-534</b>     |
| 13.                                                                                                           | The DS3 or EC1 low-speed port(s) is now ready for service.                                                                                                                                                                                                                                                                        | —                  |

Circuit Order: NTP-002---+---DLP-536  
                           |---TAD-100  
                           |---DLP-501  
                           |---DLP-511  
                           |---DLP-533  
                           |---DLP-514  
                           |---DLP-517  
                           |---DLP-534



## Delete DS3 and/or EC1 Low-Speed Port(s)

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

**1. Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**NOTE:**

A DS3 (LAA2/LAA2B) or STS1E (LAA4) circuit pack has three ports.

Obtain the circuit order instructions to delete a DS3 or EC1 port(s) and note the assigned address. A point-to-point network address requires a line, slot, and port(s) assignment. A ring network address requires a slot and port(s) assignment.

---

|    |                                                                        |                |
|----|------------------------------------------------------------------------|----------------|
| 2. | If required, connect and condition the craft interface terminal (CIT). | <b>DLP-501</b> |
|----|------------------------------------------------------------------------|----------------|

---

|    |                                                                                                                                                                                                                 |                |
|----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 3. | If required by local operating procedures, disconnect incoming signals and properly terminate outgoing DS3 or EC1 signals at the digital cross-connect bay, DACS III-2000, etc., for the port(s) being deleted. | <b>DLP-520</b> |
|----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|

---

| <b>DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO</b>                                                                                                                                                                                                                                   |                |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 4. If the receive end is equipped with DS3 circuit pack, deleting a port(s) at the transmit end of an STS-1 path will cause an NE ACTY condition at the receive end of the STS-1 path. If both ends are equipped with STS1E circuit packs, no NE ACTY condition will be observed                     |                |
| At the CIT, select CONFIGURATION-Update command and execute.                                                                                                                                                                                                                                         | —              |
| 5. Does the circuit order indicate that the circuit pack is to be removed from the shelf slot?<br><br>If <b>YES</b> , then continue with Step 6.<br>If <b>NO</b> , then continue with Step 11.                                                                                                       |                |
| 6. Verify that all three ports associated with a particular DS3 or STS1E circuit pack are to be removed or that the office records show the ports are not carrying traffic.                                                                                                                          | —              |
| 7. Remove the shelf cover from the low-speed shelf that contains the DS3 or STS1E circuit pack(s).                                                                                                                                                                                                   | <b>DLP-511</b> |
| 8. Remove the circuit pack(s) from the shelf.                                                                                                                                                                                                                                                        | <b>DLP-514</b> |
| 9. Replace the shelf cover.                                                                                                                                                                                                                                                                          | <b>DLP-511</b> |
| 10. At the CIT, select <b>CONFIGURATION - Update</b> and execute.                                                                                                                                                                                                                                    | —              |
| 11. The software release in use can be determined by using the <b>CONFIGURATION-Retrieve-Equipment:SYSCTL</b> command.<br><br>What software release is in use?<br><br>If <b>Release 4.0 or earlier</b> , then continue with Step 12.<br>If <b>Release 4.1 or later</b> , then continue with Step 14. |                |
| 12. Repeat Steps 1 through 10 at the other end of the STS-1 path.                                                                                                                                                                                                                                    | —              |

| DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO                                                                                                            |   |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 13. The DS3 or EC1 port(s) has been deleted at both ends of an STS-1 path and completes this procedure when using Release 4.0 or earlier software.                     | — |
| 14. At the other end of the STS-1 path and at the CIT, select CONFIGURATION-Update and enter. (To establish and discontinue a remote login, refer to <b>DLP-524</b> .) | — |
| 15. The DS3 or EC1 port(s) has been deleted for an STS-1 path and completes this procedure when using Release 4.1 or later software.                                   | — |

Circuit Order: NTP-003---+---TAD-100  
|---DLP-501  
|---DLP-520  
|---DLP-511  
|---DLP-514  
|---DLP-524



## Delete OC-48 Protection Line From Point-to-Point Network

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

**1. Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

Obtain the circuit order instructions and  
note which OC-48 line will be deleted.

---

|    |                                                                                                                |                |
|----|----------------------------------------------------------------------------------------------------------------|----------------|
| 2. | If required, connect the Craft Interface Terminal (CIT) to the local FT-2000 network element and condition it. | <b>DLP-501</b> |
|----|----------------------------------------------------------------------------------------------------------------|----------------|

---

|    |                                                |                |
|----|------------------------------------------------|----------------|
| 3. | Remove the appropriate high-speed shelf cover. | <b>DLP-511</b> |
|----|------------------------------------------------|----------------|

---

4. Are you at an FT-2000 end terminal or FT-2000 repeater bay/shelf?

If **FT-2000 end terminal**, then continue with Step 5.

If **FT-2000 repeater bay/shelf**, then continue with Step 20.

---

| <b>DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO</b>                                                                                                                                                                                                                                                               |                |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 5. Verify that the LNCTL circuit pack for the OC-48 line being deleted <b>is installed</b> and that its <b>FAULT</b> LED is off. If the LNCTL circuit pack was just installed, wait about 5 minutes for the <b>FAULT</b> to go off. Refer to Figure 1 for the High Speed Shelf or to Figure 2 for the Enhanced High Speed Shelf. | —              |
| 6. Remove both the TRMTR and the RCVR circuit packs for the protection OC-48 line being deleted.                                                                                                                                                                                                                                 | <b>DLP-514</b> |
| 7. At the CIT, select <b>CONFIGURATION - Update</b> and execute.                                                                                                                                                                                                                                                                 | —              |
| 8. Wait until the <b>NE ACTY</b> goes off (approximately 5 minutes).                                                                                                                                                                                                                                                             | —              |
| 9. Remove the LNCTL circuit pack for the protection OC-48 line being deleted.                                                                                                                                                                                                                                                    | <b>DLP-514</b> |
| 10. Replace the high-speed shelf cover.                                                                                                                                                                                                                                                                                          | <b>DLP-511</b> |
| 11. At the CIT, select <b>CONFIGURATION - Update</b> and execute.                                                                                                                                                                                                                                                                | —              |
| 12. Wait until the <b>NE ACTY</b> goes off (approximately 1 minute).                                                                                                                                                                                                                                                             | —              |
| 13. Is FT-2000 end terminal equipped for protection line access?<br><br>If <b>YES</b> , then continue with Step 14.<br>If <b>NO</b> , then continue with Step 20.                                                                                                                                                                | —              |
| 14. Remove the appropriate low-speed shelf cover.                                                                                                                                                                                                                                                                                | <b>DLP-511</b> |
| 15. Remove all circuit packs in the low-speed shelf.                                                                                                                                                                                                                                                                             | <b>DLP-514</b> |
| 16. Replace the low-speed shelf cover.                                                                                                                                                                                                                                                                                           | <b>DLP-511</b> |



| <b>DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO</b>                                                                                                                                                                               |                |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 17. At the CIT, select<br><b>CONFIGURATION-Retrieve-Equipment</b> and<br>All for the types and lines to obtain an<br>Equipment and Version Report.                                                                                               | —              |
| 18. Verify that the circuit packs associated with the<br>OC-48 line and the DS3s just removed do not<br>appear in the report. (A line for the associated<br>LNCTL circuit pack may still appear but all of its<br>fields should contain dashes.) | —              |
| 19. If required, repeat Steps 1 through 18 at the<br>other FT-2000 end terminal.                                                                                                                                                                 | —              |
| 20. Does the network have a repeater?<br><br>If <b>YES</b> , then continue with Step 21.<br><br>If <b>NO</b> , then continue with Step 26.                                                                                                       | <b>DLP-514</b> |
| 21. Remove the REGENR circuit packs for the<br>OC-48 line being deleted. Refer to Figure 3 for<br>the FT-2000 repeater bay/shelf.                                                                                                                | <b>DLP-514</b> |
| 22. At the CIT, select <b>CONFIGURATION-Update</b><br>and execute.                                                                                                                                                                               | —              |
| 23. Wait until the <b>NE ACTY</b> goes off (approximately<br>5 minutes).                                                                                                                                                                         | —              |
| 24. Replace the high-speed shelf cover.                                                                                                                                                                                                          | <b>DLP-511</b> |
| 25. If required, repeat Steps 21 through 24 for any<br>other FT-2000 repeater bay/shelf sites.                                                                                                                                                   | —              |
| 26. The OC-48 protection line has been deleted.                                                                                                                                                                                                  | —              |

Circuit Order: NTP-004-----DLP-501-----Operation: NTP-002  
| ---DLP-511  
| ---DLP-514-----DLP-512  
| ---DLP-510-----DLP-512

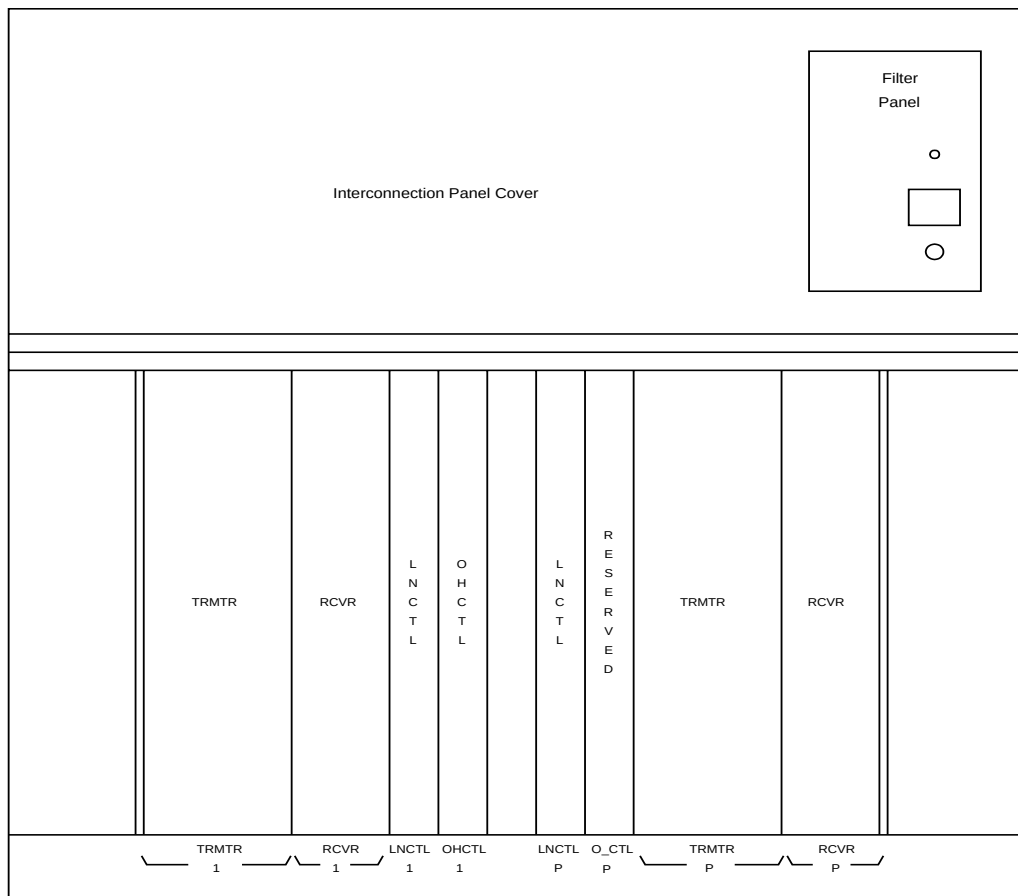


**Figure 1 – High Speed Shelf (Cover Removed)**

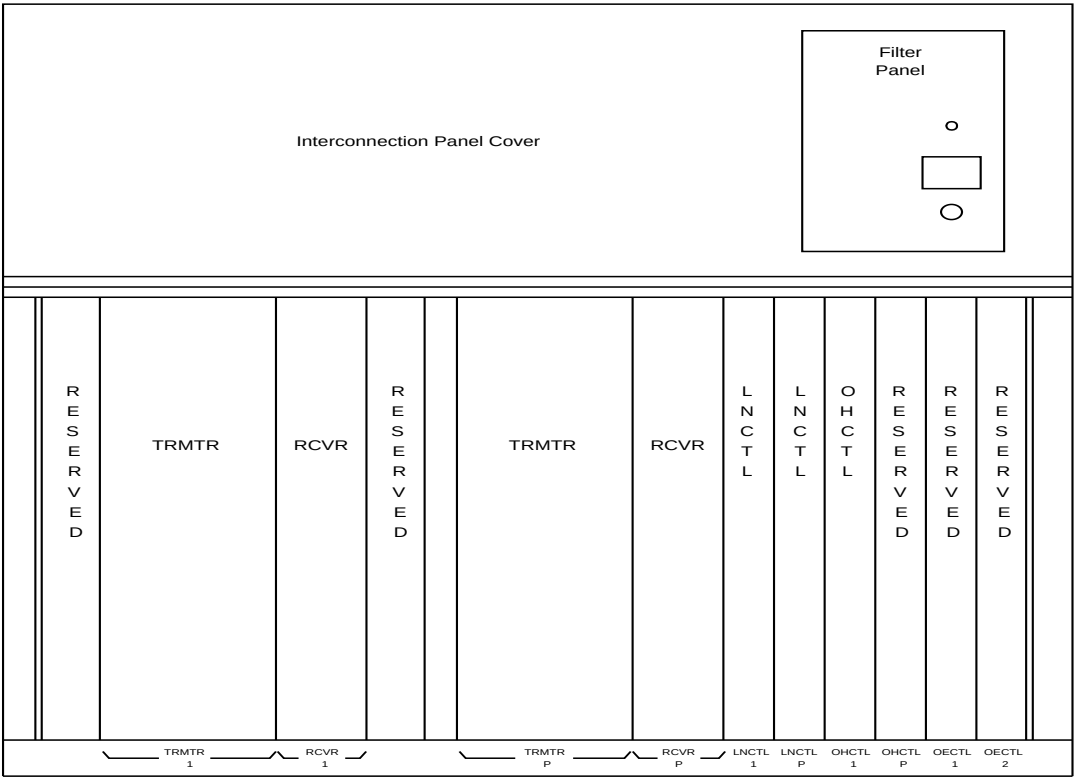
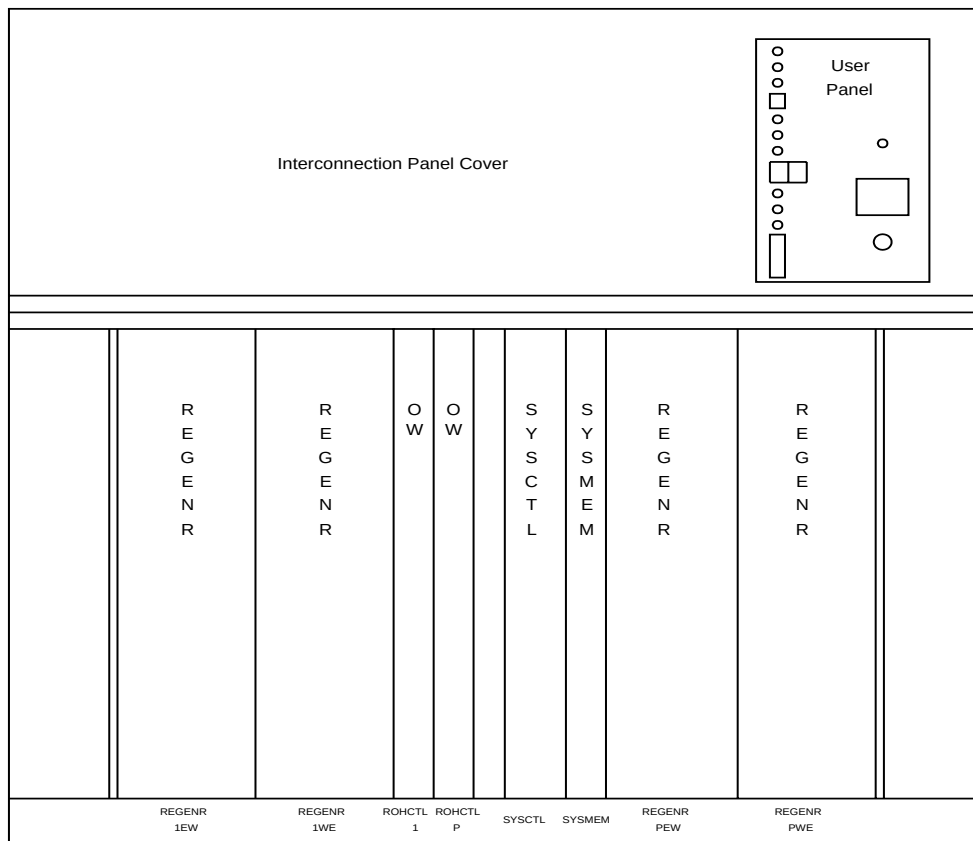


**Figure 2 – Enhanced High Speed Shelf (Cover Removed)**



tpa 814212/01

**Figure 3- FT-2000 Repeater Bay/Shelf (Cover Removed)**

## Add OC-48 Protection Line to Point-to-Point Network

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Required Test Equipment:**  
Craft Interface Terminal (CIT)  
Wrist Strap

 **WARNING:**  
*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

 **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

Obtain the circuit order instructions and note  
which OC-48 protection line will be added.

|    |                                                                                                |                |
|----|------------------------------------------------------------------------------------------------|----------------|
| 2. | If required, connect and condition the craft interface terminal (CIT).                         | <b>DLP-501</b> |
| 3. | Remove the high-speed shelf cover.                                                             | <b>DLP-511</b> |
| 4. | Install the TRMTR, RCVR, and LNCTL circuit packs and make the appropriate optical connections. | <b>DLP-514</b> |
| 5. | Replace the high-speed shelf cover.                                                            | <b>DLP-511</b> |

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                        | FOR DETAILS, GO TO            |
|------------------------------------------|--------------------------------------------------------------------------------------------------------|-------------------------------|
| 6.                                       | Repeat Steps 1 through 5 at the remote FT-2000 network element.                                        | —                             |
| 7.                                       | Wait until the <b>NE ACTY</b> LED goes off at both FT-2000 end terminals (approximately 5 minutes).    | —                             |
| 8.                                       | If required to add DS3 port(s) to this OC-48 line at this time, then refer to circuit order procedure. | <b>Circuit Order: NTP-002</b> |

```
Circuit Order: NTP-005---+---DLP-501-----Operation: NTP-002
|---DLP-511
|---DLP-514---+---DLP-512
| |---DLP-510-----DLP-512
|---Circuit Order: NTP-002
```

## Discontinue Restoration Protection Access from Point-to-Point Network

---

|                                          |                    |
|------------------------------------------|--------------------|
| DO ITEMS BELOW IN ORDER LISTED . . . . . | FOR DETAILS, GO TO |
|------------------------------------------|--------------------|

---

1. **Required Test Equipment:**  
Craft Interface Terminal (CIT)  
Wrist Strap

 **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

At the local FT-2000 end terminal, obtain the circuit order instructions to discontinue restoration protection access. Both FT-2000 end terminals must be provisioned.

- 
- |                                                                                                                   |                |
|-------------------------------------------------------------------------------------------------------------------|----------------|
| 2. If required, connect the Craft Interface Terminal (CIT) to the local FT-2000 network element and condition it. | <b>DLP-501</b> |
|-------------------------------------------------------------------------------------------------------------------|----------------|
- 

- |                                                                                                                                                           |   |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 3. At the local FT-2000 end terminal, under the CIT <b>FAULT</b> category, select <b>Switch-Line-Protection</b> and <b>Priority</b> set to <b>Reset</b> . | — |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------|---|
- 

- |                                                                                                                                                                                                                                              |  |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|
| 4. To verify the provisioning, retrieve a report by selecting <b>CONFIGURATION-Retrieve-Protection-Line</b> .<br><br>At the CIT, select <b>CONFIGURATION-Set-Protection-Line</b> and <b>Restoration Protection Access</b> set to <b>no</b> . |  |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|
-

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                      | FOR DETAILS, GO TO |
|------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 5.                                       | At the remote FT-2000 network element, repeat Step 4. This can be accomplished by establishing a remote login.                       | DLP-524            |
| 6.                                       | At both FT-2000 network elements, verify the <b>NE ACTY</b> LED is off. An assistant is required at the remote FT-2000 end terminal. | —                  |
| 7.                                       | Restoration protection access has been discontinued.                                                                                 | —                  |

Circuit Order: NTP-006-----DLP-501-----Operation: NTP-002  
|---DLP-524-----DLP-501-----Operation: NTP-002



## Establish Restoration Protection Access in Point-to-Point Network

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Required Test Equipment:**  
Craft Interface Terminal (CIT)  
Wrist Strap

 **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

At the local FT-2000 end terminal, obtain the  
circuit order instructions to establish restora-  
tion protection access. Both FT-2000 end ter-  
minals must be provisioned.

—

- 
2. At the local FT-2000 end terminal (if required),  
connect and condition the craft interface terminal  
(CIT).

**DLP-501**

- 
3. To verify the provisioning, obtain a report by selecting  
**CONFIGURATION-Retrieve-Protection-Line.**

At the local FT-2000 end terminal, at the CIT,  
select  
**CONFIGURATION-Set-Protection-Line**  
and **Restoration Protection Access**  
set to **Yes.**

—

- 
4. At the remote FT-2000 network element, repeat  
Steps 1 through 3. This can be accomplished by  
establishing a remote login.

**DLP-524**

---

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                      | FOR DETAILS, GO TO |
|------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 5.                                       | At the local FT-2000 end terminal and at the CIT, select FAULT-Switch-Line-Protection and Priority set to preemptible.               | —                  |
| 6.                                       | At both FT-2000 network elements, verify the <b>NE ACTY</b> LED is off. An assistant is required at the remote FT-2000 end terminal. | —                  |
| 7.                                       | Restoration protection access has been established.                                                                                  | —                  |

Circuit Order: NTP-007---+---DLP-501-----Operation: NTP-002  
|---DLP-524-----DLP-501-----Operation: NTP-002

## Add a Node to an In-Service 2-Fiber Ring Network

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This procedure is used to add a "new node" to an existing and **in-service** 2-fiber ring network. The "new node" is an add/drop-rings terminal with/without an FT-2000 Lightwave Booster Amplifier (FT-LBA). The "new node" **must not** be designated as the DS-NE. This procedure is written from the "new node" perspective although action is required at two other nodes. The "new node" will be connected between two operational nodes (called the West node and East node). If adding a "new node" to an out-of-service 2-fiber ring network, refer to Acceptance: NTP-006 or to Acceptance: NTP-011 (node operating with FT-LBA).

**Required Test Equipment and Personnel:**

- Craft Interface Terminal (CIT)
- Wrist Strap
- Appropriate Optical LBOs
- Technical assistance at two other nodes.

 **WARNING:**  
*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

 **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

 **NOTE:**  
*Major alarms will be active at different times during this procedure.*

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

⇒ **NOTE:**  
Only use this procedure to add a new FT-2000 add/drop-rings terminal to an **in-service** 2-fiber ring network. If the installation of the new terminal has not been completed (including circuit pack installation), notify the installation personnel or refer to 365-575-115 and/or to 365-565-500 (FT-LBA) to complete the installation.

! **CAUTION:**  
*If the ring already has NPPA provisioned or is carrying OC-12c services —then the new node must be equipped with STS-1 capable circuit packs. Otherwise, a service outage may result.*

! **CAUTION:**  
*Incorrect setting of the timing option could result in a service interruption in certain applications. Be certain that the timing option for the new bay is set to be consistent with the rest of the ring and to be consistent with the intended application. The bay defaults to “free running mode” when it is initially turned up. Provisionable timing options are discussed in Section 5 of this User/Service Manual.*

⇒ **NOTE:**  
It is assumed that installation of the new FT-2000 add/drop-rings terminal with/without FT-LBA has been completed (including circuit pack installation) for the engineered configuration. It is further assumed that the new add/drop-rings terminal with/without FT-LBA is powered and running and that the new fibers have been tested.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

⇒ **NOTE:**  
The TID (and NID for Release 5 and earlier) of the "new node" must be unique. The TIDs (and NIDs for Release 5 and earlier) cannot be the same as any others in the ring.

⇒ **NOTE:**  
Refer to Figure 1 and notice that the node to be connected to line 1W of the "new node" is referred to, in this procedure, as the West node and that the node to be connected to line 1E of the "new node" is referred to, in this procedure, as the East node.

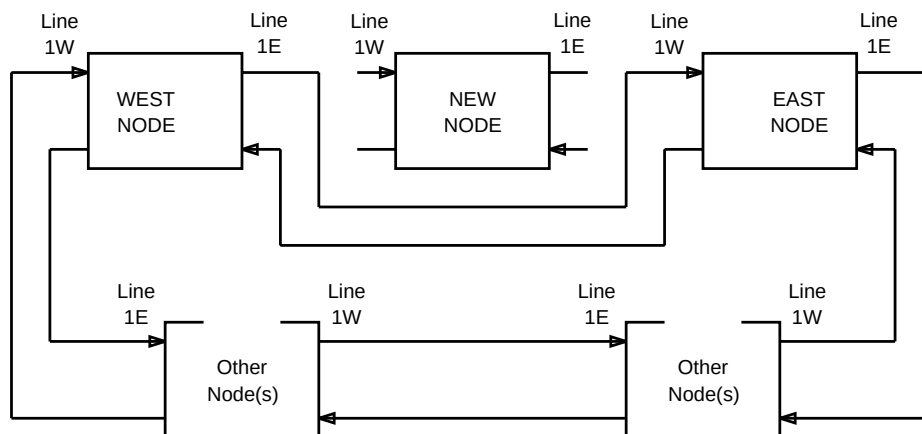
From office records, determine which nodes  
in the ring network will be called the West  
node and the East node for this procedure.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---



tpa 832675/01

**Figure 1 – Identify the East Node and the West Node**

- 
2. **CAUTION:**  
*Incorrectly provisioned source node TID and/or destination node TID can result in a service outage under certain multiple failure conditions.*

At the "new node", make any necessary tributary-to-tributary (through) cross-connections and verify that there are no unnecessary cross-connections.

**DLP-536**

- 
3. **CAUTION:**  
*A line forced switch will preempt extra traffic, if any, on the protection tributaries.*

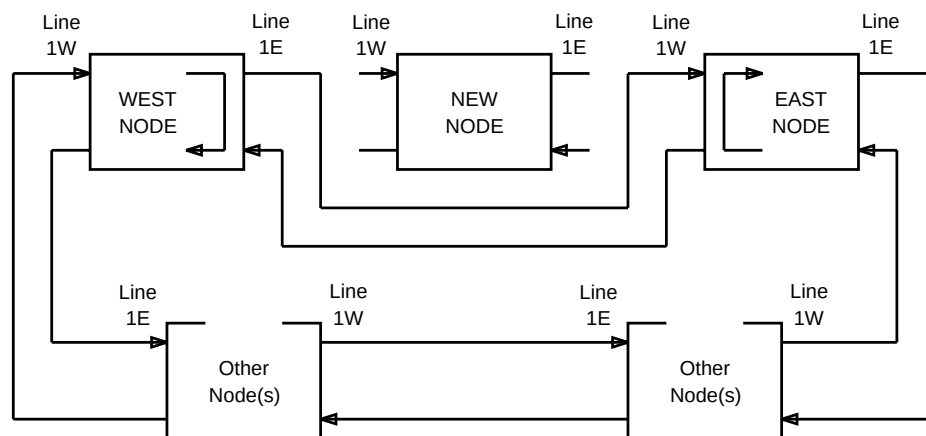
**NOTE:**  
For this step and Step 4, refer to Figure 2. The service tributaries to be connected (add/drop/through) to the "new node" will now be looped back in the opposite direction at the West node and at the East node.

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

Instruct the technician at the West node (or travel to that location) to enter the  
**FAULT-Switch-Line:1E:Forced** command at the CIT for line 1E. This makes a forced switch on line 1E and may cause traffic hits.



tpa 832676/01

**Figure 2 – Forced Switched Lines at the East Node and the West Node**

- 
4. **CAUTION:**  
*A line forced switch will preempt extra traffic, if any, on the protection tributaries.*

Instruct the technician at the East node (or travel to that location) to enter the  
**FAULT-Switch-Line:1W:Forced** command at the CIT for line 1W. This makes a forced switch on line 1W and may cause traffic hits.

- 
5. Refer to Figure 3 to help locate the OC-48 optical fibers that can now be removed from the West node (fibers connected to East node).

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

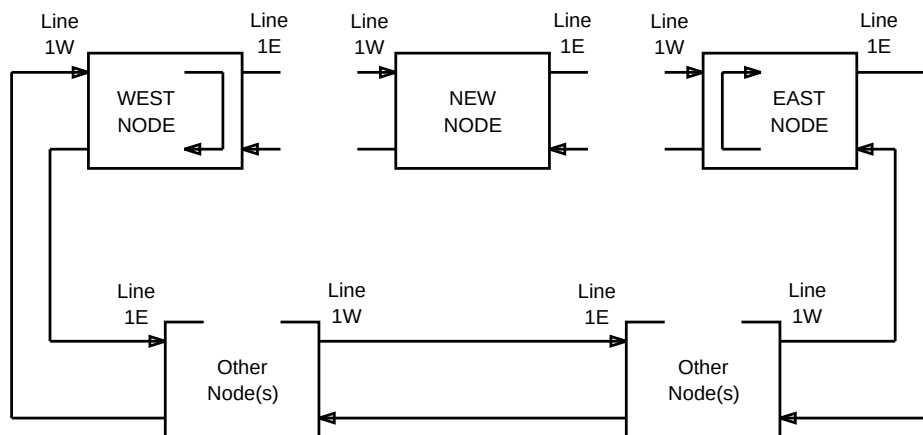
Instruct the technician at the West node to  
remove the OC-48 optical fibers (line 1E)  
connected to the East node. —

---

6. ➡ **NOTE:**  
Refer to Figure 3 to help locate the OC-48 optical fibers that can now  
be removed from the East node (fibers connected to West node).

Instruct the technician at the East node to  
remove the OC-48 optical fibers (line 1W)  
connected to the West node. —

---



tpa 832677/01

**Figure 3 – Optical Fibers Removed**

7. ➡ **NOTE:**  
Refer to Figure 4 and notice the new optical connections can now be  
made to the "new node".

➡ **NOTE:**  
A transient node ID conflict condition will be observed after the ring is  
reconnected.

---



---

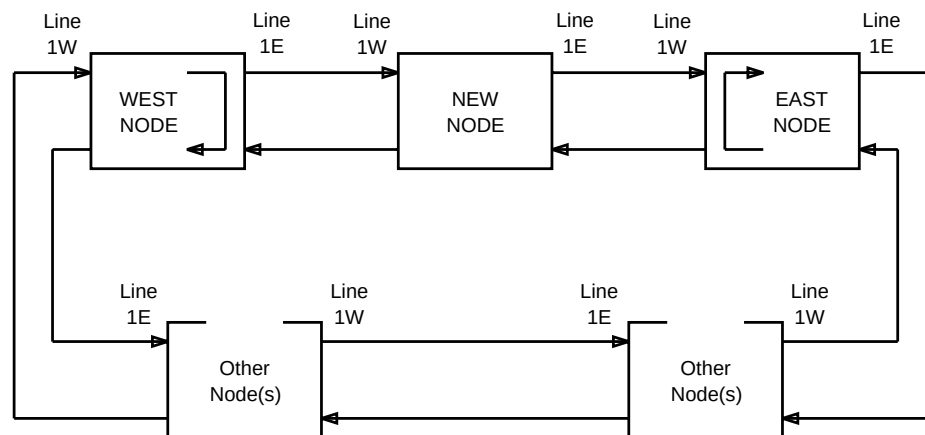
**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

⇒ **NOTE:**  
Lightguide buildouts (if required) are installed on the receive fibers of the "new node".

Select one of the receive fibers (change the lightguide buildout as necessary) and connect to the appropriate RCVR circuit pack. For example, select line 1E of the West node to be connected to line 1W of the "new node".

**DLP-512**



tpa 832678/01

**Figure 4 – Optical Fibers Connected to New Node**

- 
8. At the CIT, select **FAULT-Retrieve-Alarm** command to obtain an Active Alarms and Status Report and verify an incoming OC-48 LOS condition does not exist for either the 1E or 1W receiver (that is, whichever receive fiber that was connected to the RCVR circuit pack in Step 7) at the "new node".
-

| <b>DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO</b>                                                                                                                                                                                                                                                                                                                              |                |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 9. Select the other receive fiber (change the 0-db lightguide buildout as necessary) and connect to the appropriate RCVR circuit pack. For example, select line 1W of the East node to be connected to line 1E of the "new node".                                                                                                                                                               | <b>DLP-512</b> |
| 10. At the CIT, select <b>FAULT-Retrieve-Alarm</b> command to obtain an Active Alarms and Status Report and verify an incoming OC-48 LOS condition does not exist for either the 1E or 1W receiver at the "new node".                                                                                                                                                                           | —              |
| 11. Connect the transmit fibers (change the lightguide buildout as necessary) to the appropriate TRMTR circuit pack. For example, line 1W at the "new node" (to be connected to the West node) and line 1E (to be connected to the East node).                                                                                                                                                  | <b>DLP-512</b> |
| 12. Instruct the technician at the West node to: <ul style="list-style-type: none"><li>a. Change the lightguide buildout as necessary on the 1E receive fiber.</li><li>b. Connect it to 1E RCVR circuit pack.</li><li>c. Verify the RCVR (line 1E) does not show an incoming OC-48 LOS condition by obtaining an Active Alarms and Status Report at the CIT located at the West node.</li></ul> | —              |

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

13. Instruct the technician at the East node to: —
- a. Change the lightguide buildout as necessary on the 1W receive fiber.
  - b. Connect it to 1W RCVR circuit pack.
  - c. Verify the RCVR (line 1W) does not show an incoming OC-48 LOS condition by obtaining an Active Alarms and Status Report at the CIT located at the East node.

- 
14. At the CIT, select —  
**CONFIGURATION-Retrieve-Map-Ring**  
command to obtain a Ring Map Report and verify that the "new node" has been added to the ring as intended. (**Comm Stat** should indicate **good**.)



**CAUTION:**

*Incorrect setting of the timing option could result in a service interruption in certain applications. Be certain that the timing option for the new bay is set to be consistent with the rest of the ring and to be consistent with the intended application. The bay defaults to "free running mode" when it is initially turned up. Provisionable timing options are discussed in Section 5 of this User/Service Manual.*

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

15. Verify that the synchronization has been provisioned for this “new node” and that it is correct and consistent for this FT-2000 ring.
- 

16.  **NOTE:**  
Refer to Figure 5. The forced switch at the West node can now be reset.

Instruct the technician at the West node to enter **FAULT-Switch-Line:1E:Reset** to reset the forced switch on line 1E. An alternative method is to remotely log in to the West node and execute the command. The line switch may cause traffic hits.

Reference: **DLP-524**

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

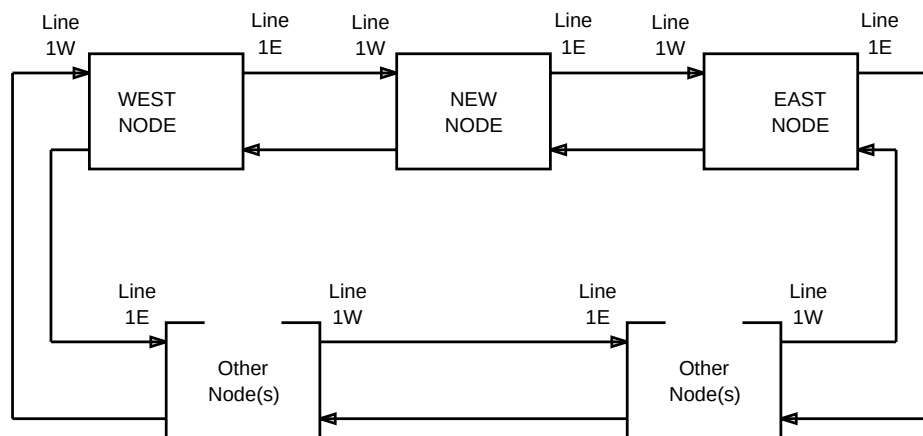
---

17. Refer to Figure 5. The forced switch at the East node can now be reset.

Instruct the technician at the East node to enter  
**FAULT-Switch-Line:1E:Reset** to reset the  
forced switch on line 1W. An alternative method is  
to remotely log in to the East node and execute the  
command. The line switch may cause traffic hits.

Reference: **DLP-524**

---



tpa 832679/01

**Figure 5 – Ring Network With New Node**

18. **NOTE:**  
The "new node" has NOW been added to the ring network.

Verify that the NE ACTY LED is off at the "new  
node" by (1) looking at the LED at the user  
panel or (2) using the CIT, log in to the new  
node and select **FAULT-Retrieve-Network  
Alarms** command to verify that there are NOT  
any active alarms.

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                     | FOR DETAILS, GO TO        |
|------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------|
| 19.                                      | Instruct the technician at the West node to verify that the <b>NE ACTY</b> LED is off at the user panel. An alternative method is to log in to the "West node" and execute the <b>FAULT-Retrieve-Alarms</b> command to verify that there are NOT any active alarms. | —                         |
| 20.                                      | Instruct the technician at the East node to verify that the <b>NE ACTY</b> LED is off at the user panel. An alternative method is to log in to the "East node" and execute the <b>FAULT-Retrieve-Alarms</b> command to verify that there are NOT any active alarms. | —                         |
| 21.                                      |  <b>NOTE:</b><br><i>Incorrectly provisioned source node TID and/or destination node TID can result in a service outage under certain multiple failure conditions.</i>              |                           |
|                                          | If required, enter any tributary-to-slot (drop) and/or slot-to-tributary (add) cross-connections at the "new node".                                                                                                                                                 | <b>DLP-536</b>            |
| 22.                                      |  <b>NOTE:</b><br>Incorrectly assigned node IDs can result in irregularities of protection switching.                                                                             |                           |
|                                          | Perform the Ring Reset procedure to allow correct node IDs to be reassigned.                                                                                                                                                                                        | <b>Operation: NTP-012</b> |
| 23.                                      | <b>STOP! YOU HAVE COMPLETED THIS PROCEDURE.</b>                                                                                                                                                                                                                     |                           |

Circuit Order: NTP-008---+---Acceptance: NTP-006  
|---Acceptance: NTP-011  
|---TAD-100  
|---DLP-536  
|---DLP-512  
|---DLP-524

## Delete a Node From an In-Service 2-Fiber Ring Network

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Overview:** This procedure is used to delete an FT-2000 OC-48 Add/Drop-Rings Terminal (Node To Be Deleted) from an existing and **in-service** 2-fiber ring network. The Node To Be Deleted may be an add/drop-rings terminal with or without an FT-2000 Lightwave Booster Amplifier (FT-LBA). The procedure is written from the Node To Be Deleted perspective although action is required at two other nodes. The Node To Be Deleted is located between two operational nodes (called the West Node and East Node).

### Required Test Equipment and Personnel:

- Craft Interface Terminal (CIT)
- Wrist Strap
- Appropriate Optical LBOs
- Technical assistance at two other nodes.



### **WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



### **CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

 **CAUTION:**  
*First, it must be verified that no traffic terminates at the Node To Be Deleted (Steps 1 through 4). Through traffic will be unaffected by this procedure.*

 **NOTE:**  
Major alarms will be active at different times during this procedure.

At the Node To Be Deleted, verify that the —  
**NE ACTY** and **FE ACTY** LEDs are off.

---

2. At the CIT, select —  
**CONFIGURATION-Retrieve-Crossconnect  
ion-STs1** to obtain an STS1 Cross-Connection  
Map Report).

---

3. Refer to the STS1 Cross-Connection Map —  
Report obtained in Step 2 and verify that there  
are no tributary-to-slot (drop) or slot-to-tributary  
(add) cross-connections. These  
cross-connections could be carrying traffic, so  
follow local operating procedures to reroute or  
terminate the traffic.

---

4.  **NOTE:**  
Refer to Figure 1 and note that the node connected to line 1W of the  
Node To Be Deleted is referred to, in this procedure, as the West Node  
and that the node connected to line 1E of the Node To Be Deleted is  
referred to, in this procedure, as the East Node.

At the CIT, select **CONFIGURATION-** —  
**Retrieve-Map-Ring** and execute to  
obtain a Ring Map Report for the Node To  
Be Deleted.

---



---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

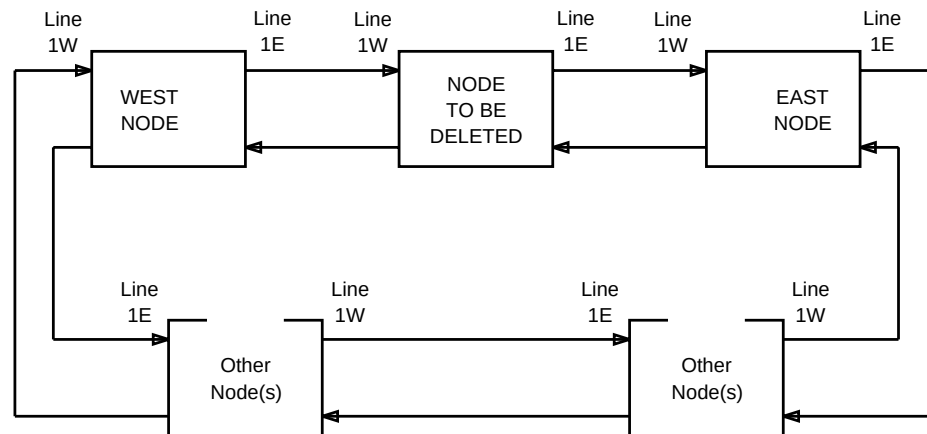


**Figure 1 – Identify the East Node and the West Node**

- 
5. Determine from the Ring Map Report which node represents the West Node and which node represents the East Node for this procedure. —  
The topmost entry in the report is the Node To Be Deleted. The East Node is second on the list and the West Node is last on the list. (The nodes are listed in a West-to-East sequence.)
- 
6. **CAUTION:**  
*The line forced switches will preempt extra traffic, if any, on the protection tributaries.*
- ⇒ **NOTE:**  
Refer to Figure 2. The service tributaries connected to the Node To Be Deleted will now be looped back in the opposite direction with OC-48 line forced switches at the West Node and at the East Node.
- ⇒ **NOTE:**  
The next two steps will cause the **ABN** and **NE ACTY** LEDs to light, but there will be no alarm even though the Node To Be Deleted is being disconnected from the rest of the ring.
-

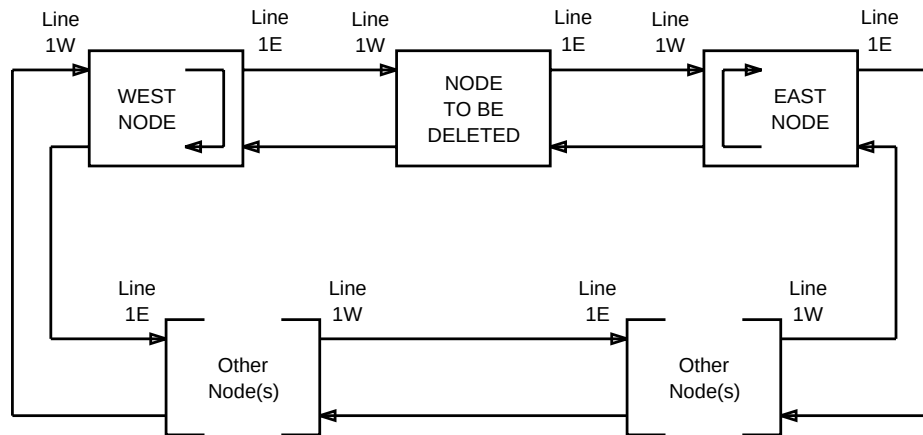
---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

Instruct the technician at the West Node (or travel to that location) to enter the **FAULT-Switch-Line:1E:Forced** command at the CIT. This makes a forced switch on line 1E and may cause traffic hits.

---



tpa 832681/01

**Figure 2 – Forced Switched Lines at the East Node and the West Node**

- 
7. Instruct the technician at the East Node (or travel to that location) to enter the **FAULT-Switch-Line:1W:Forced** command at the CIT. This makes a forced switch on line 1W and may cause traffic hits.
- 

8. **NOTE:**  
Refer to Figure 3 to help locate the OC-48 optical fibers that can now be removed at the Node To Be Deleted (fibers connected to the West Node).

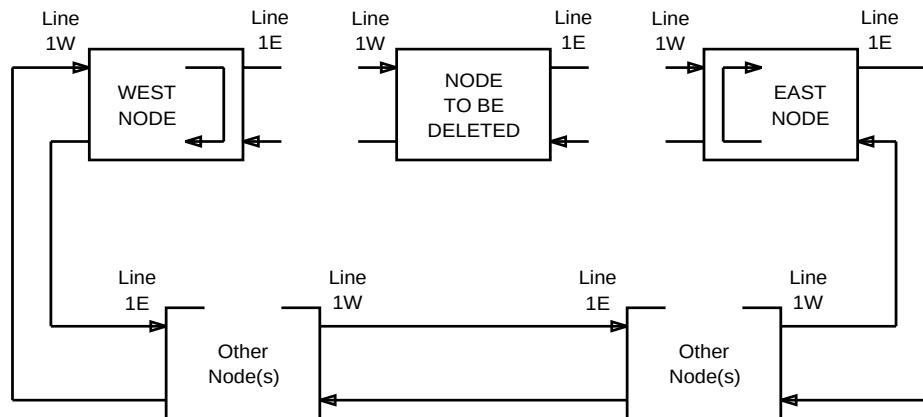
Remove the OC-48 optical fibers at the Node To Be Deleted (line 1W) that connected to the West Node.

---

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---



tpa 832682/01

**Figure 3 – Optical Fiber Removed**

- 
9.  **NOTE:**  
Refer to Figure 3 to help locate the OC-48 optical fibers that can now be removed at the Node To Be Deleted (fibers connected to the East Node).

Remove the OC-48 optical fibers at the Node To Be Deleted (line 1E) that connects to the East Node. The node is now completely isolated from the ring. However, the adjacent nodes (East and West Nodes) get a **sync mode auto switch** condition if they are "through timed" (Provisioned Mode).

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

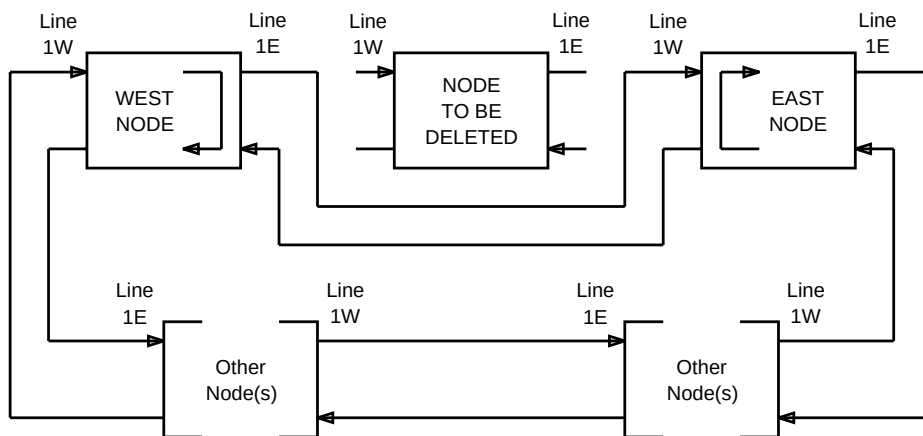
---

10. ➡ **NOTE:**  
Refer to Figure 4 and notice that new optical connections can now be made at the West Node and at the East Node (bypass the Node To Be Deleted).

- ➡ **NOTE:**  
Lightguide buildouts (if required) are installed only on the receive fibers at the East and West Nodes.

Instruct the technician at the West Node to change the lightguide buildout as necessary and reconfigure the fibers to connect line 1E of the West Node to line 1W of the East Node.

**DLP-512**



tpa 832683/01

**Figure 4 – Optical Fibers Connected to East Node and West Node**

11. Instruct the technician at the East Node to change the lightguide buildout as necessary and reconfigure the fibers to connect line 1W of the East Node to line 1E of the West Node.

**DLP-512**

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

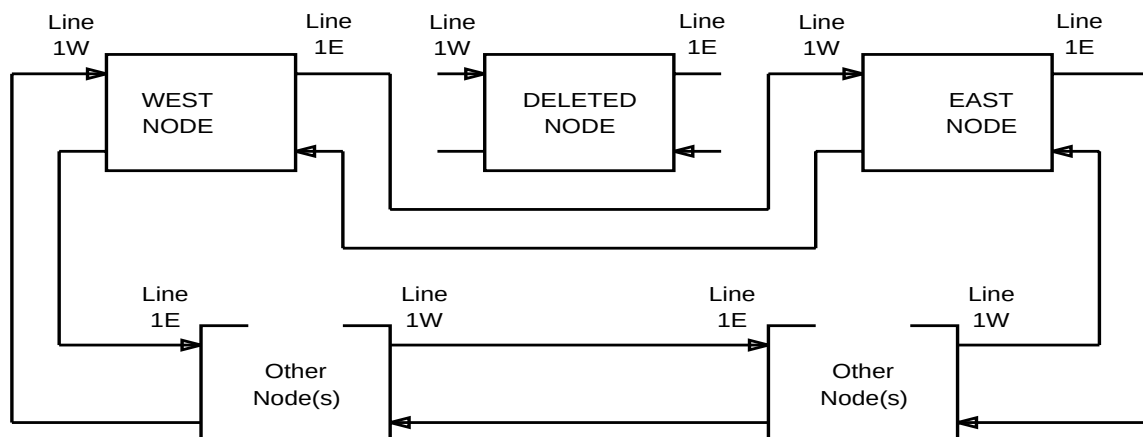
---

12. Instruct the technicians, at both the West Node and East Node, to wait one minute for the LEDs on each node's receiver circuit pack to stop flashing. If they do not stop flashing after about a minute, refer to TAP-110.

13. Whenever the receiver's LEDs stop blinking--the ring is then re-connected and a transient **node ID conflict** condition will exist. Depending on how many nodes are in the ring it may take from several minutes to a few hours for this condition to clear. —

14. ➡ **NOTE:**  
Refer to Figure 5. The forced switch at the West Node can now be reset.

Instruct the technician at the West Node to enter **FAULT-Switch-Line:1E:Reset** command at the CIT. The line switch may cause traffic hits. —



tpa 832684/01

**Figure 5 – Forced Switched Lines at the East Node and the West Node**

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

15.  **NOTE:**  
Refer to Figure 5. The forced switch at the East Node can now be reset.

Instruct the technician at the East Node to  
enter **FAULT-Switch-Line:1W:Reset**  
command at the CIT. The line switch may  
cause traffic hits.

—

16.  **NOTE:**  
Incorrectly assigned node IDs can result in irregularities of protection switching.

Perform the Ring Reset procedure to  
allow correct node IDs to be reassigned.

**Operation: NTP-012**

17.  **NOTE:**  
The Node To Be Deleted has been deleted from the ring network.

Instruct the technician at the West Node to  
verify that the NE ACTY LED is off at the  
user panel.

—

18. Instruct the technician at the East Node to verify  
that the NE ACTY LED is off at the user panel.

—

## Add OC-3/IS-3 Low Speed Circuit Pack(s)

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Overview:** This procedure is used to add a low-speed OC-3/IS-3 service by adding an OC-3/IS-3 circuit pack to a network element.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap  
Appropriate Optical Lightguide buildouts (LBO)

 **WARNING:**  
*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to the beam.*

 **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

 **NOTE:**  
Shelves are keyed to prevent circuit packs from being accidentally installed into the wrong slots.

 **NOTE:**  
This procedure may be performed after all installation wiring (fiber) and testing of the Low Speed Shelf - System Controller and/or the Low Speed Shelf - Complementary have been completed or on an in-service basis. Additional installation information is contained in 365-575-115, *FT-2000 OC-48 Lightwave System, Installation Manual*.

---

| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | FOR DETAILS, GO TO |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| Obtain the circuit order instructions to add OC-3 (LAA10 or LAA10B)/IS-3 (LAA5) circuit pack(s) and note the assigned address(es). The address requires a slot assignment.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | —                  |
| 2. If required, connect and condition the craft interface terminal (CIT).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | <b>DLP-501</b>     |
| 3.  <b>WARNING:</b><br><i>Traffic on "B" slot could be dropped. Note that restoring the original value of the OC-3/IS-3 Protection Mode parameter will change 1+1 slot pairs to 0x1 slot pairs. This will, in turn, cause all cross-connections associated with such a 1+1 slot pair to be associated with the "A" slot only. If such a transition takes place at the tail end and the "B" slot was the active slot at that time, an interruption of less than 60 msec will be observed. And, if such a transition takes place at the head end of a 1+1 protected optical line while the tail end is active on the "B" slot, the traffic will be dropped with no alarms being raised.</i> |                    |
|  <b>WARNING:</b><br><i>When a pair of slots ("A" and "B" slots) changes from 0x1 to 1+1 operation, all cross-connections added from those slots will initially select the "A" slot. This will cause a brief service hit if the original add cross-connection was from the "B" slot.</i>                                                                                                                                                                                                                                                                                                                                                                                                 |                    |
|  <b>NOTE:</b><br>OC-3/IS-3 circuit pack provisioning is done on a slot basis.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |                    |
|  <b>NOTE:</b><br>For Releases 5, 6, and 7.0 only, the alarm level parameter must be provisioned to NO if the desire is to suppress alarms when there is no optical input signal present. For releases 7.1 and later, there will be no alarm with the absence of an optical input signal unless the port has already been in-service and the input signal subsequently removed.                                                                                                                                                                                                                                                                                                          |                    |
| If required, provision the OC-3/IS-3 slot parameter(s).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | <b>DLP-517</b>     |



---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

**Supplemental Information:** If an **incoming** OC3/IS-3 LOS condition exists prior to setting the Alarm Level to NO, this condition **will not disappear** after the Alarm Level is set to NO. Only subsequent conditions will be subdued (not reported). The user may clear the condition by temporarily providing a good input signal until the condition clears or by reseating the OC3/IS-3 circuit pack. Only reseat the circuit pack if you are sure there is no live traffic on the outgoing fiber, otherwise service interruption will occur.

---

4. At the CIT, select **CONFIGURATION-Retrieve-Equipment**, then **LS INTFC** and execute to obtain an Equipage and Version Report. —

---

5. Determine from the report if the OC-3 circuit pack is already present in the slot associated with an OC-3 circuit pack to be added. A circuit pack is in the slot **if** OC3 is indicated in the **Circuit Pack** column of the report.

If an **OC3 circuit pack(s)** is present in the **slot(s)**, then continue with Step 11.  
If there is **no circuit pack(s)** present in the **slot(s)**, then continue with Step 6.  
If any **circuit pack(s)** other than **OC3** is present in the **slot(s)**, remove the circuit pack(s), perform a **CONFIGURATION-Update** command and continue with Step 6.

---

6. Remove the appropriate low-speed shelf cover. **DLP-511**

---

7. If necessary, remove the apparatus (circuit pack) blank(s) from the slot(s) requiring new circuit pack(s). **DLP-533**

---

8. Install OC-3/IS-3 circuit pack(s) into the specified slot(s) of the low-speed shelf [with the appropriate receive lightguide buildouts (LBOs)] and then connect the optical fibers per the circuit order. **DLP-514**

---

| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                               |                                                                                                                                                                                                                                                                                                                                                                                                                                                                           | FOR DETAILS, GO TO |
|------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 9.                                                                                                                     | Verify the <b>FAULT</b> LED on each OC-3/IS-3 circuit pack(s) goes off within 1 minute after the circuit pack is installed.                                                                                                                                                                                                                                                                                                                                               | —                  |
| 10.                                                                                                                    | Replace the shelf cover.                                                                                                                                                                                                                                                                                                                                                                                                                                                  | <b>DLP-511</b>     |
| 11.                                                                                                                    | Make the appropriate external cross-connections at the lightguide cross-connect panel or equivalent.                                                                                                                                                                                                                                                                                                                                                                      | —                  |
| 12.                                                                                                                    |  <b>CAUTION:</b><br><i>If the purpose of this slot/circuit pack is to provide service for an OC-3c signal—then care must be exercised to complete all three STS-1 cross-connections (in each NE of the signal's path) before applying the OC-3c signal. Otherwise, the <b>incoming (from OC3) STS LOP</b> alarm will be produced.</i>                                                    | <b>DLP-536</b>     |
| <p>At the CIT, enter all appropriate cross-connections for the newly equipped low-speed slot(s ) to the OC48 line.</p> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |                    |
| 13.                                                                                                                    |  <b>NOTE:</b><br>This step is optional. One of eight DCC channels can be assigned to a slot or pair of slots that will be equipped with OC-3/IS-3 circuit packs. The orientation of the OC-3/IS-3 circuit packs is provisioned "network" at one end and "user" at the other end. Both ends of the OC-3/IS-3 line cannot be the same and it makes no difference which end is "network." |                    |
|                                                                                                                        |  <b>NOTE:</b><br>If you are establishing DCC connectivity for the first time, you may have additional alarms. All alarms should clear after the DCC stabilizes; but if they do not, refer to the trouble clearing tab of this manual.                                                                                                                                                  |                    |
|                                                                                                                        | <p>At the CIT, select <b>CONFIGURATION - Enter -Assignment -DCC</b> command to assign a DCC channel to a slot or pair of slots.</p>                                                                                                                                                                                                                                                                                                                                       | <b>DLP-542</b>     |
| 14.                                                                                                                    | The OC-3/IS-3 circuit pack(s) is now ready for service.                                                                                                                                                                                                                                                                                                                                                                                                                   | —                  |

Circuit Order: NTP-010---+---DLP-536  
|---TAD-100  
|---DLP-501  
|---DLP-517  
|---DLP-511  
|---DLP-533  
|---DLP-514  
|---DLP-542



## Remove OC-3/IS-3 Low Speed Circuit Pack(s) From the Shelf

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

Obtain the circuit order instructions to delete an OC-3/IS-3 slot(s)/circuit pack(s) and note the assigned address. The address requires a slot assignment.

—

- 
2. If required, connect and condition the craft interface terminal (CIT).

**DLP-501**

- 
3. At the CIT, select the **CONFIGURATION-Retrieve-CKT-STs3** command (or the **CONFIGURATION-Retrieve-CKT-STs1** command) for the slot address indicated in Step 1 and obtain an STS-3 (or STS-1) Circuit Map Report.
- 

—

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

4.  **CAUTION:**  
*If the application of this slot/circuit pack was to provide service for an OC-3c signal—then care must be exercised to delete all three STS-1 cross-connections (in each NE of the signal's path) after **first** removing the OC-3c signal. Otherwise, the **incoming (from OC3) STS LOP** alarm will be produced.*

 **NOTE:**  
If you have any doubt as to whether or not the circuit pack should be removed, please confer with your supervisor.

While looking at the report, determine whether or not the slot for the circuit pack to be removed **appears** in the West or East Cross-Connect columns. If a cross-connection is shown, then traffic is possible at this slot.

Does the slot for the circuit pack that is to be removed still have cross-connections assigned?

If **YES**, then continue with Step 5.

If **NO**, then continue with Step 6.

- 
5. Delete the cross-connections for the slot of the circuit pack that is to be removed. **DLP-536**
- 

6. Does the circuit order indicate that the circuit pack is to be removed from the shelf slot?

If **YES**, then continue with Step 7.

If **NO**, then continue with Step 16.

- 
7. Remove the shelf cover from the low-speed shelf that contains the OC-3/IS-3 circuit pack(s). **DLP-511**
-

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                                                                        | FOR DETAILS, GO TO |
|------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 8.                                       | Remove the optical fibers from the OC-3/IS-3 circuit pack(s).                                                                                                                                                                                                                                                                                                          | —                  |
| 9.                                       | Remove the circuit pack(s) from the shelf.                                                                                                                                                                                                                                                                                                                             | <b>DLP-514</b>     |
| 10.                                      | Replace the shelf cover.                                                                                                                                                                                                                                                                                                                                               | <b>DLP-511</b>     |
| 11.                                      | At the CIT, select <b>CONFIGURATION - Update</b> and execute.                                                                                                                                                                                                                                                                                                          | —                  |
| 12.                                      |  <b>NOTE:</b><br>Steps 12 through 14 are optional. You may delete the DCC assignment or leave it in place without causing any interference or harm to other channels.<br><br>At the CIT, select <b>CONFIGURATION - Retrieve - Assignment - DCC</b> to obtain a DCC Assignment Report. | —                  |
| 13.                                      | From the report, does the DCC assignment exist for the slot of the circuit pack being removed?<br><br>If <b>YES</b> , then continue with Step 14.<br><br>If <b>NO</b> , then continue with Step 15.                                                                                                                                                                    | —                  |

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

|                                                                                                                                                                                                                   |                |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 14. At the CIT, select<br><b>CONFIGURATION-Delete-Assignment-DCC</b><br>to remove the DCC assignment; and repeat the<br><b>CONFIGURATION-Delete-Assignment-DCC</b><br>command for each DCC channel to be removed. | <b>DLP-542</b> |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|

---

|                                                                                                                                                                                                                                                                                                                                              |   |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 15. After the rings are separated, it is possible that a<br>DSNE not reachable and/or NE status<br>communication failure condition(s) may be<br>generated. To clear this, assign a DSNE<br>( <b>TAP-137</b> ) and AGNE ( <b>TAP-172</b> ) in the ring that<br>contains the alarms.<br><br>The OC-3/IS-3 circuit pack(s) has been<br>removed. | — |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|

---

|                                                         |  |
|---------------------------------------------------------|--|
| 16. <b>STOP! YOU HAVE COMPLETED THIS<br/>PROCEDURE.</b> |  |
|---------------------------------------------------------|--|

---

Circuit Order: NTP-011---+---TAD-100  
|---DLP-501  
|---DLP-511  
|---DLP-514  
|---DLP-536  
|---DLP-542



## Add OC-12 Low Speed Slot(s)

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This procedure is used to add a low-speed OC-12 slot to a network element and not to make cross-connections. Refer to **DLP-536** for cross-connect information for an FT-2000 OC-48 Add/Drop-Rings Terminal.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap  
Appropriate Optical Lightguide buildouts (LBO)



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**NOTE:**

Unlike for other circuit packs, the low speed shelf is not keyed to prevent the OC-12 from being accidentally installed into the wrong slots. Each OC-12 occupies four consecutive slots and must only be installed into slot locations (1A-1B-2A-2B, 3A-3B-4A-4B, 5A-5B-6A-6B, or 7A-7B-8A-8B).

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---



**NOTE:**

This procedure may be performed after all installation wiring (fiber) and testing of the Low Speed Shelf - System Controller have been completed or on an in-service basis. Additional installation information is contained in 365-575-115, *FT-2000 OC-48 Lightwave System, Installation Manual*.

Obtain the circuit order instructions to add OC-12 (T939A) circuit pack(s) and note the assigned address(es). The address requires a slot assignment.

—

- 
2. If required, connect and condition the craft interface terminal (CIT).

**DLP-501**

---

3.



**NOTE:**

For Releases 7.1 and later, the Alarm Level parameter must be provisioned to NO whenever an **unused OC-12 input is present**.

If required, provision the OC-12 slot parameter(s).

**DLP-517**

**Supplemental Information:** If an **incoming OC-12 LOS** condition exists prior to setting the Alarm Level to NO, this condition **will not disappear** after the Alarm Level is set to NO. Only subsequent conditions will be subdued (not reported). The user may clear the condition by temporarily providing a good input signal until the condition clears or by reseating the OC-12 circuit pack. Only reseat the circuit pack if you are sure there is no live traffic on the outgoing fiber, otherwise service interruption will occur.

- 
4. At the CIT, select **CONFIGURATION-Retrieve-Equipment**, then **LS INTFC** and execute to obtain an Equipage and Version Report.

—

| DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |                |
|-------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 5.                                                          | <p>Determine from the report if the OC-12 circuit pack is already present in the slot associated with an OC-12 circuit pack to be added. A circuit pack is in the slot <b>if</b> OC -12 is indicated in the Circuit Pack column of the report.</p> <p>If <b>circuit pack(s) is present in the slot(s)</b>, then continue with Step 11.</p> <p>If <b>circuit pack(s) is not present in the slot(s)</b>, then continue with Step 6.</p> <p>If any circuit pack(s) other than OC12 is present in the slot(s), remove the circuit pack(s), perform a <b>CONFIGURATION - Update</b> command, and continue with Step 6.</p> |                |
| 6.                                                          | Remove the appropriate low-speed shelf cover.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         | <b>DLP-511</b> |
| 7.                                                          | If necessary, remove the apparatus (circuit pack) blank(s) from the slot(s) requiring new circuit pack(s).                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | <b>DLP-533</b> |
| 8.                                                          | <p> <b>NOTE:</b><br/>OC-12 circuit pack requires Release 7.1 or later software.</p> <p>Install OC-12 circuit pack(s) into the specified slot(s) of the low-speed shelf [with the appropriate receive lightguide buildouts (LBOs)] and then connect the optical fibers per the circuit order.</p>                                                                                                                                                                                                                                   |                |
| 9.                                                          | Verify the <b>FAULT</b> LED on each OC-12 circuit pack(s) goes off within 1 minute after the circuit pack is installed.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               | —              |
| 10.                                                         | Replace the shelf cover.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | <b>DLP-511</b> |
| 11.                                                         | Make the appropriate external cross-connections at the lightguide cross-connect panel or equivalent.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | —              |

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

12.



**CAUTION:**

*If the purpose of this circuit pack is to provide service for an OC-12c signal then care must be exercised to complete all twelve STS-1 cross-connections (in each NE of the signals path) before applying the OC-12c signal. Otherwise, the **incoming (from OC12) STS LOP** alarm will be produced.*

At the CIT, select **CONFIGURATION-Update** and execute.



**NOTE:**

If OC-12c signals are to be provisioned, all high-speed transmitters and receivers in the ring *must* be STS1-compatible.

---

13.



**NOTE:**

This step is optional. One of eight DCC channels can be assigned to a slot (0x1 unprotected links only) that is equipped with an OC-12 circuit pack. The orientation of the OC-12 circuit packs is provisioned "network" at one end and "user" at the other end. Both ends of the OC-12 line cannot be the same and it makes no difference which end is "network."



**NOTE:**

If you are establishing DCC connectivity for the first time, you may have additional alarms, for example, multiple DSNEs defined (TAP-139) and/or remote alarms. All alarms should clear after the DCC stabilizes; but if they do not, refer to the trouble clearing tab of this manual.

At the CIT, select **CONFIGURATION-Enter - Assignment -DCC** command to assign a DCC channel to a single slot.

**DLP-542**

---

14.

The OC-12 circuit pack(s) is now ready for service.

---

Circuit Order: NTP-012---+---DLP-536

|---TAD-100  
|---DLP-501  
|---DLP-517  
|---DLP-511  
|---DLP-533  
|---DLP-514  
|---DLP-511  
|---DLP-542



## Remove OC-12 Circuit Pack(s) From the Shelf

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

Obtain the circuit order instructions to  
remove an OC-12 circuit pack(s) and note  
the assigned address. The address  
requires a slot assignment.

—

- 
2. If required, connect and condition the craft  
interface terminal (CIT).

**DLP-501**

- 
3. At the CIT, select the  
**CONFIGURATION-Retrieve-CKT-STs3**  
command (or the  
**CONFIGURATION-Retrieve-CKT-STs1**  
command) for the slot address indicated in Step  
1 and obtain an STS-3 (or STS-1) Circuit Map  
Report.

—

---

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

4.



**CAUTION:**

*If the application of this slot/circuit pack was to provide service for an OC-12c signal—then care must be exercised to delete all twelve STS-1 cross-connections (in each NE of the signal's path) after **first** removing the OC-12c signal. Otherwise, the **incoming (from OC12) STS LOP alarm** will be produced.*



**NOTE:**

If you have any doubt as to whether or not the circuit pack should be removed, please confer with your supervisor.

Looking at the report, verify that the slot for the circuit pack to be removed **does not appear** in the West or East Cross-Connect columns. If a cross-connection is shown, then traffic is possible at this slot.

Does the slot for the circuit pack to be removed still have cross-connections assigned?

If **YES**, then continue with Step 5.

If **No**, then continue with Step 6.

- 
5. Delete the cross-connections for the slot of the circuit pack that is to be removed.

**DLP-536**



**NOTE:**

When deleting Cross-Connections for an OC-12c signal, the first (lowest number) tributary must be deleted first, then the remaining STS-1 tributaries can be deleted in any order.

---



| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                                                                    | FOR DETAILS, GO TO |
|------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 6.                                       | Does the circuit order indicate that the circuit pack is to be removed from the shelf slot?<br><br>If <b>YES</b> , then continue with Step 7.<br><br>If <b>NO</b> , then continue with Step 16.                                                                                                                                                                    |                    |
| 7.                                       | Remove the shelf cover from the low-speed shelf that contains the OC-12 circuit pack(s).                                                                                                                                                                                                                                                                           | <b>DLP-511</b>     |
| 8.                                       | Remove the optical fibers from the OC-12 circuit pack(s).                                                                                                                                                                                                                                                                                                          | —                  |
| 9.                                       | Remove the circuit pack(s) from the shelf.                                                                                                                                                                                                                                                                                                                         | <b>DLP-514</b>     |
| 10.                                      | Replace the shelf cover.                                                                                                                                                                                                                                                                                                                                           | <b>DLP-511</b>     |
| 11.                                      | At the CIT, select <b>CONFIGURATION-Update</b> and execute.                                                                                                                                                                                                                                                                                                        | —                  |
| 12.                                      |  <b>NOTE:</b><br>Steps 12 through 14 are optional. You may delete the DCC assignment or leave it in place without causing any interference or harm to other channels.<br><br>At the CIT, select <b>CONFIGURATION-Retrieve-Assignment-DCC</b> to obtain a DCC Assignment Report. | —                  |
| 13.                                      | From the report, does the DCC assignment exist (the channel to be removed)?<br><br>If <b>YES</b> , then continue with Step 14.<br><br>If <b>NO</b> , then continue with Step 15.                                                                                                                                                                                   | —                  |

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

|     |                                                                                                                                                                                                   |                |
|-----|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 14. | At the CIT, select <b>CONFIGURATION-Delete-Assignment-DCC</b> to remove the DCC assignment; and repeat the <b>CONFIGURATION-Delete-Assignment-DCC</b> command for each DCC channel to be removed. | <b>DLP-542</b> |
|-----|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|

---

|     |                                                                                                                                                                                                                                                                                                                    |   |
|-----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 15. | After the rings are separated, it is possible that a DSNE not reachable and/or NE status communication failure condition(s) may be generated. To clear this, assign a DSNE ( <b>TAP-137</b> ) and AGNE ( <b>TAP-172</b> ) in the ring that contains the alarms.<br><br>The OC-12 circuit pack(s) has been removed. | — |
|-----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|

---

|     |                                                 |
|-----|-------------------------------------------------|
| 16. | <b>STOP! YOU HAVE COMPLETED THIS PROCEDURE.</b> |
|-----|-------------------------------------------------|

---

Circuit Order: NTP-011---+---TAD-100  
                  |---DLP-501  
                  |---DLP-511  
                  |---DLP-514  
                  |---DLP-511  
                  |---DLP-542

## Change an OC-3/IS-3 Link From 0x1 to 1+1 Protection Mode

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Overview:** This procedure enables you to change a Low Speed optical link from 0x1 protection mode to 1+1 protection mode without causing a service impairment.

**Required Test Equipment:**

- Craft Interface Terminal (CIT)
- Wrist Strap
- Appropriate Optical Lightguide buildouts (LBO)

 **WARNING:**  
*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

 **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

Obtain the Circuit Order work instructions and identify the slot that is specified for use as the "Protection Link".

 **NOTE:**  
The "Service slot" has an \_a (e.g., 1a, 2a, 3a, etc) slot address. While, the "Protection slot" has an \_b (e.g., 1b, 2b, 3b, etc) slot address.

 **NOTE:**  
This procedure assumes that the Service slot is already equipped with an OC-3/IS-3 circuit pack.

---

| DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO |                                                                                                                                                                                                                  |
|-------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 2.                                                          | Determine whether the identified Protection slot is already equipped. At the CIT, select <b>CONFIGURATION-Retrieve-Equipment</b> and execute to obtain a report depicting how the shelf is currently equipped. — |
| 3.                                                          | From the report, is the required protection slot equipped with an existing circuit pack?<br>If <b>NO</b> , then go to Step 12.<br>If <b>YES</b> , then continue with Step 4. —                                   |
| 4.                                                          | Is the slot already equipped with an optical circuit pack?<br>If <b>NO</b> , then go to Step 8.<br>If <b>YES</b> , then continue with Step 5. —                                                                  |
| 5.                                                          | Are fibers already terminated on the OC-3/IS-3 circuit pack?<br>If <b>NO</b> , then it is available, go to Step 12.<br>If <b>YES</b> , then continue with Step 6. —                                              |
| 6.                                                          | Determine whether the slot already has cross-connects defined. At the CIT, select the <b>CONFIGURATION-Retrieve-Crossconnect-all</b> command for this slot. —                                                    |

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

7. Look at the report, verify that the required slot does not appear in the cross-connect column. If a cross-connection is shown, then traffic is possible at the slot. —

Is there a cross-connection shown for the required protection slot?

If **YES**, then consult with your supervisor and make certain that the slot specified in the work order is correct, is to be used as the protection link, and that the slot is not currently supporting traffic. If the slot is supporting traffic, then – either a different slot must be selected or the traffic must be moved. **STOP! You cannot re-start this procedure until this issue is resolved.**

If **NO**, then go to Step 14.

- 
8.  **NOTE:** —  
At this point you have determined that the slot is already equipped with an electrical circuit pack (either a DS3 or STS-1E). You must still determine whether, or not, the circuit pack is supporting traffic.

Are coaxial cables already connected to the DS3/EC-1 circuit pack?

If **NO**, then it is available, go to Step 11.

If **YES**, then continue with Step 9.

- 
9. Determine whether the slot already has cross-connects defined. At the CIT, select the **CONFIGURATION-Retrieve-Crossconnect -All** command for this slot. —
-

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

- |     |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |                               |
|-----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------|
| 10. | Look at the report, verify that the required slot does not appear in the cross-connect column. If a cross-connection is shown, then traffic is possible at the slot.<br><br>Is there a cross-connection shown for the required slot?<br><br>If <b>YES</b> , then consult with your supervisor and make certain that the slot specified in the work order is correct, is to be used as the protection link, and that the slot is not currently supporting traffic. If the slot is supporting traffic, then – either a different slot must be selected or the traffic must be moved. <b>STOP! You cannot re-start this procedure until this issue is resolved.</b><br><br>If <b>NO</b> , then continue with Step 11. | —                             |
| 11. | Delete the DS3/STS-1E Low Speed ports and remove the circuit pack. Execute the <b>Configuration-Update</b> command.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                | <b>Circuit Order: NTP-003</b> |
| 12. | Delete all existing cross-connection(s) for the required slot (new protection link), if not already performed.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | <b>DLP-536</b>                |
| 13. | Add an OC-3/IS-3 Low Speed Slot.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | <b>Circuit Order: NTP-010</b> |
| 14. | Verify that the correct fibers are attached to the circuit pack.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | —                             |
| 15. | Change the protection slot's "Protection Mode" from 0x1 to 1+1.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    | <b>DLP-517</b>                |



**NOTE:**

As a result of changing the protection slot's "Protection Mode" the OC-3 Mode Conflict Alarm will be raised.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

- |     |                                                              |                |
|-----|--------------------------------------------------------------|----------------|
| 16. | Change the service slot's "Protection Mode" from 0x1 to 1+1. | <b>DLP-517</b> |
|-----|--------------------------------------------------------------|----------------|

⇒ **NOTE:**  
As a result of changing the service slot's "Protection Mode" the OC-3 Mode Conflict Alarm will be cleared.

- 
- |     |                                                                 |   |
|-----|-----------------------------------------------------------------|---|
| 17. | At the CIT, select the <b>CONFIGURATION-Update-All</b> command. | — |
|-----|-----------------------------------------------------------------|---|

- 
- |     |                                                                  |                |
|-----|------------------------------------------------------------------|----------------|
| 18. | Enter the appropriate cross-connection(s) for the required slot. | <b>DLP-536</b> |
|-----|------------------------------------------------------------------|----------------|

- 
19. Verify that the operating mode is 1+1 for the slot pair. At the CIT select, **CONFIGURATION-Retrieve-OC3:All** and execute to obtain a report that lists the actual "Operating Mode" of the OC-3/IS-3 circuit packs.

⇒ **NOTE:**  
It is important to understand that the slot pair could be provisioned for 1+1 protection mode, but yet the operating mode still be 0x1.

⇒ **NOTE:**  
Look at the report and for the slot pair identified in the work order, verify that the "Operating Mode" is actually 1+1.

⇒ **NOTE:**  
The \_b address, of the slot pair, is the Protection slot while the \_a address is the Service slot.

20. For the subject slot pair (\_a and \_b), is the "Operating Mode" indicating 1+1?

If **YES**, then **STOP**.

If **NO**, then contact next level of support.

---

Circuit Order: NTP-014----+----Circuit Order : NTP-003

Circuit Order: NTP-010

Circuit Order: NTP-013---+---DLP-517

|----DLP-536





## Change an OC-12 Link From 0x1 to 1+1 Protection Mode

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Overview:** This procedure enables you to change an OC-12 Low Speed optical link from 0x1 protection mode to 1+1 protection mode without causing a service impairment.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap

 **WARNING:**  
*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

 **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

Obtain the Circuit Order work instructions and \_\_\_\_\_  
identify the quad that is specified for use as the  
"Protection Link".

 **NOTE:**  
The Service and Protection quad pairs are (1b and 3b) or (5b and 7b).  
The "Service quad" has either 1b or 5b as its quad address. The  
"Protection quad" has either 3b or 7b as its quad address.

 **NOTE:**  
This procedure assumes that the Service quad is already equipped with  
an OC-12 circuit pack.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

- |    |                                                                                                                                                                                                                |   |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 2. | Determine whether the identified “Protection quad” is already equipped. At the CIT, select <b>CONFIGURATION-Retrieve-Equipment</b> and execute to obtain a report showing how the shelf is currently equipped. | — |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|

- |    |                                                                                 |   |
|----|---------------------------------------------------------------------------------|---|
| 3. | From the report, is the protection quad equipped with an existing circuit pack? | — |
|----|---------------------------------------------------------------------------------|---|

 **NOTE:**  
If the report indicates a circuit pack is present but no circuit pack is installed, perform **CONFIGURATION-Update** command.

If **NO**, then go to Step 13.  
If **YES**, then continue with Step 4.

- |    |                                                                                 |   |
|----|---------------------------------------------------------------------------------|---|
| 4. | Are the protection quad’s slot/slots already equipped with an optical pack?     | — |
|    | If <b>NO</b> , then go to Step 9.<br>If <b>YES</b> , then continue with Step 5. |   |

- |    |                                                                                  |   |
|----|----------------------------------------------------------------------------------|---|
| 5. | Is the quad already equipped with an OC-12 circuit pack?                         | — |
|    | If <b>NO</b> , then go to Step 15.<br>If <b>YES</b> , then continue with Step 6. |   |

- |    |                                                                                                   |   |
|----|---------------------------------------------------------------------------------------------------|---|
| 6. | Are fibers already terminated on the OC-12 circuit pack?                                          | — |
|    | If <b>NO</b> , then it is available, go to Step 13.<br>If <b>YES</b> , then continue with Step 7. |   |

- |    |                                                                                                                                                                 |   |
|----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 7. | Determine whether the quad already has cross-connects defined. At the CIT, select the <b>CONFIGURATION-Retrieve-Crossconnect ion-All</b> command for this slot. | — |
|----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
-

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

8. Look at the report, verify that the protection quad slots to be used do not appear in the cross-connect column. If a cross-connection is shown, then traffic is possible at the quad. —

Is there a cross-connection shown for any of the four required protection slots?

If **YES**, then consult with your supervisor and make certain that the quad specified in the work order is correct, is to be used as the protection link, and that the quad is not currently supporting traffic. If the quad is supporting traffic, then – either a different quad must be selected or the traffic must be moved. **STOP! You cannot re-start this procedure until this issue is resolved.**

If **NO**, then go to Step 20.

- 
9.  **NOTE:** —  
At this point you have determined that the protection quad slots to be used are already equipped with an electrical circuit pack (either a DS3 or STS-1E). You must still determine whether, or not, the circuit pack is supporting traffic.

Are coaxial cables already connected to the DS3/STS-1E circuit pack via the DSX cross-connect panel associated with this shelf and slot?

If **NO**, then it is available, go to Step 12.

If **YES**, then continue with Step 10.

- 
10. Determine whether the quad already has cross-connects defined. At the CIT, select the **CONFIGURATION-Retrieve-Crossconnect ion-All** command for this slot. —
-

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

- |     |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     |                               |
|-----|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------|
| 11. | Look at the report, verify that the protection quad slots do not appear in the cross-connect column. If a cross-connection is shown, then traffic is possible at the quad.                                                                                                                                                                                                                                                                                                                                                                                          | —                             |
|     | <p>Is there a cross-connection shown for any of the four required protection slots?</p> <p>If <b>YES</b>, then consult with your supervisor and make certain that the quad specified in the work order is correct, is to be used as the protection link, and that the quad is not currently supporting traffic. If the quad is supporting traffic, then – either a different quad must be selected or the traffic must be moved. <b>STOP! You cannot re-start this procedure until this issue is resolved.</b></p> <p>If <b>NO</b>, then continue with Step 12.</p> |                               |
| 12. | Delete the DS3/STS-1E Low Speed Ports and remove the circuit pack(s). Execute the <b>Configuration-Update</b> command.                                                                                                                                                                                                                                                                                                                                                                                                                                              | <b>Circuit Order: NTP-003</b> |
| 13. | Delete all existing cross-connection(s) for the new protection quad slots, if not already performed.                                                                                                                                                                                                                                                                                                                                                                                                                                                                | <b>DLP-536</b>                |
| 14. | Continue with Step 19.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              |                               |
| 15. | Are fibers already terminated on the OC-3/IS-3 circuit pack?                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        | —                             |
|     | <p>If <b>NO</b>, then it is available, go to Step 18.</p> <p>If <b>YES</b>, then continue with Step 16.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                         |                               |
| 16. | Determine whether the quad already has cross-connects defined. At the CIT, select the <b>CONFIGURATION-Retrieve-Crossconnect ion-All</b> command for this slot.                                                                                                                                                                                                                                                                                                                                                                                                     | —                             |
-

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

17. Look at the report, verify that the protection quad slots do not appear in the cross-connect column. If a cross-connection is shown, then traffic is possible at the quad. —

Is there a cross-connection shown for any of the four required protection slots?

If **YES**, then consult with your supervisor and make certain that the quad specified in the work order is correct, is to be used as the protection link, and that the quad is not currently supporting traffic. If the quad is supporting traffic, then – either a different quad must be selected or the traffic must be moved. **STOP! You cannot re-start this procedure until this issue is resolved.**

If **NO**, then continue with Step 18.

- 
- |     |                                                                                                             |                               |
|-----|-------------------------------------------------------------------------------------------------------------|-------------------------------|
| 18. | Remove OC-3/IS-3 Low Speed circuit pack(s) from the shelf. Execute the <b>Configuration-Update</b> command. | <b>Circuit Order: NTP-011</b> |
|-----|-------------------------------------------------------------------------------------------------------------|-------------------------------|
- 

- |     |                                                    |                               |
|-----|----------------------------------------------------|-------------------------------|
| 19. | Add an OC-12 Low Speed Slot (actually four slots). | <b>Circuit Order: NTP-012</b> |
|-----|----------------------------------------------------|-------------------------------|
- 

- |     |                                                                               |                |
|-----|-------------------------------------------------------------------------------|----------------|
| 20. | Verify that correct optical fibers and LBOs are attached to the circuit pack. | <b>DLP-512</b> |
|-----|-------------------------------------------------------------------------------|----------------|
- 

- |     |                                                                                                                   |                |
|-----|-------------------------------------------------------------------------------------------------------------------|----------------|
| 21. | Change the protection quad's "Protection Mode" from 0x1 to 1+1 in both the "from OC12" and "from OC48" direction. | <b>DLP-517</b> |
|-----|-------------------------------------------------------------------------------------------------------------------|----------------|
- 

 **NOTE:**  
Changing the protection quad's "Protection Mode" raises the OC-12 Mode Conflict Alarm.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

22. Change the service quad's "Protection Mode" from 0x1 to 1+1 in both the "from OC12" and "from OC48" direction. **DLP-517**



**NOTE:**

Changing the service quad's "Protection Mode" clears the OC12 Mode Conflict Alarm.

- 
23. Enter the appropriate cross-connection(s) for the protection quad. **DLP-536**
- 

24. Use **CONFIGURATION-Retrieve-OC12** command to verify that the operating mode is 1+1 for the quad pair.

If **Operating Mode is 1+1**, then STOP! You Have Completed This Procedure.

If **Operating Mode is NOT 1+1**, then contact your next level of Technical Support.

---

Circuit Order: NTP-015-----Circuit Order : NTP-003

Circuit Order: NTP-010

Circuit Order: NTP-013----DLP-517

|----DLP-536

## Change an OC-3/IS-3 Link From 1+1 to 0x1 Protection Mode

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** The steps in this procedure enable you to change an OC-3 Low Speed optical link from 1+1 protection mode to 0x1 protection mode without causing a service impairment. Tasks are performed on two separate NEs (for example; Node A, an FT-2000, and Node B). Node B can be another FT-2000, DDM-2000, or other vendor equipment. The process is as follows:
  1. At Node A, switch traffic from Protection pack to Service pack.
  2. At Node B, switch traffic from Protection pack to Service pack.
  3. At Node B, change Protection mode from 1+1 to 0x1.
  4. At Node A, change Protection mode from 1+1 to 0x1.



**NOTE:**

The "Service slot" has an **\_a** (e.g., 1a, 2a, 3a, etc) slot address. The "Protection slot" has an **\_b** (e.g., 1b, 2b, 3b, etc) slot address.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

Obtain the Circuit Order work instructions and identify the slot that is currently being used for the "Service Link" and its companion slot that is currently being used for the "Protection Link".

---

|    |                                                                        |                |
|----|------------------------------------------------------------------------|----------------|
| 2. | If required, connect and condition the craft interface terminal (CIT). | <b>DLP-501</b> |
|----|------------------------------------------------------------------------|----------------|

---

|    |                                                                                                                                                                                                                                                                                                                                |   |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 3. | At the CIT, use <b>FAULT-Retrieve-State</b> command to obtain a report that lists the current actual protection Switch State.<br><br>If <b>Service slot _a</b> Switch State is active ( <b>rx:act</b> ), go to Step 7.<br><br>If <b>Service slot _a</b> Switch State is standby ( <b>rx:stby</b> ), then continue with Step 4. | — |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|

---

|    |                                                                                                                                                                                                                                                                                                                                                                                                                      |  |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|
| 4. | At the CIT, perform the <b>FAULT-Switch-OC3:slot:priority</b> command (whereas for the <i>slot</i> enter the <b>Protection slot _b</b> that you wish to lock out and for the <i>priority</i> enter or choose the lockout priority).<br><br>You perform this command to (1) switch service from the Protection pack to the Service pack and (2) prevent further selection of the Protection pack by this near-end NE. |  |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|

---

|  |                                                                                                    |  |
|--|----------------------------------------------------------------------------------------------------|--|
|  | <b>NOTE:</b><br>Service will be switched unless there is an existing <i>switch</i> request active. |  |
|--|----------------------------------------------------------------------------------------------------|--|

---



---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

5. Did the command in Step 4 execute?

If **YES**, then go to Step 7.

If **NO**, then continue with Step 6.

- 
6. Use the **FAULT-Retrieve-Alarm** command to check for system alarms. Clear any alarms present, then repeat this procedure from Step 4. You must determine why the **SWITCH** command in Step 4 did not execute before continuing this procedure.
- 

7.  **NOTE:**  
Steps 7 through 13 assume that the far-end node B NE is an FT-2000. If this is not the case, then the equivalent sequence of commands, which are appropriate for the particular product, must be performed.

At CIT, remotely Login to far-end node B. Use **FAULT-Retrieve-State** command to obtain a report that lists the actual protection Switch State of the OC-3 circuit packs.

If **Service slot \_a** Switch State is active (**rx:act**), go to Step 11.

If **Service slot \_a** Switch State is standby (**rx:stby**), then continue with Step 8.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

8. At the CIT, perform the  
**FAULT-Switch-OC3:slot:priority**  
command (whereas for the *slot* enter the  
**Protection slot \_b** that you wish to lock out and  
for the *priority* enter or choose the lockout  
priority).

You perform this command to (1) switch service  
from the Protection pack to the Service pack and  
(2) prevent further selection of the Protection  
pack by this far-end NE.

⇒ **NOTE:**  
Service will be switched unless there is an  
existing *inhibit* request active.

- 
9. Did the command in Step 8 execute?  
  
If **YES**, then go to Step 11.  
If **NO**, then continue with Step 10.

- 
10. Use the **FAULT-Retrieve-Alarm** command  
to check for system alarms. Clear any alarms  
present, then repeat this procedure from Step 8.  
You must determine why the **SWITCH** command  
in Step 8 did not execute before continuing this  
procedure.

- 
11. While still logged into far-end node B, use  
**CONFIGURATION-Set-OC3** command to  
change the "Protection Mode" from 1+1 to 0x1  
for the protection pack/slot **\_b** address.

**DLP-517**

⇒ **NOTE:**  
As a result of changing the Protection  
Mode from 1+1 to 0x1, the  
cross-connections associated with the  
protection pack/slot are deleted.

---

| DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO                                                                                                                                                                           |                                                                                                                                                                               |                |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 12.                                                                                                                                                                                                                                   | While still logged into far-end node B, use <b>CONFIGURATION-Set-OC3</b> command to change the "Protection Mode" from 1+1 to 0x1 for the service pack/slot <b>_a</b> address. | <b>DLP-517</b> |
| 13.                                                                                                                                                                                                                                   | At the CIT, execute the <b>CONFIGURATION-Update-All</b> command.                                                                                                              | —              |
| 14.                                                                                                                                                                                                                                   | At the CIT, login to near-end node A. Use <b>CONFIGURATION-Set-OC3</b> command to change the "Protection Mode" from 1+1 to 0x1 for the protection slot <b>_b</b> address.     | <b>DLP-517</b> |
|  <b>NOTE:</b><br>As a result of changing the Protection Mode from 1+1 to 0x1, the cross-connections associated with the protection slot are deleted. |                                                                                                                                                                               |                |
| 15.                                                                                                                                                                                                                                   | While still logged into near-end node A, use <b>CONFIGURATION-Set-OC3</b> command to change the "Protection Mode" from 1+1 to 0x1 for the service slot <b>_b</b> address.     | <b>DLP-517</b> |
| 16.                                                                                                                                                                                                                                   | At the CIT, execute the <b>CONFIGURATION-Update-All</b> command.                                                                                                              | —              |
| 17.                                                                                                                                                                                                                                   | <b>STOP! You Have Completed This Procedure.</b>                                                                                                                               |                |



## Change an OC-12 Link From 1+1 to 0x1 Protection Mode

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Overview:** The steps in this procedure enable you to change an OC-12 Low Speed optical link from 1+1 protection mode to 0x1 protection mode without causing a service impairment. Tasks are performed on two separate NEs (for example; Node A, an FT-2000, and Node B). Node B can be another FT-2000, DDM-2000, or other vendor equipment. The process is as follows:
  1. At Node A, switch traffic from Protection quad to Service quad.
  2. At Node B, switch traffic from Protection quad to Service quad.
  3. At Node B, change Protection mode from 1+1 to 0x1.
  4. At Node A, change Protection mode from 1+1 to 0x1.

 **NOTE:**  
The FT-2000 OC-12 “Service and Protection” quad pairs are (**1b** and **3b**) or (**5b** and **7b**). The “Service quad” has a **1b** or **5b** quad address. The “Protection quad” has a **3b** or **7b** quad address.

### Required Test Equipment:

- Craft Interface Terminal (CIT)
- Wrist Strap

 **WARNING:**  
*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

From the instructions (work order), identify the quad pair addresses of the "Service and Protection Links" (**1b** and **3b**), or (**5b** and **7b**).

- 
- |    |                                                                        |                |
|----|------------------------------------------------------------------------|----------------|
| 2. | If required, connect and condition the craft interface terminal (CIT). | <b>DLP-501</b> |
|----|------------------------------------------------------------------------|----------------|
- 
- |    |                                                                                                                                                                                                                                                                                                                                                        |   |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 3. | At near-end Node A, use <b>FAULT-Retrieve-State</b> command to obtain a report that lists the current actual protection Switch State.<br><br>If <b>Service Quad (1b or 5b)</b> Switch State is active ( <b>rx:act</b> ), go to Step 7.<br><br>If <b>Service Quad (1b or 5b)</b> Switch State is standby ( <b>rx:stby</b> ), then continue with Step 4. | — |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
- 
- |    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           |  |
|----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|
| 4. | At the CIT, perform the <b>FAULT-Switch-OC12:slot:priority</b> command [whereas for the <i>slot</i> enter the <b>Protection slot (3b or 7b)</b> that you wish to lock out and for the <i>priority</i> enter or choose the lockout priority].<br><br>You perform this command to (1) switch service from the Protection quad ( <b>3b</b> or <b>7b</b> ) to the Service quad ( <b>1b</b> or <b>5b</b> ) for this near-end NE, and (2) prevent further selection of the Protection quad by this near-end NE. |  |
|----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--|
- 
-  **NOTE:**  
Service will be switched unless there is an existing *inhibit* request active.
-

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

5. Did the command in Step 4 execute?

If **YES**, then go to Step 7.

If **NO**, then continue with Step 6.

- 
6. Use the **FAULT-Retrieve-Alarm** command to check for system alarms. Clear any alarms present, then repeat this procedure from Step 4. You must determine why the **SWITCH** command in Step 4 did not execute before continuing this procedure.
- 

7.  **NOTE:**  
Steps 7 through 13 assume that the far-end node B NE is an FT-2000. If this is not the case, then the equivalent sequence of commands, which are appropriate for the particular product, must be performed.

At CIT, remotely Login to far-end node B. Use **FAULT-Retrieve-State** command to obtain a report that lists the actual protection Switch State of the OC-12 circuit packs.

If **Service Quad (1b or 5b)** Switch State is active (**rx:act**), go to Step 11.

If **Service Quad (1b or 5b)** Switch State is standby (**rx:stby**), then continue with Step 8.

---

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

8. At the CIT, perform the **FAULT-Switch-OC12:slot:priority** command [whereas for the *slot* enter the **Protection slot (3b or 7b)** that you wish to lock out and for the *priority* enter or choose the lockout priority].

You perform this command to (1) switch service from the Protection quad (**3b** or **7b**) to the Service quad (**1b** or **5b**) for this near-end NE, and (2) prevent further selection of the Protection quad by this far-end NE.

⇒ **NOTE:**  
Service will be switched unless there is an existing *inhibit* request active.

- 
9. Did the command in Step 8 execute?

If **YES**, then go to Step 11.

If **NO**, then continue with Step 10.

- 
10. Use the **FAULT-Retrieve-Alarm** command to check for system alarms. Clear any alarms present, then repeat this procedure from Step 8. You must determine why the **SWITCH** command in Step 8 did not execute before continuing this procedure.

- 
11. While still logged into far-end node B, use **CONFIGURATION-Set-OC12** command to change the "Protection Mode" from 1+1 to 0x1 for the protection quad (**3b** or **7b**)/slot address.

**DLP-517**

⇒ **NOTE:**  
As a result of changing the Protection Mode from 1+1 to 0x1, the cross-connections associated with the protection quad/slot are deleted.

---



---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

|     |                                                                                                                                                                                                          |                |
|-----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 12. | While still logged into far-end node B, use<br><b>CONFIGURATION-Set-OC12</b> command to<br>change the "Protection Mode" from 1+1 to 0x1<br>for the service quad ( <b>1b</b> or <b>3b</b> )/slot address. | <b>DLP-517</b> |
|-----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|

---

|     |                                                                     |   |
|-----|---------------------------------------------------------------------|---|
| 13. | At the CIT, execute the<br><b>CONFIGURATION-Update-All</b> command. | — |
|-----|---------------------------------------------------------------------|---|

---

|     |                                                                                                                                                                                                      |                |
|-----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 14. | At the CIT, login to near-end node A. Use<br><b>CONFIGURATION-Set-OC12</b> command to<br>change the "Protection Mode" from 1+1 to 0x1<br>for the protection quad ( <b>3b</b> or <b>7b</b> ) address. | <b>DLP-517</b> |
|-----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|

---

 **NOTE:**  
As a result of changing the Protection  
Mode from 1+1 to 0x1, the  
cross-connections associated with the  
protection quad are deleted.

---

|     |                                                                                                                                                                                                      |                |
|-----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 15. | While still logged into near-end node A, use<br><b>CONFIGURATION-Set-OC12</b> command to<br>change the "Protection Mode" from 1+1 to 0x1<br>for the service quad ( <b>1b</b> or <b>5b</b> ) address. | <b>DLP-517</b> |
|-----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|

---

|     |                                                                     |   |
|-----|---------------------------------------------------------------------|---|
| 16. | At the CIT, execute the<br><b>CONFIGURATION-Update-All</b> command. | — |
|-----|---------------------------------------------------------------------|---|

---

|     |                                                 |  |
|-----|-------------------------------------------------|--|
| 17. | <b>STOP! You Have Completed This Procedure.</b> |  |
|-----|-------------------------------------------------|--|

---

DLP-501

DLP-517



## Establish Cross-Connection Between FT-2000 1+1 OC-3/OC-12 Interface and DDM-2000 OC-3/OC-12 Ring Interface (Does Not Apply to DDM-2000 OC-3 Release 15 or Later Releases)

---

DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Overview:** This procedure uses special provisioning to provision an FT-2000 OC-3/OC-12 low-speed interface (see Figure 1, Node 3) for 1+1 protection and connect it to a DDM-2000 OC-3, OC-12, or FiberReach system **ring** interface. This procedure is useful when a DDM-2000 ring release is needed for feature or compatibility reasons but interconnection with a 1+1 interface is also required. The FT-2000 can be any release that provides an OC-3/OC-12 low-speed interface. However, DCC connectivity requires compatible DDM-2000 and FT-2000 software releases. DDM-2000 OC-3 Release 15.0, or later, will provide interconnection of its main ring interface to a 1+1 OC-3/OC-12 interface without special provisioning of the FT-2000 low-speed interface.

### Required Test Equipment:

- Craft Interface Terminal (CIT)



### **WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to the beam.*



### **CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

If **OC-3 interface**, use the **CONFIGURATION - Set - OC3** command to set one of the service slots (1A, 2A, 3A, 4A, 5A, 6A, 7A, or 8A) for **1 + 1** protection in the “from OC3” direction and **0 x 1** protection in the “from OC48” direction.

 **NOTE:**  
The FT-2000 ADR Terminal does not support mixed LAA10/LAA10B operation in a **1+1** configuration. To configure a OC-3 low speed interface for **1+1** configuration, any two adjacent low speed slots (for example, 2A and 2B) **MUST** be equipped with the same type OC3 (1.3 STD) circuit pack, and not a mix of the two.

If **OC-12 interface**, use the **CONFIGURATION - Set - OC12** command to set one of the service quads (1B or 5B) for **1 + 1** protection in the “from OC12” direction and **0 x 1** protection in the “from OC48” direction.

- 
2.  **NOTE:**  
Steps 2 and 3 reference the same tributary.

Use the **CONFIGURATION - Enter - Crossconnection** command to provision an add (or 1-way DRI) cross-connection from the service slot/quad.

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

- |                                                                                                                                                                                                                                          |   |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 3. Use the <b>CONFIGURATION - Enter - Crossconnection</b> command to provision a drop cross-connection to the service slot/quad and a drop cross-connection to the corresponding protection slot/quad.                                   | — |
| <hr/>                                                                                                                                                                                                                                    |   |
| 4. At the CIT, select the <b>CONFIGURATION - Enter - Assignment - DCC</b> command to assign a DCC channel, if required, to the service slot/quad and to the corresponding protection slot/quad, and set the orientation of the channels. | — |
| <hr/>                                                                                                                                                                                                                                    |   |
| 5. Cross-connections and DCC assignments have been established between the FT-2000 OC-3/OC-12 low speed interface and the DDM-2000 OC-3/OC-12 ring interface.                                                                            | — |
- 

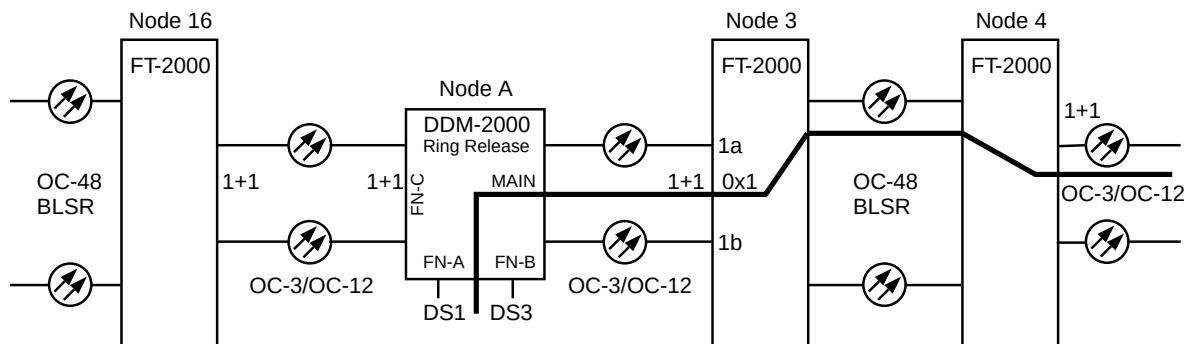


**Figure 1 – DDM-2000 OC-3/OC-12 Ring to FT-2000 1+1 OC-3/OC-12 Interface**



## Operation Task Index

---

### FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO

---

|                                                                                                          |         |
|----------------------------------------------------------------------------------------------------------|---------|
| Connect Two FT-2000 Rings Via an Optical Link . . . . .                                                  | NTP-011 |
| Copy Software From One Network Element to Another Network Element . .                                    | NTP-005 |
| Disconnect Dual Ring Interworking (DRI) STS-3 (or STS-1)<br>Cross-Connections . . . . .                  | NTP-004 |
| Establish Dual Ring Interworking (DRI) STS-3 (or STS-1)<br>Cross-Connections . . . . .                   | NTP-003 |
| Identify Primary/Secondary and Source/Destination Nodes . . . . .                                        | NTP-006 |
| Install Software (Initial Installation/Upgrade/Change) into CIT and<br>FT-2000 Network Element . . . . . | NTP-002 |
| Merge Two OC-48 Rings. . . . .                                                                           | NTP-009 |
| Optical Upgrade From STS-3 to STS-1 Granularity . . . . .                                                | NTP-010 |
| Provision 1-Way-DRI Video Distribution Circuit for Entire Ring . . . . .                                 | NTP-007 |
| Provision STS-1/STS-3 Non-Preemptible Protection Access (NPPA) . . . .                                   | NTP-008 |
| Ring Reset . . . . .                                                                                     | NTP-012 |





## Install Software (Initial Installation/Upgrade/Change) into CIT and FT-2000 Network Element

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap



**CAUTION:**

*When installing software, the CIT-PC software must be the same release or a higher release than the software currently running in the FT-2000 network element.*



**CAUTION:**

*All network elements within a particular network must be running the same software release. The CIT-PC software must be the same release as the network elements or a higher release.*



**NOTE:**

Disks available include the 5-1/2 inch (1.2M-byte) floppy, the 3-1/2 inch (720k-byte) floppy, or CD-ROM.



**NOTE:**

For a general description on using a personal computer in the following procedures, refer to **DLP-529**.

Verify the software disks or CD-ROM consists of INSTALL DISK, CIT-PC DISK, GENERIC and DATA numbered disks, and the appropriate documentation as follows:

—

---

**FT-2000 Repeater Bay/Shelf**

|                                                                                               |                   |
|-----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 2.1.0-RPTR</i> | Comcode 106944671 |
|-----------------------------------------------------------------------------------------------|-------------------|

|                                                                                               |                   |
|-----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 4.0.0-RPTR</i> | Comcode 107027468 |
|-----------------------------------------------------------------------------------------------|-------------------|

|                                                                                               |                   |
|-----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 6.0.1-RPTR</i> | Comcode 107889644 |
|-----------------------------------------------------------------------------------------------|-------------------|

**FT-2000 Add/Drop-Rings Terminal**

|                                                                                              |                   |
|----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 3.0.1-ADR</i> | Comcode 107095390 |
|----------------------------------------------------------------------------------------------|-------------------|

|                                                                                              |                   |
|----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 4.0.0-ADR</i> | Comcode 107027484 |
|----------------------------------------------------------------------------------------------|-------------------|

|                                                                                              |                   |
|----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 4.1.0-ADR</i> | Comcode 107027476 |
|----------------------------------------------------------------------------------------------|-------------------|

|                                                                                              |                   |
|----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 5.0.1-ADR</i> | Comcode 107361875 |
|----------------------------------------------------------------------------------------------|-------------------|

|                                                                                              |                   |
|----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 5.0.3-ADR</i> | Comcode 107717068 |
|----------------------------------------------------------------------------------------------|-------------------|

|                                                                                              |                   |
|----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 6.0.1-ADR</i> | Comcode 107375776 |
|----------------------------------------------------------------------------------------------|-------------------|

|                                                                                              |                   |
|----------------------------------------------------------------------------------------------|-------------------|
| <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 6.0.2-ADR</i> | Comcode 107741787 |
|----------------------------------------------------------------------------------------------|-------------------|

---

|  |                                                                                               |                   |
|--|-----------------------------------------------------------------------------------------------|-------------------|
|  | <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 7.0.1-ADR</i>  | Comcode 107883670 |
|  | <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 7.0.3-ADR</i>  | Comcode 107972671 |
|  | <i>FT-2000 OC-48 Lightwave System<br/>Software Release Description<br/>Release 7.1.0-ADR</i>  | Comcode 107743437 |
|  | <i>FT-2000 OC-48 Lighthwave System<br/>Software Release Description<br/>Release 7.2.0-ADR</i> | Comcode 108035734 |
|  | <i>FT-2000 OC-48 Lighthwave System<br/>Software Release Description<br/>Release 7.2.1-ADR</i> | Comcode 108134743 |
|  | <i>FT-2000 OC-48 Lighthwave System<br/>Software Release Description<br/>Release 7.2.2-ADR</i> | Comcode 108216961 |
|  | <i>FT-2000 OC-48 Lighthwave System<br/>Software Release Description<br/>Release 7.2.3-ADR</i> | Comcode 108265943 |
|  | <i>FT-2000 OC-48 Lighthwave System<br/>Software Release Description<br/>Release 7.2.4-ADR</i> | Comcode 108491895 |
|  | <i>FT-2000 OC-48 Lighthwave System<br/>Software Release Description<br/>Release 7.2.5-ADR</i> | Comcode 108491895 |
|  | <i>FT-2000 OC-48 Lighthwave System<br/>Software Release Description<br/>Release 7.2.7-ADR</i> | Comcode 108722810 |
|  | <i>FT-2000 OC-48 Lighthwave System<br/>Software Release Description<br/>Release 7.2.8-ADR</i> | Comcode 109017970 |
|  | <i>FT-2000 OC-48 Lighthwave System<br/>Software Release Description<br/>Release 7.2.9-ADR</i> | Comcode 109150284 |

---

| *FT-2000 OC-48 Lighthwave System*  
| *Software Release Description*  
| *Release 7.2.10-ADR*

Comcode 109191668

⇒ **NOTE:**  
The feature "Sync Messaging" has two states. The default state is **Enabled**. The alternate state is **Disabled**. The B2 optical transmitters do not (for R7.2) support the Sync Messaging feature. Therefore, if a node contains B2 transmitters and is being upgraded with 7.2 generic software--an alarm will be raised for each B2 transmitter. This Major Alarm is defined as "Sync Mode Equipment Conflict". Refer to TAP-102 for details on how to clear this alarm.

- 
2. Refer to and perform the appropriate software download procedure (initial installation/upgrade/change) per the software release description for your particular release.
-

## Establish Dual Ring Interworking (DRI) STS-3 (or STS-1) Cross-Connections

---

### DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO

---

1. **Overview:** This operations procedure is used to establish the necessary cross-connections at the primary and secondary nodes of an FT-2000 2-fiber ring network. These cross-connections are necessary to support dual ring interworking operation with a second SONET ring network. This procedure can be performed from any node in the FT-2000 2-fiber ring network.

*Cross-connections made to the wrong points can cause service interruptions.*

Dual ring interworking (DRI) operation is only supported in Software Releases 4.1 and later.

For Releases 6 and earlier, an electrical low-speed interface circuit pack (DS3 and/or STS1E) has three low-speed ports. All three ports associated with the slot will be affected by a slot-to-tributary or a tributary-to-slot cross-connection.

For Releases 6 and earlier, an optical low-speed interface circuit pack (OC-3) operates on a slot basis and is affected by a slot-to-tributary or a tributary-to-slot cross-connection.

Obtain the operation instructions (work order) —  
that indicates the required cross-connections.

- 
2.  **NOTE:**  
The primary node normally carries traffic via a DRI cross-connection.

From the operation instructions, identify and —  
record the target identifier (TID) of the primary  
node.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

3. The secondary node carries traffic only if the primary node is unable to carry traffic.

From the operation instructions, identify and record the cross-connections to be made at the secondary node.

—

- 
4. If required, connect the craft interface terminal (CIT) to the local FT-2000 network element and condition it.

**DLP-501**

- 
5. Establish a login to the secondary node. (To remotely establish a login, refer to **DLP-524.**)

—

- 
6.  **NOTE:**  
The normal cross-connection at the secondary node is a 2-way cross-connection, however, a 1-way cross-connection is used for half-duplex transmission.

At the CIT, select  
**CONFIGURATION-Retrieve-Crossconnect  
ion-STS3 (or STS1)** and execute to obtain  
an STS3 (or STS1) Cross-Connection Map  
Report. Section 11 shows examples of reports.

—

- 
7. Compare the report with the operation instructions and record any cross-connections to be added and/or deleted at the secondary node.

—

- 
8.  **CAUTION:**  
*The cross-connections to be deleted MUST NOT be carrying traffic; otherwise, service interruptions will occur.*

Delete any unnecessary cross-connections associated with the tributary(ies) and/or the slot(s) to be cross-connected. Use the  
**CONFIGURATION-Delete-Crossconnectio  
n-STS3 (or STS1)** command to delete a cross-connection.

**DLP-536**

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

9.



**CAUTION:**

*Incorrectly provisioned source node TID and/or destination node TID can result in a service outage under certain multiple failure conditions.*

Enter the required cross-connection(s) as necessary. Use the

**DLP-536**

**CONFIGURATION-Enter-Crossconnection**

**-STS3 (or STS1)** command to make a cross-connection.

- 
10. This step is optional. If the cross-connection just made was to a slot containing an OC-3 circuit pack, you may want to provision a DCC assignment to establish DCC connectivity with the NE at the other end of the OC-3 line.

You may enter/change a DCC assignment on an OC-3 line.

**DLP-542**

- 
11. At the CIT, select  
**CONFIGURATION-Retrieve-Crossconnection-STs3 (or STS1)** and execute to obtain an STS3 (or STS1) Cross-Connection Map Report. Section 11 shows examples of reports.

—

- 
12. Verify that the cross-connections in the report match those in the operation instructions.

—

- 
13. This step is optional. If the cross-connection just deleted was to a slot containing an OC-3 circuit pack, you may want to provision a DCC assignment to remove DCC connectivity with the network element at the other end of the OC-3 line.

You may delete a DCC assignment on an OC-3 line.

**DLP-542**

- 
14. Discontinue the login at the secondary node.  
(To discontinue a remote login, refer to **DLP-524.**)

—

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

- |       |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                |
|-------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 15.   | Establish a login to the primary node identified in Step 2. (To establish a remote login, refer to <b>DLP-524.</b> )                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | —              |
| <hr/> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                |
| 16.   | <p> <b>NOTE:</b><br/>The normal cross-connections needed at the primary node are a 1-way-dri cross-connection and a drop-and-continue cross-connection (bridged tributary-to-slot and same tributary-to-tributary).</p> <p>At the CIT, select<br/><b>CONFIGURATION-Retrieve-Crossconnect ion-STS3 (or STS1)</b> and execute to obtain an STS3 (or STS1) Cross-Connection Map Report. Section 11 shows examples of reports.</p>                                                                                                                                                                                                      | —              |
| <hr/> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                |
| 17.   | Compare the report with the operation instructions and record any cross-connections to be added and/or deleted at the primary node.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | —              |
| <hr/> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                |
| 18.   | <p> <b>CAUTION:</b><br/><i>The cross-connections to be deleted MUST NOT be carrying traffic; otherwise, service interruptions will occur.</i></p> <p> <b>NOTE:</b><br/>If the slot-to-tributary (add) cross-connection of the required 1-way-dri cross-connection already exists, do not delete it.</p> <p>Delete any unnecessary cross-connections associated with the tributary(ies) and/or the slot(s) to be cross-connected. Use the <b>CONFIGURATION-Delete-Crossconnectio n-STS3 (or STS1)</b> command to delete a cross-connection.</p> | <b>DLP-536</b> |
| <hr/> |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |                |
| 19.   | <p> <b>CAUTION:</b><br/><i>Incorrectly provisioned source node TID and/or destination node TID can result in a service outage under certain multiple failure conditions.</i></p>                                                                                                                                                                                                                                                                                                                                                                                                                                                  |                |
-



---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

Enter the required 1-way-dri cross-connection(s) using one of these commands. Use the **CONFIGURATION-Enter-Crossconnection-STS3 (or STS1)** command to make a cross-connection, or if the slot-to-tributary (add) component of the desired 1-way-dri cross-connection already exists, use the **CONFIGURATION-Enter-Roll-STS3 (or STS1)** command to change it to a 1-way-dri cross-connection.

**DLP-536**

- 
20.  **NOTE:**  
This step is optional. If the cross-connection just made was to a slot containing an OC-3 circuit pack, you may want to provision a DCC assignment to establish DCC connectivity with the network element at the other end of the OC-3 line.

You may enter/change a DCC assignment on an OC-3 line.

**DLP-542**

- 
21.  **NOTE:**  
This step is optional, but it is recommended. The primary node has a 1-way-dri cross-connection. An incoming low-speed signal failure on the associated port or slot will be protected by a DRI protection switch so service will not be affected. You may wish to change the alarm level associated with an incoming low-speed signal failure from the original value of CR (critical) to MN (minor).

At the CIT, select the appropriate command, **CONFIGURATION-Set - T3**, **CONFIGURATION-Set - EC1**, or **CONFIGURATION-Set - OC3** to provision the Alarm Level to MN (minor).

—

- 
22. At the CIT, select **CONFIGURATION-Retrieve-Crossconnection-STS3 (or STS1)** and execute to obtain an STS3 (or STS1) Cross-Connection Map Report. Section 11 shows examples of reports.

—

---

| <b>DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO</b> |                                                                                                                                                                                                                                                                                                                                  |                |
|--------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 23.                                                                | Verify that the cross-connections in the report match those in the operation instructions.                                                                                                                                                                                                                                       | —              |
| 24.                                                                | <p><b>⇒ NOTE:</b><br/>This step is optional. If the cross-connection just deleted was to a slot containing an OC-3 circuit pack, you may want to provision the DCC assignment to remove DCC connectivity with the network element at the other end of the OC-3 line.</p> <p>You may delete a DCC assignment on an OC-3 line.</p> | <b>DLP-542</b> |
| 25.                                                                | <p><b>⇒ NOTE:</b><br/>This step is optional and only applies if there are no low-speed connections to an external network(s).</p> <p>Test DRI cross-connections for path integrity.</p>                                                                                                                                          | <b>DLP-541</b> |
| 26.                                                                | Discontinue the login at the primary node. (To discontinue a remote login, refer to <b>DLP-524</b> .)                                                                                                                                                                                                                            | —              |
| 27.                                                                | The necessary cross-connections have been made in the primary and secondary nodes, and they are ready to support dual ring interworking (DRI) operation with the second SONET ring network.                                                                                                                                      | —              |
| 28.                                                                | Refer to <b>DLP-536</b> to make any other necessary cross-connections at other nodes in the FT-2000 2-fiber ring network.                                                                                                                                                                                                        | —              |

Operation: NTP-003---+---DLP-501  
                   | ---DLP-524  
                   | ---DLP-53  
                   | ---DLP-542  
                   | ---DLP-541

## Disconnect Dual Ring Interworking (DRI) STS-3 (or STS-1) Cross-Connections

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This operations procedure is used to disconnect the cross-connections at the primary and secondary nodes of an FT-2000 2-fiber ring network. These cross-connections were necessary to support dual ring interworking operation with a second SONET ring network. This procedure can be performed from any node in the FT-2000 2-fiber ring network.

 **CAUTION:**  
*Cross-connections deleted at the wrong points can cause service interruptions.*

 **NOTE:**  
Dual ring interworking (DRI) operation is only supported in Software Releases 4.1 and later.

 **NOTE:**  
For Releases 6 and earlier, an electrical low-speed interface circuit pack has three low-speed ports. All three ports associated with the slot will be affected by a slot-to-tributary or a tributary-to-slot cross-connect deletion.

 **NOTE:**  
For Releases 6 and earlier, an optical low-speed interface circuit pack (OC-3) operates on a slot basis and is affected by a slot-to-tributary or a tributary-to-slot cross-connection deletion.

Obtain the operation instructions (work order) that indicate which cross-connections are to be deleted.

---

| DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO |                                                                                                                                                                                                                                                                                                                                                                                                        |                |
|-------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 2.                                                          | <p> <b>NOTE:</b><br/>The primary node normally carries traffic via a DRI cross-connection.</p> <p>From the operation instructions, identify and record the target identifier (TID) of the primary node.</p>                                                                                                           | —              |
| 3.                                                          | <p> <b>NOTE:</b><br/>The secondary node carries traffic only if the primary node is unable to carry traffic.</p> <p>From the operation instructions, identify and record the cross-connections to be deleted at the secondary node.</p>                                                                               | —              |
| 4.                                                          | If required, connect the craft interface terminal (CIT) to the local FT-2000 network element and condition it.                                                                                                                                                                                                                                                                                         | <b>DLP-501</b> |
| 5.                                                          | Establish a login to the secondary node. (To remotely establish a login, refer to <b>DLP-524</b> .)                                                                                                                                                                                                                                                                                                    | —              |
| 6.                                                          | <p>The cross-connection to be deleted at the secondary node is a 1-way slot-to-tributary (add) cross-connection.</p> <p>At the CIT, select<br/><b>CONFIGURATION-Retrieve-Crossconnect ion-STS3 (or STS1)</b> and execute to obtain an STS3 (or STS1) Cross-Connection Map Report. Examples of these reports with a description of the output parameters are provided in Section 11 of this manual.</p> | —              |
| 7.                                                          | Compare the report with the operation instructions and record any slot-to-tributary (add) cross-connections to be deleted at the secondary node.                                                                                                                                                                                                                                                       | —              |

| DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO |                                                                                                                                                                                                                                                                                                                                                                                                        |                |
|-------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 8.                                                          | Delete any unnecessary slot-to-tributary (add) cross-connections identified in Step 7. Use the <b>CONFIGURATION-Delete-Crossconnection-STS3 (or STS1)</b> command to delete a cross-connection.                                                                                                                                                                                                        | <b>DLP-536</b> |
| 9.                                                          |  <b>NOTE:</b><br>This step is optional. If the cross-connection just deleted was to a slot containing an OC-3 circuit pack, you may want to provision a DCC assignment to remove DCC connectivity with the network element at the other end of the OC-3 line.<br><br>You may delete a DCC assignment on an OC-3 line. | <b>DLP-542</b> |
| 10.                                                         | At the CIT, select <b>CONFIGURATION-Retrieve-Crossconnection-STS3 (or STS1)</b> and execute to obtain an STS3 (or STS1) Cross-Connection Map Report. Section 11 shows examples of reports.                                                                                                                                                                                                             | —              |
| 11.                                                         | Verify that the cross-connection(s) in the report matches those in the operation instructions.                                                                                                                                                                                                                                                                                                         | —              |
| 12.                                                         | Discontinue the login at the secondary node. (To discontinue a remote login, refer to <b>DLP-524.</b> )                                                                                                                                                                                                                                                                                                | —              |
| 13.                                                         | Establish a login to the primary node identified in Step 2. (To establish a remote login, refer to <b>DLP-524.</b> )                                                                                                                                                                                                                                                                                   | —              |
| 14.                                                         | The cross-connection(s) to be deleted and/or replaced at the primary node is a 1-way-dri cross-connection(s).<br><br>At the CIT, select <b>CONFIGURATION-Retrieve-Crossconnection-STS3 (or STS1)</b> and execute to obtain an STS3 (or STS1) Cross-Connection Map Report.                                                                                                                              | —              |

| DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO |                                                                                                                                                                                                                                                                                                                                                                                                                        |
|-------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 15.                                                         | Compare the report with the operation instructions and record any cross-connections to be deleted and/or replaced at the primary node. —                                                                                                                                                                                                                                                                               |
| 16.                                                         | Are any existing 1-way-dri cross-connections to be deleted rather than replaced? —<br>If <b>YES</b> , then continue with Step 17.<br>If <b>NO</b> , then continue with Step 18.                                                                                                                                                                                                                                        |
| 17.                                                         | Delete any unnecessary 1-way-dri cross-connections identified in Step 15. Use the <b>CONFIGURATION-Delete-Crossconnection-STS3 (or STS1)</b> command to delete a cross-connection. <b>DLP-536</b>                                                                                                                                                                                                                      |
| 18.                                                         | This step is optional. If the cross-connection just deleted was to a slot containing an OC-3 circuit pack, you may want to provision a DCC assignment to remove DCC connectivity with the network element at the other end of the OC-3 line.<br><br>You may delete a DCC assignment on an OC-3 line. <b>DLP-542</b>                                                                                                    |
| 19.                                                         | Roll any unnecessary 1-way-dri cross-connection(s) to 1-way cross-connection(s). Use the <b>CONFIGURATION-Enter-Roll-STS3 (or STS1)</b> command to modify a cross-connection. —                                                                                                                                                                                                                                        |
| 20.                                                         |  <b>NOTE:</b><br>This step is optional. If the cross-connection just rolled was to a slot containing an OC-3 circuit pack, you may want to provision a DCC assignment to remove DCC connectivity with the network element at the other end of the OC-3 line.<br><br>You may delete a DCC assignment on an OC-3 line. <b>DLP-542</b> |

| DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO |                                                                                                                                                                                                                                                                                                                                                                                                                            |
|-------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 21.                                                         | Establish a login to all downstream nodes<br>(destination node and every intermediate node)<br>and update the STS-3 (or STS-1)<br>cross-connection map with the "new" source<br>node using the<br><b>CONFIGURATION-Enter-Crossconnection<br/>-STS3 (or STS1)</b> command. (The secondary<br>node was the source node for the 1-way-dri<br>cross-connection.) (To remotely establish a<br>login, refer to <b>DLP-524</b> .) |
| 22.                                                         | At the CIT, select<br><b>CONFIGURATION-Retrieve-Crossconnect<br/>ion-STs3 (or STS1)</b> and execute to obtain<br>an STS3 (or STS1) Cross-Connection Map<br>Report. Section 11 shows examples of reports.                                                                                                                                                                                                                   |
| 23.                                                         | Verify that the cross-connections in the report<br>match those in the operation instructions.                                                                                                                                                                                                                                                                                                                              |
| 24.                                                         | Discontinue the login at the primary node. (To<br>discontinue a remote login, refer to <b>DLP-524</b> .)                                                                                                                                                                                                                                                                                                                   |
| 25.                                                         | The necessary cross-connections have been<br>disconnected in this FT-2000 2-fiber ring<br>network. It no longer supports dual ring<br>interworking (DRI) operation with the second<br>SONET ring network.                                                                                                                                                                                                                  |

Operation: NTP-004---+---DLP-501  
|---DLP-524  
|---DLP-536





## Copy Software From One Network Element to Another Network Element

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This procedure is used to copy the software in one FT-2000 network element to another FT-2000 network element through the data communications channel (DCC) residing in an OC-3 or OC-48 signal. All of the FT-2000 network elements must be running Release 5.0 or later software. This procedure can be accomplished while the FT-2000 ring is **in service**.

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap

⇒ **NOTE:**  
Release 5 allows copying software over the OC-48 line. Release 6 allows copying software over either the OC-3 or OC-48 lines.

⇒ **NOTE:**  
For a general description on using a personal computer in the following procedure, refer to **DLP-529**.

If required, connect the craft interface terminal (CIT) to the local FT-2000 network element and condition it.

**DLP-501**

- 
2. ⇒ **NOTE:**  
The CIT user must have a "privileged" security level.  
Log in to the CIT using the expert mode by entering **cit expert**. —
-

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 | FOR DETAILS, GO TO |
|------------------------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 3.                                       | Verify that there are no active alarms or status conditions by selecting the <b>FAULT-Retrieve-Alarms (and Status):All</b> or by pressing function key <b>F9</b> . Review the Active Alarms and Status Report and clear any conditions before starting this procedure.                                                                                                                                                                                                                                                                                                                                                                          | —                  |
| 4.                                       | At the CIT, select <b>CONFIGURATION-Retrieve-Map-Network</b> command to obtain the Network Map Report or select <b>CONFIGURATION-Retrieve-Map Ring</b> command to obtain the Ring Map Report for release 5. An example of these reports with a description of the output parameters is provided in Section 11 of this manual.                                                                                                                                                                                                                                                                                                                   | —                  |
| 5.                                       |  <b>NOTE:</b><br>The software in the network will be updated by starting with the topmost FT-2000 network element (TID) in the report (except the DS-NE) and moving down the list of TIDs (nodes) one at a time. This approach updates the network software in an East direction on a section-by-section basis and ends with the DS-NE.<br><br>Refer to the appropriate software release description for your particular release and perform the <b>Remote (DCC) Download</b> procedure. The software release descriptions are listed in "Operation: NTP-002". | —                  |

## Identify Primary/Secondary and Source/Destination Nodes

---

|                                                 |                           |
|-------------------------------------------------|---------------------------|
| <b>DO ITEMS BELOW IN ORDER LISTED . . . . .</b> | <b>FOR DETAILS, GO TO</b> |
|-------------------------------------------------|---------------------------|

---

1. **Overview:** This procedure is used to identify the primary, secondary, source, and destination nodes of a circuit provisioned for dual ring interworking (DRI). (For an understanding of these terms, refer to the Glossary and Chapters 8 and 9 of this manual.) This procedure can be performed from any node in an FT-2000 2-fiber ring network.

Dual ring interworking (DRI) operation is only supported in Software Releases 4.1 and later.

The terms **source** and **destination** are used for slots, tributaries, lines, and nodes when making cross-connections. However, this procedure will only use **source** and **destination** in reference to nodes that describe a 1-way circuit (one direction of transmission).

If required, connect the craft interface terminal (CIT) to the FT-2000 network element and condition it.

**DLP-501**

- 
- |                                                                                            |   |
|--------------------------------------------------------------------------------------------|---|
| 2. Establish a login to a node. (To remotely establish a login, refer to <b>DLP-524</b> .) | — |
|--------------------------------------------------------------------------------------------|---|
- 
- |                                                                                                                                                                                                         |   |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 3. From the operation instructions, determine the tributary to be used in identifying the primary, secondary, source, and destination nodes of that circuit. Each tributary could have different nodes. | — |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
- 
- |                                                                                                                                                    |   |
|----------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 4. At the CIT, select <b>CONFIGURATION-Retrieve-Timeslot-STS3 (or STS1)</b> command and execute to obtain an STS-3 (or STS1) Tributary Map Report. | — |
|----------------------------------------------------------------------------------------------------------------------------------------------------|---|
-

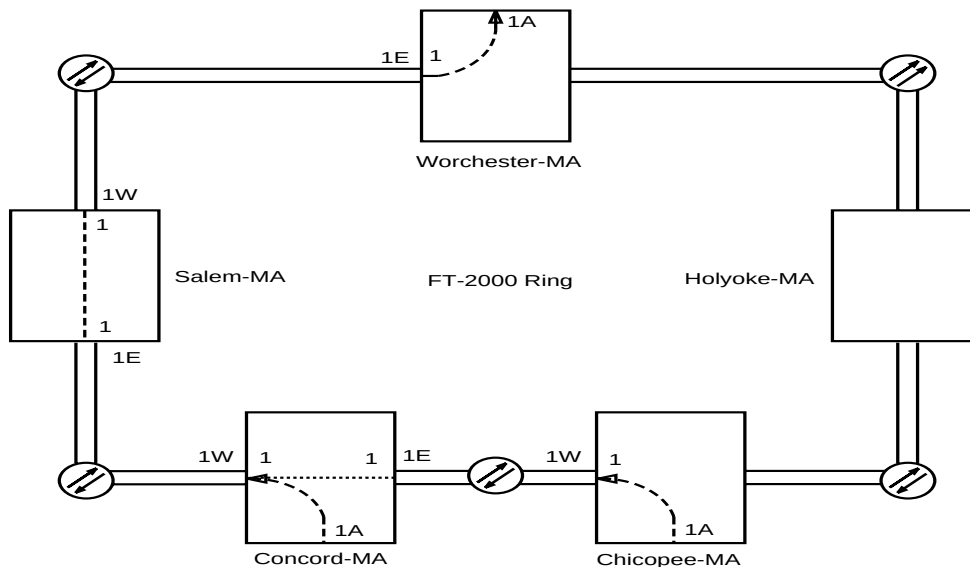
---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

- |    |                                                                                                                                                                                                                                                                                                                                                                                       |   |
|----|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 5. | From the STS-3 (or STS1) Tributary Map Report, identify the source, destination, primary, and secondary nodes by referring to Figure 1 and to Exhibit 1 for STS3 or to Exhibit 2 for STS1. The exhibits contain an explanation to Figure 1. Figure 1 and Exhibit 1 or Exhibit 2 use tributary 1 and slot 1a for examples and that Worcester-MA is the location requesting the report. | — |
| 6. | Discontinue the login to the node. (To discontinue a remote login, refer to <b>DLP-524</b> .)                                                                                                                                                                                                                                                                                         | — |
| 7. | This completes the procedure to identify the primary, secondary, source, and destinations nodes of a DRI circuit.                                                                                                                                                                                                                                                                     | — |
- 

Operation: NTP-006-----DLP-501  
+---DLP-524



tpa 847809/01

**Figure 1 – Five Node Ring with DRI Cross-Connections**

## STS3 Explanation to Figure 1

```

/# STS-3 Tributary Map Report
=====
At Node TID West Cross-connection East TID
=====
Worcester-MA 1a <--- 1e-1 Concord-MA

Salem-MA
 Worcester-MA 1w-1 <----- 1e-1 Concord-MA

Concord-MA 1w-1 <--- 1a 1e-1
 Worcester-MA 1w-1 <..... 1e-1 Chicopee-MA

Chicopee-MA
 Worcester-MA 1w-1 <--- 1a

Holyoke-MA
 1w-1 1e-1

*/

```

This report shows the primary, secondary, source and destination nodes. Observe the following from the report (no DRI switch means traffic is coming from the primary node):

- Scan the report in the **Cross-connection** column and locate a 1-way DRI. A 1-way DRI is a 1-way add cross-connection and a 1-way through cross-connection sharing the same outgoing tributary. Note that one of these cross-connections will be active (dashed line) while the other will be standby (dotted line). In this report, the **At Node** column indicates Concord-MA is provisioned with a 1-way DRI cross-connection.
- The **primary node** is a node at which a 1-way DRI cross-connection has been provisioned. In this report, Concord-MA is the primary node. Concord-MA is also the **active source node** because the add cross-connection is dashed and not dotted.
- The **secondary node** is given in the TID column associated with the incoming tributary of the 1-way DRI. In this report, the secondary node is to the right (East) of the 1-way DRI, Chicopee-MA. Chicopee-MA is also the **standby source node** because the through cross-connect is dotted.
- The **destination node** is given in the TID column associated with the outgoing tributary of the 1-way DRI. In this report, the **destination node** is to the left (West) of the 1-way DRI, Worcester-MA.
- Note: If a DRI switch occurs and the secondary node becomes the active source node, the through cross-connection will be dashed and the add cross-connection will be dotted.

Additional information on this type of report is described in Chapter 11 of this manual.

## STS1 Explanation to Figure 1

```

/* STS-1 Tributary Map Circuit Report
=====
Source Source Destination Destination Primary
Ring Node TID AID AID TID State
=====
Worcester-MA
Worcester-MA ls-1a-1 1e-1-2 Salem-MA

Salem-NH
Concord-MA 1e-1-2 1w-1-2 Worcester-MA

Concord-MA
Concord-MA ls-1a-2 1w-1-2 Worcester-MA ACT
Chicopee-MA 1e-1-2 1w-1a-2 Worcester-MA STBY

Chicopee-MA
Worcester-MA 1w-1-2 ls-1a-2 Chicopee-MA

Holyoke-MA

*/

```

This report shows the primary, secondary, source and destination nodes. Observe the following from the report (no DRI switch means traffic is coming from the primary node):

- Scan the report in the **Primary State** column and locate a 1-way DRI. A 1-way DRI is a 1-way add cross-connection and a 1-way through cross-connection sharing the same outgoing line and tributary. Note that one of these cross-connections will be active (ACT) while the other will be standby (STBY). In this report, the **Ring Node** column indicates Concord-MA is provisioned with a 1-way DRI cross-connection.
- The **primary node** is a node at which a 1-way DRI cross-connection has been provisioned. In this report, Concord-MA is the primary node. Concord-MA is also the **active source node** because the add cross-connection is indicated by ACT in the **Primary State** column.
- The **secondary node** is given in the **Source TID** column associated with the incoming tributary of the 1-way DRI. In this report, the secondary node is to the right (East) of the 1-way DRI, Chicopee-MA. Chicopee-MA is also the **standby source node** because the through cross-connect at Concord-MA is indicated by STBY in the **Primary State** column.
- The **destination node** is given in the **Destination TID** column associated with the outgoing tributary of the 1-way DRI. In this report, the **destination node** is to the left (West) of the 1-way DRI, Worcester-MA.
- Note: If a DRI switch occurs and the secondary node becomes the active source node, the through cross-connection will become **ACT** and the add cross-connection will become **STBY**.

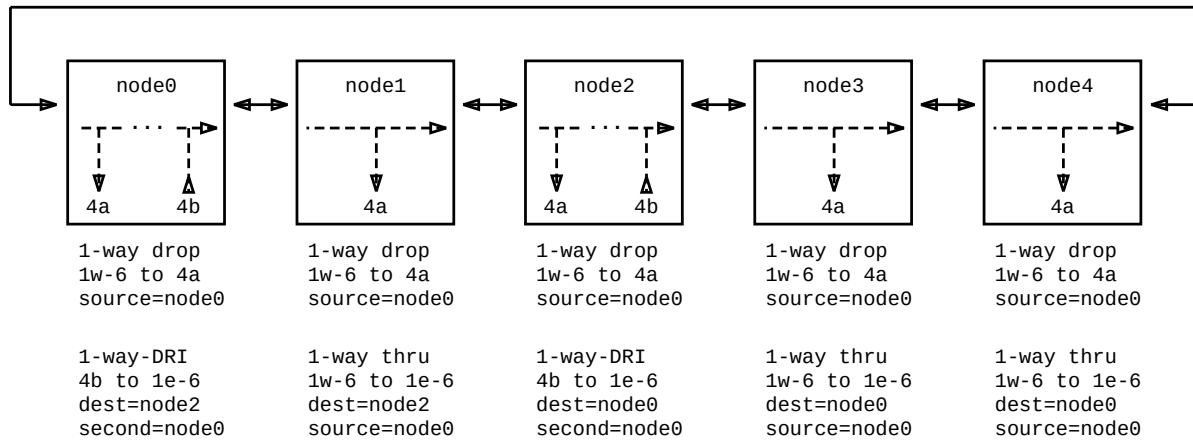
Additional information on this type of report is described in Chapter 11 of this manual.

## Provision 1-Way-DRI Video Distribution Circuit for Entire Ring

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  | FOR DETAILS, GO TO |
|------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 1.                                       | <p><b>Overview:</b> This procedure is used to provision all FT-2000 nodes throughout an entire ring that uses a 1-way-DRI (dual ring interworking) circuit for video distribution. Examples are used to illustrate target identifier (TID) provisioning rules to prevent alarm conditions under normal operations. This procedure can be performed from any node in an FT-2000 2-fiber ring network. Additional information may be found in Chapter 2, "Applications," and in Chapter 8, "Administration and Provisioning."</p> <p>⇒ <b>NOTE:</b><br/>Dual ring interworking (DRI) operation is only supported in Software Releases 4.1 and later.</p> <p>⇒ <b>NOTE:</b><br/>The terms <b>source</b> and <b>destination</b> are used for slots, tributaries, lines, and nodes when making cross-connections. However, this procedure will only use <b>source</b> and <b>destination</b> in reference to nodes that describe a 1-way circuit (one direction of transmission).</p> <p>If required, connect the craft interface terminal (CIT) to the FT-2000 network element and condition it.</p> | <b>DLP-501</b>     |
| 2.                                       | Establish a login to a node. (To remotely establish a login, refer to <b>DLP-524</b> .)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          | —                  |
| 3.                                       | From the operation instructions, determine the same tributary to be used at all nodes for the video circuit.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | —                  |
| 4.                                       | At the CIT, select<br><b>CONFIGURATION-Retrieve-Timeslot-STS3</b><br>command and execute to obtain an STS-3 Tributary Map Report for the tributary.                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              | —                  |

| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   | FOR DETAILS, GO TO |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| <p>5. From the STS-3 Tributary Map Report, identify the source, destination, primary, secondary, and intermediate nodes and provision those nodes by referring to the examples in Figures 1, 2, and 3. The following TID rules should be observed when specifying the source node TID, secondary node TID, and destination node TID.</p> <p>A. The <b>source node TID</b> (1-way cross-connections) and the <b>secondary node TID</b> (1-way-DRI cross-connections) must be the <i>same</i> at all nodes. Which TID is chosen is arbitrary but is normally one of the nodes having a 1-way-DRI cross-connection.</p> <p>B. The <b>destination node TID</b> depends on exactly what cross-connections are present. To determine the destination node TID from the local node, assign the following:</p> <ol style="list-style-type: none"> <li>1. <b>X</b> is the TID of the local node.</li> <li>2. <b>Y</b> is the TID of the next downstream node (transmit direction) with a 1-way-DRI cross-connection. X and Y will never be the same node.</li> <li>3. <b>Z</b> is the TID of the last node with a 1-way drop cross-connection when following the circuit from node X to node Y. Z can be node X or Y, some other node between X and Y, or null if there are no 1-way drop cross-connections between X and Y.</li> <li>4. If Z is X or null, use the TID of node Y as the destination node TID.</li> <li>5. Otherwise, use the TID of node Z as the destination node TID.</li> </ol> |                    |
| <p>6. Discontinue the login to the node. (To discontinue a remote login, refer to <b>DLP-524</b>.)</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     | —                  |
| <p>7. This completes the procedure to provision a 1-way-DRI circuit for video distribution.</p>                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            | —                  |





tpa 849524/01

**Figure 1 – Five Node Ring (Two Nodes with DRI Cross-Connections and Five Drops)**

In Figure 1, the topology is a five node ring with two DRIs protecting each other at node 0 and at node 2. Each node has a drop cross-connection from the West direction on time slot 6. In addition, node 1, node 3, and node 4 have a through (continue) cross-connection. TID rule A applies to all source nodes and secondary nodes. TID rule B applies to all destination nodes.

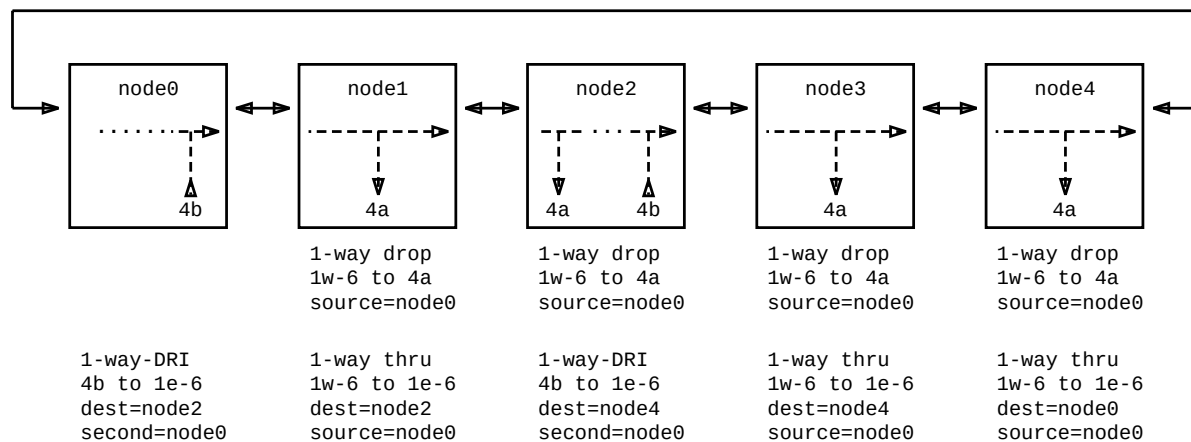
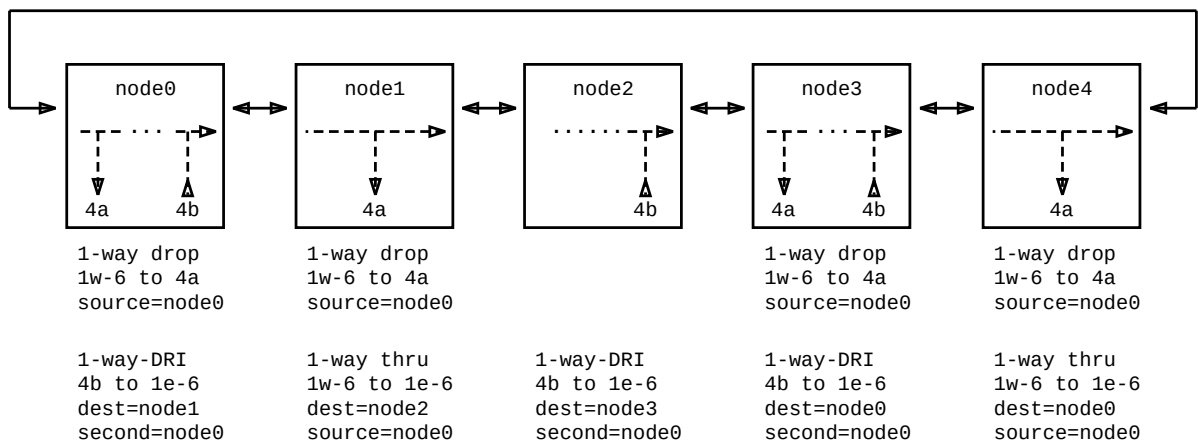


**Figure 2 – Five Node Ring (Two Nodes with DRI Cross-Connections and Four Drops)**

In Figure 2, the topology is the same as in Figure 1 but node 0 does not have a 1-way drop cross-connection. Applying TID rule A to all source nodes and secondary nodes implies using the same TID as node 0 (or, equivalently, the TID of node 2) for all cross-connections at all nodes. Application of TID rule B gives different results since there is no longer a drop cross-connection at node 0. Following are two examples:

- At node 4, TID rule B.4 applies (X = node 4, Y = node 1 and Z = node 4), so the destinations TID of the 1-way through is set to node 0.
- At node 2, TID rule B.5 applies (X = node 2, Y = node 0 and Z = node 4), so the destination node TID is set to the same as the TID for node 4. TID rule B.5 also applies to node 3.



tpa 849526/01

**Figure 3 – Five Node Ring (Three Nodes with DRI Cross-Connections and Four Drops)**

In Figure 3, the topology is the same as in Figure 1 but node 2 does not have a 1-way drop cross-connection. Applying TID rule A to all source nodes and secondary nodes implies using the same TID as node 0 (or, equivalently, the TID of node 2) for all cross-connections at all nodes. Application of TID rule B gives different results since there is no longer a drop cross-connection at node 2. Following are examples:

- At node 0: X=node 0, Y=node 2, and Z=node 1 where TID rule B.5 implies Z=node 1 be used.
- At node 1: X=node 1, Y=node 2, Z= node 1 where TID rule B.4 implies Y=node 2 be used.
- At node 2: X=node 2, Y=node 3, and Z = node 3 where TID rule B.5 implies Z=node 3 be used.
- At node 3: X = node 3, Y = node 0, and Z = node 0 where TID rule B.5 implies Z = node 0 be used.
- At node 4: X = node 4, Y = node 0, and Z = node 0 where TID rule B.5 implies Z = node 0 be used.



## Provision STS-1/STS-3 Non-Preemptible Protection Access (NPPA)

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** Normally, this feature will be provisioned via TIRKS. However, it can also be provisioned from the CIT. This NTP describes the steps that must be performed in order to add/delete/change NPPA traffic.

 **CAUTION:**  
*Each add/delete/change will affect the same timeslot in every node of the FT-2000 ring. An add/delete/change need only be entered at a single node. To delete or change an NPPA assignment, you should make all changes at the node where that assignment was first made.*

 **CAUTION:**  
*When a protection tributary is selected for non-preemptible protection access, the corresponding STS-3 tributary of service capacity at every node of the ring becomes unprotected.*

---

 **NOTE:**  
Although Release 7.1.0 only provides NPPA at the STS-3 level, it still requires STS-1 capable (B-5-optics) circuit packs. Release 7.2 provides NPPA at both STS-3 and STS-1 levels (B5-optics are still required).

If required, connect the craft interface terminal (CIT) to the FT-2000 network element and condition it.

**DLP-501**

- 
2. Establish a login to a node. (To remotely establish a login, refer to **DLP-524**.)
  3. At the CIT, select the CONFIGURATION-Retrieve-Protection STS3 or (STS1) command and execute to retrieve current values of the local protection access state variables for each tributary.

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**



**NOTE:**

This report lists the state variables of each STS3 tributary for both the East and West fiber-links. There are three possible state variables. They are as follows:

- a. Preemptible (where a signal fail or signal degrade condition on the service STS-3 tributaries causes traffic to be automatically switched to the protection STS-3 tributaries, preempting extra traffic).
- b. Permanent (where the protection capacity is used, on a permanent basis, to carry extra traffic; for example, video transport).
- c. Temporary (where service traffic is temporarily made unprotected and the non-preemptible protection capacity is used for DACS-based network restoration purposes).

- 
- |    |                                                                                                                                                                         |   |
|----|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 4. | At the CIT, select the<br>CONFIGURATION-Enter-Protection-STS3 or<br>(STS1) command and execute to<br>add/delete/change the state variable for the desired<br>tributary. | — |
|----|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
- 
- |    |                                                                                                                                                                                                                                                                            |   |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 5. | Verify that the state variable, for the specified<br>tributary, has actually been changed per the previous<br>(Step 4) command. At the CIT, select the<br>CONFIGURATION-Retrieve-Protection STS3<br>or (STS1) command and execute to retrieve the<br>(new) current values. | — |
|----|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
- 
- |    |                                                                                                                                                  |   |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 6. | Are there additional NPPA changes to be made?<br><br>If <b>YES</b> , then continue with Step 2.<br><br>If <b>NO</b> , then continue with Step 7. | — |
|----|--------------------------------------------------------------------------------------------------------------------------------------------------|---|
- 
- |    |                                                                                           |   |
|----|-------------------------------------------------------------------------------------------|---|
| 7. | The NAPA add/delete/change operation has been<br>performed. This completes the procedure. | — |
|----|-------------------------------------------------------------------------------------------|---|
-

## Merge Two OC-48 Rings

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** The steps in this procedure detail how to merge (combine) two small OC-48 rings into a single larger OC-48 ring.

⇒ **NOTE:**  
For discussion purposes, the two rings are referred to as ring-1 and ring-2. Figure 1 is an example that serves to illustrate ring-1 and ring-2. Both rings must be accessible via a CIT (either dial-up or direct connection). It is assumed that there are enough fibers and that all new fibers have already been tested and installed.

### Required Test Equipment:

CIT  
Wrist Strap

### Required Personnel:

Have installation/maintenance personnel at all four nodes (two nodes of ring-1 and two nodes of ring-2) where fibers are to be physically moved (originals disconnected/new connected).

- 
- |    |                                                                                     |                |
|----|-------------------------------------------------------------------------------------|----------------|
| 2. | If required, connect the CIT to the local FT-2000 network element and condition it. | <b>DLP-501</b> |
|----|-------------------------------------------------------------------------------------|----------------|
- 

- |    |                                                                            |   |
|----|----------------------------------------------------------------------------|---|
| 3. | ⇒ <b>NOTE:</b><br>The CIT user(s) must have a "privileged" security level. | — |
|----|----------------------------------------------------------------------------|---|

Log in to the CIT (on both ring-1 and ring-2) using the expert mode by entering `cit expert`.

- 
- |    |                                                                                                                                                                                                                                                                                    |                |
|----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 4. | For ring-1, verify that there are no active alarms or status conditions by selecting the FAULT-Retrieve-Alarms (and Status):All or by pressing function key <b>F9</b> . Review the Active Alarms and Status Report and clear any conditions before continuing with this procedure. | <b>TAP-102</b> |
|----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
-

| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                                                                                                                                                                                                                 | FOR DETAILS, GO TO |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 5. For ring-2, verify that there are no active alarms or status conditions by selecting the FAULT-Retrieve-Alarms (and Status):All or by pressing function key <b>F9</b> . Review the Active Alarms and Status Report and clear any conditions before continuing with this procedure. Refer to TAP-102 as appropriate.                                                                                                                                   | —                  |
| 6. Verify that each node, of both ring-1 and ring-2, is running the same release of software.                                                                                                                                                                                                                                                                                                                                                            | —                  |
| <div> <b>NOTE:</b><br/>If it is determined that the software on one or more NEs must be upgraded - then follow the procedures specified in the software release description associated with the particular software release.</div>                                                                                                                                      |                    |
| 7. Verify that each node, for both ring-1 and ring-2, has a unique TID. At the CIT connected to any NE within ring-1, select CONFIGURATION-Retrieve-Map-Network command (R6.0.0 and later) to obtain the Network Map Report or select CONFIGURATION-Retrieve-Map Ring command to obtain the Ring Map Report for release 5 and earlier. An example of these reports with a description of the output parameters is provided in Section 11 of this manual. | —                  |
| <div> <b>NOTE:</b><br/>The NID is a provisionable parameter for releases prior to R6.0.0 of ADR. Therefore, for these releases also verify that each node, for both ring-1 and ring-2, has a unique NID.</div>                                                                                                                                                        |                    |
| 8. At the CIT connected to any NE within ring-2, select CONFIGURATION-Retrieve-Map-Network command (R6.0.0 and later) to obtain the Network Map Report or select CONFIGURATION-Retrieve- Map Ring command to obtain the Ring Map Report for release 5 and earlier.                                                                                                                                                                                       |                    |



---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

**Provision Through Cross-Connections**

---

9.



**WARNING:**

*All traffic that is presently routed directly between the nodes adjacent to where the rings will be opened requires new through cross-connections provisioned in the nodes of the other ring. This must be done prior to merging the rings. Otherwise, service will be dropped when the merge is completed.*



**NOTE:**

In order to establish the through cross connections it may be necessary to roll some of the existing cross connections in either or both rings.

At the CIT connected to ring-1, select the  
CONFIGURATION-Retrieve-Crossconnect -STS...  
..... command to obtain the cross-connect map report.  
Refer to Figure 2.

- 
10. From the ring-1 cross-connect map report, identify all traffic that must have a "through" cross-connection defined for one or more NEs of ring-2. At the CIT connected to ring-2, select the CONFIGURATION - Enter - Crossconnection -STS..... command, at each NE as appropriate, to define the "through" cross-connections. Refer to Figure 2.



**NOTE:**

This will cause non-service affecting system alarms until the procedure is completed.

- 
11. At the CIT connected to ring-2 select the  
CONFIGURATION-Retrieve-Crossconnect -STS1  
command to obtain the cross-connect map report.
-

---

|                                                 |                           |
|-------------------------------------------------|---------------------------|
| <b>DO ITEMS BELOW IN ORDER LISTED . . . . .</b> | <b>FOR DETAILS, GO TO</b> |
|-------------------------------------------------|---------------------------|

---

12. From the ring-2 cross-connect map report, identify all traffic that must have a "through" cross-connection defined for one or more NEs of ring-1. At the CIT connected to ring-1, select the CONFIGURATION-Enter-Crossconnection-STS..... command, at each NE as appropriate, to define the "through" cross-connections. Refer to Figure 2.

⇒ **NOTE:**  
This will cause non-service affecting system alarms until the procedure is completed.

---

**Change one of the DS-NE=YES to NO**

---

13.  **WARNING:**  
*A single ring can have only one DS-NE. Therefore, prior to merging ring-1 with ring-2 it will be necessary to change one of the existing DS-NE=YES nodes to **NO**. Failure to do this could result in a service interruption.*

From the CIT, access the NE to be changed and select the SECURITY-Set-Network\_Element command and change DS-NE = YES to DS-NE = **NO**. Refer to the example provided in Figure 3.

⇒ **NOTE:**  
At the node where the DS-NE is changed, the OHCTL (and the TOHCTL pack if used) will reboot. This reboot will cause DCC failures within this ring.

⇒ **NOTE:**  
Also, after the reboot takes place, all nodes within that ring will have system alarms such as: DS-NE not reachable, NE Stat Comm Failure, Circuit audit suspended, etc. And, a remote login cannot be performed at the rebooted node.

---

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

**Remove traffic from fibers that are to be disconnected.**

---

14. Perform a Protection Switch on each of the four nodes involved in the merge. **Make sure that the Forced Switch is performed on the proper direction (E or W) at the respective four nodes.**



**NOTE:**

Switching hits will occur on service carried through the fibers that were "Force Switched". Also, using Figure 4 as our point of discussion, you should observe the following typical alarms:

Node A: ABN line-1E line forced switch at near end

Node C: ABN line-1W line forced switch at near end

Node 2: ABN line-1W line forced switch at near end

Node 3: ABN line-1E line forced switch at near end

There may be other alarms at the various nodes during this procedure.

---

**Prepare to Disconnect Existing Fibers**

---

15. Instruct the installation/maintenance personnel, at node A to disconnect the East transmit fiber (point AET) and also verify that the West receiver (point CWR) has an incoming OC-48 LOS alarm and its FAULT LED is flashing. Refer to the example provided in Figure 5.
  16. Instruct the installation/maintenance personnel, at node C, to disconnect the West receive fiber (point CWR).
-

---

|                                                 |                           |
|-------------------------------------------------|---------------------------|
| <b>DO ITEMS BELOW IN ORDER LISTED . . . . .</b> | <b>FOR DETAILS, GO TO</b> |
|-------------------------------------------------|---------------------------|

---

17. Instruct the installation/maintenance personnel, at node C, to disconnect the West transmit fiber (point CWT) and also verify that the East receiver (point AER) has an incoming OC-48 LOS alarm and its FAULT LED is flashing. Refer to the example provided in Figure 5.

---

18. Instruct the installation/maintenance personnel, at node A, to disconnect the East receive fiber (point AER).

---

19. Instruct the installation/maintenance personnel, at node 2, to disconnect the West transmit fiber (point 2WT) and also verify that the East receiver (point 3ER) has an incoming OC-48 LOS alarm and its FAULT LED is flashing. Refer to the example in Figure 5.

---

20. Instruct the installation maintenance personnel, at node 3, to disconnect the East receive fiber (point 3ER).

---

21. Instruct the installation maintenance personnel, at node 3, to disconnect the East transmit fiber (point 3ET) and also verify that the West receiver (point 2WR) has an incoming OC-48 LOS alarm and its FAULT LED is flashing. Refer to the example in Figure 5.

---

22. Instruct the installation maintenance personnel, at node 2, to disconnect the West receive fiber (point 2WR).

---

**Prepare to Connect New Fibers**

---

23. Instruct the installation/maintenance personnel, at node 2, to connect the new West transmit fiber (point 2WT). Refer to the example in Figure 6.

---

24. Instruct the installation/maintenance personnel, at node A, to measure the received optical power level at the new East receive fiber (point AER). Select and install the correct optical LBO - then connect the fiber to the receiver. Verify that the incoming OC-48 alarm and flashing FAULT LED clear after about 1 minute.

---

| DO ITEMS BELOW IN ORDER LISTED . . . . .                                                                                                                                                                                                                                                                                                                                                                         | FOR DETAILS, GO TO |
|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 25. Instruct the installation/maintenance personnel, at node A, to connect the new East transmit fiber (point AET). Refer to the example in Figure 6.                                                                                                                                                                                                                                                            |                    |
| 26. Instruct the installation/maintenance personnel, at node 2, to measure the received optical power level at the new West receive fiber (point 2WR). Select and install the correct optical LBO - then connect the fiber to the receiver. Verify that the incoming OC-48 alarm and flashing FAULT LED clear after about 1 minute.                                                                              |                    |
| 27. Instruct the installation/maintenance personnel, at node 3, to connect the new East transmit fiber (point 3ET). Refer to the example in Figure 6.                                                                                                                                                                                                                                                            |                    |
| 28. Instruct the installation/maintenance personnel, at node C, to measure the received optical power level at the new West receive fiber (point CWR). Select and install the correct optical LBO - then connect the fiber to the receiver. Verify that the incoming OC-48 alarm and flashing FAULT LED clear after about 1 minute.                                                                              |                    |
| 29. Instruct the installation/maintenance personnel, at node C, to connect the new West transmit fiber (point CWT). Refer to the example in Figure 6.                                                                                                                                                                                                                                                            |                    |
| 30. Instruct the installation/maintenance personnel, at node 3, to measure the received optical power level at the new East receive fiber (point 3ER). Select and install the correct optical LBO - then connect the fiber to the receiver. Verify that the incoming OC-48 alarm and flashing FAULT LED clear after about 1 minute.                                                                              |                    |
| 31. Allow 5 to 10 minutes for the new ring to settle. From the CIT access each of the four nodes where fibers were changed and select the CONFIGURATION-Retrieve-Map-Network command (R6.0.0 and later) to obtain the Network Map Report or select the CONFIGURATION-Retrieve-Map Ring command to obtain the Ring Map Report for release 5 and earlier. Verify that the new map contains all the expected nodes. |                    |

---

|                                                 |                           |
|-------------------------------------------------|---------------------------|
| <b>DO ITEMS BELOW IN ORDER LISTED . . . . .</b> | <b>FOR DETAILS, GO TO</b> |
|-------------------------------------------------|---------------------------|

---

**Remove the Forced Switch - MERGE the Two Rings**

---

32. From the CIT access each of the four nodes where fibers were changed and select the  
FAULT-Switch-Line:1W/1E:Reset command to reset each node. Figure 7 is an example of what the ring should look like after the Protection Switch has been removed.

 **NOTE:**  
Switching hits will occur on service traffic carried through the fibers to be reset.

- 
33. This completes the "Merge Two OC-48 Rings" procedure. Additional new services can now be added between all nodes on this new ring.
- 

Operation: NTP-009---+---DLP-501  
                  |---TAP-102

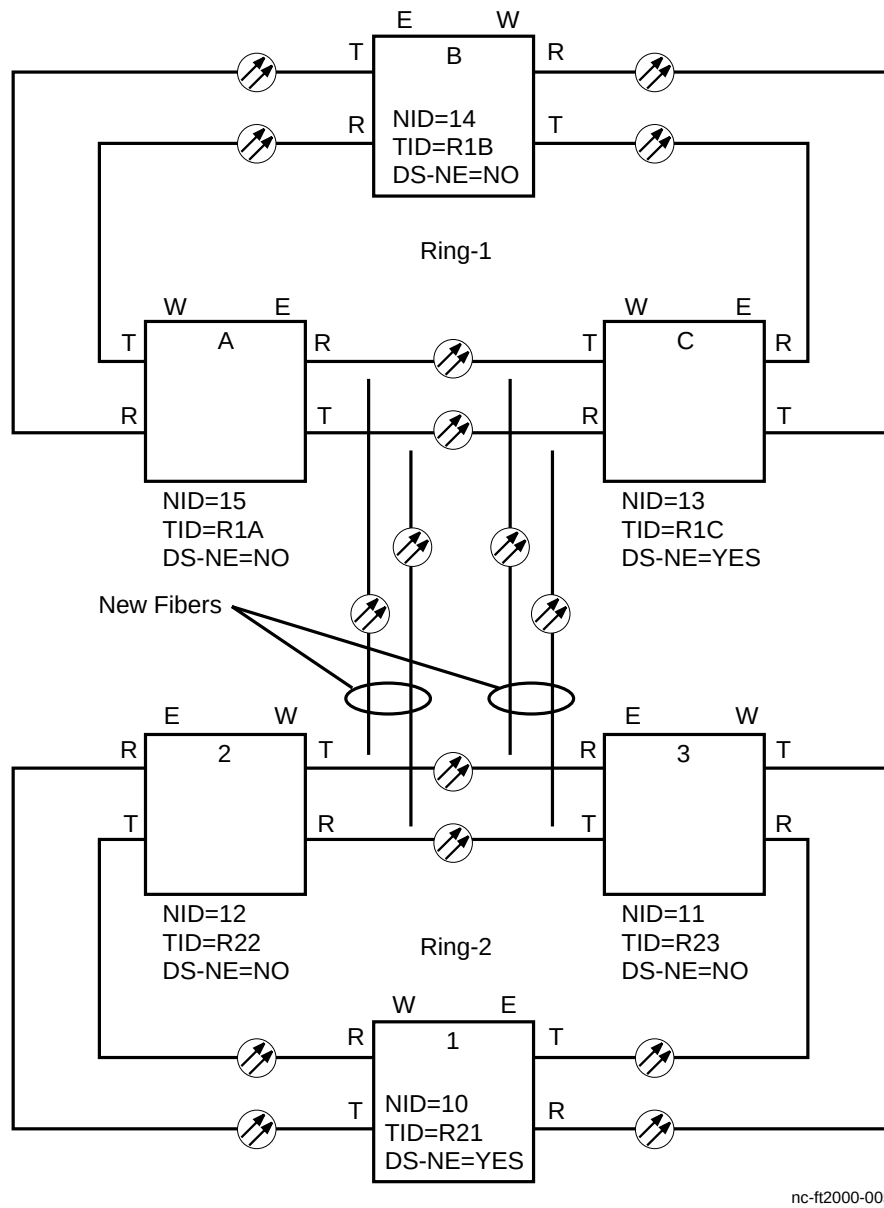


**Figure 1 – Example of Two Small Rings - Before Beginning of Merge**

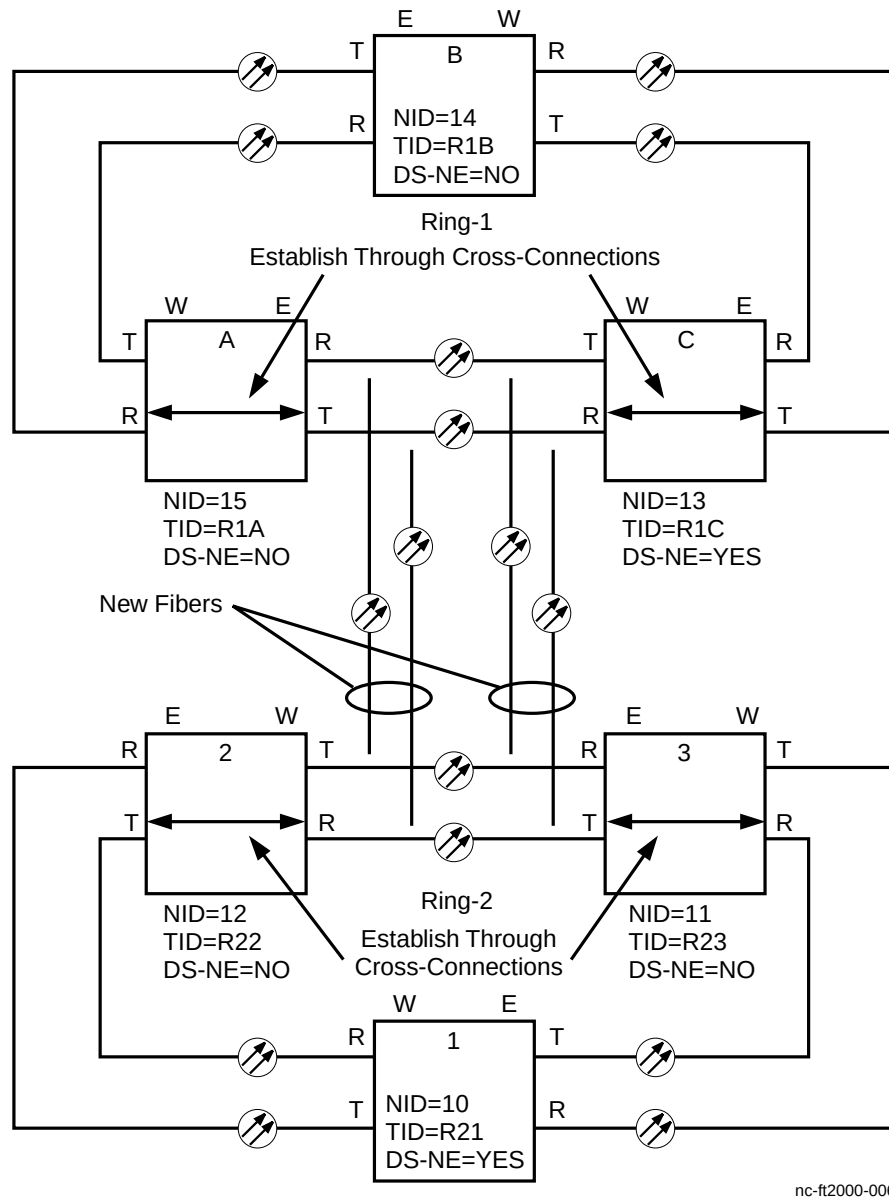


**Figure 2 – Example With Through Cross-Connections Provisioned**



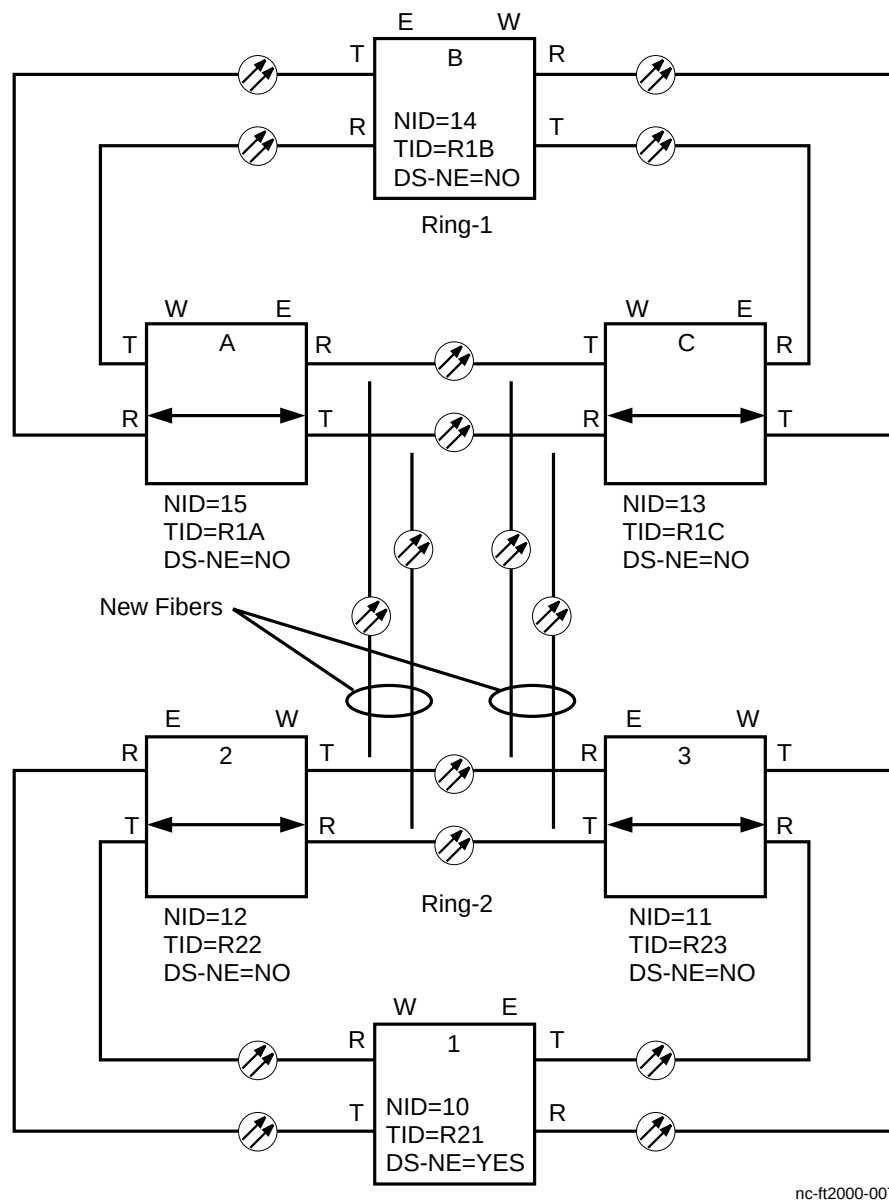


**Figure 3 – Example With DS-NE Changed From YES to NO**

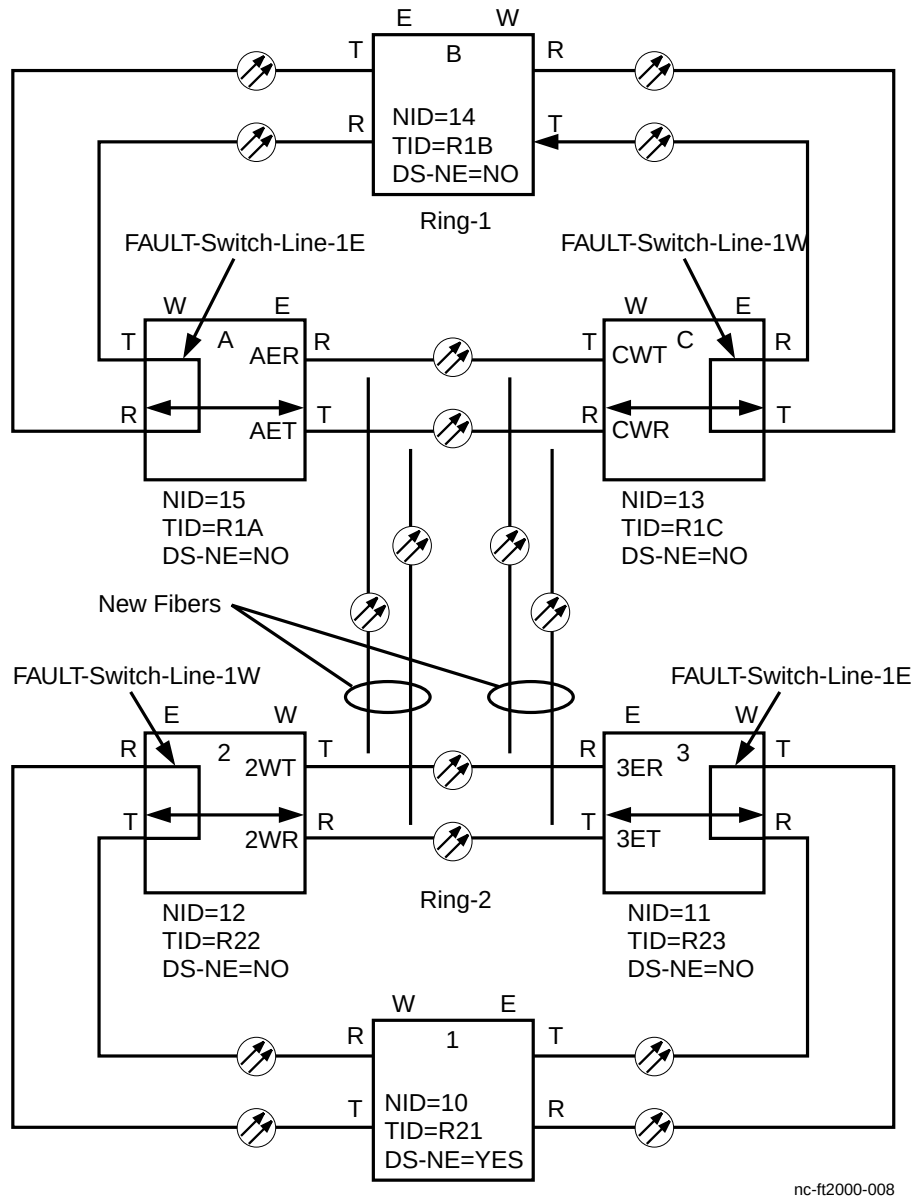


**Figure 4 – Example Depicting Four Nodes With Forced Protection Switch**

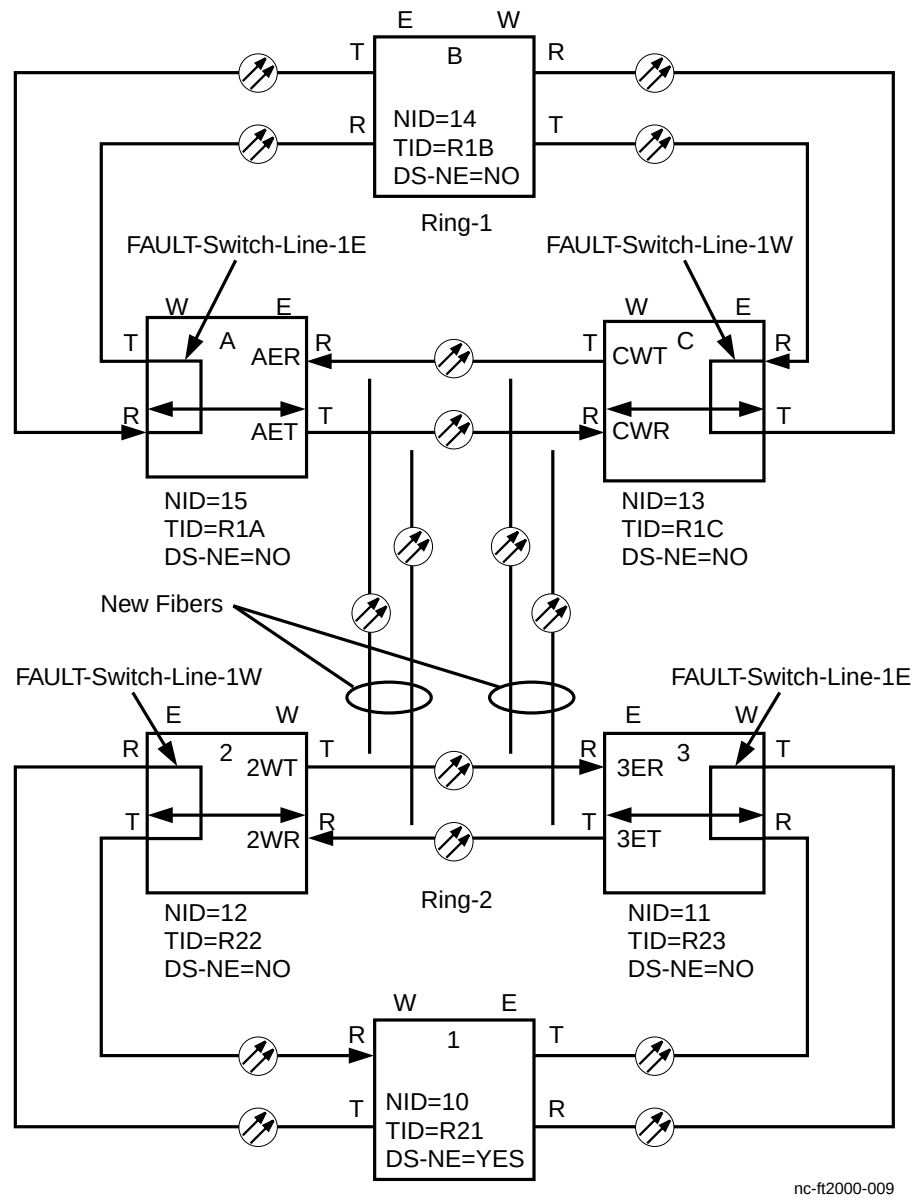


**Figure 5 – Example Depicting Rings With Existing Fibers Disconnected**

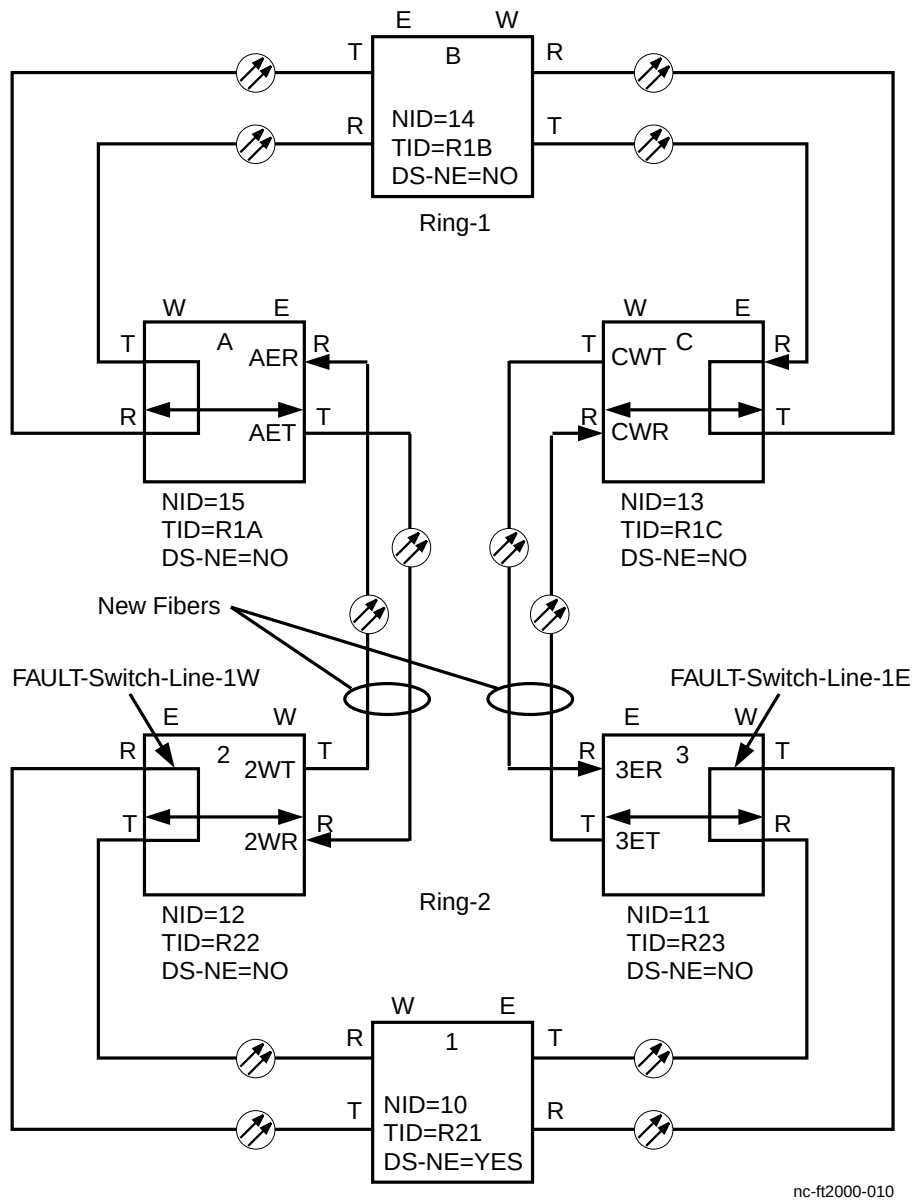


**Figure 6 – Example Depicting Ring With New Fibers Connected**

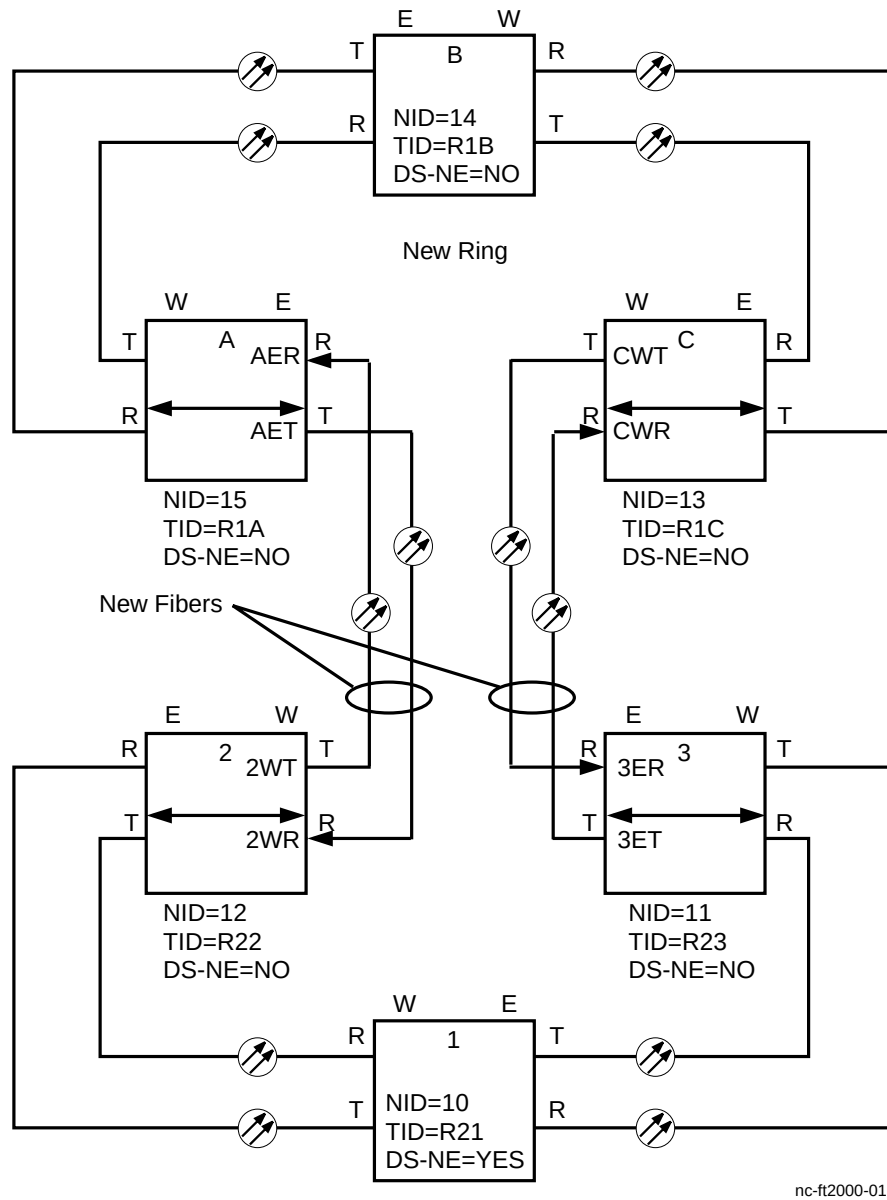


**Figure 7 – Example Depicting Ring With Protection Switch Removed**



## Optical Upgrade From STS-3 to STS-1 Granularity

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** This procedure is used for upgrading an FT-2000 OC-48 ADR ring from STS-3 granular optical packs to STS-1 granular optical packs.

⇒ **NOTE:**  
The STS-3 optical packs to be upgraded (replaced) are generally referred to as "2-type". More specifically, these packs contain one of the following designations \_\_\_B2, \_\_\_C2, or \_\_\_J2. The STS-1 optical packs are referred to as "5-type". Similarly, these packs contain **one** of the following designations \_\_\_B5, \_\_\_C5, or \_\_\_J5. Generally, it will be desirable to only replace a B2 with a B5, a C2 with a C5, etc.,.

⇒ **NOTE:**  
In order to protect all service, only two nodes in the ring can have their optical circuit packs upgraded at any one time.

⇒ **NOTE:**  
The two nodes being upgraded must have their common span "Protection Switched". This will cause a hit on services being carried on the span that is switched. Figure 1 depicts a three node segment of an FT-2000 ADR ring. For reference purposes, this figure is used to support the steps within this procedure.

---

**Required Test Equipment:**

Craft Interface Terminal (CIT)  
Wrist Strap

- 
3. If required, connect the CIT to the local FT-2000 NE and condition it. DLP-501
  4. Log in to the CIT.
-

| <b>DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO</b>                                                                                                                                                                                                                                                                                                                                                                                                                                                      |         |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|
| 5. In order to take advantage of the STS-1 related features the NE must be loaded with R7.0.1 or later software. If it is necessary to load (upgrade) the software of the ring, the recommendation is to upgrade the software at all NEs within the ring before beginning to upgrade any of the NE's optical packs. As necessary, refer to the "Software Release Description" that accompanies the particular software and to "Operation: <b>NTP-005</b> " for steps on how to copy software from one NE to another NE. | NTP-005 |
| 6. After the software upgrade is completed and the ring is running R7.x or later software, verify that there are no active alarms or status conditions by selecting the FAULT-Retrieve-Alarms (and Status):All or by pressing function key F9. Review the Active Alarms and Status Report and clear any conditions before continuing with this procedure.                                                                                                                                                               | TAP-102 |
| 7. Identify the two nodes to be upgraded and assign personnel to be at those two nodes (node A and node B).                                                                                                                                                                                                                                                                                                                                                                                                             | —       |
| 8. <b>Make sure that the Forced Switch is performed on the proper direction (E or W) at the respective two nodes.</b><br><br>From the CIT, access the NEs to be Protection Switched. For node "A" select the <b>FAULT-Switch-Line-1E:Forced</b> command.                                                                                                                                                                                                                                                                | —       |
| 9. For node "B" select the <b>FAULT-Switch-Line-1W:Forced</b> command.                                                                                                                                                                                                                                                                                                                                                                                                                                                  | —       |



---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

Switching hits will occur on service carried through the fibers that were "Forced Switched". Also, using Figure 1 as our point of discussion, you should observe the following typical alarms:  
A:ABN line-1E line forced switch at near end  
B:ABN line-1W line forced switch at near end

There may be other alarms at the various nodes during this procedure.

---

|     |                                                                                                                                                                                                                                                                                                      |   |
|-----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---|
| 10. | Instruct the personnel at node "_" (e.g., A) to replace the 1E transmitter and 1E receiver with the new "5"-type optical packs.                                                                                                                                                                      | — |
| 11. | Instruct the personnel at node "_" (e.g., B) to replace the 1W transmitter and 1W receiver with the new "5"-type optical packs.<br><br>Instruct the personnel, at the two subject nodes, to install any optical attenuators that may be required. As necessary, clean and then reconnect the fibers. | — |
| 12. | At the two subject nodes and from the CIT, enter the command <b>CONFIGURATION-Update</b> .                                                                                                                                                                                                           | — |
| 13. | Verify that the <b>FAIL LEDs</b> on the receivers at the two subject nodes stop flashing within about one minute after reconnecting the fibers.                                                                                                                                                      | — |
| 14. | At node "_" (e.g., A) and from the CIT, enter the command <b>FAULT-Switch-Line:1E:Reset</b> to reset the Forced Switch.                                                                                                                                                                              | — |
| 15. | At node "_" (e.g., B) and from the CIT, enter the command <b>FAULT-Switch-Line:1W:Reset</b> to reset the Forced Switch.                                                                                                                                                                              | — |

---

| DO ITEMS BELOW IN ORDER LISTED . . . . . |                                                                                                                                                                                                                                                                        | FOR DETAILS, GO TO |
|------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|
| 16.                                      | Verify that there are no active alarms or status conditions by selecting the FAULT-Retrieve-Alarms (and Status):All or by pressing function key <b>F9</b> . Review the Active Alarms and Status Report and clear any conditions before continuing with this procedure. | TAP-102            |
| 17.                                      | Are there additional nodes to be upgraded?<br><br>If <b>NO</b> , then go to Step 19.<br><br>If <b>YES</b> , then continue.<br><br>As necessary, assign personnel to the next two nodes to be upgraded (e.g., B and C).                                                 | —                  |
| 18.                                      | Repeat steps 7 through 17, replacing site A with B and B with C, etc,. Repeat these steps until all pairs of nodes around the ring have been completed.                                                                                                                | —                  |
| 19.                                      | Now, STS-1 granular add and drop cross-connections can be established to nodes in the ring where STS-1 granular optical packs are installed. This completes the procedure.                                                                                             | —                  |

## Connect Two FT-2000 Rings via an Optical Link

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

1. **Overview:** The steps in this procedure detail how to connect two separate FT-Rings via an OC-3/IS-3/OC-12 link.

**⇒ NOTE:**  
For discussion purposes, the two rings are referred to as ring-1 and ring-2. Figure 1 is an example that serves to illustrate ring-1 and ring-2. Both rings must be accessible via a CIT (either dial-up or direct connection). It is assumed that there are enough fibers and that all new fibers have already been tested and installed. Furthermore, it is also assumed that the Low Speed shelf in both rings already has an OC-3/IS-3/OC-12 installed. Should this assumption be incorrect—then refer to Circuit Order NTP-010 and Circuit Order NTP-012.

### Engineering Considerations:

- a. It is not necessary that the two separate FT-rings be running the same release of software.
- b. However, in order for DCC communication to functions between the two FT-rings, both rings must be running compatible OSI stack releases. Specifically, both rings must be running R6.0 to R7.2 releases, OR, both rings must be running R8.1 or later releases.
- c. If transport between the two separate rings is all that is desired (and no DCC communication for this OC-3 link is desired)—then there is no restriction that both rings be running compatible OSI stack releases.
- d. Additionally, in order for DCC communication to function between the two FT-rings, it will be necessary to administer one ring as “User” side and the other ring as “Network” side.

### Required Test Equipment:

- CIT
  - Wrist Strap
-

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

**Required Personnel:**

Have installation/maintenance personnel at both (two) nodes where new fibers are to be connected.

---

- |    |                                                                                     |                |
|----|-------------------------------------------------------------------------------------|----------------|
| 2. | If required, connect the CIT to the local FT-2000 network element and condition it. | <b>DLP-501</b> |
|----|-------------------------------------------------------------------------------------|----------------|
- 

- |    |                                                                                                                                                            |  |
|----|------------------------------------------------------------------------------------------------------------------------------------------------------------|--|
| 3. |  <b>NOTE:</b><br>The CIT user(s) must have a “privileged” security level. |  |
|----|------------------------------------------------------------------------------------------------------------------------------------------------------------|--|
- 

Log in to the CIT (on both ring-1 and ring-2) using the expert mode by entering **cit expert**.

---

- |    |                                                                                                                                                                                                                                                                                           |                |
|----|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 4. | For ring-1, verify that there are no active alarms or status conditions by selecting the <b>FAULT-Retrieve-Alarms (and Status):All</b> or by pressing function key <b>F9</b> . Review the Active Alarms and Status Report and clear any conditions before continuing with this procedure. | <b>TAP-102</b> |
|----|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
- 

- |    |                                                                                                                                                                                                                                                                                           |                |
|----|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 5. | For ring-2, verify that there are no active alarms or status conditions by selecting the <b>FAULT-Retrieve-Alarms (and Status):All</b> or by pressing function key <b>F9</b> . Review the Active Alarms and Status Report and clear any conditions before continuing with this procedure. | <b>TAP-102</b> |
|----|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
- 

|                           |                |
|---------------------------|----------------|
| <b>Connect New Fibers</b> | <b>DLP-503</b> |
|---------------------------|----------------|

---

- |    |                                                                                                                                             |   |
|----|---------------------------------------------------------------------------------------------------------------------------------------------|---|
| 6. | Instruct the installation/maintenance personnel, at ring-2 and node-2, to connect the new transmit fiber. Refer to the example in Figure 2. | — |
|----|---------------------------------------------------------------------------------------------------------------------------------------------|---|
-

| DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO                                                                                                                                                                                                                                                                                            |                |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 7. Instruct the installation/maintenance personnel, at ring-1 and node A, to measure the received optical power level. Select and install the correct optical LBO on the receiver input—then connect the fiber to the LBO. Verify that the incoming OC-3/IS-3/OC-12 alarm and flashing <b>FAULT LED</b> clear after about 1 minute.                    | <b>DLP-512</b> |
| 8. Instruct the installation/maintenance personnel, at ring-1 and node A, to connect the new transmit fiber. Refer to the example in Figure 3.                                                                                                                                                                                                         | —              |
| 9. Instruct the installation/maintenance personnel, at ring-2 and node 2, to measure the received optical power level at the new receive fiber. Select and install the correct optical LBO on the receiver—then connect the fiber to the LBO. Verify that the incoming OC-3/IS-3/OC-12 alarm and flashing <b>FAULT LED</b> clear after about 1 minute. | —              |
|  <b>NOTE:</b><br>This completes the physical connections. Figure 4 depicts what the network should now look like.                                                                                                                                                   |                |
| 10. Repeat Steps 2 through 9 for any additional low-speed links that are to be connected.                                                                                                                                                                                                                                                              | —              |
| 11. Use the CIT and log into ring-1 and node-A using the expert mode by entering <b>cit expert</b> .                                                                                                                                                                                                                                                   |                |
| 12. From the work order, identify what cross-connections are required for that particular node—and enter those cross-connections.                                                                                                                                                                                                                      | <b>DLP-536</b> |
| 13. From the work order, determine whether a DCC assignment is required for that particular node. If so, then enter the assignment.                                                                                                                                                                                                                    | <b>DLP-542</b> |

---

**DO ITEMS BELOW IN ORDER LISTED . . . . . FOR DETAILS, GO TO**

---

- |     |                                                                                                                                      |                |
|-----|--------------------------------------------------------------------------------------------------------------------------------------|----------------|
| 14. | Use the CIT and log into ring-2 and node-2 using the expert mode by entering <b>cit expert</b> .                                     | —              |
| 15. | From the work order, identify what cross-connections are required for that particular node. If so, then enter the cross-connections. | <b>DLP-536</b> |
| 16. | From the work order, determine whether a DCC assignment is required for that particular node. If so, then enter the assignment.      | <b>DLP-542</b> |
| 17. | This completes the procedure to connect two FT-rings via OC-3 links.                                                                 | —              |

Operation: NTP-011----DLP-501

|---TAP-102

|---DLP-503

|---DLP-512

|---DLP-536

|---DLP-542

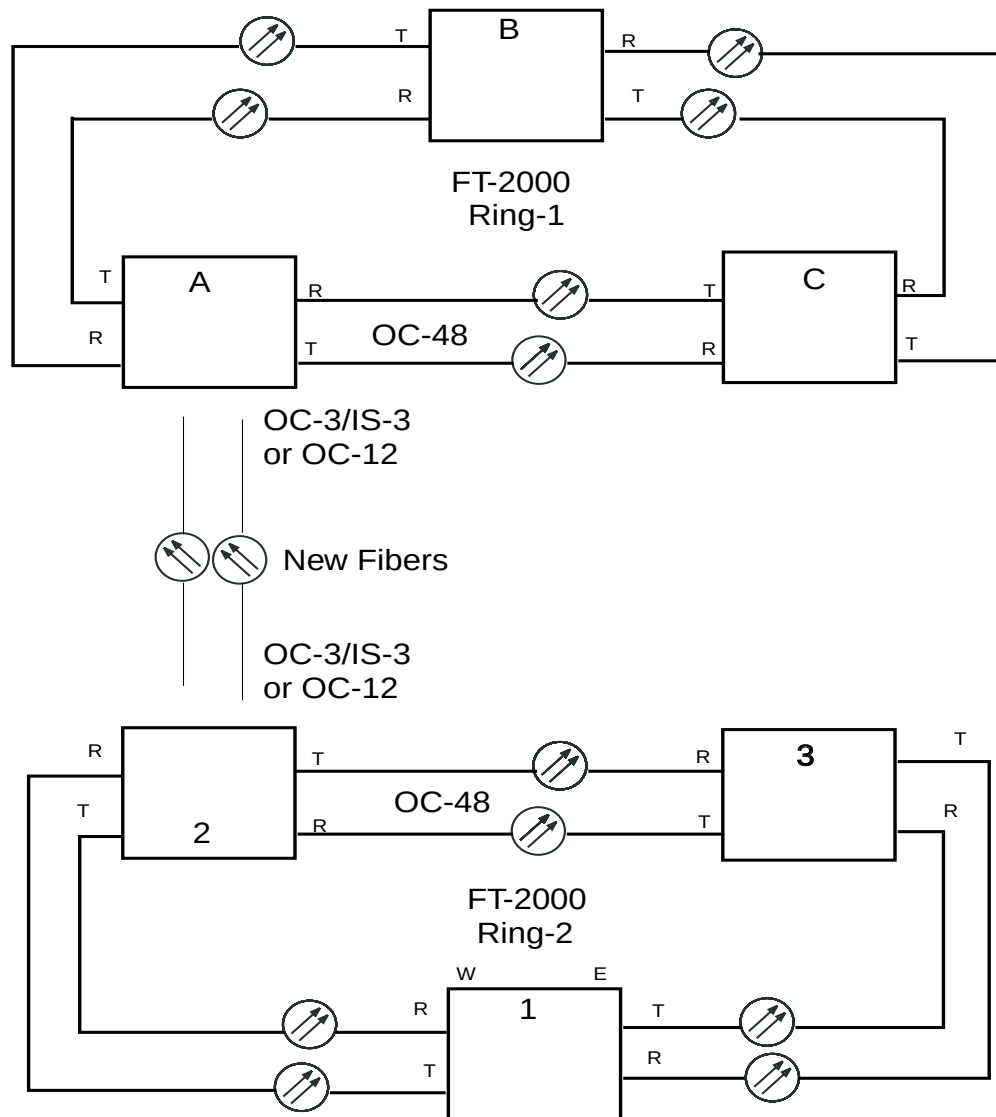


**Figure 1 – Example of Two FT-Rings—That are to be Connected via a Low-Speed Optical Link**

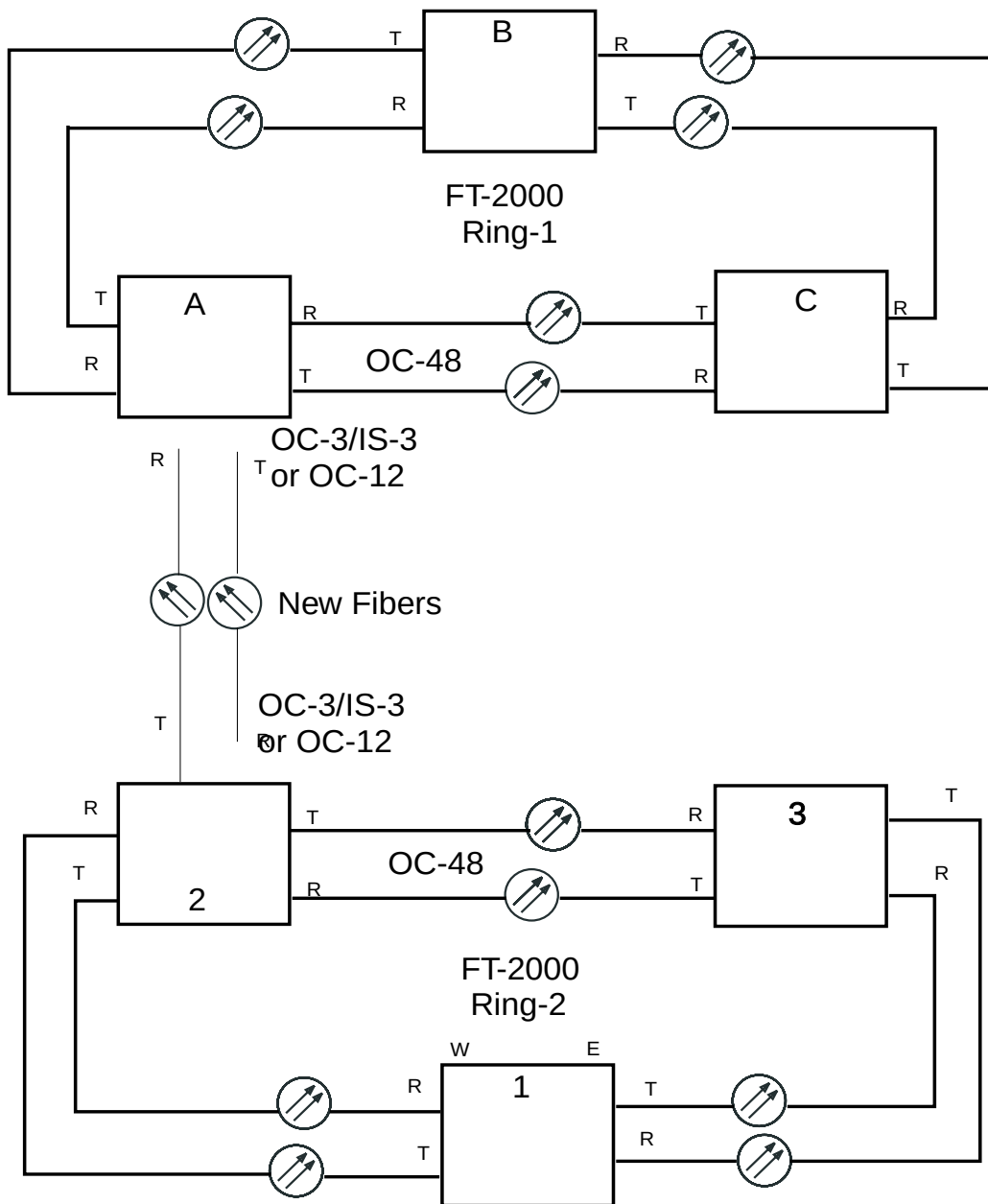


**Figure 2 – Example of Connecting First Fiber for LBO Measurement**



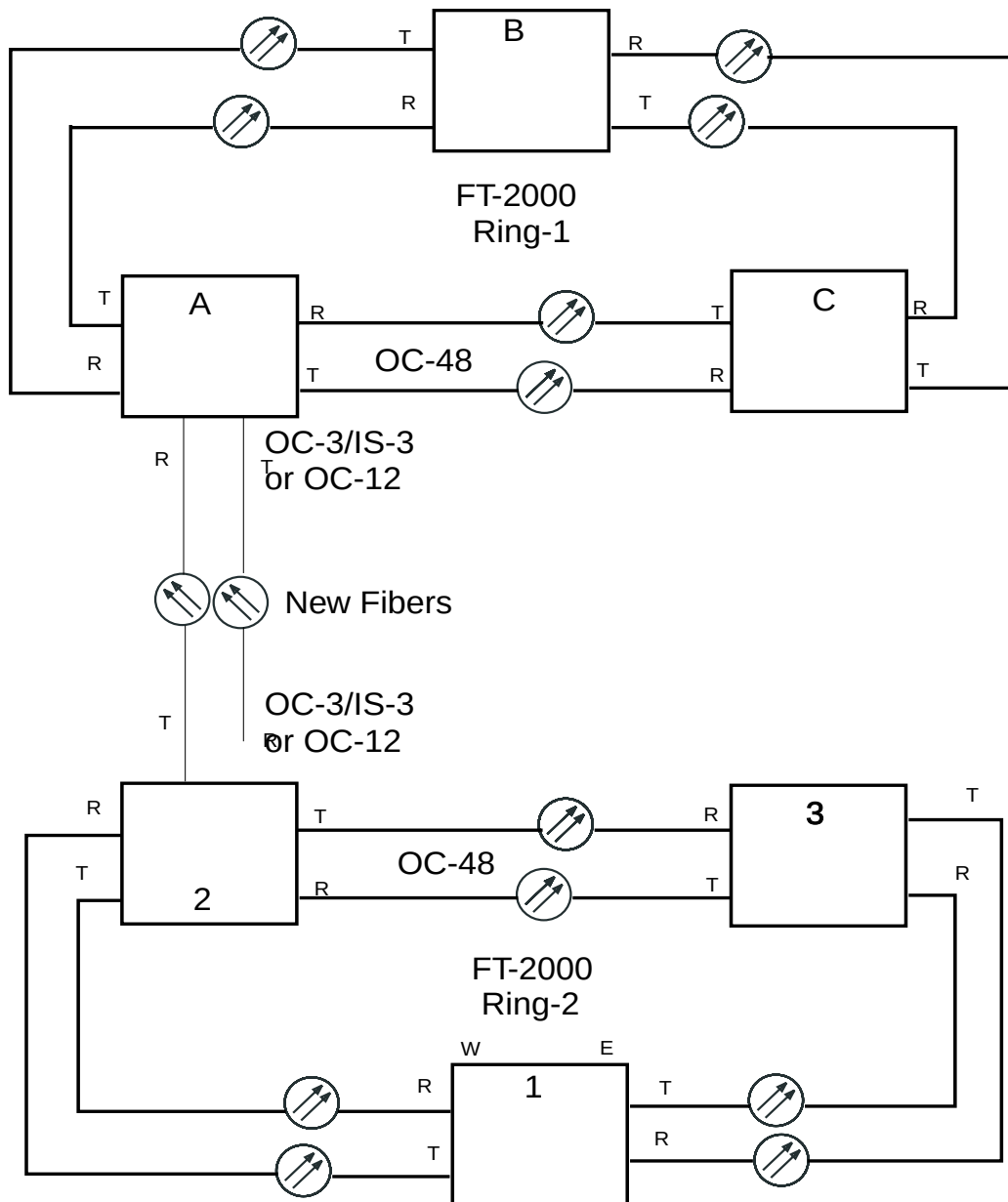


Figure 3 – Example of Connecting Second Fiber for LBO Measurement

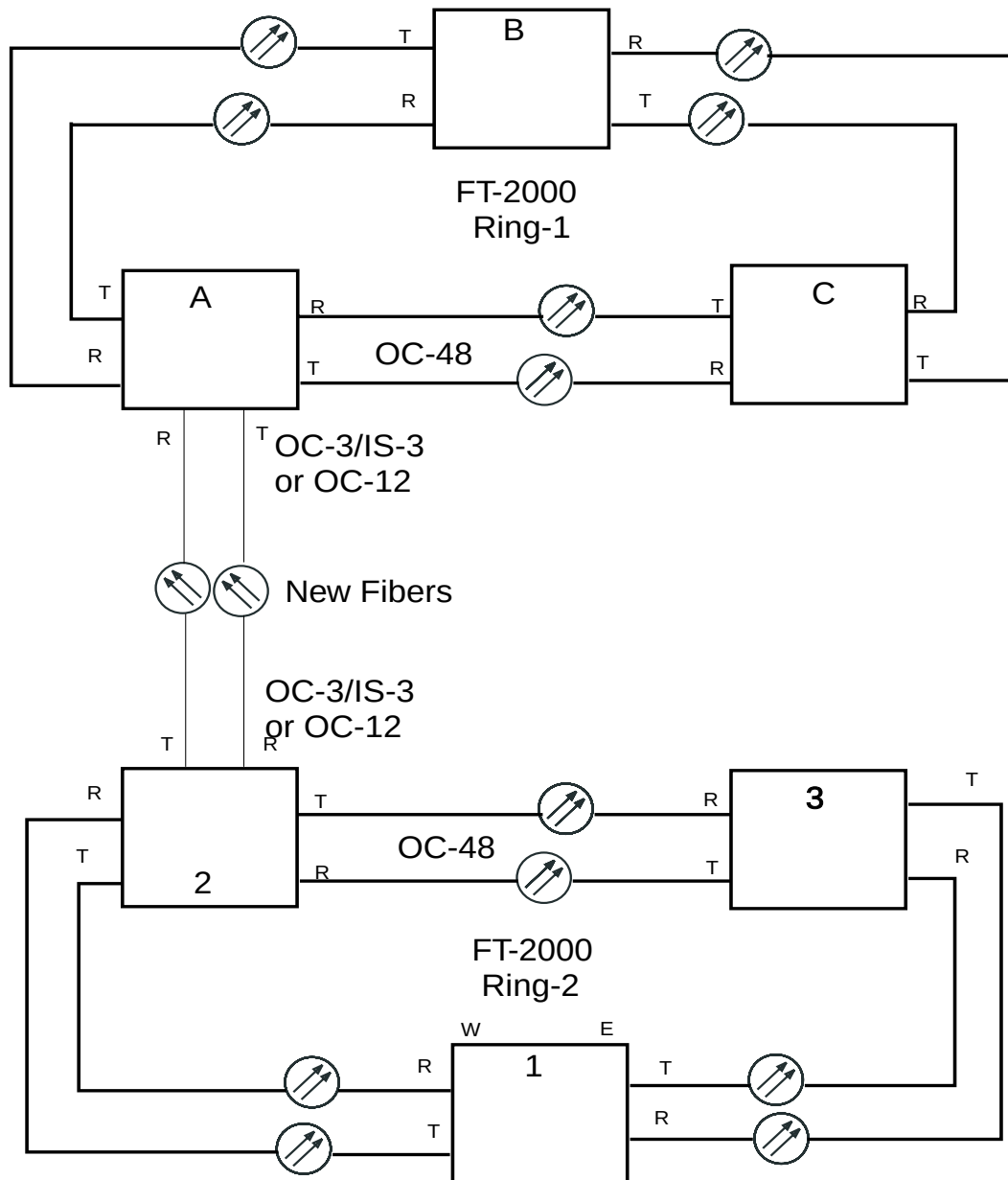


**Figure 4 – Example Depicting Two FT-Rings Fully Connected via a Low-Speed Optical Link**

## Ring Reset

⇒ **NOTE:**  
READ THIS ENTIRE PROCEDURE BEFORE BEGINNING THE RING RESET.  
CONTACT YOUR NEXT LEVEL OF SUPPORT IF ANY PART OF THIS  
PROCEDURE IS NOT CLEAR.

⚠ **CAUTION:**  
*Prior to resetting a network element in an operating ring, it is highly recommended that the status of each FT-2000 Network Element be verified as alarm free.*

⚠ **CAUTION:**  
*Perform this procedure during a maintenance window. Notify the Alarm Center of what you are about to do and ask them to notify you if they see any change in adjoining equipment. This will aid you when you lose visibility during the reset.*

⇒ **NOTE:**  
Because a node reset essentially mines for hardware failures, human intervention may be required at the site of a failure.

⇒ **NOTE:**  
For Release 7.2 and earlier, this procedure is recommended to be performed at the DSNE, subsequently referred to as the "Local Node". For TARP software releases, any node can be used as the local node.

⇒ **NOTE:**  
It is recommended that spare circuit packs be secured (and are available) with respect to guidelines provided in the sparing section of the FT-2000 OC-48 Applications, Planning, and Ordering Guide (APOG).

⇒ **NOTE:**  
It is recommended that a spare SYSMEM be loaded with the current operating generic and, if possible, with current provisioning data.

⇒ **NOTE:**  
A spare SYSCTL should be immediately available.

1. Each node's current provisioning data should be immediately available. Save this information on your hard drive as follows:

Press F3 to activate the CIT program to the OUTPUT MODE. Press the F7 function key labeled "Print". Select Output to File. Select ON (the "?" key activates the ON/OFF menu). Then type in the path to identify where to put the file [example: c:\cit(bay ID).out]. Press F7 to exit the menu.

This will allow you to save all the retrieved data for that bay to a text file on your hard drive. Be sure to remote to all nodes and enter a unique file name for each bay for which data will be saved. Retrieve the following data at each node:

- CONFIGURATION-Retrieve-Crossconnection-All
- CONFIGURATION-Retrieve-Equipment-All
- CONFIGURATION-Retrieve-Map-Ring
- CONFIGURATION-Retrieve-Synchronization
- CONFIGURATION-Retrieve-OC3
- CONFIGURATION-Retrieve-OC12
- SECURITY-Retrieve-Network Element
- SECURITY-Retrieve-Security-Port-All

2. Retire all existing alarms in the ring.
3. At the CIT, perform the CONFIGURATION-Retrieve-Map-Ring command.

Print (or write down) the TIDs in the order in which they appear in the map report. The first entry in the report is the "Local Source Node" where your CIT is connected.

4. Beginning with the first node listed below the "Local Node" on the list, perform a remote login by pressing the F2 key. Select from the pull-down menu the remote node you want.
5. When you are logged in to the remote node, execute the FAULT-Reset command and type a "y". Hit the <ENTER> or RETURN key to reset the remote node. The reset process at the remote node will terminate the remote login session after a minute or two.



**NOTE:**

The reset for each node will take approximately 10 to 15 minutes to complete, but it is NOT necessary to wait for a node to finish resetting before continuing to the next node on the ring map list.

6. Immediately continue the reset for the remaining nodes to the EAST on the ring map list. Be sure to proceed in the order (top to bottom) in which the TIDs appear on the list.
7. After the RESET command has been executed at all the remote nodes in your network, perform the RESET command at the "Local Node."
8. After the "Local Node" reset is completed, log in and execute the CONFIGURATION-Retrieve-Map-Ring command to verify that the current ring map looks identical to the map recorded at the beginning of this procedure. Also verify that the data saved in Step 1 is still complete.

"Squelch Map Inconsistent" and "Circuit Provisioning Error" alarms can be cleared by comparing the "Current" cross-connections with the "Before" cross-connections. Any provisioning errors can be corrected by using the CONFIGURATION-Enter-Cross Connection command. If the "Circuit Provisioning Error" alarm persists, you may clear it with a second reset. If alarms continue in the ring, refer to the Trouble Clearing Section of the User/Service Manual, TAP-102.

Notify the Alarm Center that your activities are completed.

9. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Trouble Clearing Task Index

---

### **FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                                                                                                                                                                                |         |
|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|
| Address 'CP Reset in Progress' . . . . .                                                                                                                                                                                       | TAP-151 |
| Address 'Extra Traffic Preempted'. . . . .                                                                                                                                                                                     | TAP-143 |
| Address 'Inc. (from OC3) DCC Failure' or Address 'Inc. (from OC12)<br>DCC Failure' . . . . .                                                                                                                                   | TAP-169 |
| Address 'Incoming (From OC-48) DS3 AIS' . . . . .                                                                                                                                                                              | TAP-109 |
| Address 'Line FERF' or 'Incoming OC-48 Line RFI/Yellow' . . . . .                                                                                                                                                              | TAP-121 |
| Address 'Path AIS', 'Path Yellow', 'Incoming (from OC-48)<br>DS3 LOF', 'Inc(from STSX) Line RFI/Yellow', 'Inc(from OC48)<br>Path RFI/Yellow', 'Incoming OC3 Line RFI/Yellow', and 'Incoming<br>OC12 Line RFI/Yellow' . . . . . | TAP-122 |
| Address 'Reset in Progress' . . . . .                                                                                                                                                                                          | TAP-124 |
| Address 'TG Warming Up'. . . . .                                                                                                                                                                                               | TAP-154 |
| Address 'TID Address Map Full' . . . . .                                                                                                                                                                                       | TAP-170 |
| Address 'TL-1 Link Failure' . . . . .                                                                                                                                                                                          | TAP-106 |
| Address 'Traffic Squelched'. . . . .                                                                                                                                                                                           | TAP-146 |
| Address an Unexplained Loss of Service . . . . .                                                                                                                                                                               | TAP-166 |
| Address Condition at Remote Repeater Bay/Shelf . . . . .                                                                                                                                                                       | TAP-147 |
| Address Environmental Input Condition . . . . .                                                                                                                                                                                | TAP-107 |
| Address Incoming Signal Failure . . . . .                                                                                                                                                                                      | TAP-110 |
| Address Missing or Incorrect Response . . . . .                                                                                                                                                                                | TAP-108 |
| Clear (Optical Parameter) 'Out-of-Range'. . . . .                                                                                                                                                                              | TAP-135 |
| Clear 'APS Channel Failure' . . . . .                                                                                                                                                                                          | TAP-105 |
| Clear 'APS Provisioning Conflict' . . . . .                                                                                                                                                                                    | TAP-150 |
| Clear 'Circuit Audit Suspended' . . . . .                                                                                                                                                                                      | TAP-161 |
| Clear 'Circuit Pack Failure' . . . . .                                                                                                                                                                                         | TAP-111 |
| Clear 'Circuit Pack Removed' . . . . .                                                                                                                                                                                         | TAP-112 |
| Clear 'Circuit Provisioning Error' . . . . .                                                                                                                                                                                   | TAP-162 |

---

**FIND YOUR JOB IN THE LIST BELOW. . . . . THEN GO TO**

---

|                                                                               |         |
|-------------------------------------------------------------------------------|---------|
| Clear 'Clock Out of Range' . . . . .                                          | TAP-134 |
| Clear 'CP (Unknown Type) Failure' . . . . .                                   | TAP-123 |
| Clear 'CP (Unknown Type) Removed' . . . . .                                   | TAP-148 |
| Clear 'CPYPGM:IP (tid)' . . . . .                                             | TAP-159 |
| Clear 'DCC Startup in Progress' . . . . .                                     | TAP-129 |
| Clear 'DS3 Path Unequipped' . . . . .                                         | TAP-156 |
| Clear 'DS-NE Not Reachable' . . . . .                                         | TAP-137 |
| Clear 'Duplicate Node ID Defined' or 'Duplicate TID Defined' . . . . .        | TAP-138 |
| Clear 'Excessive Holdover' . . . . .                                          | TAP-117 |
| Clear 'Fan Failure' . . . . .                                                 | TAP-118 |
| Clear 'FT-LBA (Condition)' . . . . .                                          | TAP-157 |
| Clear 'Fuse/Power Failure' . . . . .                                          | TAP-119 |
| Clear 'Inc. (From OC48) Sync Ref Fail' . . . . .                              | TAP-152 |
| Clear 'Incoming (From OC-48) STS LOP' . . . . .                               | TAP-174 |
| Clear 'Incoming DCC Failure' or 'Inc. (From OC48) DCC Failure' . . . . .      | TAP-120 |
| Clear 'Incoming OC3 APS Byte Fail' or 'Incoming OC12 APS Byte Fail' . . . . . | TAP-160 |
| Clear 'Incoming OC-48 APS Byte Fail' . . . . .                                | TAP-132 |
| Clear 'Incoming OC48 APS Default Byte' . . . . .                              | TAP-168 |
| Clear 'Incoming OC-48 Invalid F Byte' . . . . .                               | TAP-145 |
| Clear 'LCLAN Failure' . . . . .                                               | TAP-175 |
| Clear 'Multiple DS-NEs Defined' . . . . .                                     | TAP-139 |
| Clear 'NE Status Comm Failure' . . . . .                                      | TAP-172 |
| Clear 'Node ID Conflict' . . . . .                                            | TAP-140 |
| Clear 'OC3/OC12 Mode Conflict (Equipment, Slot Pair, X-Connect)' . . . . .    | TAP-158 |
| Clear 'Path Integrity Failure' . . . . .                                      | TAP-163 |
| Clear 'Path TCA', 'Section TCA', or 'Line TCA' . . . . .                      | TAP-144 |
| Clear 'Ring Incomplete/Open' . . . . .                                        | TAP-141 |
| Clear 'Ring Startup in Progress' . . . . .                                    | TAP-142 |
| Clear 'Site Provisioning Conflict' . . . . .                                  | TAP-130 |



---

**FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                                                                                  |         |
|----------------------------------------------------------------------------------------------------------------------------------|---------|
| Clear 'Software Download in Progress' . . . . .                                                                                  | TAP-131 |
| Clear 'STS1 Squelch Map Conflict' . . . . .                                                                                      | TAP-164 |
| Clear 'STS3 Squelch Map Conflict', 'STS1 Squelch Map Conflict',<br>or 'Squelch Map Conflict' . . . . .                           | TAP-153 |
| Clear 'STS3 Squelch Map Inconsistent' or 'STS1 Squelch<br>Map Inconsistent' . . . . .                                            | TAP-165 |
| Clear 'SYSMEM Failure' or 'SYSCTL Failure' . . . . .                                                                             | TAP-113 |
| Clear 'SYSMEM Removed' . . . . .                                                                                                 | TAP-133 |
| Clear 'SYSMEM Unrecognizable Code' . . . . .                                                                                     | TAP-125 |
| Clear 'SYSMEM/SYSCTL Code Mismatch' . . . . .                                                                                    | TAP-126 |
| Clear 'Unexpected CP Type' . . . . .                                                                                             | TAP-127 |
| Clear 'X-Conn Provisioned But Not Est' . . . . .                                                                                 | TAP-173 |
| Clear Alarm When 'NE ACTY' and 'FE ACTY' LEDs Are Off<br>(Cannot Log in to a Network Element) . . . . .                          | TAP-136 |
| Clear Automatic Switch Condition . . . . .                                                                                       | TAP-115 |
| Clear DRI Switch . . . . .                                                                                                       | TAP-149 |
| Clear Local Trouble When 'USE CIT' LED is Lighted . . . . .                                                                      | TAP-102 |
| Clear Local Trouble When 'USE CIT' LED is Off . . . . .                                                                          | TAP-101 |
| Clear NE ACTY and/or FE ACTY (Regardless of Alarm Level) . . . . .                                                               | TAP-103 |
| Clear Potential Loss of Service Due to Double Failure . . . . .                                                                  | TAP-167 |
| Clear Suspected Failure in FT-2000 Network Element(s) Via<br>Remote Modem (or Datakit <sup>®</sup> Network) Connection . . . . . | TAP-155 |
| Clear Trouble in CIT (CIT Does Not Respond to Commands) . . . . .                                                                | TAP-128 |
| Clear Trouble Report . . . . .                                                                                                   | TAP-104 |
| Clear Trouble When 'ABN' LED Is Lighted . . . . .                                                                                | TAP-114 |
| Clear Unsuccessful Circuit Pack Replacement . . . . .                                                                            | TAP-116 |
| Maintenance Philosophy . . . . .                                                                                                 | TAD-100 |
| Restore Network Element Operation After Power Loss . . . . .                                                                     | TAP-171 |



## Maintenance Philosophy

**Overview:** The maintenance philosophy is built on gathering FT-2000 OC-48 Lightwave System information at two levels. The first maintenance level is a local visual inspection of the user panel (and the circuit pack LEDs when the cover is removed.) Most internal hardware problems can be detected and corrected at this level. The second maintenance level requires a craft interface terminal (CIT) to obtain detailed information on the FT-2000 network elements. The CIT retrieves detailed reports about performance monitoring, alarms and status, and configurations for the local and remote FT-2000 network elements. Performance monitoring is not accessible at the FT-2000 repeater bay/shelf.

### Trouble Analysis Procedures

The trouble analysis procedures in this document involve replacing faulty circuit packs and obtaining performance reports and/or alarm and status reports for analysis to determine the status of the FT-2000 System. If a trouble cannot be corrected, the procedures direct the technician to obtain help. This means that the trouble is of a nature that requires local maintenance engineering or a higher level technical assistance.

### RTAC Assistance

The technician must make the decision regarding trouble analysis, corrective action, and obtaining assistance. The technician and the local technical support staff may choose to continue trouble analysis based on their knowledge or experience on the FT-2000 System, or they may elect to obtain assistance by calling the Regional Technical Assistance Center **1-800-225-RTAC** in accordance with local procedures.

### CARES Assistance

The CTS organization provides an online system, the Customer Assistance Request Entry System (CARES), to track all customer assistance requests. The CTS CARES communicates details about assistance requests, product bulletins, and other critical information to customer sites. To access this site, customers will need a Customer Web Access login. To inquire about obtaining a CTS CARES Customer Web Access login, please visit us at <http://www.lucent.com/support>.

### Diagnostic Dictionary

The Diagnostic Dictionary tool gives the user access to previously detected symptoms, problems, temporary fixes, cautions, and solutions. These may be usable in diagnosing, correcting, or avoiding an FT-2000 System problem.

### **Compatibility Data**

The Compatibility Data tool gives the user access to hardware configuration data that is compatible to a user-specified software generic.

### **News and Bulletin**

The News and Bulletin tool allows the user to process News and Bulletin information. News consists of messages that may be of general interest to the user. Bulletins contain information of a more urgent nature.

### **Circuit Pack Failures**

Circuit pack failures are identified by LEDs and reported by FT-2000 network alarms and reports.

Circuit packs are replaced in a specified sequence, but only one circuit pack is replaced at a time. If trouble is not cleared after replacing a circuit pack, the original circuit pack should be reinstalled. This reduces the chances of returning nondefective circuit packs for repair.

### **Electrostatic Discharge (ESD) Considerations**

Any integrated circuit on a circuit pack can be damaged by static electricity that builds up within a work area, particularly in areas with low relative humidity. This static buildup on work surfaces and on personnel and their clothing is produced by the various charging effects of even simple movements and by contact between various objects.

As a rule, the greatest potential for electrostatic damage occurs in areas with the lowest relative humidity. But, because such damage can occur anywhere, all personnel handling circuit packs should take the following precautions:

1. Since materials such as food wrappers, plastics, and Styrofoam containers tend to generate static electricity, keep them away from all circuit packs.
2. Be sure to read all warning labels on bags and cartons before opening any packaging.
3. If possible, open all circuit packs at a static-safe work position using properly grounded wrist straps and table mats that can dissipate static electricity.
4. Whenever possible, wait to remove circuit packs from their protective antistatic packaging until it is time to insert them into a shelf.
5. Never touch a circuit pack's components, conductors, or connector pins. Handle all circuit packs only by the faceplate or latch or by the top and bottom outermost edges.

6. When handling circuit packs (storing, installing, removing, etc.) or when working on backplanes, always wear a grounded wrist strap or wear a heel strap and stand on a grounded, static-dissipating floor mat.
7. Always store and transport circuit packs in static-safe packages. (Shielding is not required unless specified.)
8. When removing a circuit pack from the shelf, immediately put it into a static-safe package.
9. Try to keep relative humidity above 20 percent.
10. FT-2000 network elements are equipped with grounding jacks for connecting the static ground wrist strap. The jacks are located on the user panel and on the filter panel.
11. Keep the electromagnetic interference (EMI)/ESD protective front shelf covers closed at all times. Close the cover immediately after a maintenance procedure such as replacing a circuit pack.



## Clear Local Trouble When 'USE CIT' LED Is Off

**Overview:** This procedure is used to replace a circuit pack with a lighted **FAULT** LED at the local FT-2000 network element. Alternatively, the craft interface terminal (CIT) may be used in clearing the trouble.



**CAUTION:**

*Do not remove the SYSCTL circuit pack even if its **FAULT** LED is lighted because the provisioning data (current values) could be lost. Current values were provisioned with the **Set** command.*

1. Are you using the CIT to clear trouble?  
If **NO**, then continue with Step 2.  
If **YES**, then go to **TAP-102**.
2. Momentarily depress and release the **ACO/LOCTR** pushbutton on the user panel to silence any audible alarms.
3. Does the **LINE LOCTR** display in the user panel show a line (**1** or **P**) or a shelf (**H** or **L**)?  
If **LINE (1 or P)**, then continue with Step 4.  
If **SHELF (H or L)**, then continue with Step 5.
4. Depress and hold the **ACO/LOCTR** pushbutton. The **LINE LOCTR** display will indicate the shelf with the failed circuit pack. The shelf is associated with an OC-48 line and its direction is visually indicated by a lighted **E** (East) or **W** (West) LED.



**NOTE:**

An **H** indicates a High Speed Shelf or Enhanced High Speed Shelf at the bottom of the bay. An **L** indicates a Low Speed Shelf - System Controller or Low Speed Shelf - Complementary in the middle of the bay.

5. Remove the cover on the shelf just identified.  
Reference: **DLP-511**
6. Is one and only one **FAULT** LED continuously lighted?  
If **YES**, then continue with Step 7.  
If **NO**, then go to **TAP-108**.
7. Is the **FAULT** LED lighted on either the SYSCTL or SYSMEM circuit pack?

If **YES**, then go to **TAP-102**.

If **NO**, then continue with Step 8.



**NOTE:**

A series number **S1:2** is the same series number as **S1:4**. The 2 and 4 refer to minor changes within series 1.

8. Obtain a replacement circuit pack with the same or a higher series number and install it in place of the circuit pack with the lighted **FAULT** LED. As required, make the appropriate optical connections on the OC-3, IS-3, OC-12, RCVR, TRMTR, or REGENR circuit pack(s).

Reference: **DLP-514**

9. Wait for the time indicated in Table A:

**Table A - Mini Reset Waiting Times**

| <b>CP Inserted</b> | <b>Waiting Time</b> |
|--------------------|---------------------|
| LNCTL *            | 10 minutes          |
| OHCTL              | 5 minutes           |
| TG3 †              | 5 minutes           |
| TOHCTL             | 5 minutes           |
| All Others         | 35 seconds          |

\*Inserting an LNCTL circuit pack starts a mini reset that can take up to 10 minutes to complete. During this time, LEDs on other circuit packs will be lighted due to software provisioning.

† When a TG3 circuit pack is first inserted, it will be switched out of service until it warms up. This can take as long as 5 minutes.

10. Is the **FAULT** LED off on the circuit pack just replaced?

If **YES**, then continue with Step 11.

If **NO**, then go to **TAP-116**.

11. Replace the shelf cover.

Reference: **DLP-511**

12. Are all LEDs off at the user panel except the **PWR ON** LED?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then go to **Trouble Clearing: IXL-001**.



## Clear Local Trouble When 'USE CIT' LED Is Lighted

**Overview:** This procedure is used to clear the conditions listed in the Active Alarms and Status Report in the order listed, from top to bottom, regardless of the indicated alarm severity, date, or time. The craft interface terminal (CIT) is required for this procedure.

1. Is the CIT conditioned and connected to the FT-2000 network element?  
If **YES**, then continue with Step 2.  
If **NO**, then complete **DLP-501** and return to this procedure.
2. At the CIT, select FAULT-Retrieve-Alarm (and Status):All to obtain an Active Alarms and Status Report.

⇒ **NOTE:**  
The report shown below is an example that shows several entries. There may be only one entry in the actual report.

3. Record the information in the Source Address and the Description columns for the topmost entry in the Active Alarms and Status Report.

```
Lucent Technologies FT-2000 96-11-19 23:15:15 EST
M Retrieve-Alarm:All: COMPLD
/* Active Alarms and Status Report
=====
Alarm Source Date Time Srv Description
Level Address Detected
=====
CR ls-1-1a 05-19 23:15:41 SA DS3 INTFC removed

ABN tg-1-2 05-19 23:15:42 nsa sync reference manual switch
ABN tg-1-2 05-19 23:15:42 nsa sync reference lockout

NE ACTY line-1 05-19 23:15:42 nsa line auto switch
NE ACTY line-1-p1 05-19 23:15:42 nsa path AIS
*/
```

4. Was "env - (1-16)", the Source Address recorded in Step 3?

If **YES**, then go to **TAP-107**.

If **NO**, then continue with Step 5.

5. Find the Description information that was recorded in Step 3 in Table A and go to the referenced TAP.

**Table A - Trouble Conditions**

| <b>Description</b>         | <b>Go To TAP</b> |
|----------------------------|------------------|
| APS channel failure        | TAP-105          |
| APS provisioning conflict  | TAP-150          |
| circuit audit suspended    | TAP-161          |
| circuit provisioning error | TAP-162          |
| clock out of range         | TAP-134          |
| CP reset in progress       | TAP-151          |
| CP (unknown type ) failure | TAP-123          |
| CP (unknown type ) removed | TAP-148          |
| CPYPGM:IP tid              | TAP-159          |
| DCC startup in progress    | TAP-129          |
| DRI auto switch            | TAP-149          |
| DRI manual switch          | TAP-149          |
| DS3 INTFC failure          | TAP-111          |
| DS3 INTFC removed          | TAP-112          |
| DS3 loopback               | TAP-114          |
| DS3 path unequipped        | TAP-156          |
| DS-NE not reachable        | TAP-137          |
| duplicate node id defined  | TAP-138          |
| duplicate TID defined      | TAP-138          |
| EC1 loopback               | TAP-114          |
| excessive holdover         | TAP-117          |
| extra traffic preempted    | TAP-143          |
| fan failure                | TAP-118          |

*Continued on next page*

**Table A - Trouble Conditions (Contd)**

| <b>Description</b>               | <b>Go To TAP</b> |
|----------------------------------|------------------|
| FT-LBA fan failure               | TAP-157          |
| FT-LBA fuse/power failure        | TAP-157          |
| FT-LBA 1EW failure               | TAP-157          |
| FT-LBA 1WE failure               | TAP-157          |
| fuse/power failure (A,B)         | TAP-119          |
| inc. (from DS1) sync ref fail    | TAP-110          |
| inc. (from OC3) DCC failure      | TAP-169          |
| inc. (from OC12) DCC failure     | TAP-169          |
| inc. (from OC48) DCC failure     | TAP-120          |
| inc. (from OC48) path RFI/yellow | TAP-122          |
| inc. (from OC48) sync ref fail   | TAP-152          |
| inc. (from STSX) line RFI/yellow | TAP-122          |
| incoming DCC failure             | TAP-120          |
| incoming (from DSX) DS3 LOS      | TAP-110          |
| incoming (from DSX) DS3 SD       | TAP-110          |
| incoming (from OC3) STS LOP      | TAP-110          |
| incoming (from OC12) STS LOP     | TAP-110          |
| incoming OC3 AIS                 | TAP-110          |
| incoming OC12 AIS                | TAP-110          |
| incoming OC3 APS byte fail       | TAP-160          |
| incoming OC12 APS byte fail      | TAP-160          |
| incoming OC3 LOF                 | TAP-110          |
| incoming OC3 LOS                 | TAP-110          |
| incoming OC3 SD                  | TAP-110          |
| incoming OC3 SF                  | TAP-110          |
| incoming OC3 line RFI/yellow     | TAP-122          |

*Continued on next page*

**Table A - Trouble Conditions (Contd)**

| <b>Description</b>              | <b>Go To TAP</b> |
|---------------------------------|------------------|
| incoming OC12 LOS               | TAP-110          |
| incoming (from OC-48) DS3 AIS   | TAP-109          |
| incoming (from OC-48) DS3 LOF   | TAP-122          |
| incoming (from OC48) path AIS   | TAP-110          |
| incoming (from OC48) STS LOP    | TAP-174          |
| incoming (from STSX1) EC1 LOF   | TAP-110          |
| incoming (from STSX1) EC1 LOP   | TAP-110          |
| incoming (from STSX1) EC1 LOS   | TAP-110          |
| incoming (from STSX1) EC1 SD    | TAP-110          |
| incoming (from STSX1) line AIS  | TAP-110          |
| incoming OC-48 AIS              | TAP-110          |
| incoming OC-48 APS byte fail    | TAP-132          |
| incoming OC-48 APS default byte | TAP-168          |
| incoming OC48 B1 parity error   | TAP-147          |
| incoming OC-48 invalid F byte   | TAP-145          |
| incoming OC-48 line RFI/yellow  | TAP-121          |
| incoming OC-48 LOF              | TAP-110          |
| incoming OC-48 LOS              | TAP-110          |
| incoming OC-48 LOS/LOF          | TAP-147          |
| incoming OC-48 SD               | TAP-110          |
| incoming OC-48 SF               | TAP-110          |
| incoming sync ref failure       | TAP-110          |
| inhibit alarms-office alarms    | TAP-114          |
| inhibit alarms-parallel telem   | TAP-114          |
| inhibit alarms-X.25             | TAP-114          |
| laser bias current-out-of-range | TAP-135          |

*Continued on next page*

**Table A - Trouble Conditions (Contd)**

| <b>Description</b>             | <b>Go To TAP</b> |
|--------------------------------|------------------|
| line auto switch               | TAP-115          |
| line FERF                      | TAP-121          |
| line forced switch             | TAP-114          |
| line forced switch at far end  | TAP-114          |
| line forced switch at near end | TAP-114          |
| line lockout                   | TAP-114          |
| line lockout at far end        | TAP-114          |
| line lockout at near end       | TAP-114          |
| line manual switch             | TAP-114          |
| line manual switch at far end  | TAP-114          |
| line manual switch at near end | TAP-114          |
| line switch inhibit            | TAP-114          |
| line switch inhibit - far end  | TAP-114          |
| line switch inhibit - near end | TAP-114          |
| line TCA                       | TAP-144          |
| LNCTL failure                  | TAP-111          |
| LNCTL removed                  | TAP-112          |
| logins inhibited               | TAP-114          |
| LS auto switch                 | TAP-115          |
| LS forced switch               | TAP-114          |
| LS lockout                     | TAP-114          |
| LS loopback                    | TAP-114          |
| LS manual switch               | TAP-114          |
| LSSW failure                   | TAP-111          |
| LSSW removed                   | TAP-112          |
| multiple DS-NEs defined        | TAP-139          |
| NE status comm failure         | TAP-172          |
| node ID conflict               | TAP-140          |

*Continued on next page*

**Table A - Trouble Conditions (Contd)**

| <b>Description</b>             | <b>Go To TAP</b> |
|--------------------------------|------------------|
| non-preemptible protn access   | TAP-114          |
| OC3 auto switch at far end     | TAP-115          |
| OC3 auto switch at near end    | TAP-115          |
| OC3 forced switch at far end   | TAP-114          |
| OC3 forced switch at near end  | TAP-114          |
| OC3 INTFC failure              | TAP-111          |
| OC3 INTFC removed              | TAP-112          |
| OC3 inhibit at near end        | TAP-114          |
| OC3 lockout at far end         | TAP-114          |
| OC3 lockout at near end        | TAP-114          |
| OC3 loopback (from OC3)        | TAP-114          |
| OC3 loopback (from OC48)       | TAP-114          |
| OC3 mode conflict (equipment)  | TAP-158          |
| OC3 mode conflict (slot pair)  | TAP-158          |
| OC3 mode conflict (x-connect)  | TAP-158          |
| OC12 auto switch at far end    | TAP-115          |
| OC12 auto switch at near end   | TAP-115          |
| OC12 forced switch at far end  | TAP-114          |
| OC12 forced switch at near end | TAP-114          |
| OC12 INTFC failure             | TAP-111          |
| OC12 INTFC removed             | TAP-112          |
| OC12 inhibit at near end       | TAP-114          |
| OC12 lockout at far end        | TAP-114          |
| OC12 lockout at near end       | TAP-114          |
| OC12 loopback (from OC48)      | TAP-114          |
| OHCTL failure                  | TAP-111          |
| OHCTL removed                  | TAP-112          |
| optical parameter out-of-range | TAP-135          |

*Continued on next page*

**Table A - Trouble Conditions (Contd)**

| <b>Description</b>             | <b>Go To TAP</b> |
|--------------------------------|------------------|
| path AIS                       | TAP-122          |
| path integrity failure         | TAP-163          |
| path TCA                       | TAP-144          |
| path yellow                    | TAP-122          |
| preemptible protn access       | TAP-114          |
| RCVR failure                   | TAP-111          |
| RCVR removed                   | TAP-112          |
| receiver parametr-out-of-range | TAP-135          |
| REGENR failure                 | TAP-111          |
| REGENR removed                 | TAP-112          |
| repeater bay environment input | TAP-147          |
| repeater bay equipment failure | TAP-147          |
| reset in progress              | TAP-124          |
| ring incomplete/open           | TAP-141          |
| ring startup in progress       | TAP-142          |
| section TCA                    | TAP-144          |
| site provisioning conflict     | TAP-130          |
| software download in progress  | TAP-131          |
| STS1E INTFC failure            | TAP-111          |
| STS1E INTFC removed            | TAP-112          |
| STS1 squelch map conflict      | TAP-164          |
| STS3 squelch map conflict      | TAP-153          |
| STS1 squelch map inconsistent  | TAP-165          |
| STS3 squelch map inconsistent  | TAP-165          |
| squelch map conflict           | TAP-153          |
| sync mode auto switch          | TAP-115          |
| sync mode equipment conflict   | TAP-114          |
| sync mode forced switch        | TAP-114          |

*Continued on next page*

**Table A - Trouble Conditions (Contd)**

| <b>Description</b>             | <b>Go To TAP</b> |
|--------------------------------|------------------|
| sync reference auto switch     | TAP-115          |
| sync reference forced switch   | TAP-114          |
| sync reference lockout         | TAP-114          |
| sync reference manual switch   | TAP-114          |
| SYSCTL failure                 | TAP-113          |
| SYSMEM failure                 | TAP-113          |
| SYSMEM removed                 | TAP-133          |
| SYSMEM/SYSCTL code mismatch    | TAP-126          |
| SYSMEM unrecognizable code     | TAP-125          |
| test alarm in progress         | TAP-114          |
| test auto turnup in progress   | TAP-114          |
| test telemetry in progress     | TAP-114          |
| TG auto switch                 | TAP-115          |
| TG manual switch               | TAP-114          |
| TG warming up                  | TAP-154          |
| TG3 failure                    | TAP-111          |
| TG3 removed                    | TAP-112          |
| TID address map full           | TAP-170          |
| TL-1 link failure              | TAP-106          |
| TOHCTL failure                 | TAP-111          |
| TOHCTL removed                 | TAP-112          |
| traffic squelched              | TAP-146          |
| transmit parametr-out-of-range | TAP-135          |
| TRMTR failure                  | TAP-111          |
| TRMTR removed                  | TAP-112          |
| unexpected CP type             | TAP-127          |
| x-conn provisioned but not est | TAP-173          |



## Clear NE ACTY and/or FE ACTY (Regardless of Alarm Level)

**Overview:** This procedure is used to clear one or more conditions indicated by a lighted **NE ACTY** and/or **FE ACTY** LED(s) on the user panel. (These conditions may or may not be accompanied by an alarm.)

1. Are you responsible for maintaining the remote FT-2000 or DDM-2000 network element (NE) as well as the local FT-2000 NE?  
If **YES**, then continue with Step 2.  
If **NO**, then continue with Step 21.
2. Is the **FE ACTY** LED lighted on the local user panel?  
If **YES**, then continue with Step 3.  
If **NO**, then continue with Step 21.
3. Is the CIT conditioned and connected to the **local FT-2000 NE**?  
If **YES**, then continue with Step 4.  
If **NO**, then complete **DLP-501** and return to this procedure.
4. At the CIT, select **FAULT-Retrieve-Network Alarms** to obtain a Network Alarms Report (Figure 1 shows an example for Release 6 and later; other examples for earlier releases are shown in Chapter 11 of this manual.)  
  
 **NOTE:**  
The Network Alarms Report will look like the example in Figure 1 if the local NE is an FT-2000 Add/Drop-Rings Terminal with Release 6 or later software. Earlier software releases produce slightly different reports which are shown in Chapter 11 of this manual.
5. Referring to the column headings of the Network Alarms Report obtained in Step 4, is the rightmost column labeled **Local Ring** (as in Figure 1), **TID**, or **Event Description**?  
If **Local Ring**, then continue with Step 9.  
If **TID**, then continue with Step 9.  
If **Event Description**, then continue with Step 6.

|                               |                                       |          |         |          |            |
|-------------------------------|---------------------------------------|----------|---------|----------|------------|
| MINOR                         | Lucent Technologies FT-2000 6.0.0-ADR |          |         | Delta-2  | LOCAL ONLY |
| Delta-2 95-01-19 23:15:15 EST |                                       |          |         |          |            |
| M                             | Retrieve-Network Alarms: COMPLD       |          |         |          |            |
| /* Network Alarms Report      |                                       |          |         |          |            |
| =====                         |                                       |          |         |          |            |
| System                        | Date                                  | Time     | TID     | Product  | Local      |
| Alarm                         |                                       |          |         | Type     | Ring       |
| =====                         |                                       |          |         |          |            |
| MN                            | 01-19                                 | 21:18:47 | Delta-2 | FT-2000  | Yes        |
| MN                            | 01-19                                 | 21:18:27 | Delta-3 | FT-2000  | Yes        |
| MN                            | 01-19                                 | 21:20:57 | Echo-1  | DDM-2000 | No         |
| */                            |                                       |          |         |          |            |

**Figure 1 – Example of Network Alarms Report using Release 6 or Higher.**

- Referring to the topmost entry in the Network Alarms Report obtained in Step 4, is there any text in the Event Description field?

If **YES**, then continue with Step 7.

If **NO**, then continue with Step 9.



**NOTE:**

The condition to be cleared is at a **remote FT-2000 Repeater Bay/Shelf**.

- Referring to the topmost entry in the Network Alarms Report obtained in Step 4, issue a Trouble Report to the FT-2000 Repeater Bay/Shelf location with the indicated Source Address. The Trouble Report should include the text from the Event Description field.

- STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



**NOTE:**

The Target ID (TID) of the local NE appears at the top of the CIT screen between the NE name and LOCAL ONLY (or LOCAL).



**NOTE:**

Depending on the software release running in the local FT-2000 NE, the column in the Network Alarms Report indicating the TID associated with a particular condition may be labeled TID as in Figure 1 or Source Address.

⇒ **NOTE:**  
If the local TID appears in the Network Alarms Report and is among the group of entries with the highest alarm level, the trouble condition is first cleared at the local NE before proceeding with trouble clearing at another NE.

9. Is the **local FT-2000 NE** (TID) in the group with the highest alarm level?  
If **YES**, then continue with Step 21.  
If **NO**, then continue with Step 10.

⇒ **NOTE:**  
Trouble clearing begins with the topmost entry in the report.

⇒ **NOTE:**  
The trouble to be cleared is at a **remote FT-2000 or DDM-2000 NE**.  
Remember, when the following procedures refer to the local CIT, that it is really logged in to a remote NE and that any corrective action will apply to the remote NE.

10. Press the CIT function key **F2**, labeled REMOTE.
11. Referring to the topmost entry in the Network Alarms Report obtained in Step 4 and using the TID, log in to the remote NE.  
Reference: **DLP-524**
12. Were you able to log in to the remote NE?  
If **YES**, then continue with Step 13.  
If **NO**, then continue with Step 19.

⇒ **NOTE:**  
You may require assistance at the remote FT-2000 or DDM-2000 NE to replace a circuit pack(s) or to observe LEDs.

13. Is the remote NE an FT-2000 NE or a DDM-2000 NE?  
If **FT-2000 NE**, then continue with Step 14.  
If **DDM-2000 NE**, then continue with Step 16.
14. Clear the particular condition at the remote FT-2000 NE.  
Reference: **TAP-102**
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

16. Obtain the appropriate reference DDM-2000 User/Service Manual as follows:
  - 363-206-202 for DDM-2000 OC3 for Releases 2 through 7.2
  - 363-206-280 for DDM-2000 OC3 for Release 8 and Higher
  - 363-206-207 for DDM-2000 OC12/OC12 Regenerator for Releases 1 through 3.1
  - 363-206-290 for DDM-2000 OC12 for Release 5 and Higher
17. Locate the appropriate task to be cleared from the trouble clearing index (IXL) of the User/Service Manual based on the alarm level obtained from the report in Step 4.

**Nice to Know:** The IXL lists tasks according to alarm level as follows:

| <b>System Alarm</b> | <b>DDM-2000 OC3</b> | <b>DDM-2000<br/>OC12/OC12 Regenerator</b> |
|---------------------|---------------------|-------------------------------------------|
| CR                  | Tap-101             | Tap-101                                   |
| MJ                  | Tap-102             | Tap-102                                   |
| MN                  | Tap-112             | Tap-112                                   |

18. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
19. Issue a Trouble Report to the remote FT-2000 or DDM-2000 NE.
20. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
21. Is the **NE ACTY** LED lighted on the user panel?
  - If **YES**, then continue with Step 22.
  - If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

 **NOTE:**  
The trouble to be cleared is at the **local FT-2000 NE**.

22. Is the **USE CIT** LED lighted on the local user panel?
  - If **YES**, then go to **TAP-102**.
  - If **NO**, then go to **TAP-101**.
23. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear Trouble Report

**Overview:** This procedure is used to clear a trouble report that was received from a remote location. The remote location is receiving a bad signal or other condition from this local FT-2000 network element.

1. Is the **NE ACTY** LED lighted on the user panel?  
If **YES**, then continue with Step 2.  
If **NO**, then continue with Step 3.
2. Is the **USE CIT** LED lighted?  
If **NO**, then go to **TAP-101**.  
If **YES**, then go to **TAP-102**.
3. Is a local CIT available for use?  
If **YES**, then continue with Step 4.  
If **NO**, then continue with Step 14.

⇒ **NOTE:**  
With Release 6.0 or later software, an outgoing OC3 DCC failure can be caused by a DCC assignment to an LS INTFC slot that does not contain a good OC3 circuit pack.

4. Is Release 6.0 or later software being used?  
If **YES**, then continue with Step 5.  
If **NO**, then continue with Step 10.
5. At the CIT, select **CONFIGURATION-Retrieve-Assignment-DCC** command to determine the LS INTFC slot associated with each existing DCC assignment.
6. At the CIT, select **CONFIGURATION-Retrieve-Equipment** command to determine whether or not each LS INTFC slot identified in Step 5 contains a good OC3 circuit pack.

7. Does any LS INTFC slot(s) with an existing DCC assignment **NOT** contain a good OC3 circuit pack.  
If **YES**, then continue with Step 8.  
If **NO**, then continue with Step 10.
8. **Either** install an OC3 circuit pack in the slot(s) identified in Step 7 or use the **CONFIGURATION-Delete-Assignment-DCC** command to delete the DCC assignment(s) to that slot(s).
9. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
10. Test the LEDs at the local user panel as follows: At the CIT, select **FAULT-Test-LED** and **User Panel**, then execute.
11. Did all the LEDs on the user panel light?  
If **YES**, then continue with Step 12.  
If **NO**, then go to **TAP-108**.
12. Obtain an Active Alarms and Status Report as follows: At the CIT, select **FAULT-Retrieve-Alarm**, then **ALL** for alarm levels.
13. Are any alarm or status conditions indicated on the report?  
If **YES**, then go to **TAP-102**.  
If **NO**, then continue with Step 14.
14. Make a visual inspection of the outgoing cable(s) and connection(s) and correct any problem(s) found.  
Reference: **DLP-528**
15. Was any problem(s) found with the outgoing cable(s) or connection(s).  
If **YES**, then continue with Step 16.  
If **NO**, then return a NO TROUBLE FOUND conclusion to the report (no active local alarms or visual problems).

 **NOTE:**  
OC-48 and/or OC-3 optical lines can be tested by using optical line meters and/or optical time domain reflectometers (OTDR) to help locate a fiber bend or cut. Refer to local operating procedures for further guidance in addressing this condition.

16. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'APS Channel Failure'

**Overview:** An 'APS channel failure' cannot be cleared by itself. It is always associated with one or more other failures at the local FT-2000 network element or at a remote FT-2000 network element. This procedure provides instructions to identify and clear the associated failure or failures.

1. Look for the associated failure or failures in the near end Active Alarm and Status Report. The only possibilities are:
  - Incoming protection line failure such as:
    - incoming OC-48 AIS
    - incoming OC-48 LOF
    - incoming OC-48 LOS
    - incoming OC-48 SF
    - incoming OC-48 SD.
  - RCVR failure or RCVR removed
  - TRMTR failure or TRMTR removed
  - Controller failure or controller removed such as:
    - OHCTL failure
    - OHCTL removed
    - LNCTL failure
    - LNCTL removed.
2. Did you find **only one**, **more than one**, or **none** of the associated failures listed in Step 1 in the report?
  - If **more than one**, then continue with Step 3.
  - If **only one**, then continue with Step 5.
  - If **none**, then the trouble is not in the local FT-2000 network element but may be the OC-48 optical line. Initiate a Trouble Report to the remote FT-2000 network element.

3. Referring only to the topmost failure from the Active Alarm and Status Report identified in Step 1, find this failure in Table A of **TAP-102** and go to the referenced TAP.
4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
5. Find the identified failure from Step 1, then locate this condition in Table A of **TAP-102** and go to the referenced TAP.
6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Address 'TL-1 Link Failure'

1. Does the Active Alarms and Status Report show a controller failure (LNCTL, OHCTL, or SYSCTL circuit pack)?  
    If **YES**, then go to **TAP-102**.  
    If **NO**, then continue with Step 2.
2. Initiate a Trouble Report to the responsible Operations System stating an incoming TL1 link failure has been detected on this network.
3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Address Environmental Input Condition

1. Does the **Description(s)** associated with the "**env-(1-16)**" **Source Address** begin with the string **FT-LBA**?
  - If **YES**, then refer to the LBA maintenance documentation to clear the trouble condition.
  - If **NO**, then continue with Step 2.
2. Consult and follow the local maintenance procedures to clear the indicated environmental condition. This condition may be cleared by something as simple as closing a door and completing a contact closure or the resetting of a relay switch, thus clearing the condition. The environmental condition is external to the FT-2000 network element.
3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Address Missing or Incorrect Response

**Overview:** You were sent here from another procedure because the FT-2000 Network Element responded in an unexpected manner. This unexpected response was a missing or incorrect LED indication or CIT message.

1. Review, and keep in mind, the unexpected response that sent you to this TAP.
2. Was the unexpected response from an LED or from the CIT?

If from an **LED**, then continue with Step 3.

If from the **CIT**, then continue with Step 9.

3. At the CIT, select **FAULT-Test-LED** and execute.

Response: All LEDs are lighted for 10 seconds, off for 10 seconds, then they return to their normal condition.

4. Did the LED(s) of interest operate as indicated above?

If **YES**, then continue with Step 5.

If **NO**, then continue with Step 13.

⇒ **NOTE:**  
The LED operation appears to be normal.

⇒ **NOTE:**  
If you are using Release 6 or 7 software **and** if an Active Alarms and Status Report (use **FAULT-Retrieve-Alarms**) shows **OHCTL failure** for both 1E and 1W **and** if neither LED is lighted at the OHCTL circuit packs (1E and 1W), **then** the problem appears to be the TOHCTL circuit pack.

5. Replace the TOHCTL circuit pack.

Reference: **DLP-514**

6. Is the same unexpected response still observed?

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **YES**, then continue with Step 7.

7. Return to the procedure that sent you here. If the same unexpected response is observed again, contact the appropriate maintenance support organization for further technical assistance.

8. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

9. At the CIT, select **FAULT -Reset** and execute.
10. Wait 10 minutes for the Reset to complete. During this time the lighting of LEDs and clicking of relays will be observed.
11. Return to the procedure that sent you here. If the same unexpected response is observed again, contact the appropriate maintenance support organization for further technical assistance.
12. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
13. Replace the circuit pack, user panel, or filter panel containing the LED(s) that did not operate as indicated in Step 4.

Reference:   For Circuit Packs: DLP-514  
                  For Panels: Drawing ED-6G994-30 (E-Bay)  
                  For Panels: Drawing ED-7G001-33 (D-Bay)

14. Return to the procedure that sent you here. If the same unexpected response is observed again, contact the appropriate maintenance support organization for further technical assistance.
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Address 'Incoming (from OC-48) DS3 AIS'

1. Consult and follow the local maintenance procedures for addressing an incoming (from OC-48) DS3 Alarm Indication Signal (AIS) condition.
2. If required by local maintenance procedures, initiate a Trouble Report to the remote FT-2000 network element stating that an incoming (from OC-48) DS3 AIS condition has been detected on an incoming OC-48 optical line. The referenced DLP will help you locate or identify the source of the **incoming (from OC-48) DS3 AIS** condition.

Reference: **DLP-535**

3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Address Incoming Signal Failure

1. One of the following incoming signal failures was detected:

- Incoming (from DSX) LOS (loss of signal)
- Incoming (from DSX) SD (signal degrade)
- Incoming (from OC3) STS LOP (loss of pointer)
- Incoming (from OC12) STS LOP (loss of pointer)
- Incoming (from OC-48) path AIS (alarm indication signal)
- Incoming (from STSX) EC1 line AIS (alarm indication signal)
- Incoming (from STSX) EC1 LOF (loss of frame)
- Incoming (from STSX) EC1 LOP (loss of pointer)
- Incoming (from STSX) EC1 LOS (loss of signal)
- Incoming (from STSX) EC1 SD (signal degrade)
- Incoming OC3 AIS (alarm indication signal)
- Incoming OC12 AIS (alarm indication signal)
- Incoming OC3 LOF (loss of frame)
- Incoming OC12 LOF (loss of frame)
- Incoming OC3 LOS (loss of signal)
- Incoming OC12 LOS (loss of signal)
- Incoming OC3 SD (signal degrade)
- Incoming OC12 SD (signal degrade)
- Incoming OC3 SF (signal failure)
- Incoming OC12 SF (signal failure)
- Incoming OC12 APS byte fail
- Incoming (from OC12) DCC failure
- Incoming (from OC12) STS LOP (loss of pointer)
- Incoming OC12 line RFI/yellow
- Incoming OC-48 AIS (alarm indication signal)
- Incoming OC-48 LOF (loss of frame)
- Incoming OC-48 LOS (loss of signal)
- Incoming OC-48 SD (signal degrade)
- Incoming OC-48 SF (signal failure)
- Incoming sync ref failure.

Refer to the active Alarms and Status Report (**FAULT-Retrieve-Alarm**) and determine the type of incoming signal ailure.

2.

⇒ **NOTE:**  
If the provisioning is not completed properly (ALL appropriate Cross-Connections provisioned in each NE of a signal's path) before an OC-3c or OC-12c service/signal is applied, then the **incoming (from OC3) STS LOP**, or **incoming (from OC12) STS LOP** will be produced. As a contrast, before deleting any of the Cross-Connections in an OC-3c or OC-12c signal's path, the signal must have been previously removed.

⇒ **NOTE:**  
Under very unusual circumstances, a stuck **sts1-all STS LOP** alarm may occur. If this happens, use the **FAULT-System-Reset** command (after clearing any other alarms first) to clear this invalid alarm.

Refer to the Active Alarms and Status Report (**FAULT-Retrieve-Alarm**) and determine the type of incoming signal failure.

Is the incoming signal failure an **incoming (from OC3) STS LOP**, or **incoming (from OC12) STS LOP**?

If **NO**, then go to Step 3.

If **YES**, then initiate a Trouble Report to the source of the failed incoming signal indicating the type of failure. And, ask that they verify that all Cross-Connections for the signal path have been provisioned correctly.

If the source of the failed incoming signal returns a Trouble Report indicating "no trouble found," then follow the prescribed operating procedures to fault isolate the cable/fiber carrying the failed incoming signal.

**Reference: DLP-535**

3.



**CAUTION:**

*If an **incoming OC3 LOS** or **incoming OC12 LOS** condition exists prior to setting the Alarm Level to NO, this condition **will not disappear** after the Alarm Level is set to NO. Only subsequent conditions will be subdued (not reported). The user may clear the condition by temporarily providing a good input signal until the condition clears or by reseating the OC3 or OC12 circuit pack. Only reseal the circuit pack if you are sure there is no live traffic on the outgoing fiber; otherwise, service interruption will occur.*

Is the incoming signal failure an **incoming** (from DSX) **LOS**, **incoming** (from STSX) **EC1 LOS**, **incoming OC3 LOS**, **incoming OC12 LOS**, **incoming OC-48 LOS**, or **incoming sync ref** failure?

If **YES**, then continue with Step 4.

If **NO**, then continue with Step 8.

4. Make a visual inspection of the incoming signal cable(s) and connection(s) and correct any problems found.

Reference: **DLP-528**

If a visual inspection does not reveal any problems, you may consider making an optical or electrical measurement per local operating instructions.

**Suggestion 1:** OC-3/OC-12/OC-48 optical lines can be tested by using an optical line meter and/or an optical time domain reflectometer (OTDR) to help locate a fiber bend or cut.

**Suggestion 2:** Low-speed signals (DS3/EC-1/OC-3/OC-12) can be tested by using a transmitter/receiver test set.

**Suggestion 3:** Sync reference cables can be tested by using a DS1 error rate test set (ERTS) to verify the DS1 signal.

5. At the CIT, select **FAULT-Retrieve-Alarms (and Status)** to obtain the Active Alarms and Status Report.
6. Referring to the report obtained in Step 5, is the same incoming signal failure still listed?

If **YES**, then replace the receive circuit pack indicated in the **FAULT-Retrieve-Alarms (and Status)** report from Step 5.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

7. Did the same incoming signal failure clear?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, remove the replacement circuit pack and return the original circuit pack to the slot, then continue with Step 8.

8.



**NOTE:**

At this point, it appears that the local terminal is operating correctly. The trouble is in the line or in the far-end terminal.

Initiate a Trouble Report to the source of the failed incoming signal indicating the type of failure. If the source of the failed incoming signal returns a Trouble Report indicating "no trouble found," then follow the prescribed operating procedures to fault isolate the cable/fiber carrying the failed incoming signal.

Reference: **DLP-535**

9. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'Circuit Pack Failure'

**Overview:** This procedure is used to replace a circuit pack with a lighted **FAULT** LED in response to one of these indicated failures:

DS3 INTFC failure  
LNCTL failure  
LSSW failure  
OC3 INTFC failure  
OC12 INTFC failure  
OHCTL failure  
RCVR failure

REGENR failure  
STS1E INTFC failure  
TG3 failure  
IS3 INTFC failure  
TOHCTL failure  
TRMTR failure.



**CAUTION:**

*A potential service interruption can be avoided for DS3, STS1E, OC3, IS3, OC12, TRMTR, RCVR, or TG3 circuit packs by following this instruction. After a replacement circuit pack has been inserted, the **FAULT** LED should go out within 35 seconds. If the **FAULT** LED remains lighted after the 35 seconds, immediately remove the replacement circuit pack before 110 seconds elapses.*

1. From a failure condition above, identify the appropriate shelf using Table A:

**Table A - Shelf and Associated Circuit Packs**

| Failed Circuit Pack                                         | Shelf                                                                        |
|-------------------------------------------------------------|------------------------------------------------------------------------------|
| DS3<br>LSSW<br>OC3<br>STS1E<br>IS3<br>OC12<br>TG3<br>TOHCTL | Low Speed Shelf - Complementary<br>or<br>Low Speed Shelf - System Controller |
| LNCTL<br>OHCTL<br>RCVR<br>TRMTR                             | High Speed Shelf<br>or<br>Enhanced High Speed Shelf                          |
| REGENR                                                      | Repeater Bay/Shelf                                                           |

2. Remove the appropriate shelf cover.

Reference: **DLP-511**

3. Is the **FAULT** LED lighted on the circuit pack identified in the **Source Address** column (Active Alarms and Status Report)?

If **YES**, then continue with Step 4.

If **NO**, then go to **TAP-108**.

4. Is the circuit pack a low speed interface (DS3 or STS1E)?

If **YES**, then continue with Step 5.

If **NO**, then continue with Step 9.

⇒ **NOTE:**  
For DS3 and STS1E circuit packs only: A verification of either an automatic or a CIT provisioned protection switch is made before removing the low speed circuit pack from the shelf.

5. At the CIT, select **FAULT-Retrieve-State:LS INTFC** command and obtain a State Report on the circuit pack being replaced.
6. From the report, is the low speed slot (circuit pack to be removed) in a standby state?

If **YES**, then continue with Step 8.

If **NO**, then continue with Step 7.

7. At the CIT, select **FAULT-Switch-Low Speed:Slot** command for the circuit pack being removed and execute. Use the **FAULT-Retrieve-State:LS INTFC** command to verify, if desired.
8. Verify that all outputs of a DS3 or STS1E circuit pack are properly terminated.

Reference: **DLP-520**

9. Is the circuit pack a TG3 circuit pack?

If **YES**, then continue with Step 10.

If **NO**, then continue with Step 13.

⇒ **NOTE:**  
For TG3 circuit packs only: A verification of a CIT provisioned protection switch is made before removing the TG3 circuit pack from the shelf.

10. At the CIT, select **FAULT-Retrieve-State:TG** command and obtain a State Report on the circuit pack being replaced.

11. From the report, is the TG3 circuit pack to be removed in a standby state with a manual switch?

If **YES**, then continue with Step 13.

If **NO**, then continue with Step 12.

12. At the CIT, select **FAULT-Switch-Synchronization-Hardware** command for the circuit pack being removed and execute a manual switch. Use the **FAULT-Retrieve-State:TG** command to verify, if desired.



**NOTE:**

A series number **S1:2** is the same series number as **S1:4**. The 2 and 4 refer to minor changes within series 1.



**NOTE:**

Power to each shelf must be applied before replacing any circuit packs. For example, a loss of power to a high speed shelf could show a failure of the RCVRs in the shelf as the alarm.



**NOTE:**

An IS-3 circuit pack requires multimode fiber for proper performance monitoring operation.

13. Obtain a replacement circuit pack with the same or a higher series number and install it in place of the circuit pack with the lighted **FAULT** LED. As required, make the appropriate optical connections on the OC3, IS3, OC12, RCVR, TRMTR, or REGENR circuit pack(s).

Reference: **DLP-514**

14. Wait for the time indicated in Table B.

**Table B - Mini Reset Waiting Times**

| <b>CP Inserted</b> | <b>Waiting Time</b> |
|--------------------|---------------------|
| LNCTL *            | 10 minutes          |
| OHCTL              | 5 minutes           |
| TG3 †              | 5 minutes           |
| TOHCTL             | 5 minutes           |
| All Others         | 30 seconds          |

\* Inserting an LNCTL circuit pack starts a mini reset that can take up to 10 minutes to complete. During this time, LEDs on other circuit packs will be lighted.

† When a TG3 circuit pack is first inserted, it will be switched out of service until it warms up. This can take as long as 5 minutes. You can confirm that it is in the standby state by using the **FAULT-Retrieve-State** command for the TG slot.

15. Was the circuit pack just replaced a TG3 circuit pack?  
If **YES**, then continue with Step 16.  
If **NO**, then continue with Step 22.
16. At the CIT, select **FAULT-Switch-Synchronization-Hardware** command for the circuit pack just replaced and execute a switch reset. Use the **FAULT-Retrieve-State:TG** command to verify, if desired.
17. Is the **FAULT** LED lighted on the replacement circuit pack?  
If **YES**, then continue with Step 18.  
If **NO**, then continue with Step 20.
18. At the CIT, select **CONFIGURATION-Retrieve-Synchronization** and obtain a Synchronization Report.
19. From the report, is the associated TG3-1/TG3-2 Clock Range Status at or near +110 percent or –110 percent.  
If **YES**, then issue a trouble report stating an out-of-range external timing reference.  
If **NO**, then go to **TAP-116**.
20. Replace the shelf cover.  
Reference: **DLP-511**
21. Is the **NE ACTY** LED lighted at the user panel.  
If **YES**, then go to **Trouble Clearing: IXL-001**.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE**.
22. Is the **FAULT** LED lighted on the replacement circuit pack?  
If **YES**, then go to **TAP-116**.  
If **NO**, then continue with Step 23.
23. Replace the shelf cover.  
Reference: **DLP-511**
24. Is the **NE ACTY** LED lighted at the user panel.  
If **YES**, then go to **Trouble Clearing: IXL-001**.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE**.



## Clear 'Circuit Pack Removed'

**Overview:** This procedure is used to clear one of the conditions below by (1) installing the missing circuit pack or (2) updating the SYSMEM circuit pack to reflect the current equipage:

DS3 INTFC removed  
LNCTL removed  
LSSW removed  
OC3 INTFC removed  
OC12 INTFC removed  
TRMTR removed

REGENR removed  
STS1E INTFC removed  
TG3 removed  
TOHCTL removed  
OHCTL removed  
RCVR removed.

1. Identify the appropriate shelf associated with the removed circuit pack, using Table A:

| Table A - Shelf and Associated Circuit Packs |                                                                              |
|----------------------------------------------|------------------------------------------------------------------------------|
| Removed Circuit Pack                         | Shelf                                                                        |
| DS3                                          | Low Speed Shelf - Complementary<br>or<br>Low Speed Shelf - System Controller |
| LSSW                                         |                                                                              |
| OC3                                          |                                                                              |
| STS1E                                        |                                                                              |
| OC12                                         |                                                                              |
| TG3                                          |                                                                              |
| TOHCTL                                       | High Speed Shelf<br>or<br>Enhanced High Speed Shelf                          |
| LNCTL                                        |                                                                              |
| OHCTL                                        |                                                                              |
| RCVR                                         |                                                                              |
| TRMTR                                        | Repeater Bay/Shelf                                                           |
| REGENR                                       |                                                                              |

2. Remove the appropriate shelf cover.

Reference: **DLP-511**

3. Is the indicated slot empty?  
If **YES**, then continue with Step 4.  
If **NO**, then go to **TAP-108**.
4. Do the office records show that the slot should contain a circuit pack?  
If **YES**, then continue with Step 8.  
If **NO**, then continue with Step 5.
5. At the CIT, select **CONFIGURATION-Update** (records the current hardware arrangement in SYSMEM circuit pack).
6. Replace the shelf cover.  
Reference: **DLP-511**
7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
8. Obtain the missing circuit pack and install it into the appropriate slot. As required, make the appropriate optical connections on the OC3, RCVR, TRMTR, or REGENR circuit pack(s).  
Reference: **DLP-514**
9. Wait for the time indicated in Table B:

| <b>Table B - Mini Reset Waiting Times</b> |                     |
|-------------------------------------------|---------------------|
| <b>CP Inserted</b>                        | <b>Waiting Time</b> |
| LNCTL *                                   | 10 minutes          |
| OHCTL                                     | 5 minutes           |
| TG3 †                                     | 5 minutes           |
| TOHCTL                                    | 5 minutes           |
| All Others                                | 1 minute            |

\* Inserting an LNCTL circuit pack starts a mini reset that can take up to 10 minutes to complete. During this time, LEDs on other circuit packs will be lighted.

† When a TG3 circuit pack is first inserted, it will be switched out of service until it warms up. This can take as long as 5 minutes. You can confirm that it is in the standby state by using the **FAULT-Retrieve-State** command for the TG slot.

10. Is the **FAULT** LED lighted on the replacement circuit pack?  
If **YES**, then replace shelf cover and go to **Trouble Clearing: IXL-001**.  
If **NO**, then continue with Step 11.

11. Replace shelf cover.

Reference: **DLP-511**

12. Is the **NE ACTY** LED lighted at the user panel?

If **YES**, then go to **Trouble Clearing: IXL-001**.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'SYSMEM Failure,' or 'SYSCTL Failure'

**Overview:** Use this procedure to clear a SYSMEM failure or a SYSCTL failure. Starting with Release 7.2.9 and Release 9.1.1, the FT-2000 OC-48 Lightwave System enhances SYSMEM failure detection and allows the system to maintain communications under certain SYSMEM failure conditions.



**CAUTION:**

*Do not remove the SYSCTL circuit pack unless instructed to do so by this procedure. If removed, provisioning data (current values) could be lost. Current values were provisioned with the **Set** commands.*

1. If the bay is in service, then remove the LNCTL circuit pack.
- 2.



**NOTE:**

Begin with the replacement of the SYSMEM circuit pack even though the SYSCTL circuit pack **FAULT** LED may be lighted.

Remove the SYSMEM circuit pack and install a replacement circuit pack that has the same software release and the same or a higher series number. For example, an LAA25 with series number **S1:2** is the same series number as **S1:4**. The 2 and 4 refer to minor changes within series 1.

Reference: **DLP-514**

3. Wait 10 minutes or until the **MJ** LED is off at the user panel, whichever comes first.
4. At the CIT, select **CONFIGURATION-Retrieve-Equipment** and **SYSCTL** to obtain an Equipage and Version Report.
5. Refer to the Equipage and Version Report and compare the software release stored in SYSMEM [shown under **system-( )** entry in report] with the software release running in the FT-2000 [shown under **sysctl-** entry in report].

6. Are software releases identical?  
If **YES**, then go to Step 8.  
If **NO**, then continue with Step 7.
7. Install identical software into the SYSMEM circuit pack that is running in the FT-2000.  
Reference:   **Operation: NTP-002**
8. At the CIT, select **FAULT -Reset** and execute.
9. Wait 10 minutes or until the **MJ** LED is off at the user panel, whichever comes first. Reset takes approximately 10 minutes but some user panel LEDs flash and relays click in about a minute indicating that reset has started.
10. At the CIT, select **FAULT -Retrieve-Alarm** to obtain an Active Alarm and Status Report.
11. Does the **SYSCCTL failure** or **SYSMEM failure** appear under the **Description** column of the report?  
If **YES**, then continue with Step 12.  
If **NO**, then go to Step 17.
12. Reinstall the SYSMEM circuit pack that was removed in Step 2.



**NOTE:**

For circuit pack and software compatibility, refer to Table 7-1 in Section 7, "Circuit Pack Description."

13. Remove the SYSCCTL circuit pack and install a replacement circuit pack having the same or a higher series number. For example, an LAA23 with series number **S1:2** is the same series number as **S1:4**. The 2 and 4 refer to minor changes within series 1.  
Reference:   **DLP-514**
14. Wait 10 minutes or until the **MJ** LED is off at the user panel, whichever comes first.
15. At the CIT, select **FAULT -Retrieve-Alarm** to obtain an Active Alarm and Status Report.

16. Does a **SYSCTL failure** or **SYSMEM failure** appear under the **Description** column of the report?
  - If **YES**, then go to **TAP-108**.
  - If **NO**, then continue with Step 17.
17. At the CIT, select **CONFIGURATION-Retrieve-Crossconnection**. Verify that all crossconnections are present and correct.
  - If the cross-connections are **correct**, then go to Step 19.
  - If the cross-connections are **corrupted**, then continue with Step 18.
18. Cross-connections must be reentered using an out-of-service spare bay. Follow the procedures listed in this step to restore cross-connections using an out-of-service spare bay.
  - ⇒ **NOTE:**  
If cross-connections are corrupted, they cannot be reentered with the LNCTL circuit pack removed from the bay. And, if the LNCTL is reinserted, the corrupted cross-connect data will overwrite the actual cross-connections in the High Speed Optical circuit packs.
  1. Remove all circuit packs from the out-of-service spare bay.
  2. Remove the good SYSMEM circuit pack from the in-service bay and insert it into the spare bay.
  3. Remove the good SYSCTL circuit pack from the in-service bay and insert it into the spare bay.
  4. Insert a LNCTL in the spare bay.
  5. Insert an OHCTL in the **OHCTL 1** (1W) slot in the spare bay.
  6. Login to the spare bay and enter crossconnections and TID.
  7. Remove the SYSMEM circuit pack and reinstall it into the in-service bay.
  8. Remove the SYSCTL circuit pack and reinstall it into the in-service bay.
  9. Login to the in-service bay and repeat this procedure from Step 17.
19. Reinsert the LNCTL circuit pack that was removed in Step 1.
20. At the CIT, select **FAULT-Reset** and execute.
21. Is the **NE ACTY** LED off at the user panel?
  - If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE**.
  - If **NO**, then go to **Trouble Clearing: IXL-001**.





## Clear Trouble When 'ABN' LED Is Lighted

**Overview:** This procedure is used to clear one of the following conditions:

|                                |                                   |
|--------------------------------|-----------------------------------|
| DS3 loopback                   | LS loopback                       |
| EC1 loopback                   | LS manual switch                  |
| inhibit alarms-office alarms   | non-preemptible protn access      |
| inhibit alarms-parallel telem  | OC3 forced switch at far end      |
| inhibit alarms-X.25            | OC3 forced switch at near end     |
| line forced switch             | OC3 inhibit switch at near end    |
| line forced switch at far end  | OC3 lockout at far end            |
| line forced switch at near end | OC3 lockout at near end           |
| line lockout                   | OC3 loopback (from OC3)           |
| line lockout at far end        | OC3 loopback (from OC48)          |
| line lockout at near end       | preemptible protn access          |
| line manual switch             | sync mode equipment conflict      |
| line manual switch at far end  | sync mode forced switch           |
| line manual switch at near end | sync reference forced switch      |
| line switch inhibit            | sync reference lockout            |
| line switch inhibit - far end  | sync reference manual switch      |
| line switch inhibit - near end | TG manual switch                  |
| logins inhibited               | test alarm in progress            |
| LS forced switch test          | auto turnup in progress           |
| LS lockout                     | test telemetry in progress        |
| OC12 forced switch at far end  | OC12 forced switch at near end    |
| OC12 lockout at far end        | OC12 inhibit at near end          |
| OC12 loopback (from OC48)      | OC12 lockout inhibit at near end. |



**NOTE:**

The indicated condition was initiated by a user command. You must determine, by consulting with the appropriate maintenance personnel, whether the need for the user-initiated condition still exists before proceeding.

1. Does the need for the user initiated condition still exist?
  - If **YES**, then continue with Step 2.
  - If **NO**, then continue with Step 3.

2. Referring to the Active Alarms and Status Report on the CIT screen, are any other conditions indicated?  

If **YES**, then go to **TAP-102** to clear the remaining condition(s), ignoring the condition that sent you here.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
3. Use the CIT to clear the indicated condition by performing the function listed in Table A.
4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

**Table A - Determine Command for Condition**

| <b>Condition To Be Cleared</b> | <b>Perform Function</b>          |
|--------------------------------|----------------------------------|
| inhibit alarms-office alarms   | FAULT-Allow-Alarm-All            |
| inhibit alarms-parallel telem  |                                  |
| inhibit alarms-X.25            |                                  |
| line forced switch             | FAULT-Switch-Line-All-Reset      |
| line forced switch at far end  |                                  |
| line forced switch at near end |                                  |
| line lockout                   |                                  |
| line lockout at far end        |                                  |
| line lockout at near end       |                                  |
| line manual switch             |                                  |
| line manual switch at far end  |                                  |
| line manual switch at near end |                                  |
| line switch inhibit            |                                  |
| line switch inhibit - far end  |                                  |
| line switch inhibit - near end |                                  |
| non-preemptible protn access   |                                  |
| preemptible protn access       |                                  |
| OC3 forced switch at far end   | FAULT-Switch-<LS type>-All-Reset |
| OC3 forced switch at near end  |                                  |
| OC12 forced switch at far end  |                                  |
| OC12 forced switch at near end |                                  |
| OC3 inhibit at near end        |                                  |
| OC12 inhibit at near end       |                                  |
| OC3 lockout at far end         |                                  |
| OC3 lockout at near end        |                                  |
| OC12 lockout at far end        |                                  |
| OC12 lockout at near end       |                                  |

**Table A - Determine Command for Condition**

| <b>Condition To Be Cleared</b> | <b>Perform Function</b>                                     |
|--------------------------------|-------------------------------------------------------------|
| logins inhibited               | SECURITY-Set-Security-Network<br>Element-Logins Allowed:Yes |
| LS forced switch               | FAULT-Switch-Low Speed-All-Reset                            |
| LS lockout                     |                                                             |
| LS manual switch               |                                                             |
| DS3 loopback                   |                                                             |
| EC1 loopback                   | FAULT-Release-Loopback-<LS type>-All                        |
| LS loopback                    |                                                             |
| OC3 loopback (from OC3)        |                                                             |
| OC12 loopback (from OC48)      |                                                             |
| OC3 loopback (from OC48)       |                                                             |
| sync mode equipment conflict   | CONFIGURATION-Set-OC48-Sync<br>Messages-Disabled            |
| sync mode forced switch        | FAULT-Switch-Synchronization-<type<br>of switch>-Reset      |
| sync reference forced switch   |                                                             |
| sync reference lockout         |                                                             |
| sync reference manual switch   |                                                             |
| TG manual switch               |                                                             |
| test alarm in progress         | Wait for the test to complete.                              |
| test auto turnup in progress   |                                                             |
| test telemetry in progress     |                                                             |



## Clear Automatic Switch Condition

**Overview:** This procedure is used to clear one of the following automatic protection switch conditions:

|                              |                             |
|------------------------------|-----------------------------|
| line auto switch             | sync mode auto switch       |
| LS auto switch               | sync reference auto switch  |
| OC3 auto switch at far end   | TG auto switch.             |
| OC3 auto switch at near end  | OC12 auto switch at far end |
| OC12 auto switch at near end |                             |

1. Which switch is to be cleared?

- If **TG auto switch**, then continue with Step 2.
- If **LS auto switch**, then continue with Step 8.
- If **sync mode auto switch**, then continue with Step 14.
- If **sync reference auto switch**, then continue with Step 14.
- If **line auto switch**, then continue with Step 17.
- If **OC3 auto switch at near end**, then continue with Step 29.
- If **OC3 auto switch at far end**, then continue with Step 29.
- If **OC12 auto switch at near end**, then continue with Step 29.
- If **OC12 auto switch at far end**, then continue with Step 29.

### TG Auto Switch Condition

2. Is there one or more other conditions with the same TG **Source Address** in the report?



**NOTE:**

Conditions are shown in the **Description** column of the Active Alarms and Status Report.

If **YES**, then continue with Step 3.

If **NO**, then continue with Step 5.

3. Identify the topmost other condition in the report with the same TG **Source Address**. Find this condition in Table A of **TAP-102** and go to the referenced TAP.
4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

5. Wait 5 minutes or until the **NE ACTY** LED goes off, whichever comes first.



**NOTE:**

When a TG3 circuit pack is first inserted, it will be switched out of service until it warms up. This can take as long as 5 minutes. You can confirm that it is in this state by using the **FAULT-Retrieve-State** command for the TG slot.

6. Is the **NE ACTY** LED off?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then consult the appropriate maintenance support organization for further technical assistance.

7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

### LS Auto Switch Condition

8. Is there one or more other conditions with the **same** low-speed interface **Source Address** in the report?



**NOTE:**

Conditions are shown in the **Description** column of the Active Alarms and Status Report.

If **YES**, then continue with Step 9.

If **NO**, then continue with Step 11.

9. Identify the topmost other condition in the report with the same TG **Source Address**. Find this condition in Table A of **TAP-102** and go to the referenced TAP.
10. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
11. Wait for the provisioned Wait To Restore interval to expire or until the **NE ACTY** LED goes off, whichever comes first.



**NOTE:**

An LS auto switch will remain in effect for a provisioned interval (the Wait To Restore interval) after the cause of the switch is cleared. You can use the **CONFIGURATION-Retrieve-Protection-Line** command to determine the provisioned value of the Wait To Restore interval.

12. Is the **NE ACTY** LED off?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then consult the appropriate maintenance support organization for further technical assistance.

13. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Sync Mode Auto Switch or Sync Reference Auto Switch Conditions

14. Is there one or more other conditions with the same **Source Address** in the report?

⇒ **NOTE:**  
Conditions are shown in the **Description** column of the Active Alarm and Status Report.

If **YES**, then continue with Step 15.

If **NO**, then consult the appropriate maintenance support organization for further technical assistance.

15. Identify the topmost other condition in the report with the **same Source Address**. Find this condition in Table A of **TAP-102** and go to the referenced TAP.

16. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Line Auto Switch Condition

⇒ **NOTE:**  
The failure to be cleared is a **line auto switch** condition.

17. Is there one or more other conditions with the **same line Source Address** in the report?

⇒ **NOTE:**  
Conditions are shown in the **Description** column of the Active Alarms and Status Report.

If **YES**, then continue with Step 18.

If **NO**, then continue with Step 20.

⇒ **NOTE:**  
While following the instructions in the procedure(s) to which you will now be sent, you **MUST** ignore the **line auto switch** condition in the Active Alarms and Status Report. For example, if you are asked whether there are any alarm or status conditions listed and the only condition shown in the report is **line auto switch**, the answer is NO.

⇒ **NOTE:**  
After clearing the condition identified in Step 17, continue the trouble clearing procedure using TAP-102 at the network element that initially reported the **line auto switch** condition.

18. Find the condition identified in Step 17 in Table A of **TAP-102** and go to the indicated TAP to clear that condition (ignoring the **line auto switch** condition throughout the remainder of the trouble clearing procedure).

19. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

⇒ **NOTE:**  
The **line auto switch** may be associated with a condition at the first FT-2000 Add/Drop-Rings Terminal in the indicated direction in a ring network or at the other FT-2000 End Terminal in a point-to-point network.

20. From office records, determine which network element is at the other end of the switched line.

⇒ **NOTE:**  
You may not be able to login to the other network element. If you cannot login to that network element, you will need assistance there to continue with this procedure.

21. At the network element identified in Step 20, use a CIT to obtain an Active Alarms and Status Report.

⇒ **NOTE:**  
It is possible that a **line auto switch** will also be observed at this network element. If so, you **MUST** ignore the **line auto switch** condition in the Active Alarms and Status Report.

22. Referring to the report obtained in Step 21, are there any conditions other than "**line auto switch**" listed?

If **YES**, then continue with Step 23.

If **NO**, then continue with Step 26.

23. Referring to the report obtained in Step 21, identify the topmost condition other than **line auto switch** condition.



⇒ **NOTE:**  
While following the instructions in the procedure(s) to which you will now be sent, you **MUST** ignore the **line auto switch** condition in the Active Alarms and Status Report. For example, if you are asked whether there are any alarm or status conditions listed and the only condition shown in the report is **line auto switch**, the answer is NO.

⇒ **NOTE:**  
After clearing the condition identified in Step 22, continue the trouble clearing procedure using TAP-102 at the network element that initially reported the **line auto switch** condition.

24. Find the condition identified in Step 22 in Table A of **TAP-102** and go to the indicated TAP to clear that condition (ignoring the **line auto switch** condition throughout the remainder of the trouble clearing procedure).

25. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

⇒ **NOTE:**  
A line switch will remain in effect for a provisioned interval (the Wait To Restore interval) after the cause of the switch is cleared. You can use the **CONFIGURATION-Retrieve-Protection-Line** command to determine the provisioned value of the Wait To Restore interval.

26. Wait for the provisioned Wait To Restore interval to expire or until the **NE ACTY** LED goes off, whichever comes first.

27. Is the **NE ACTY** LED off?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then consult the appropriate maintenance support organization for further technical assistance.

28. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

### **OC3/OC12 Auto Switch at Near End or at Far End Condition**

⇒ **NOTE:**  
The failure to be cleared is an **OC3/OC12 auto switch at near end** or an **OC3/OC12 auto switch at far end** condition and will remain in effect for a provisioned interval (Wait To Restore interval) after the cause of the switch is cleared. You can use the **CONFIGURATION-Retrieve-Protection-Low Speed** command to determine the provisioned value of the Wait To Restore interval.

⇒ **NOTE:**  
The **OC3/OC12 auto switch at near end** or the **OC3/OC12 auto switch at far end** may be associated with a condition on the OC-3/OC12 line or at the distant network element located at the other end of the OC-3/OC12 line (FT-2000 or DDM-2000).

⇒ **NOTE:**  
Conditions are shown in the **Description** column of the Active Alarms and Status Report.

29. Is there one or more other conditions with the **same slot Source Address** in the report?

If **YES**, then continue with Step 30.

If **NO**, then continue with Step 32.

⇒ **NOTE:**  
While following the instructions in the procedure(s) to which you will now be sent, you **MUST** ignore the **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** condition in the Active Alarms and Status Report. For example, if you are asked whether there are any alarm or status conditions listed and the only condition shown in the report is **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end**, the answer is NO.

⇒ **NOTE:**  
After clearing the other condition identified in Step 29, continue the trouble clearing procedure using TAP-102 at the network element that initially reported the **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** condition.

30. Find the condition identified in Step 29 in Table A of **TAP-102** and go to the indicated TAP to clear that condition (ignoring the **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** condition throughout the remainder of the trouble clearing procedure).
31. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
32. Do you have maintenance responsibilities for the network element at the other end of the OC-3/OC12 line?
- If **YES**, then continue with Step 34.
- If **NO**, then issue a Trouble Report to the network element stating the error condition you are receiving.

33. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

34. From office records, determine the type of network element at the other end of the OC-3/OC12 auto switched line.

35. Is the remote network element an FT-2000 add/drop-rings terminal?

If **YES**, then continue with Step 36.

If **NO**, then refer to the proper trouble clearing documentation for the type of network element. For example, DDM-2000 OC-3 Multiplexer, User/Service Manual, 363-206-202.



**NOTE:**

You will need to travel to the remote network element or ask for assistance to continue with this procedure.

36. At the network element identified in Step 32, use a CIT to obtain an Active Alarms and Status Report.



**NOTE:**

It is possible that an **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** will also be observed at this network element. If so, you **MUST** ignore the **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** condition in the Active Alarms and Status Report.

37. Referring to the report obtained in Step 36, are there any conditions other than **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** listed?

If **YES**, then continue with Step 38.

If **NO**, then continue with Step 41.

38. Referring to the report obtained in Step 36, identify the topmost condition other than **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** condition.



**NOTE:**

While following the instructions in the procedure(s) to which you will now be sent, you **MUST** ignore the **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** condition in the Active Alarms and Status Report. For example, if you are asked whether there are any alarm or status conditions listed and the only condition shown in the report is **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end**, the answer is NO.

⇒ **NOTE:**  
After clearing the condition identified in Step 36, continue the trouble clearing procedure using TAP-102 at the network element that initially reported the **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** condition.

39. Find the condition identified in Step 36 in Table A of **TAP-102** and go to the indicated TAP to clear that condition (ignoring the **OC3/OC12 auto switch at near end** and/or **OC3/OC12 auto switch at far end** condition throughout the remainder of the trouble clearing procedure).
40. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

⇒ **NOTE:**  
An OC3/OC12 auto switch will remain in effect for a provisioned interval (the Wait To Restore interval) after the cause of the switch is cleared. You can use the **CONFIGURATION-Retrieve-Protection-Low Speed** command to determine the provisioned value of the Wait To Restore interval.

41. Wait for the provisioned Wait To Restore interval to expire or until the **NE ACTY** LED goes off, whichever comes first.
42. Is the **NE ACTY** LED off?
  - If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
  - If **NO**, then follow local maintenance procedures to clear what appears to be trouble with the OC-3/OC-12 line. You may consult the appropriate maintenance support organization for further technical assistance.
43. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear Unsuccessful Circuit Pack Replacement

**Overview:** Only use this procedure in the unlikely event that you were sent here by another procedure (for example, **TAP-111**) because a circuit pack replacement did not clear the associated failure indicator(s). This procedure is used to replace other circuit packs that could possibly cause such a failure indication(s).



**CAUTION:**

*A potential service interruption can be avoided for DS3, STS1E, OC3, OC12, TRMTR, or RCVR circuit packs by following this instruction. After a replacement circuit pack has been inserted, the **FAULT LED** should go out within 35 seconds (optical packs may still FLASH if no signal is present). If the **FAULT LED** remains lighted after the 35 seconds, immediately remove the replacement circuit pack before 110 seconds elapses. In addition, and if the system crashes (all LEDs lighted), remove the pack that caused the crash and wait for the bay to recover. Then, install a different version of the replacement circuit pack and then reseal the LNCTL circuit pack. Do not reinstall the pack that caused the crash into an in-service system. Return it for repair.*



**NOTE:**

A series number **S1:2** is the same series number as **S1:4**. The 2 and 4 refer to minor changes within series 1.



**NOTE:**

In this procedure, the circuit pack that first indicated "failed" is called the original circuit pack. The replacement circuit pack that did not clear the failure is called the original replacement circuit pack (same type of circuit pack). If the failure still exists, then one or more circuit packs which are called alternative replacement circuit packs are substituted.

1. What software version is running in the network element? (The software version is displayed at the top of the CIT screen).

If **Version 3.0.0 or lower**, then continue with Step 6.

If **Version 4.0.0 or higher**, then continue with Step 2.



**NOTE:**

The Equipage and Version Report generated by the **Retrieve-Equipment** command indicates an **alternative CP** to be replaced if replacing the circuit

⇒ **NOTE:**  
 pack identified in the Alarm and Status Report failed to clear the trouble indication.

2. At the CIT, select **Retrieve-Equipment** to obtain an Equipage and Version Report.
3. Referring to the entry in the Equipage and Version Report for the circuit pack just replaced, note the type and slot address of the alternative replacement circuit pack (shown as **alternative CP** in the report).
4. Obtain a circuit pack of the type identified in Step 3 and install it in the slot address identified in Step 3 (replacing an apparently good circuit pack).

Reference: **DLP-514**

5. Is the **FAULT** LED lighted on the *original replacement circuit pack*?  
 If **YES**, then go **TAP-108**.  
 If **NO**, then continue with Step 14.
6. Find the original circuit pack in the first column of Table A.

**Table A - Circuit Pack Replacements**

| Original     | Alternative Replacement Circuit Pack Order |        |        |       |               |           |      |
|--------------|--------------------------------------------|--------|--------|-------|---------------|-----------|------|
| Circuit Pack | 1                                          | 2      | 3      | 4     | 5             | 6         | 7    |
| DS3*         | LNCTL                                      | SYSCTL | SYSMEM | TRMTR | RCVR          | DS3       | —    |
| LAA14 OW     | LAA14 OW                                   | —      | —      | —     | —             | —         | —    |
| LNCTL        | SYSCTL                                     | SYSMEM | TRMTR  | RCVR  | TG3           | DS3/STS1E | LSSW |
| LSSW         | LNCTL                                      | SYSCTL | SYSMEM | TRMTR | RCVR          | DS3/STS1E | —    |
| OC3          | LNCTL                                      | SYSCTL | SYSMEM | TRMTR | RCVR          | TOHCTL    | OC3  |
| OC12         | LNCTL                                      | SYSCTL | SYSMEM | TRMTR | RCVR          | TOHCTL    | OC12 |
| OHCTL        | SYSCTL                                     | SYSMEM | TRMTR  | RCVR  | DS3/STS1E     | —         | —    |
| RCVR         | LNCTL                                      | SYSCTL | SYSMEM | TG3   | DS3/STS1E/OC3 | —         | —    |
| REGENR       | REGENR                                     | SYSCTL | SYSMEM | —     | —             | —         | —    |
| STS1E*       | LNCTL                                      | SYSCTL | SYSMEM | TRMTR | RCVR          | STS1E     | —    |
| SYSCTL       | SYSMEM                                     | —      | —      | —     | —             | —         | —    |
| SYSMEM       | SYSCTL                                     | —      | —      | —     | —             | —         | —    |
| TG3          | LNCTL                                      | SYSCTL | SYSMEM | TRMTR | RCVR          | DS3/STS1E | —    |
| TOHCTL       | SYSCTL                                     | SYSMEM | TRMTR  | RCVR  | OC3           | —         | —    |
| TRMTR        | LNCTL                                      | SYSCTL | SYSMEM | TG3   | DS3/STS1E/OC3 | —         | —    |

\* Remember to replace the protection circuit packs.



**NOTE:**

All of the following circuit pack replacements, except for the SYSCTL and the SYSMEM, apply only to the line containing the original circuit pack.

7. Identify the first alternative replacement circuit pack (shown in the column labeled **1**).
8. Replace only one circuit pack identified as the first alternative replacement circuit pack.

Reference: **DLP-514**

9. Is the **FAULT** LED still lighted on the original replacement circuit pack?  
If **YES**, then continue with Step 10.  
If **NO**, then continue with Step 14.
10. Reinstall the circuit pack replaced in Step 8.
11. Repeat Steps 8 through 10 for any additional circuit packs of the same type identified in Step 6.
12. Repeat Steps 6 through 11 for each remaining alternative replacement circuit pack type until either (1) the **FAULT** LED on the original replacement circuit pack is off or (2) all the alternative replacement circuit pack types have been tried. Then, continue with Step 13.
13. Is the **FAULT** LED lighted on the original replacement circuit pack?  
If **YES**, then go to **TAP-108**.  
If **NO**, then continue with Step 14.



**NOTE:**

The remaining steps help prevent returning nondefective circuit packs for repair.

14. Is the NE running Release 5.0.3, R7.0.1, or a later release?  
If **NO**, then continue with Step 18.  
If **YES**, then reinstall the original circuit pack.
15. Is the **FAULT** LED lighted on the original circuit pack?  
If **YES**, then continue with Step 16.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
16. Reinstall the *original replacement circuit pack*.
17. Verify the **FAULT** LED is off on the *original replacement circuit pack*.
18. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Clear 'Excessive Holdover'



**NOTE:**

Another condition is affecting the synchronization of the FT-2000. This condition is causing the FT-2000 to operate in a synchronization holdover mode for an excessive amount of time.

1. Refer to the **Description** column of an Active Alarms and Status Report and find another condition (not **excessive holdover**) affecting synchronization in the FT-2000.
2. Find this second condition (identified in Step 1) in Table A of **TAP-102** and go to the referenced TAP.
3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'Fan Failure'

**Overview:** This procedure is used to clear a fan failure condition caused by a tripped circuit breaker or a defective fan assembly.

1. Is the **POWER ON** LED lighted on the fan assembly?  
If **YES**, then continue with Step 5.  
If **NO**, then continue with Step 2.
2. Is either circuit breaker, labeled **POWER RESET A** and **POWER RESET B**, on fan assembly tripped?  
If **YES**, then continue with Step 3.  
If **NO**, then continue with Step 9.

⇒ **NOTE:**  
If circuit breaker(s) continues to trip after repeated attempts to reset, then continue with Step 9.

3. Reset the tripped circuit breaker(s).
4. Is the **POWER ON** LED lighted on the fan assembly?  
If **YES**, then continue with Step 5.  
If **NO**, then continue with Step 9.
5. Is the **ALM** LED lighted on the fan assembly?  
If **YES**, then continue with Step 9.  
If **NO**, then continue with Step 6.
6. Press and hold in the **ALM TST** pushbutton switch for 20 seconds (checks the control board and the LEDs).
7. Release the **ALM TST** pushbutton switch.
8. Is the **ALM** LED off (within 20 seconds)?  
If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**  
If **NO**, then continue with Step 9.
9. For (E-Bay), remove the fan assembly ED-6G992-30 and replace it with a new fan assembly (see 365-575-104, *FT-2000 OC-48 Lightwave System, Releases*

*1, and 2, Installation Manual or 365-575-115, FT-2000 OC-48 Lightwave System, Releases 3, 4, 5, 6, and 7, Installation Manual).*

For (D-Bay), refer to DLP-546 "Replace Defective Fan Unit in D-Bay."

10. Test the new fan assembly.

Reference:    **DLP-513**

11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'Fuse/Power Failure'

**Overview:** This procedure is used to clear a fuse/power failure by replacing a blown fuse (lighted) or isolating the power failure to a shelf.

1. Is a fuse lighted (blown) at the top of the bay or at the repeater shelf?  
If **YES**, then continue with Step 2.  
If **NO**, then continue with Step 4.
2. Replace the lighted fuse with a new fuse.  
Reference: **DLP-515**
3. Did the fuse blow (light) again?  
If **YES**, then continue with Step 4.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
4. Obtain the appropriate drawing for the bay/shelf part to be replaced (see the "Related Documentation/Training" part in the "About This Document" section). For example, the E-Bay user panel (ED-6G994-30, G1) is found on drawing ED-6G999-30. Another helpful source is 365-575-115, *FT-2000 OC-48 Lightwave System, Releases 3, 4, 5, 6, and 7, Installation Manual* and 365-575-104, *FT-2000 OC-48 Lightwave System, Releases 1 and 2, Installation Manual*.

### Shelf Trouble-Clearing Suggestions

- If the shelf has power but the **PWR ON** LED is off, then replace the user panel or filter panel.  
Reference: **Drawing ED-6G994-30** (E-Bay)  
Reference: **Drawing ED-7G001-33** (D-Bay)
  - If the user panel **PWR ON** LED or any other LEDs are defective, replace the user panel.
  - If the shelf has no power, check the 1186A power filter boards (or equivalent) or replace the user panel.
5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'Incoming DCC Failure' or 'Inc. (From OC48) DCC Failure'



**CAUTION:**

*Do not reset the FT-2000 NEs while trying to clear this problem! This will result in a "Ring Incomplete/Open" that will seriously complicate repair of the problem.*



**NOTE:**

Failure of the incoming data communications channel (DCC) is usually associated with hardware failure(s) at the source of the incoming OC-48 signal or with other troubles involving the incoming OC-48 signal from that source.



**NOTE:**

The **inc. (from OC48) DCC failure** condition applies to Release 6 and later whereas the **incoming DCC failure** condition applies to Release 5 and earlier.

1. At the CIT, select **FAULT-Retrieve-Alarms (and Status)** to obtain the Active Alarms and Status Report.
2. Referring to the report obtained in Step 1, are there any conditions other than **inc. (from OC48) DCC failure** or **incoming DCC failure** listed?  
If **YES**, then continue with Step 3.  
If **NO**, then continue with Step 6.
3. Referring to the report obtained in Step 1, identify the topmost condition other than the **inc. (from OC48) DCC failure** or **incoming DCC failure** condition.



**NOTE:**

While following the instructions in the procedure(s) to which you will now be sent, you **MUST** ignore the **inc. (from OC48) DCC failure** or the **incoming DCC failure** condition in the Active Alarms and Status Report. For example, if you are asked whether there are any alarm or status conditions listed and the only condition shown in the report is **inc. (from OC48) DCC failure** or the **incoming DCC failure**, the answer is NO.

⇒ **NOTE:**  
After clearing the condition identified in Step 3, continue the trouble clearing procedure using TAP-102 at the network element that initially reported the **inc. (from OC48) DCC failure** or the **incoming DCC failure** condition.

4. Find the condition identified in Step 3 in Table A of **TAP-102** and go to the indicated TAP to clear that condition [ignoring the **inc. (from OC48) DCC failure** or **incoming DCC failure** condition throughout the remainder of the trouble clearing procedure].
5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
6. From office records, determine which network element is the source of the failed DCC input.

⇒ **NOTE:**  
The source of the failed DCC input is the other FT-2000 End Terminal in a point-to-point network, or the first (adjacent) FT-2000 Add/Drop-Rings Terminal in the indicated direction.

7. At the source of the failed DCC input, use the CIT to obtain an Active Alarms and Status Report.

⇒ **NOTE:**  
You may not be able to log in to the network element sending the failed DCC input. If you cannot log in to that network element, you will need assistance there to continue with this procedure.

8. Referring to the report obtained in Step 7, are there any conditions other than **inc. (from OC48) DCC failure** or **incoming DCC failure** listed?

⇒ **NOTE:**  
It is possible that an **inc. (from OC48) DCC failure** or an **incoming DCC failure** will also be observed at this network element. If so, you **MUST** ignore the **inc. (from OC48) DCC failure** or the **incoming DCC failure** condition in the Active Alarms and Status Report.

If **YES**, then continue with Step 9.

If **NO**, then continue with Step 12.

9. Referring to the report obtained in Step 7, identify the topmost condition other than the **inc. (from OC48) DCC failure** or the **incoming DCC failure** condition.

⇒ **NOTE:**  
While following the instructions in the procedure(s) to which you will now be sent, you **MUST** ignore the **inc. (from OC48) DCC failure** or the



**incoming DCC failure** condition in the Active Alarms and Status Report. For example, if you are asked whether there are any alarm or status conditions listed and the only condition shown in the report is **inc. (from OC48) DCC failure** or **incoming DCC failure**, the answer is NO.



**NOTE:**

After clearing the condition identified in Step 9, continue the trouble clearing procedure using TAP-102 at the network element that initially reported the **inc. (from OC48) DCC failure** or the **incoming DCC failure** condition.

10. Find the condition identified in Step 9 in Table A of TAP-102 and go to the indicated TAP to clear that condition [ignoring the **inc. (from OC48) DCC failure** or the **incoming DCC failure** condition throughout the remainder of the trouble clearing procedure].
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
12. At the network element identified in Step 6, replace the OHCTL circuit pack for the line associated with the failed DCC output.

**Reference: DLP-514**



**NOTE:**

The **inc. (from OC48) DCC failure** or the **incoming DCC failure** is possibly due to a "silent" failure of the OHCTL circuit pack at the network element sending the failed DCC input or at the network element detecting it.

13. Wait 5 minutes for the OHCTL circuit pack to reboot.
14. Is the **inc. (from OC48) DCC failure** or the **incoming DCC failure** condition still reported at the network element where it was initially detected?  
If **YES**, then continue with Step 15.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
15. At the network element that originally reported the **inc. (from OC48) DCC failure** or the **incoming DCC failure** condition, replace the OHCTL circuit pack for the line associated with the failed DCC output.
16. Wait 5 minutes for the circuit pack to reboot.
17. Is the **inc. (from OC48) DCC failure** or the **incoming DCC failure** condition still reported at the network element where it was initially detected?  
If **YES**, then continue with Step 18.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
18. Consult the appropriate maintenance support organization for further technical assistance.

**19. STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Address 'Line FERF' or 'Incoming OC-48 Line RFI/Yellow'



**NOTE:**

A remote FT-2000 network element has detected a bad OC-48 signal from this network element. The "line FERF" or "incoming OC-48 line RFI/yellow" condition indicates that the trouble is in the local FT-2000 network element or in the OC-48 optical line. The condition is normally cleared by correcting an associated condition(s).

1. At the CIT, select **FAULT-Retrieve-Alarms (and Status)** and execute to obtain the Active Alarms and Status Report.
2. Referring to the report obtained in Step 1, are there any conditions other than **line FERF, incoming OC-48 line RFI/yellow, incoming DCC failure, extra traffic preempted, and/or line auto switch** listed?



**NOTE:**

An **incoming DCC failure, line auto switch** and/or **extra traffic preempted** condition(s) may also accompany the **line FERF** or **incoming OC-48 line RFI/yellow** condition.

If **YES**, then continue with Step 3.

If **NO**, then continue with Step 5.

3. Referring to the report obtained in Step 1, identify the topmost condition other than the **line FERF** or **incoming OC-48 line RFI/yellow** condition.
4. Find the condition identified in Step 3 in Table A of **TAP-102** and go to the indicated TAP to clear that condition.
5. Make a visual inspection of the outgoing OC-48 optical cable(s) and connection(s) and correct any problem(s) found.

Reference: **DLP-528**

6. At the CIT, select **FAULT-Retrieve-Alarms (and Status)** and execute to obtain a new Active Alarms and Status Report.

7. Referring to the report obtained in Step 6, is the **line FERF** or **incoming OC-48 line RFI/yellow** condition still listed?

If **YES**, then initiate a Trouble Report to the remote FT-2000 network element (the source of the **line FERF** condition) indicating the **line FERF** condition.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

8. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

**Address 'Path AIS', 'Path Yellow',  
'Incoming (from OC-48) DS3 LOF',  
'Inc(from STSX) Line RFI/Yellow',  
'Inc(from OC48) Path RFI/Yellow', and  
'Incoming OC3 Line RFI/Yellow'  
'Incoming OC12 Line RFI/Yellow'**

1. Consult and follow the local maintenance procedures regarding how to address the low-speed or the high-speed condition(s) shown below. The condition (trouble) is not in the local FT-2000 network element.

path AIS  
path yellow  
incoming (from OC3) STS LOP  
incoming (from OC12) STS LOP  
incoming (from OC-48) DS3 LOF  
inc(from STSX) line RFI/yellow  
inc(from OC48) path RFI/yellow  
incoming OC3 line RFI/yellow  
incoming OC12 line RFI/yellow  
incoming OC12 LOF  
incoming OC12 SF  
incoming OC12 SD  
incoming OC12 AIS  
incoming OC12 APS byte fail

2. Obtain an Active Alarms and Status Report and determine if other conditions are to be cleared.

If **YES**, then find the topmost **other** condition in Table A of **TAP-102** and go to the referenced TAP.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'CP (Unknown Type) Failure'

1. If the affected circuit pack was just installed, wait for the time indicated in Table A to allow the software in the affected circuit pack to start up.

**Table A - Mini Reset Waiting Times**

| CP Inserted | Waiting Time |
|-------------|--------------|
| LNCTL *     | 10 minutes   |
| OHCTL       | 5 minutes    |
| TG3 †       | 5 minutes    |
| TOHCTL      | 5 minutes    |
| All Others  | 35 seconds   |

\* Inserting an LNCTL circuit pack starts a mini reset that can take up to 10 minutes to complete. During this time, LEDs on other circuit packs will be lighted.

† When a TG3 circuit pack is first inserted, it will be switched out of service until it warms up. This can take as long as 5 minutes. You can confirm that it is in the standby state by using the **FAULT-Retrieve-State** command for the TG slot.

⇒ **NOTE:**  
The circuit pack in the indicated slot has failed, but its type is unknown to the software. The type or code appears on the circuit pack faceplate.

2. Consult office records to determine the type of circuit pack to be installed in the slot.

⇒ **NOTE:**  
Section 3, "Platform Descriptions," lists the circuit pack codes that are supported by the slot for a particular shelf. Also listed is the compatible software for the particular circuit pack code.

3. Obtain and install a replacement circuit pack determined in Step 2. As required, make the appropriate optical connections on the OC-3, IS-3, OC-12, RCVR, TRMTR, or REGENR circuit pack(s).

Reference: **DLP-514**

4. Wait for the time indicated in Table A for the download of software.

5. Did the **FAULT** LED on the replacement circuit pack go off?  
If **YES**, then continue with Step 6.  
If **NO**, then go to **TAP-116**.
6. Is the **NE ACTY** LED lighted?  
If **YES**, then go to **Trouble Clearing: IXL-001**.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE**.



## Address 'Reset in Progress'



**NOTE:**

A software reset is in progress. This involves downloading software from SYSMEM to SYSCTL, to LNCTLs, and to OHCTLs and to board controllers on all other circuit packs. This can take as long as 10 minutes. During this time, the miscellaneous discrete control points are deactivated.

1. Is the **MJ** LED lighted?  
If **YES**, then wait 10 minutes before proceeding with Step 2.  
If **NO**, then continue with Step 5.
2. At the CIT, select **FAULT-Retrieve-Alarm** to obtain an updated Active Alarm and Status Report.
3. Is **reset in progress** still reported?  
If **YES**, then continue with Step 4.  
If **NO**, then continue with Step 7.
4. Wait 10 minutes or until the **MJ** LED is off at the user panel.
5. At the CIT, select **FAULT-Retrieve-Alarm** to obtain updated Active Alarm and Status Report.
6. Is **reset in progress** still reported?  
If **YES**, then go to **TAP-108**.  
If **NO**, then continue with Step 7.
7. Is the **NE ACTY** LED lighted?  
If **YES**, then go to **Trouble Clearing: IXL-001**.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE**.



## Clear 'SYSMEM Unrecognizable Code'

**Overview:** The software code stored in the nonvolatile memory of the SYSMEM circuit pack is corrupted or its version is not recognized by the SYSCTL circuit pack. The recommended way to clear this condition is to download new software code from the CIT to the SYSMEM circuit pack. Another way to clear this condition is to install a replacement SYSMEM circuit pack loaded with the correct code.

1. At the CIT, select **CONFIGURATION-Retrieve-Equipment** and **sysctl** to obtain an Equipage and Version Report.
2. Refer to the report and record the software release information from the line immediately under the **sysctl** entry. For example, **FT-2000 RELEASE 2.1.0**. This software release is now running in the SYSCTL circuit pack.
3. Do you want to download new software or replace the SYSMEM circuit pack?

To **download new software**, go to **OPERATION: NTP-002**.

To **replace the SYSMEM circuit pack**, continue with Step 4.

4. Remove the shelf cover from the Low Speed Shelf - System Controller or from the FT-2000 repeater bay/shelf.
5. Is the **FAULT** LED flashing at the SYSMEM circuit pack?

If **YES**, then continue with Step 6.

If **NO**, then go to **TAP-108**.

6. Obtain a replacement SYSMEM circuit pack containing an exact copy of the software identified in Step 2.
7. Remove the SYSMEM circuit pack and install the replacement SYSMEM circuit pack.

Reference: **DLP-514**

8. Wait several minutes for the FT-2000 to stabilize (indicated by the **FAULT** LED on the SYSMEM circuit pack being continuously lighted, flashing, then off.)
9. Is the **FAULT** LED continuously lighted on the SYSMEM circuit pack?

If **YES**, then continue with Step 10.

If **NO**, then continue with Step 12.

10. Is a second replacement SYSMEM circuit pack available?  
If **YES**, then continue with Step 11.  
If **NO**, then continue with Step 13.
11. Replace the SYSMEM circuit pack with the second replacement SYSMEM circuit pack (loaded with an exact copy of the software identified in Step 2).  
Reference: **DLP-514**
12. Is the **FAULT** LED flashing on the SYSMEM circuit pack?  
If **YES**, then go to **TAP-102**.  
If **NO**, then continue with Step 15.
13. Reinstall the original SYSMEM circuit pack.  
Reference: **DLP-514**
14. Install new software (same release as currently running in the SYSCTL circuit pack.)  
Reference: **Operation: NTP-002**.
15. Replace the shelf cover.  
Reference: **DLP-511**
16. Is the **NE ACTY** LED lighted at the user panel?  
If **YES**, then go to **Trouble Clearing: IXL-001**.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE**.

## Clear 'SYSMEM/SYSCTL Code Mismatch'

**Overview:** The software code stored in the nonvolatile memory of the SYSMEM circuit pack is different from the code running in the FT-2000 network element. The 'SYSMEM/SYSCTL code mismatch' condition can be cleared by (1) executing Reset that causes the SYSMEM circuit pack to download code to the FT-2000 network element or (2) by downloading a copy from the CIT to the SYSMEM circuit pack.



**NOTE:**

During the duration of the code mismatch, several other indications exist [MJ at control panel and **FAULT** LEDs on LNCTL and OHCTL (1W and 1E) circuit packs]. Ignore these indications until the code mismatch condition has been cleared, then clear any remaining trouble conditions.

1. At the CIT, select **CONFIGURATION-Retrieve-Equipment** and **sysctl** to obtain an Equipage and Version Report.
2. Refer to the report and record the software release information from the line immediately under the **sysctl** entry, for example, **FT-2000 RELEASE 2.1.0**. This software release is now running in the SYSCTL circuit pack.
3. Refer to the report and record the software release information from the line immediately under the **sysmem** entry, for example, **FT-2000 RELEASE 2.1.0**. This software release is stored in the nonvolatile memory of the SYSMEM circuit pack.
4. Are the software releases recorded in Steps 2 and 3 different from each other?  
If **YES**, then continue with Step 5.  
If **NO**, then go to **TAP-108**.
5. Determine which of the two software releases is supposed to be running in the FT-2000 network element.
6. Is the software release running in the FT-2000 network element (software release recorded in Step 2) supposed to be running in the FT-2000 network element?  
If **YES**, then continue with Step 7.  
If **NO**, then continue with Step 9.

7. Go to the **OPERATION: NTP-002** to install the same software release in the SYSMEM circuit pack that is running in the FT-2000 network element.
8. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
9. At the CIT, select **FAULT - Reset** and execute. During the reset time, the miscellaneous discrete control points will be deactivated.
10. Wait 10 minutes or until the **MJ** LED is off at the user panel.
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'Unexpected CP Type'

**Overview:** This condition in Release 2 simply means that a defective circuit pack has been used. Circuit pack keying prevents different circuit packs from being installed into other type slots.



**NOTE:**

The **unexpected CP type** condition is issued whenever the FT-2000 software detects a difference between what it expects in a circuit pack (CP) slot and which circuit pack is actually in the slot. This condition is normally cleared by correcting the difference (wrong CP type or incomplete records).

1. Are you using Release 2.1 (or lower) software or Release 3.0 (or higher) software?  
    If **Release 2.1 (or lower) software**, then continue with Step 2.  
    If **Release 3.0 (or higher) software**, then continue with Step 10.
2. At the CIT, select **CONFIGURATION-Retrieve-Equipment:All** to obtain an Equipage and Version Report.
3. Determine exactly what type of circuit pack is supposed to be in that slot and its address (slot, shelf, and line.)
4. Obtain a replacement circuit pack based on the Equipage and Version Report.
5. If required, remove the shelf cover.  
    Reference: **DLP-511**
6. Replace the suspected defective circuit pack with the replacement.  
    Reference: **DLP-514**
7. If required, replace the shelf cover.  
    Reference: **DLP-511**
8. At the CIT, select **FAULT-Retrieve-Alarm** to obtain an Active Alarm and Status Report.
9. Does the "unexpected CP type" condition still exist?  
    If **YES**, then go to **TAP-108**.  
    If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



**NOTE:**

The **unexpected CP type** condition is issued whenever the FT-2000 software detects a difference between what it expects in a circuit pack slot and which circuit pack is actually in the slot. This condition is normally cleared by correcting the difference (wrong circuit pack type or incomplete records).

10. At the CIT, select **CONFIGURATION-Retrieve-Equipment:All** to obtain the Equipment Report.
11. Determine from office records the correct circuit pack type for the slot in question.
12. Compare the Equipment Report with the office records and determine which indicates the correct circuit pack type.
13. Is the correct circuit pack type actually plugged into the slot?
  - If **YES**, then continue with Step 14.
  - If **NO**, then continue with Step 16.
14. At the CIT, select **CONFIGURATION-Update** to update the network element.
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
16. Obtain the correct circuit pack type according to the correct circuit pack source.



## Clear Trouble in CIT (CIT Does Not Respond to Commands)

**Overview:** This procedure is used to clear trouble within the Craft Interface Terminal (CIT) such as a software hang-up or a malfunction.

1. Was a notice frame (warning message) displayed on the CIT?  
If **YES**, then follow the instructions displayed on the screen and/or ask for technical assistance from your FT-2000 administrator.  
If **NO**, then continue with Step 2.
2. Press function key **F5**, labeled **EXIT**.  
 **NOTE:**  
Some keys on the keyboard may be locked up (inoperative).
3. Wait 1 minute or until the PC prompt (typically **C:>**) is displayed.
4. Are the keys on the keyboard operative?  
If **YES**, then continue with Step 7.  
If **NO**, then continue with Step 5.
5. Perform a hard boot on the PC by pressing the "RESET" switch or by turning the power switch off and then back on.
6. Did the PC reboot (**C:>** is displayed)?  
If **YES**, then continue with Step 7.  
If **NO**, then replace the PC.
7. Start the CIT software (change to directory where software is located, enter **cit**, and press ).
8. Is the FT-2000 CIT-PC software running?  
If **YES**, then go to Step 9.  
If **NO**, then verify that you are in the correct directory and/or ask for assistance from your FT-2000 technical support.

9. Does the FT-2000 respond to an attempt to login to the CIT(DCE) port on the front of the bay?
  - If **YES**, then go to Step 12.
  - If **NO**, then continue.
10. The CIT(DCE) port may be configured for TL1 messages, rather than CIT commands. This can be verified by any one of the following three options:
  - a. Connect the CIT-PC to the CIT(DTE) port labeled P158A on the interconnect panel of the low speed and attempting to login. A null modem (which is described in co.ntp002) is required between this port and the CIT-PC.
  - b. Have someone remote login to the bay and verify the CIT port settings with the command **SECURITY-Retrieve-Security-Port**. For front access using the CIT(DCE) port, the port must be "enabled" and the port type must be "CIT".
  - c. From the CIT program connected to the CIT(DCE) port, use the F3 (dial) key and try to access the bay using TL1 commands.
11. Does the problem still exist?
  - If **YES**, then contact your FT-2000 technical support organization and ask for assistance.
  - If **NO**, then continue.
12. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'DCC Startup in Progress'



**NOTE:**

The data communication channel (DCC) is starting up. This interaction between the FT-2000 network elements takes from 5 to 7 minutes.

1. Wait 7 minutes or until the **NE ACTY** LED goes off.
2. Did the "DCC startup in progress" condition clear?
  - If **YES**, then continue with Step 3.
  - If **NO**, then go to **TAP-108**.
3. Is the **NE ACTY** LED lighted?
  - If **YES**, then go to **Trouble Clearing: IXL-001**.
  - If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE**.



## Clear 'Site Provisioning Conflict'

1. Is this a point-to-point network or a ring network?  
If **point-to-point network**, then continue with Step 2.  
If **ring network**, then continue with Step 6.
2. Determine from office records which FT-2000 end terminal is to be "Site 0."  
 **NOTE:**  
In an FT-2000 point-to-point network, one and only one of the two FT-2000 end terminals must be provisioned to be "Site 0."
3. At each FT-2000 end terminal, obtain a report as follows: at the CIT, select **SECURITY-Retrieve-Network Element** and execute. Determine the value (either yes or no) of the **Site 0** parameter.
4. At the incorrectly provisioned FT-2000 end terminal and at the CIT, select **SECURITY-Set-Network Element** and execute to change the **Site 0** parameter to the correct value.
5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
6. Check the OC-48 optical connections to the adjacent nodes in both directions to verify that the East (West) TRMTR here is connected to the West (East) RCVR there and that the East (West) RCVR here is connected to the West (East) TRMTR there. Rearrange any incorrect connections.  
 **NOTE:**  
A likely cause of a **site provisioning conflict** condition is that the East (West) TRMTR at one node is connected to the East (West) RCVR at the next node.
7. At the CIT, select **FAULT-Retrieve-Alarm (and Status)** to obtain another Active Alarms and Status Report.
8. Does the **site provisioning conflict** condition still appear in the report?  
If **YES**, then continue with Step 9.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
9. Consult the appropriate maintenance support organization for further technical assistance.
10. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'Software Download in Progress'



**NOTE:**

A software download can take as long as 2 hours at 9600 baud (less time at the higher baud rate of 19,200 or 38,400).

1. Wait for the download to be completed.
2. Did the **software download in progress** condition clear?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then refer the trouble to the people doing the software download.





## Clear 'Incoming OC-48 APS Byte Fail'



**NOTE:**

This condition is associated with a signal degrade (SD) or an automatic protection switch (APS) channel failure on the protection line.

1. At the CIT, select **FAULT-Retrieve-Alarm** to obtain an Active Alarms and Status Report.
2. Is the **incoming OC-48 SD** condition shown on the report for the protection line?
  - If **YES**, then go to **TAP-110**.
  - If **NO**, then continue with Step 3.
3. Is the **APS channel failure** condition shown on the report for the protection line?
  - If **YES**, then go to **TAP-105**.
  - If **NO**, then go to **TAP-108**.



## Clear 'SYSMEM Removed'



**CAUTION:**

*Do not remove the SYSCTL circuit pack. If removed, provisioning data (current values) will be lost. Current values were provisioned with the **Set** command.*

1. Obtain a replacement SYSMEM circuit pack having the same or a higher series number.
2. Install the replacement SYSMEM circuit pack.

Reference: **DLP-514**

3. Wait 10 minutes or until the **MJ** LED goes off.
4. Is the **NE ACTY** LED off?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then go to **TAP-103**.



## Clear 'Clock Out of Range'

⇒ **NOTE:**  
This condition indicates that the operating frequency of one or both of the TG3 circuit packs supplying synchronization to the TRMTR circuit packs has gone out of the recommended range.

1. At the CIT, select **FAULT-Retrieve-Alarm** to obtain an Active Alarms and Status Report.
2. Is one or more of the conditions listed in Table A indicated in the report?

If **YES**, then find this second associated condition (identified below) in Table A of **TAP-102** and go to the referenced TAP.

If **NO**, then continue with Step 3.

**Table A - Possible Associated Conditions**

|                           |  |
|---------------------------|--|
| incoming sync ref failure |  |
| TG3 failure               |  |
| TG3 removed               |  |

3. At the CIT, select **CONFIGURATION-Retrieve-Synchronization** to obtain a report.
4. From the Synchronization Report (Step 3), record the values of the **TG1 Clock Range Status** and **TG2 Clock Range Status** parameters.
5. Refer to local operating procedures for further guidance in addressing this condition.

⇒ **NOTE:**  
This condition, by itself, does not indicate a failure with the FT-2000 network element.



## Clear (Optical Parameter) 'Out-Of-Range'

⇒ **NOTE:**  
This condition indicates that one or more of the operating parameters of a RCVR, TRMTR, or REGENR circuit pack has exceeded the recommended range.

1. At the CIT, select **FAULT-Retrieve-Alarm** to obtain an Active Alarms and Status Report.
2. Is one or more of the conditions listed in Table A indicated in the report?

If **YES**, then find this second associated condition (identified below) in Table A of **TAP-102** and go to the referenced TAP.

If **NO**, then continue with Step 3.

**Table A - Possible Associated Conditions**

|                    |  |
|--------------------|--|
| incoming OC-48 LOS |  |
| RCVR failure       |  |
| TRMTR failure      |  |
| REGENR failure     |  |
| line FERF          |  |

⇒ **NOTE:**  
This condition, by itself, does not indicate a failure with the FT-2000 network element.

⇒ **NOTE:**  
At an FT-2000 end terminal and at the CIT, select **PERFORMANCE-Retrieve-PM-Optics** to obtain a report that shows the transmit power, receive power, and the laser bias current with respect to time.

⇒ **NOTE:**  
OC-48 optical lines can be tested by using optical line meters and/or optical time domain reflectometers (OTDR) to help locate a fiber bend or cut.

3. Refer to local operating procedures for further guidance in addressing this condition.





## Clear Alarm When 'NE ACTY' and 'FE ACTY' LEDs Are Off (Cannot Log in to a Network Element)

**Overview:** This procedure is used to clear an active office alarm originating at an FT-2000 network element when both the **NE ACTY** and the **FE ACTY** LED on the user panel are off. This procedure is also used when a login cannot be established via any port to a Network Element.

1. Is the green **POWER ON** LED on the user panel lighted?  
If **YES**, then continue with Step 2.  
If **NO**, then go to **TAP-119**.
2. Is the CIT conditioned and connected to the FT-2000 network element?  
If **YES**, then continue with Step 3.  
If **NO**, then complete **DLP-501** and return to this procedure.

⇒ **NOTE:**  
It is unlikely that the CIT will be able to communicate with the network element under these conditions.

3. At the CIT, select **FAULT-Retrieve-Alarm** to try to obtain an Active Alarms and Status Report.
4. Are you able to communicate with the network element (obtained an Active Alarms and Status Report)?  
If **YES**, then continue with Step 5.  
If **NO**, then continue with Step 6.
5. Make a note that the office alarm outputs and/or the **NE ACTY** and **FE ACTY** LEDs on the user panel are not operating properly and go to **TAP-108**.

⇒ **NOTE:**  
The SYSCTL and/or the SYSMEM circuit pack is either failed or removed.

6. Remove the shelf cover from the Low Speed Shelf - System Controller.
7. Are SYSCTL and SYSMEM circuit packs installed?  
If **YES**, then continue with Step 8.

If **NO**, then continue with Step 16.

8. If the bay is still in service, remove the LNCTL circuit pack.
9. Remove the SYSMEM circuit pack and install a replacement circuit pack having the same or a higher series number. For example, a series number **S1:2** is the same series number as **S1:4**. The 2 and 4 refer to minor changes within series 1.

Reference: **DLP-514**

10. Wait 10 minutes. (If an Active Alarms and Status Report is obtained during this time and the Description Column shows a **reset in progress** condition, don't worry because this is a normal software download operation.)
11. Does one or both of the following conditions exist?

- a. The **NE ACTY** and/or **FE ACTY** LED are lighted.
- b. The office alarm(s) is off.

If **YES**, then continue with Step 19.

If **NO**, then continue with Step 12.

12. Reinstall the original SYSMEM circuit pack removed in Step 9.

Reference: **DLP-514**

13. Remove the SYSCTL circuit pack and install a replacement circuit pack having the same or a higher series number. For example, a series number **S1:2** is the same series number as **S1:4**. The 2 and 4 refer to minor changes within series 1.

Reference: **DLP-514**

14. Wait 10 minutes.
15. Does one or both of the following conditions exist?

- a. The **NE ACTY** and/or **FE ACTY** LED are lighted.
- b. The office alarm(s) is off.

If **YES**, then continue with Step 19.

If **NO**, then contact your local maintenance support group for a higher level of technical assistance.

16. Install a replacement SYSMEM and/or SYSCTL circuit pack in the appropriate slot(s).

Reference: **DLP-514**

17. Wait 10 minutes, or until the **NE ACTY** or **FE ACTY** LED is on, or until the office alarm(s) is off, whichever comes first.
18. Does one or both of the following conditions exist?
  - a. The **NE ACTY** and/or **FE ACTY** LED is lighted.
  - b. The office alarm(s) is off.

If **YES**, then continue with Step 19.

If **NO**, then contact your local maintenance support group for a higher level of technical assistance.
19. Log in to the network element as directed by the instructions on the CIT screen.
20. At the CIT, select **CONFIGURATION-Retrieve-Equipment** and **SYSCTL** to obtain an Equipage and Version Report.
21. Refer to the Equipage and Version Report and compare the software version stored in the SYSMEM (shown under the **sysmem** entry in the report) and the software version running in the network element (shown under the **sysctl** entry in the report) with office records. If office records are not available, log in remotely to another network element and obtain a report to determine what software version is running in the network.
22. Is the correct software version stored in the SYSMEM and running in the network element?

If **YES**, then continue with Step 30.

If **NO**, then continue with Step 23.
23. Install the appropriate software into the SYSMEM circuit pack.

Reference:   **Operation: NTP-002**
24. At the CIT, select **FAULT-Reset** and execute.
25. Wait 10 minutes or until the **MJ** LED is off at the user panel, whichever comes first. Reset takes approximately 10 minutes, but some user panel LEDs flash and relays click in about a minute, indicating that reset has started.
26. Log in to the network element as directed by the instructions on the CIT screen.

27. At the CIT, select **CONFIGURATION-Retrieve-Crossconnection**. Verify that all cross-connections are present and are correct.

If the cross-connections are correct, proceed to Step 28.

If the cross-connections are corrupt, they must be corrected.

**Reference: DLP-536**

28. Insert the LNCTL circuit pack. This will cause a reset of the OHCTL packs, the TOHCTL pack, and all transmission packs.
29. If there is a stuck "Circuit Audit Suspended" alarm, reset the bay by executing **FAULT -Reset**.
30. Is the **NE ACTY** LED off?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE**.

If **NO**, then go to the **Trouble Clearing: IXL-001**

## Clear 'DS-NE Not Reachable'

1. Before performing the rest of this procedure, refer to **IXL-001** and clear any other alarms that could be responsible for a 'DS-NE Not Reachable' condition.

**Reference:**   **IXL-001**

2. Does the "DS-NE Not Reachable" condition still persist?

    If **Yes**, continue with Step 4.

    If **No**, then continue with Step 3.

3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

- 4.



**NOTE:**

This condition indicates that one of the network elements (nodes) in the ring network has not been provisioned "DS-NE."

Determine from office records which node is designated the "Directory Server" (DS-NE) in the ring network.

- 5.



**NOTE:**

When the ring network is in this state, you will probably not be able to establish remote login to one or more of the other nodes. If a remote login is possible and if you need additional help, refer to DLP-524. Otherwise, local CIT assistance will be required at those nodes.

Ask for assistance at the DS-NE location or travel to that location and condition and connect the CIT to the network element.

**Reference:**   **DLP- 501**

6. At the CIT connected to the node designated to be the DS-NE, select **SECURITY-Set-Network Element** and provision the **Directory Server** parameter (DS-NE) of that network element to **Yes**. The remaining nodes in the ring network must be provisioned to **No** for the **Directory Server** parameter.

**Nice to Know:** If you are using Release 4.1 or later software, you can verify that only one node is provisioned as the DS-NE by obtaining a Ring Map Report (at the CIT, select **CONFIGURATION-Retrieve-Map-Ring**. If you are using Release 3 or 4.0 software, you must log in to each node in the ring network to verify the **yes** or **no** DS-NE parameter value (at the CIT, select **CONFIGURATION-Retrieve-Network Element**).

7. Log off from the network element.
8. Wait 5 minutes for the ring diagnostics to stabilize. After this time, the **DS-NE not reachable** condition should be cleared.
9. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'Duplicate Node ID Defined' or 'Duplicate TID Defined'



**NOTE:**

A recently entered node identifier (NID) (release 5 and earlier) or target identifier (TID) value is a duplicate of another network element in the ring network.

1. Determine from office records the correct NID or TID for the network elements showing the duplicate values.
2. Log in to the network element that is in error. If a remote login is required and you need additional help, refer to **DLP-524**.
3. At the CIT, select **SECURITY-Set-Network Element** and enter the correct NID (release 5 and earlier) or TID for that network element.
4. Log off from the network element, as required.
5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Clear 'Multiple DS-NEs Defined'

⇒ **NOTE:**  
This condition means that two or more network elements in a network have their "Directory Server" (DS-NE) parameter provisioned to "Yes." There must be one and only one DS-NE in the network.

1. At the CIT, select **CONFIGURATION-Retrieve-Map-Network** to obtain a Network Map Report. The topmost entry shows the TID of the local node, which is where the report is requested, followed by all remaining nodes in a west-to-east direction.

⇒ **NOTE:**  
To clear the **multiple DS-NEs defined** condition, all nodes in the network must first be provisioned to **No**, then only the designated node is provisioned to **Yes**.

2. At the CIT, select **SECURITY-Set-Network Element** and provision the **Directory Server** parameter to **No**.

⇒ **NOTE:**  
When the network is in this state, you will probably not be able to establish remote login to one or more of the other nodes. If a remote login is possible and if you need additional help, refer to DLP-524. Otherwise, local CIT assistance will be required at those nodes.

3. Ask for assistance at the next node on the report or travel to that location and condition and connect the CIT to the network element.

Reference: **DLP-501**

4. After you are logged in to that node, repeat Step 2.
5. Log off from the network element.
6. Repeat Steps 3, 4, and 5 for each of the remaining nodes in the network.

⇒ **NOTE:**  
A transient **squelch map conflict** condition may appear for up to 2 minutes or until the network stabilizes at each node that was incorrectly designated as the DS-NE.

7. Determine from office records which network element is to be the DS-NE.

8. Log in to that network element (the designated DS-NE). If a remote login is possible and you need additional help, refer to **DLP-524**.
9. At the CIT, select **SECURITY-Set-Network Element** and provision the **Directory Server** parameter to **Yes**.
10. At the CIT, select **CONFIGURATION-Retrieve-Map-Network** to obtain a Network Map Report.
11. Verify from the report that **one and only one node** is provisioned as the DS-NE.

**Nice to Know:** If you are using Release 6 or later software, you can verify that only one node is provisioned as the DS-NE by obtaining a Network Map Report (at the CIT, select **CONFIGURATION-Retrieve-Map-Network**). If you are using Release 4.1 or 5 software, you can verify that only one node is provisioned as the DS-NE by obtaining a Ring Map Report (at the CIT, select **CONFIGURATION-Retrieve-Map-Ring**). If you are using Release 3 or 4.0 software, you must log in to each node in the ring to verify the **yes** or **no** DS-NE parameter value (at the CIT, select **CONFIGURATION-Retrieve-Network Element**).

12. Log off from the network element.
13. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'Node ID Conflict'



**NOTE:**

This condition is normally a transient condition.

1. Wait 5 minutes or until the **NE ACTY** LED on the user panel goes off.
2. At the CIT, select **FAULT-Retrieve-Alarms** to obtain an Active Alarms and Status Report.
3. Has the **node ID conflict** condition cleared?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then continue with Step 4.



**NOTE:**

The **node ID conflict** alarm may have occurred during node addition/deletion if the procedure in this manual was not followed properly, or it may have occurred after an isolated node was restored.

4. Try removing and reconnecting the OC-48 fibers associated with the line direction shown in the CIT Active Alarms and Status Report.
5. Has the **node ID conflict** condition cleared?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then go to **TAP-108** to clear the unexpected response.



## Clear 'Ring Incomplete/Open'



**NOTE:**

This condition indicates that the designated DS-NE network element is unable to create a complete ring map because of a controller or transmission failure in the ring network.

1. At the CIT, select **FAULT-Retrieve-Network Alarms** to obtain a Network Alarms Report.
2. Does a controller circuit pack (SYSCTL, SYSMEM, LNCTL, or OHCTL) condition or an incoming OC-48 condition appear in the Active Alarms and Status Reports?

If **YES**, then go to **TAP-102** to clear the indicated condition(s).

If **NO**, then continue with Step 3.



**NOTE:**

You cannot obtain a valid Ring Map Report while this condition exists.

3. Consult office records to determine the Target Identifiers (TIDs) of the other nodes in the ring.
  4. Establish a remote login to any one of the other network elements identified in Step 3. If you need help with the remote login, refer to **DLP-524**.
  5. At the CIT, select **FAULT-Retrieve-Alarms** to obtain an Active Alarms and Status Report for that network element.
  6. Does a controller circuit pack (SYSCTL, SYSMEM, LNCTL, or OHCTL) condition or an incoming OC-48 condition appear in the Active Alarms and Status Reports?
- If **YES**, then go to **TAP-102** to clear the indicated condition(s).
- If **NO**, then continue with Step 7.
7. Repeat Steps 4 through 6 for each of the remaining network elements identified in Step 3.
  8. Is the **ring incomplete/open** condition still active at the original network element?

If **YES**, then continue with Step 9.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

9. Consult the appropriate maintenance support organization for further technical assistance.
10. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'Ring Startup in Progress'



**NOTE:**

This condition is normally a transient condition.

1. Wait 5 minutes or until the **NE ACTY** LED on the user panel goes off.
2. At the CIT, select **FAULT-Retrieve-Alarms** to obtain an Active Alarms and Status Report.
3. Has the **ring startup in progress** condition cleared?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then go to **TAP-108** to clear the unexpected response.





## Address 'Extra Traffic Preempted'



**NOTE:**

This condition indicates that the extra traffic routed over the protection tributaries (9 through 16) has been preempted by a line protection switch. This condition (**extra traffic preempted**) is normally cleared by correcting the associated condition(s) that caused the automatic line protection switch.

1. At the CIT, select **FAULT-Retrieve-Alarms (and Status)** to obtain the Active Alarms and Status Report.
2. Referring to the report obtained in Step 1, are there any conditions other than **extra traffic preempted** listed?
  - If **YES**, then continue with Step 3.
  - If **NO**, then initiate a Trouble Report to the remote FT-2000 network element (or source of failure) stating the **extra traffic preempted** condition.
3. Is the **line auto switch** condition reported?
  - If **YES**, then go to **TAP-115** to clear this condition that was caused by an automatic line switch.
  - If **NO**, then continue with Step 4.
4. Referring to the report obtained in Step 1, identify the topmost condition other than the **extra traffic preempted** and **line auto switch** conditions.
5. Find the condition identified in Step 4 in Table A of **TAP-102** and go to the indicated TAP to clear that condition.



## Clear 'Path TCA', 'Section TCA', or 'Line TCA'

1. This condition indicates that a threshold-crossing alert (TCA) has been issued. The alert means that the signal degrade threshold has been exceeded.



**NOTE:**

At the CIT, select **FAULT-Retrieve-Alarms (and Status)** to obtain the Active Alarms and Status Report.

2. Referring to the report obtained in Step 1, are there any conditions other than **path TCA, section TCA, or line TCA** listed?  
If **YES**, then continue with Step 3.  
If **NO**, then consult and follow local operating procedures for further guidance in addressing this condition.
3. Referring to the report obtained in Step 1, identify the topmost condition other than the **path TCA, section TCA, or line TCA** conditions.
4. Find the condition identified in Step 3 in Table A of **TAP-102** and go to the indicated TAP to clear that condition.



## Clear 'Incoming OC-48 Invalid F Byte'

1. Referring to the Active Alarms and Status Report on the CIT screen, is the Source Address for the "incoming OC-48 invalid F byte" condition that of an FT-2000 Repeater Bay/Shelf or of an OC-48 line?

If **FT-2000 Repeater Bay/Shelf**, then continue with Step 2.

If **OC-48 Line**, then continue with Step 4.



**NOTE:**

The condition to be cleared is at a remote FT-2000 Repeater Bay/Shelf.

2. Go to **TAP-147** to address the "incoming OC-48 invalid F byte" condition.
3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



**NOTE:**

The **incoming OC-48 invalid F byte** condition is issued whenever slot provisioning occurs at a repeater. This condition is normal during this automatic slot provisioning. This condition is normally cleared by correcting the associated condition(s) that caused the **incoming OC-48 invalid F byte** condition.

4. Wait 5 minutes for the condition to clear.
5. At the CIT, select **FAULT-Retrieve-Alarms (and Status)** to obtain the Active Alarms and Status Report.
6. Referring to the report obtained in Step 5, are there any conditions other than **incoming OC-48 invalid F byte** listed?  
If **YES**, then continue with Step 7.  
If **NO**, then continue with Step 10.
7. Referring to the report obtained in Step 5, identify the topmost condition other than the **incoming OC-48 invalid F byte** condition.
8. Find the condition identified in Step 7 in Table A of **TAP-102** and go to the indicated TAP to clear that condition.
9. Has the **incoming OC-48 invalid F byte** condition cleared?  
If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**  
If **NO**, then go back to Step 5.

10. Are you responsible for maintaining the remote FT-2000 network element(s) as well as the local FT-2000 network element?

If **YES**, then continue with Step 11.

If **NO**, then initiate a Trouble Report to the remote FT-2000 network element (or source of failure) stating the **incoming OC-48 invalid F byte** condition.

11. At the CIT, select **FAULT-Retrieve-Network Alarms** to obtain a Network Alarms Report.
12. Establish a remote login to the network element appearing at the topmost position in the Network Alarms Report. If a remote login is required and you need additional help, refer to **DLP-524**.
13. At the CIT, select **FAULT-Retrieve-Alarms** to obtain an Active Alarms and Status Report for that network element.
14. Repeat Steps 5 through 10, and 12 and 13 for each of the remaining network elements listed in the Network Alarms Report from Step 11.
15. Has the **incoming OC-48 invalid F byte** condition cleared?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then go to **TAP-108** to clear the unexpected response.

## Address 'Traffic Squelched'



**NOTE:**

The **traffic squelched** condition indicates that multiple failures have occurred in the ring network and have resulted in the traffic being squelched (stopped) to eliminate any possibility of it being misrouted. A **traffic squelched** condition is normally cleared by correcting the associated condition(s) that caused the **traffic squelched** condition.

1. At the CIT, select **FAULT-Retrieve-Alarms (and Status)** to obtain the Active Alarms and Status Report.
2. Referring to the report obtained in Step 1, are there any conditions other than **traffic squelched** listed?  
If **YES**, then continue with Step 3.  
If **NO**, then initiate a Trouble Report to the remote FT-2000 network element (or source of failure) stating the **traffic squelched** condition.
3. Referring to the report obtained in Step 1, identify the topmost condition other than the **traffic squelched** condition.
4. Find the condition identified in Step 3 in Table A of **TAP-102** and go to the indicated TAP to clear that condition.





## Address Condition at Remote Repeater Bay/Shelf

**Overview:** This procedure is used to address a condition at a remote FT-2000 Repeater Bay/Shelf in response to one of these indicated conditions:

- Incoming 0C48 B1 parity error optical parameter out-of-range
- Incoming 0C48 invalid F byte repeater bay environment input
- Incoming 0C48 LOS/LOF repeater bay equipment failure



**NOTE:**

The condition to be cleared is at a remote FT-2000 Repeater Bay/Shelf.

1. Referring to the Active Alarms and Status Report on the CIT screen, note and record the Source Address for the condition that sent you to this procedure (the topmost condition in the report).
2. Issue a Trouble Report to the FT-2000 Repeater Bay/Shelf location with the indicated Source Address. The Trouble Report should include the text from the Description field of the report.
3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'CP (Unknown Type) Removed'



**NOTE:**

The **Source Address** of the removed circuit pack is shown in the Active Alarms and Status Report.

1. Identify the appropriate shelf associated with the removed circuit pack as indicated in Table A.

**Table A - Source Addresses and Associated Shelves**

| Source Address                                  | Associated Shelf                                                             |
|-------------------------------------------------|------------------------------------------------------------------------------|
| ls-( )<br>lssw-( )<br>tg-( )                    | Low Speed Shelf - Complementary<br>or<br>Low Speed Shelf - System Controller |
| trmtr-( )<br>rcvr-( )<br>lnctl-( )<br>ohctl-( ) | High Speed Shelf<br>or<br>Enhanced High Speed Shelf                          |
| regenr-( )                                      | Repeater Bay/Shelf                                                           |

2. Remove the appropriate shelf cover.  
Reference: **DLP-511**
3. Is the indicated slot empty?  
If **YES**, then continue with Step 4.  
If **NO**, then go to **TAP-108**.
4. Do office records show that the slot should contain a circuit pack?  
If **YES**, then continue with Step 8.  
If **NO**, then continue with Step 5.
5. At the CIT, select **CONFIGURATION - Update** (records the current hardware arrangement in SYSMEM circuit pack).

6. Replace the shelf cover.

Reference: **DLP-511**

7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

8. Obtain the type of circuit pack determined from office records and install it in the appropriate slot. As required, make the appropriate optical connections on the OC-3, IS-3, OC-12, RCVR, TRMTR, or REGENR circuit pack(s).

Reference: **DLP-514**

9. Wait for the time indicated in Table B:

| <b>Table B - Mini Reset Waiting Times</b> |                     |
|-------------------------------------------|---------------------|
| <b>CP Inserted</b>                        | <b>Waiting Time</b> |
| LNCTL *                                   | 10 minutes          |
| OHCTL                                     | 5 minutes           |
| TG3 †                                     | 5 minutes           |
| TOHCTL                                    | 5 minutes           |
| All Others                                | 35 seconds          |

\* Inserting an LNCTL circuit pack starts a mini reset that can take up to 10 minutes to complete. During this time, LEDs on other circuit packs will be lighted.

† When a TG3 circuit pack is first inserted, it will be switched out of service until it warms up. This can take as long as 5 minutes. You can confirm that it is in the standby state by using the **FAULT-Retrieve-State** command for the TG slot.

10. Is the **FAULT** LED lighted on the replacement circuit pack?

If **YES**, then replace shelf cover and go to **Trouble Clearing: IXL-001**.

If **NO**, then continue with Step 11.

11. Replace shelf cover.

Reference: **DLP-511**

12. Is the **NE ACTY** LED lighted at the user panel.

If **YES**, then go to **Trouble Clearing: IXL-001**.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear DRI Switch

**Overview:** This procedure is used to clear one of the following conditions:

- DRI auto switch
- DRI manual switch

1. Which switch condition is to be cleared?

If **DRI auto switch**, then continue with Step 2.

If **DRI manual switch**, then continue with Step 8.



**NOTE:**

Conditions are shown in the "**Description**" column of the Active Alarms and Status Report.

2. Is there one or more other conditions with the same STS3 (or STS1) tributary **Source Address** in the report?  
If **YES**, then continue with Step 3.  
If **NO**, then continue with Step 5.
3. Identify the topmost other conditions in the report with the same STS3 (or STS1) tributary **Source Address**. Find this condition in Table A of **TAP-102** and go to the referenced TAP.
4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



**NOTE:**

If the system is running R7.0.x or earlier software, the automatic DRI switch was caused by an incoming loss of signal condition on the associated low-speed input. The loss of signal condition is no longer active. The nonrevertive automatic DRI switch must now be reset.

5. At the CIT, select the **FAULT-Switch-Path-STS3 (or STS1)** command with a priority of RESET to reset the switch.
6. If the NE is running R7.1.0 or later software, the automatic DRI switch may have been caused by loss of signal (LOS), loss of Frame (LOF), and, with SONET interfaces by Alarm Indication Signal (AIS-P). It will automatically revert to normal upon restoral of the signal.
7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



**NOTE:**

The DRI manual switch was initiated by a user command. You must determine, by consulting with the appropriate maintenance personnel, whether the need for the DRI manual switch still exists before proceeding.

8. Does the need for the DRI manual switch still exist?
  - If **YES**, then continue with Step 9.
  - If **NO**, then continue with Step 10.
9. Referring to the Active Alarms and Status Report on the CIT screen, are any conditions indicated other than the DRI manual switch?
  - If **YES**, then go to **TAP-102** to clear the remaining condition(s) ignoring the DRI manual switch condition.
  - If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
10. At the CIT, select the **FAULT-Switch-Path-STS3 (or STS1)** command with a priority of RESET to reset the switch.
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'APS Provisioning Conflict'

⇒ **NOTE:**  
An "APS provisioning conflict" condition means that the line protection switching configuration (1xN or 1+1) or directionality (unidirectional or bidirectional) is provisioned differently at the two ends of the line.

1. Referring to the topmost entry in the Active Alarms and Status Report on the CIT screen, is the **Source Address** of the line with an "APS provisioning conflict" condition that of an OC-48, OC-3, or an OC-12 line?
  - If **OC-48**, then continue with Step 2.
  - If **OC-3**, then continue with Step 7.
  - If **OC-12**, then continue with Step 7.
2. At the CIT, select the **CONFIGURATION-Retrieve-Protection-Line** command to obtain a Network Element Protection Switching Provisioning Report and note the entries in the report for "Configuration" and "Directional."
3. Do the values of "Configuration" and "Directional" recorded in Step 2 match those from the office records?
  - If **YES**, then continue with Step 7.
  - If **NO**, then continue with Step 4.
4. At the CIT, select the **CONFIGURATION-Set-Protection-Line** command to provision the correct values for "Configuration" and/or "Directional."
5. At the CIT, select the **FAULT-Retrieve-Alarm** command to obtain another Active Alarms and Status Report.
6. Does the "APS provisioning conflict" condition still appear in the report?
  - If **YES**, then continue with Step 7.
  - If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

⇒ **NOTE:**  
The FT-2000 OC-3 interface is always provisioned for 1+1 unidirectional protection switching or it is unprotected (0x1).

⇒ **NOTE:**  
For FT-2000 Release 7.1 the OC-12 interface is always provisioned for 1+1 unidirectional protection switching or it is 0x1 unprotected.

7. Issue a Trouble Report to the network element at the other end of the line indicating the "APS provisioning conflict" condition and the provisioned "Configuration" and "Directional" values recorded at this end of the line.
8. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Address 'CP Reset In Progress'

⇒ **NOTE:**  
When a circuit pack is installed, it is downloaded with the necessary software. While this is occurring, the **CP reset in progress** condition will be active.

⇒ **NOTE:**  
The **CP reset in progress** condition can also be reported for the OHCTL condition if the **SECURITY-Set-Network Element** command is used to change the value of the DS-NE parameter.

1. Find the circuit pack with the **CP reset in progress** condition in Table A and wait for the indicated time.

**Table A - Circuit Pack Reset Time**

| <b>Circuit Pack</b> | <b>Time</b> |
|---------------------|-------------|
| SYSCTL              | 10 minutes  |
| SYSTEM              | 10 minutes  |
| LNCTL               | 10 minutes  |
| OHCTL               | 5 minutes   |
| TOHCTL              | 5 minutes   |
| All Others          | 35 seconds  |

2. At the CIT, select the **FAULT-Retrieve-Alarm** and execute to obtain another Active Alarms and Status Report.
3. Is the **CP reset in progress** condition still indicated for the same circuit pack?

If **YES**, then go to **TAP-108**.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE**.



## Clear 'Inc. (from OC48) Sync Ref Fail'



**NOTE:**

An "**inc. (from OC48) sync ref fail**" condition is associated with another condition that prevents the TG circuit pack from receiving a good synchronization reference signal from the associated RCVR circuit pack.

1. Does an "incoming OC-48 LOS," "RCVR failure," or "RVCR removed" condition also appear in the Active Alarms and Status Report on the CIT screen?

If **YES**, then find that condition in Table A of **TAP-102** and go to the indicated Trouble Clearing procedure.

If **NO**, then go to **TAP-108**.

2. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'STS3 Squelch Map Conflict', 'STS1 Squelch Map Conflict', or 'Squelch Map Conflict'

⇒ **NOTE:**  
An **STS3 (or STS1) squelch map conflict** (Release 5 and later) or **squelch map conflict** (Release 4.1) condition is most likely caused by the controller circuits being unable to associate any network element's target identifier (TID) with the value supplied for a source or destination node of a cross-connection.

⇒ **NOTE:**  
The Destination/Source(s) with an **STS3 (or STS1) squelch map conflict** or **squelch map conflict** condition are indicated by an asterisk (\*) appearing immediately before the TID in the report.

⇒ **NOTE:**  
The following are possible causes of the **STS3 (or STS1) squelch map conflict** or the **squelch map conflict** condition:

- A condition could have been caused when the network was being upgraded from Release 3 or 4 to Release 4.1 or later. The pre-upgrade cross-connections had nonexistent NIDs with their Source/Destination.
- A possible cause is the user entered a nonexistent TID while executing the **CONFIGURATION-Enter-Crossconnect-STS3 (or STS1)** or the **CONFIGURATION-Enter-Roll-STS3 (or STS1)** command.
- A node was deleted from the ring but not the cross-connections to that node from the remaining nodes in the ring.
- A possible cause of this condition is that the wrong TID/NID has been entered when executing the **SECURITY-Set-Network Element** command.

1. At the CIT, select the **CONFIGURATION-Retrieve-Crossconnection-STS3 (or STS1)** command and execute to obtain an STS-3 (or STS-1) Cross-Connection Map Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
2. Find the cross-connection with the invalid flag (invalid flag is specified with \*TID).

3. Consult office records to determine the proper TID and/or NID for the Source/Destination of the troublesome cross-connection (\*TID).
4. Is the TID/NID from the report (Step 2) the same as that specified in the office records (Step 3).  
    If **YES**, then continue with Step 5.  
    If **NO**, then continue with Step 8.
5. Log in to the node with the incorrect TID/NID. If a remote login is required and you need additional help, refer to **DLP-524**.
6. At the CIT, select the **SECURITY-Set-Network Element** command and execute to correct the TID and/or NID.
7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
8. Is the Source/Destination node of the troublesome cross-connection from a node that has been deleted?  
    If **YES**, then continue with Step 9.  
    If **NO**, then continue with Step 13.
9. Consult office records and verify that the cross-connection is no longer needed.
10. Identify the nodes with the incorrect cross-connections and log in to the nodes one at a time.
11. At the CIT, select the **CONFIGURATION-Delete-Crossconnection-STS3 (or STS1)** command and execute to delete the cross-connections for the deleted node.
12. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
13. Consult office records to determine the correct TID/NID for the Source/Destination of the troublesome cross-connection (\*TID).
14. At the CIT, select the **CONFIGURATION-Enter-Crossconnection-STS3 (or STS1)** command and correct the Source/Destination for each of the cross-connections with an asterisk in the STS-3 (or STS-1) Cross-Connection Map Report.
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Address 'TG Warming Up'



**NOTE:**

When a TG circuit pack is installed (or if it fails and recovers while still inserted), it must be allowed to reach a stable operating temperature before it is switched into service. This warming up process takes as long as 5 minutes.

1. Wait 5 minutes or until the **NE ACTY** LED on the local FT-2000 user panel goes off, whichever comes first.
2. At the CIT, select **FAULT-Retrieve-Alarm** and execute to obtain another Active Alarms and Status Report.
3. Is the "**TG warming up**" condition still active for the same circuit pack?

If **YES**, then go to **TAP-108**.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Clear Suspected Failure in FT-2000 Network Element(s) Via Remote Modem (or Datakit<sup>®</sup> Network) Connection

**Overview:** This procedure is used to clear a suspected failure in one or more FT-2000 network elements when you are **not** physically located at any of the network elements in the network. If you are located at one of the network elements, go to Trouble Clearing: IXL-001 and select the procedure, from among the others listed, that most closely matches the actual situation.

1. Is the CIT conditioned and remotely connected, via modem or *Datakit* network, to an FT-2000 network element in the network?

If **YES**, then continue with Step 2.

If **NO**, then complete **DLP-501** and **DLP-521** and return to this procedure.

⇒ **NOTE:**  
Throughout this procedure, the FT-2000 network element to which you are now connected will be called the "local" network element.

2. At the CIT, select **FAULT-Retrieve-Network-Alarms** to obtain a Network Alarms Report (Figure 1 shows an example for Release 6; other examples for earlier releases are shown in Chapter 11 of this manual).
3. Are there any entries in the Network Alarms Report?

If **YES**, then continue with Step 5.

If **NO**, then continue with Step 4.

⇒ **NOTE:**  
There are no active alarm or status conditions in the network(s).

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

⇒ **NOTE:**  
The Network Alarms Report will look like the example in Figure 1 if the local network element is an FT-2000 Add/Drop-Rings Terminal with Release 6 or later software. Earlier software releases produce slightly different reports which are shown in Chapter 11 of this manual.

5. Referring to the column headings of the Network Alarms Report obtained in Step 2, is the rightmost column labeled **Local Ring** (as in Figure 1), **TID**, or **Event Description**?

If **Local Ring**, then continue with Step 9.

If **TID**, then continue with Step 9.

If **Event Description**, then continue with Step 6.

6. Referring to the topmost entry in the Network Alarms Report obtained in Step 2, is there any text in the **Event Description** field?

If **YES**, then continue with Step 7.

If **NO**, then continue with Step 9.

⇒ **NOTE:**  
The condition to be cleared is at an FT-2000 Repeater Bay/Shelf. You cannot log in to the Repeater Bay/Shelf from a remote location.

7. Referring to the topmost entry in the Network Alarms Report obtained in Step 2, issue a Trouble Report about the FT-2000 Repeater Bay/Shelf location with the indicated **Source Address**. The Trouble Report should include the text from the **Event Description** field.
8. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

⇒ **NOTE:**  
The Target ID (TID) of the local FT-2000 network element appears at the top of the CIT screen between the software release designation and **LOCAL**.

⇒ **NOTE:**  
Depending on the software release running in the local FT-2000 network element, the column in the Network Alarms Report indicating the TID associated with a particular condition may be labeled **TID** (as in Figure 1) or **Source Address**.

9. Referring to the topmost entry in the Network Alarms Report obtained in Step 2, is the entry in the **TID** or **Source Address** column the TID of the local FT-2000 network element?

If **YES**, then continue with Step 11.

If **NO**, then continue with Step 10.

⇒ **NOTE:**  
The trouble to be cleared is at another FT-2000 network element. Press the CIT function key **F2**, labeled **REMOTE**, and log in to the other FT-2000 network element with the TID identified in Step 9.

Reference: **DLP-524**

⇒ **NOTE:**  
From this point on, this procedure is written to support troubleshooting performed by someone physically located at the FT-2000 network element in question. For example, the procedure assumes that you can see the LEDs on the equipment, and obviously, you can not see them. The procedure also assumes that you can perform visual inspections and replace circuit packs, and you can not do these things either. At some point in this procedure, you will likely require assistance at the FT-2000 network element in question.

⇒ **NOTE:**  
When the procedure asks if the **NE ACTY** LED is lighted, you can use the CIT to select **FAULT-Retrieve-Alarm (and Status)** to obtain an Active Alarms and Status Report. If there are one or more entries in the report, you can assume that **NE ACTY** is lighted.

⇒ **NOTE:**  
When the procedure asks if the **FAULT** LED is lighted on a particular circuit pack, you can use the CIT to select **FAULT-Retrieve-Alarm (and Status)** to obtain an Active Alarms and Status Report. If the circuit pack in question is shown as failed in the report, you can assume that its **FAULT** LED is lighted.

10. Go to **TAP-102** to clear the alarm and/or status condition(s) at the FT-2000 network element you are logged in to.

11. STOP! YOU HAVE COMPLETED THIS PROCEDURE.

```
CRITICAL Lucent Technologies FT-2000 3.0.0-ADR Delta-1 LOCAL

Delta-1 93-01-19 23:15:15 EST
M Retrieve-Network Alarms: COMPLD
/* Network Alarms Report

=====
System Date Time TID Product Local
Alarm Type Ring
=====
MN 01-19 21:18:47 Delta-2 FT-2000 Yes
MN 01-19 21:18:27 Delta-3 FT-2000 Yes
MN 01-19 21:20:57 Echo-1 DDM-2000 No
*/
```

**Figure 1 – Example of Network Alarms Report using Release 6 or Later Software  
(Refer to Chapter 11 for Examples of Earlier Software Releases)**

## Clear 'DS3 Path Unequipped'

**Overview:** The **DS3 path unequipped** condition means that a DS3 drop-direction port in the network element lacks a continuous path back to a DS3 source. This procedure is used to locate the missing cross-connection(s) and/or circuit pack(s) in that path.

1. Referring to the Active Alarms and Status Report on the CIT screen, note and record the **Source Address** of the DS3 port reporting the **DS3 path unequipped** condition.
2. At the CIT, select **CONFIGURATION-Retrieve-Crossconnection-STS3 (or STS1)** to obtain an STS-3 (or STS-1) Cross-Connection Map Report.
3. Referring to the STS-3 (or STS-1) Cross-Connection Map Report, note and record the tributary (1-16 for STS3 or 1-48 for STS1) and line (1E or 1W) to which the affected DS3 port is cross-connected.
4. At the CIT, select **CONFIGURATION-Retrieve-Map-Ring** to obtain a Ring Map Report.
5. Referring to the Ring Map Report, note and record the TID of the next network element in the affected path. (The network element terminating line 1E is the second entry in the report. The network element terminating line 1W is the bottom entry in the report.)

⇒ **NOTE:**  
If you are already remotely connected to a network element, you must **LOGOUT** (use **F4** key) before you can remotely log in to another network element.

6. At the CIT, press the function key **F2**, labeled **REMOTE**, and log in to the network element identified in Step 5.

⇒ **NOTE:**  
The next two steps will determine whether a cross-connection exists at this network element for the path being traced. If the path left the previous network element on line 1E, it will arrive at this network element on line 1W and vice versa.

7. At the CIT, select **CONFIGURATION-Retrieve-Crossconnection-STS3 (or STS1)** to obtain an STS-3 (or STS-1) Cross-Connection Map Report.

8. Referring to the STS-3 (or STS-1) Cross-Connection Map Report, is a through cross-connection, an add cross-connection, or no cross-connection shown for the appropriate tributary (1-16 for STS3 or 1-48 for STS1) and line (1E or 1W)?

If a **through cross-connection**, then go back to Step 4.

If an **add cross-connection**, then continue with Step 9.

If **no cross-connection**, then continue with Step 18.

9. Referring to the STS-3 (or STS-1) Cross-Connection Map Report, note and record the LS INTFC slot associated with the add cross-connection.
10. At the CIT, select **CONFIGURATION-Retrieve-Equipment: All** to obtain an Equipage and Version Report.
11. Referring to the Equipage and Version Report, is a DS3 circuit pack (CP), an STS1E or OC3 CP, or no CP shown for the LS INTFC slot identified in Step 10?

If a **DS3 CP**, then continue with Step 12.

If an **STS1E or OC3 CP**, then continue with Step 14.

If **no CP**, then continue with Step 16.



**NOTE:**

There is no apparent reason for the **DS3 path unequipped** condition.

12. Go to TAP-108 to clear the incorrect response.
13. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



**NOTE:**

The DS3 path does not originate within the FT-2000 ring network.

14. Issue a Trouble Report to the source of the path associated with the LS INTFC slot identified in Step 9.
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



**NOTE:**

The **DS3 path unequipped** condition is due to the path's not having a DS3 source. The condition can be cleared by installing a DS3 CP or by deleting all of the cross-connections associated with the path at all of the associated nodes.

16. Install a DS3 circuit pack in the LS INTFC slot identified in Step 9 (**DLP-514**) or delete all of the cross-connections associated with the path at all of the associated nodes (**DLP-536**).
17. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



**CAUTION:**

*Incorrectly provisioned source node TID and/or destination node TID can result in a service outage under certain multiple failure conditions.*



**NOTE:**

The **DS3 path unequipped** condition is due to one or more missing cross-connections, including one at this network element. The condition can be cleared by entering the missing cross-connection(s) or by deleting all of the cross-connections associated with the path at all of the associated nodes.

18. Referring to office records, enter the missing cross-connection(s) or delete all of the cross-connections associated with the path at all of the associated nodes (**DLP-536**).
19. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Clear 'FT-LBA (Condition)'

**Overview:** This procedure is used to clear a condition at an FT-2000 Lightwave Booster Amplifier (FT-LBA) in response to one of these indicated conditions at the FT-2000 network element:

|                    |                           |
|--------------------|---------------------------|
| FT-LBA fan failure | FT-LBA fuse/power failure |
| FT-LBA 1EW failure | FT-LBA 1WE failure        |

1. Are you responsible for maintaining the FT-LBA?  
If **YES**, then continue with Step 2.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
2. Clear the FT-LBA trouble condition(s) by using the FT-LBA user documentation (365-565-500).
3. To verify that the trouble condition has been cleared, select **FAULT-Retrieve-Alarm** at the CIT to obtain an Active Alarms and Status Report from the FT-2000 network element and verify the condition is not shown.
4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'OC3/OC12 Mode Conflict (Equipment, Slot Pair, X-Connect)'

⇒ **NOTE:**  
An OC3/OC12 mode conflict ( ) means that the addressed slot has a Provisioned Mode of "1+1" and an Operating Mode of "0x1."

1. What is the condition to be cleared?
  - If **OC3 mode conflict (equipment)**, then continue with Step 2.
  - If **OC12 mode conflict (equipment)**, then continue with Step 2.
  - If **OC3 mode conflict (slot pair)**, then continue with Step 6.
  - If **OC12 mode conflict (slot pair)**, then continue with Step 6.
  - If **OC3 mode conflict (x-connect)**, then continue with Step 10.
  - If **OC12 mode conflict (x-connect)**, then continue with Step 10.

⇒ **NOTE:**  
For an LS INTFC slot to have an Operating Mode of "1+1," both the A slot and the B slot of the slot pair must be equipped with OC3/OC12 circuit packs.

2. At the CIT, select **CONFIGURATION-Retrieve-Equipment:LS INTFC** to obtain an Equipage and Version Report.
3. Use the first and second columns in the report to locate the slot with no circuit pack installed or the wrong type of circuit pack (DS3 or STS-1E) installed.

4. Install an OC3/OC12 circuit pack or execute the **CONFIGURATION-Update** command and replace the wrong type circuit pack with an OC3/OC12 circuit pack.

Reference: **DLP-514**

5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
6. For an LS INTFC slot to have an Operating Mode of "1+1," both the A slot and the B slot of the slot pair must have a Provisioned Mode of "1+1." At the CIT, select **CONFIGURATION-Retrieve-OC3(or OC12):All** to obtain an OC3/OC12 Provisioning Report.

7. Use the **Provisioned** and **Operating** columns in the report to identify the slot pair that is not provisioned correctly (mismatched).
8. At the CIT, select **CONFIGURATION-Set-OC3(or OC12)** command to correctly provision the slot (must match).  
Reference: **DLP-517**
9. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
10. For an LS INTFC slot to have an Operating Mode of "1+1," the two slots (A and B) cannot be cross-connected to different tributaries in the applicable direction of transmission. Depending upon the release you are using, at the CIT select **CONFIGURATION-Retrieve-Crossconnection-All** to obtain a Cross-Connection Map Report.
11. Use the **Cross-Connection** column in the report to locate the invalid cross-connection(s).
12. At the CIT, select **CONFIGURATION-Delete-Crossconnection** command to correct the invalid cross-connection(s).
13. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'CPYPGM:IP (tid)'

⇒ **NOTE:**  
This condition means that software (Release 5.0 and later) is being copied from this FT-2000 network element to the FT-2000 network element whose target identification (TID) appears in the message.

⇒ **NOTE:**  
The software download is performed in the background (approximately 1 hour) and is from one SYSMEM circuit pack to another SYSMEM circuit pack. Up to 15 downloads can occur simultaneously. See the Software Release Description Known Problems section for limitations on simultaneous downloads with some software releases. The first known limitation was with Release 7. The limit is 8 simultaneous copies.

⇒ **NOTE:**  
You **cannot cancel** the background download **once it is initiated**.

1. If desired, you may perform other CIT-PC activities during this time, for example, checking the status of the download [use the **CONFIGURATION-Retrieve-Alarm (and Status)** command].

If an error message appears on the screen during the background download, reinitiate the software download per **Operation: NTP-005**.

2. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'Incoming OC3 APS Byte Fail' or 'Incoming OC12 APS Byte Fail'

⇒ **NOTE:**  
This condition means a hardware and/or software failure at this FT-2000 network element or at the network element at the opposite end of the indicated OC-3/OC-12 line (could be a DDM-2000 network element or an FT-2000 network element).

1. At the CIT, select **FAULT-Retrieve-Alarms (and Status)** command to obtain an Active Alarms and Status Report.

⇒ **NOTE:**  
The specific condition is found in the **Description** column of the report.

2. Is there one or more other conditions in the report?

If **YES**, then continue with Step 3.

If **NO**, then issue a Trouble Report to the opposite end of the OC-3 line stating that you are receiving an **incoming OC3 APS byte fail** or **incoming OC12 APS byte fail** condition.

3. Identify the topmost condition other than the **incoming OC3 APS byte fail** or **incoming OC12 APS byte fail** condition in the report. Find this condition in Table A of **TAP-102** and proceed to the referenced TAP.
4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Clear 'Circuit Audit Suspended'

⇒ **NOTE:**  
This condition means that the background circuit audit has stopped running possibly caused by a data communication channel (DCC) failure somewhere in the network.

⇒ **NOTE:**  
On startup, an incomplete ring, will cause a "circuit audit suspended" alarm. And, while the ring remains incomplete, the "circuit audit suspended" alarm will not clear.

1. Did you just remove and reseal the LNCTL circuit pack in another procedure?  
If **YES**, then reset the network element (use the **FAULT-Reset** command). **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**  
If **NO**, then continue with Step 2.
2. Are you performing an auto turnup test (toward the DSX, STSX1, or LGX)?  
If **YES**, then reset the network element (use the **FAULT-Reset** command). **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**  
If **NO**, then continue with Step 3.
3. At the CIT, select the **CONFIGURATION-Retrieve-Map-Ring** command to obtain a Ring Map Report.

⇒ **NOTE:**  
It may take up to 5 minutes from the time that the cause of **circuit audit suspended** is corrected until the actual alarm disappears.

4. Does a **FAIL** indication appear in the **DCC Status** column of the report?  
If **YES**, then continue with Step 5.  
If **NO**, then wait for 5 minutes, if the **circuit audit suspended** condition still exists, then contact your next level of technical support.
5. Determine from the report the target identification (TID) associated with the **FAIL** indication in the **DCC Status** column.

6. Use the TID identified in Step 5 to locate the FT-2000 network element. You must dispatch to that location or ask for assistance from that location.
7. At the CIT, select **CONFIGURATION-Retrieve-Alarms (and Status)** command to obtain an Active Alarms and Status Report.
8. Does the **incoming DCC failure** condition appear in the **Description** column of the report?
  - If **YES**, then go to **TAP-120** (referenced from **TAP-102**).
  - If **NO**, then go to **TAP-102** to clear any other condition(s) in the report except for the **circuit audit suspended** condition.
9. If all previous steps have failed to clear the **circuit audit suspended** condition, at the CIT, select **FAULT-Reset** and execute. During the reset time, the miscellaneous discrete control points will be deactivated.
10. Wait 10 minutes or until the **MJ** LED is off at the user panel.
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'Circuit Provisioning Error'

⇒ **NOTE:**  
This condition means an internal audit function found a circuit with incorrectly provisioned source or destination.

⇒ **NOTE:**  
The most likely causes of this condition are (1) the provisioned add/drop source/destination TID for the selected drop slot does not match the actual source/destination TIDs or (2) a secondary source cross-connection is missing for a DRI circuit.

1. At the CIT, select **CONFIGURATION-Retrieve-Circuit-STS3 (or STS1)** command for the slot with the **circuit provisioning error** condition and execute to obtain an STS-3 (or STS-1) Circuit Map Report. The report lists all the cross-connections that make up this circuit. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
2. From the report obtained in Step 1, identify the tributary of the circuit.
3. At the CIT, select **CONFIGURATION-Retrieve-Timeslot-STS3 (or STS1)** command for the tributary identified in Step 2 and execute to obtain an STS-3 (or STS-1) Tributary Map Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
4. From the report obtained in Step 3, identify the actual source and destination nodes in the report.
  - **Hint 1:** For a 1-way cross-connection, the actual source TID is the TID (listed in the **At Node** column) of the node where the add cross-connection exists. The actual destination TID is the TID (listed in the **At Node** column) of the node where the drop cross-connection exists.
  - **Hint 2:** For a DRI cross-connection, the actual source is the TID (listed in the **At Node** column) of the secondary node.
  - **Hint 3:** For a drop-and-continue cross-connection, the actual destination is the TID (listed in the **At Node** column) of the node where the last drop in the circuit is shown.
5. From the report obtained in Step 3, identify the provisioned source TID (listed in the **TID** column on the left side of the report) and the destination TID (listed in the **TID** column on the right side).

6. Compare the actual TID obtained in Step 3 with the provisioned TID obtained in Step 5 and identify any inconsistencies between the actual and provisioned node TIDs.
7. Do the actual TIDs match the provisioned TIDs?  
    If **NO**, then continue with Step 8.  
    If **YES**, then continue with Step 12.
8. Log in to the node with the TID inconsistency. If a remote login is required and you need additional help, refer to **DLP-524**.
9. At the CIT, select **CONFIGURATION-Enter-Crossconnection-STs3 (or STs1)** command to correct the problem (invalid source/destination provisioning).
10. If remotely logged in to the node (network element), log out.
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
12. Log in to the node with the missing cross-connection. If a remote login is required and you need additional help, refer to **DLP-524**.
13. At the CIT, select **CONFIGURATION-Enter-Crossconnection-STs3 (or STs1)** command to correct a missing secondary source cross-connection for DRI circuit.
14. If remotely logged in to the node (network element), log out.
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'Path Integrity Failure'

⇒ **NOTE:**  
The **path integrity failure** condition may appear as a transient condition.

1. Wait 5 minutes before continuing with this procedure if you haven't already done so.

⇒ **NOTE:**  
This condition means a complete circuit from a drop cross-connection back to an add cross-connection does not exist. The circuit path is open.

- ⇒ **NOTE:**  
The most likely cause of this condition is one or more missing through and/or add cross-connection(s).
2. At the CIT, select **CONFIGURATION-Retrieve-Circuit-STS3 (or STS1)** command to obtain an STS-3 (or STS-1) Circuit Map Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
  3. Compare this report with office records to locate the node with the missing cross-connection(s). A missing cross-connection is indicated in the report by a question mark (?) in the **Cross-Connect** column.
  4. Log in to the node with the missing cross-connection(s). If a remote login is required and you need additional help, refer to **DLP-524**.
  5. At the CIT, select **CONFIGURATION-Enter-Crossconnection-STS3** command to correct the problem [missing cross-connection(s)].
  6. If remotely logged in to the node (network element), log out.
  7. If necessary, repeat Steps 4, 5, and 6 at other nodes missing a cross-connection(s).
  8. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'STS1 Squelch Map Conflict'



**NOTE:**

The TAP-164 steps were removed from this document because TAP-153 includes steps to clear an STS1 squelch map conflict.





## Clear 'STS3 Squelch Map Inconsistent' or 'STS1 Squelch Map Inconsistent'



**NOTE:**

An **STS3 squelch map inconsistent** condition (Release 5 and later) or an **STS1 squelch map inconsistent** condition (Release 7 and later) is most likely caused by the provisioned target identifier (TID) for the Source and/or Destination Node(s) at an intermediate node is not the actual TID for the Source and/or Destination Node(s).

1. Does an **STS3 (or STS1) squelch map conflict** condition exists along with an **STS3 (or STS1) squelch map inconsistent** condition?  
  
If **YES**, then go to **TAP-102** and clear the **STS3 (or STS1) squelch map conflict** condition before using this procedure.  
  
If **NO**, then continue with Step 2.
2. At the CIT, select **CONFIGURATION-Retrieve-Circuit-STS3 (or STS1)** command for the slot with the **STS3 (or STS1) squelch map inconsistent** condition and execute to obtain an STS-3 (or STS-1) Circuit Map Report. The report lists all the cross-connections that make up this circuit. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
3. From the report obtained in Step 2, identify the tributary of the circuit.
4. At the CIT, select **CONFIGURATION-Retrieve-Timeslot-STS3 (or STS1)** command for the tributary identified in Step 3 and execute to obtain an STS-3 (or STS-1) Tributary Map Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
5. From the report obtained in Step 4, identify the actual source and destination nodes in the report.
  - **Hint 1:** For a 1-way cross-connection, the actual source TID is the TID (listed in the **At Node** column) of the node where the add cross-connection exists. The actual destination TID is the TID (listed in the **At Node** column) of the node where the drop cross-connection exists.
  - **Hint 2:** For a DRI cross-connection, the actual source is the TID (listed in the **At Node** column) of the secondary node.

- **Hint 3:** For a drop-and-continue cross-connection, the actual destination is the TID (listed in the **At Node** column) of the node where the last drop in the circuit is shown.
6. From the report obtained in Step 4, identify the provisioned source TID (listed in the **TID** column on the left side of the report) and the destination TID (listed in the **TID** column on the right side).
  7. Compare the actual TID obtained in Step 4 with the provisioned TID obtained in Step 6 and identify any inconsistencies between the actual and provisioned node TIDs.
  8. Log in to the node with the TID inconsistency. If a remote login is required and you need additional help, refer to **DLP-524**.
  9. At the CIT, select **CONFIGURATION-Enter-Crossconnection-STS3 (or STS1)** command to correct the problem (invalid source/destination provisioning).
  10. Log out of the network element at the intermediate node.
  11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Address an Unexplained Loss of Service



**CAUTION:**

*This condition can be caused by an incorrectly provisioned source and/or destination node TID and can result in a service outage under certain multiple failure conditions.*



**NOTE:**

For Release 4.1 and earlier, it is difficult to detect an incorrect source node TID and/or destination node TID. The method presented here is to individually compare the provisioned values with the work order which is normally done at time of installation/acceptance.



**NOTE:**

For Release 5 and later, an incorrect provisioned source and/or destination node TID is automatically indicated by a **site provisioning conflict** condition and is documented in **TAP-130**.

1. At the CIT, select the **CONFIGURATION-Retrieve-Map-Ring** command to obtain a Ring Map Report.
2. Compare the actual node TIDs listed on the report with the work order (instructions).
3. Do the node TIDs match (report and work order)?
  - If **NO**, then continue with Step 4.
  - If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
4. Make the appropriate correction (node TID, cross-connection, etc.).
5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear Potential Loss of Service Due to Double Failure

**Overview:** This procedure is used to clear an obscure double failure state which can, under some conditions, result in a potential loss of service. This procedure must not be used unless one of the specific double failure conditions identified in Steps 2 through 5 is present. Because a wide variety of specific failure messages could be associated with this double failure condition, this procedure is written in more general terms than some other trouble clearing procedures.

1. Is Release 5 or later software running in the network element?  
If **YES**, then continue with Step 2.  
If **NO**, then continue with Step 6.
2. Was there a loss of -48 V power to either the high-speed shelf or to the entire bay followed by a line failure on one of the spans terminated by this network element?  
If **YES**, then continue with Step 8.  
If **NO**, then continue with Step 3.
3. Was there a line failure on one of the spans terminated by this network element followed by a loss of -48 V power to either the high-speed shelf or to the entire bay, followed, in turn, by a clearing of the loss of -48 V power?  
If **YES**, then continue with Step 8.  
If **NO**, then continue with Step 4.
4. Was there a line failure on one of the spans terminated by this network element, followed by a reset of an OHCTL circuit pack for any reason, followed, in turn, by a hardware reset of an east-facing TRMTR, RCVR or an LS INTFC cross-connected to an east-facing tributary?  
If **YES**, then continue with Step 8.  
If **NO**, then continue with Step 5.

5. Was an LS INTFC, TRMTR, or RCVR reset or changed out while the ADR terminal was isolated from the rest of the ring or when the OC-48 signals were optically looped back?

If **YES**, then continue with Step 8.

If **NO**, then continue with Step 6.



**NOTE:**

The double failure condition addressed by this procedure is not present.

6. Go to **TAP-102** and clear all individual trouble conditions at this network element.
7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
8. Delete and reenter all cross-connections involving east-facing tributaries by using the **CONFIGURATION-Delete-Crossconnection:STS3 (or STS1)** command. Use the **CONFIGURATION-Enter-Crossconnection:STS3 (or STS1)** command to reenter the cross-connections.
9. At each of the nodes adjacent to the affected node, lock out high-speed protection switching by using the **FAULT-Switch-Line:All:Lockout** command. This prevents a line switch to protection anywhere in the ring and drops any active switch already in the ring.
10. Once the line failure at the affected node is cleared, reset the high-speed protection lockout at both adjacent nodes by using the **FAULT-Switch-Line:All:Reset** command. This drops the lockouts and terminates any Wait to Restore interval in progress.
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'Incoming OC48 APS Default Byte'



**NOTE:**

This condition is normally a transient condition caused by a Reset in progress at an upstream OC-48 source.

1. Wait 5 minutes or until the **NE ACTY** LED on the user panel goes off.
2. At the CIT, select **FAULT-Retrieve-Alarms (and Status):All** command and execute to obtain an Active Alarms and Status Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
3. Has the **incoming OC48 APS default byte** condition cleared?  
    If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**  
    If **NO**, then continue with Step 4.
4. Issue a Trouble Report to the source of the incoming OC-48 signal stating the **incoming OC48 APS default byte** condition.
5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Address 'Inc. (From OC3) DCC Failure' or Address 'Inc. (From OC12) DCC Failure'

⇒ **NOTE:**  
This condition means the Data Communications Channel (DCC) on the indicated OC-3/OC-12 port has failed. The failure is most likely caused by a hardware failure at the source of the incoming OC-3/OC-12 signal.

⇒ **NOTE:**  
This condition will most likely prevent a remote login to the source of the incoming OC-3/OC-12 signal.

1. Issue a Trouble Report to the source of the incoming OC-3/OC-12 signal stating that you are receiving an **inc. (from OC3) DCC failure**, or **inc. (from OC12) DCC failure** condition and that they are to assume there is no trouble at this (local) network element.
2. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Address 'TID Address Map Full'

⇒ **NOTE:**  
This condition means the network accessible to the local network element via its Data Communications Channel (DCC) connections has grown beyond the limit of 32.

⇒ **NOTE:**  
This condition prevents the completion of the **CONFIGURATION-Retrieve-Map-Network** command to obtain a Network Map Report which normally lists the network elements in the network.

⇒ **NOTE:**  
This condition can only be cleared locally and by removing the provisionable DCC connections at either the OC-3 lines, or the OC-12 lines.

3. Determine from office records if any DCC connections are engineered for the local network element.
4. At the CIT, select **CONFIGURATION-Retrieve-Assignment-DCC** command and execute to obtain a DCC Assignment Report.
5. Does the DCC information from the office records and the DCC Assignment Report agree?  
    If **NO**, then continue with Step 4.  
    If **YES**, then continue with Step 7.

⇒ **NOTE:**  
The extra DCC connection(s) that appears on the report must be removed.

6. At the CIT, select **CONFIGURATION-Delete-Assignment-DCC** command and execute to remove the extra DCC connection(s).
7. At the CIT, select **FAULT-Retrieve-Alarm (and Status)** command and execute to obtain an Active Alarms and Status Report.

8. Is the **TID address map full** condition still present?

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **YES**, then continue with Step 7.



**NOTE:**

This step is performed only if there is no extra DCC connection(s).

9. Notify the network administrator of the continued **TID address map full** condition after all extra DCC connections have been removed. Nothing else can be done locally to clear the trouble.

10. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Restore Network Element Operation After Power Loss

⇒ **NOTE:**  
The network element has had its power restored and one or more circuit packs have an "unexpected" or "unrecognized" failure.

11. Is the green **POWER ON** LED on the user panel lighted?

If **NO**, then go to **TAP-119**.

If **YES**, then continue with Step 2.

⇒ **NOTE:**  
If the low-speed and/or the high-speed shelf cover is removed, you will see one or more **FAULT** LEDs lighted.

12. Wait for the time indicated in Table A.

**Table A - Mini Reset Waiting Times**

| CP Inserted | Waiting Time |
|-------------|--------------|
| LNCTL *     | 10 minutes   |
| OHCTL       | 5 minutes    |
| TG3 †       | 5 minutes    |
| TOHCTL      | 5 minutes    |
| All Others  | 35 seconds   |

\* Inserting an LNCTL circuit pack starts a mini reset that can take up to 10 minutes to complete. During this time, LEDs on other circuit packs will be lighted.

† When a TG3 circuit pack is first inserted, it will be switched out of service until it warms up. This can take as long as 5 minutes. You can confirm that it is in the standby state by using the **FAULT-Retrieve-State** command for the TG slot.

13. If the low-speed and/or the high-speed shelf cover is removed, you will see one or more **FAULT** LEDs lighted. At the CIT, select **FAULT-Retrieve-Alarm (and status):All** command and execute to obtain an Active Alarms and Status Report.

14. Are any conditions listed in the **Descriptive** column of the report?  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**  
If **YES**, then continue with Step 5.
15. At the CIT, select **CONFIGURATION-Reset** command and execute to reset the network element.
16. Wait for the time indicated in Table A.



**NOTE:**

If the low-speed and/or the high-speed shelf cover is removed, you will see one or more **FAULT** LEDs lighted.

17. At the CIT, select **FAULT-Retrieve-Alarm (and status):All** command and execute to obtain an Active Alarms and Status Report.
18. Are any conditions listed in the **Descriptive** column of the report?  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**  
If **YES**, then go to **TAP-102.**
19. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clear 'NE Status Comm Failure'

**Overview:** This condition could arise from situations such as one or more nodes are not reachable because of a DCC failure (condition will timeout after 4 hours if a node was intentionally removed from the subnetwork). Remember, that for this procedure, a network element (NE) and a node are interchangeable

1.



**NOTE:**

This procedure assumes that you are logged into the NE with the **NE status comm failure** condition.



**NOTE:**

This condition is complicated because it may be caused by other trouble conditions, therefore, these other conditions must be cleared first. If you have previously obtained any reports, one or all may currently be inaccurate (not up to date).

At the CIT, select **FAULT-Retrieve-Alarms** command and execute to obtain an Active Alarms and Status Report.

2. Is the **NE status comm failure** condition still shown?

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **YES**, then continue with Step 3.

3. At the CIT, select **CONFIGURATION-Retrieve-Map-Network** command and execute to obtain a Network Map Report.

4. Is an **inc. (0C3) DCC failure** or **inc. (0C12) DCC failure** condition shown on the report?

If **NO**, then continue with Step 7.

If **YES**, then clear this condition first (refer to **TAP-169**) and return to this procedure.

5. Wait 5 minutes after clearing the condition.

6. Obtain another Active Alarms and Status Report. Is the **NE status comm failure** condition still shown?  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**  
If **YES**, then continue with Step 7.
7. Is an **inc. (0C48) DCC failure** condition shown on the report?  
If **NO**, then continue with Step 9.  
If **YES**, then clear this condition first (refer to **TAP-120**) and return to this procedure.
8. Wait 5 minutes after clearing the condition.
9. Obtain another Active Alarms and Status Report. Is the **NE status comm failure** condition still shown?  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**  
If **YES**, then continue with Step 10.
10. Is a trouble condition listed in the **Descriptive** column of the report?  
If **NO**, then go to **IXL-001** to start trouble clearing.  
If **YES**, then go to **TAP-102**.
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'X-Conn Provisioned But Not Est'

**Overview:** This condition arises from situations when hardware is not available, such as a low-speed circuit pack not being physically inserted into a slot.

1. At the CIT, select **FAULT-Retrieve-Alarms** command and execute to obtain an Active Alarms and Status Report.
2. From the report, determine the slot and/or tributary address of the cross-connection.
3. If necessary, remove the low-speed shelf cover.
4. Verify that the circuit pack is inserted into the shelf per office records.
5. Obtain another Active Alarms and Status Report.
6. Is the **x-conn provisioned but not est** condition still shown in the report?

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **YES**, then contact your next level of technical support.



## Clear 'Incoming (From OC-48) STS LOP'

**Overview:** An incoming (from OC-48) STS LOP condition may occur from any of four different specific circuit pack failure. These circuit pack failures should be diagnosed in the following prioritized order:

- a. Local NE TRMTR
- b. Local NE RCVR
- c. Upstream NE TRMTR
- d. Local NE, one or more Low speed interfaces (only if the tributary is drop connected).

⇒ **NOTE:**  
If no attempt to clear any solid circuit pack failures has been made prior to now, this should be done before clearing the LOP condition.

1. Does a 'circuit pack failure' condition appear in the Active Alarms and Status Report (also indicated by a lighted **FAULT** LED)?

If **NO**, then continue with Step 2.

If **YES**, then go to **TAP-111**.

⇒ **NOTE:**  
Pointer processor devices (which detect STS-3 LOP) are located on the local low-speed interface circuit packs and on the local high-speed TRMTR circuit packs.

⇒ **NOTE:**  
STS-3 Pointers are monitored for all tributaries, those with and those without cross-connections. However, a protection switch is made only for tributaries that have cross-connections.

⇒ **NOTE:**  
You may need to travel to the node with the incoming condition or ask for assistance at that node.

2. Log in to the node with the incoming (from OC-48) STS LOP condition (called the local node.) If a remote login is required and you need additional help, refer to **DLP-524**.
3. From the Active Alarms and Status Report, identify and record the Source Address (AID) associated with the STS LOP condition and note the East or West line direction of the tributary (sts-1e-# or sts-1w-# where # is the tributary number [value 1 through 16]).
4. At the CIT, select FAULT-Retrieve-History command and execute to obtain a Maintenance History Report.
5. Determine from the report (in Step 4) if a repeating pattern is occurring between the East and West TRMTR circuit packs. For example (Figure 1), a repeating pattern is occurring between line-1e with TRMTR failure and TRMTR failure-cleared **and** line-1w with TRMTR failure and TRMTR failure-cleared.

If **NO**, then continue with Step 6.

If **YES**, then continue with Step 10.

| /* Maintenance History Report |          |                 |                   |                                      |
|-------------------------------|----------|-----------------|-------------------|--------------------------------------|
| =====                         |          |                 |                   |                                      |
| Date<br>of Event              | Time     | System<br>Alarm | Source<br>Address | Event<br>Description                 |
| =====                         |          |                 |                   |                                      |
| 04-26                         | 13:45:31 | MN              | sts-3-1w-6        | incoming (from OC48) STS LOP-cleared |
| 04-26                         | 13:30:11 | MN              | line-1w           | line auto switch                     |
| 04-26                         | 13:29:52 | CR              | trmtr-1w          | TRMTR failure-cleared                |
| 04-26                         | 13:29:51 | CR              | trmtr-1w          | TRMTR failure                        |
| 04-26                         | 13:28:11 | NE ACTY         | line-1e           | incoming OC-48 line RFI/yellow       |
| 04-26                         | 13:28:21 | CR              | trmtr-1e          | TRMTR failure-cleared                |
| 04-26                         | 13:28:20 | CR              | trmtr-1e          | TRMTR failure                        |
| .                             | .        | .               | .                 | .                                    |
| .                             | .        | .               | .                 | .                                    |
| .                             | .        | .               | .                 | .                                    |
| 04-26                         | 13:27:31 | CR              | sts-3-1w-6        | incoming (from OC48) STS LOP         |
| */                            |          |                 |                   |                                      |

**Figure 1 – Example Showing Repeating "TRMTR Failure/Cleared" Pattern**

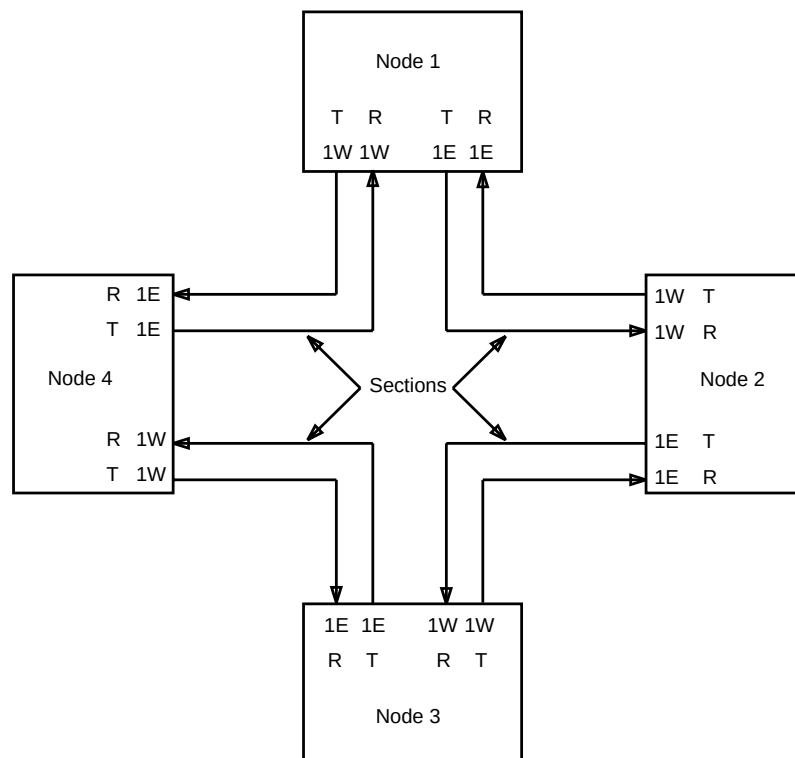


**CAUTION:**

*It is advisable to contact your next level of technical support before replacing the opposite side TRMTR circuit pack. Service may be lost when the TRMTR circuit pack is replaced due to a double failure occurring at the local node.*

6. Replace the **opposite side** TRMTR circuit pack at the local node as indicated by the Source Address from Step 3 (Figure 2). For example, if Node 2 is reporting an incoming (from OC-48) STS LOP condition on sts-1w-6, then replace the 1e transmitter at Node 2 (local node).

Reference: **DLP-514**



tpa 844515/01

**Figure 2 – Four-Node Ring Network Example**

7. After the new TRMTR boots, obtain another Active Alarm and Status Report (use FAULT-Retrieve-ALarms command or **F9** key).

8. Does the incoming (from OC-48) STS LOP condition still appear in the Active Alarm and Status Report?

If **NO**, then go to Step 35.

If **YES**, then continue.

9. Replace the TRMTR circuit pack just installed in Step 6 with the original TRMTR circuit pack because the trouble still exists.

Reference: **DLP-514**



**CAUTION:**

*Any traffic on tributaries 9 through 16 of the opposite line side (line that's not receiving the LOP) will be preempted while the high speed protection switch is active.*



**CAUTION:**

*It is advisable to contact your next level of technical support before making the high speed protection switch. Service may be lost if the switch is made to failed circuit packs (double failure).*

10. At the local node, select FAULT-Switch-Line:Forced command and execute for the **line identified in Step 3** (protection line now active).
11. At the local node and using the source address and line recorded in Step 3, replace the RCVR circuit pack that's receiving the incoming LOP failure.

Reference: **DLP-514**

12. Obtain another Active Alarm and Status Report (use FAULT-Retrieve-Alarms command or **F9** key).
13. Does the incoming (from OC-48) STS LOP condition still appear in the Active Alarm and Status Report?

If **NO**, then go to Step 34.

If **YES**, then continue.

14. Replace the (NEW) RCVR circuit pack just installed in Step 12 with the original (OLD) RCVR circuit pack because the trouble still exists.

Reference: **DLP-514**



**CAUTION:**

*It is advisable to contact your next level of technical support before replacing the same line side TRMTR circuit pack (same line that's receiving the LOP condition). Service may be lost when the TRMTR circuit pack is replaced due to a double failure occurring at the local node.*

15. Replace the **same side** TRMTR circuit pack at the local node as indicated by the Source Address from Step 3 (Figure 2). For example, if Node 2 is reporting an incoming (from OC-48) STS LOP condition on sts-1w-6, then replace the 1w transmitter at Node 2 (local node).

Reference: **DLP-514**

16. Obtain another Active Alarm and Status Report (use FAULT-Retrieve-Alarms command or **F9** key).
17. Does the incoming (from OC-48) STS LOP condition still appear in the Active Alarm and Status Report?

If **NO**, then go to Step 34.

If **YES**, then continue.

18. Replace the same side TRMTR circuit pack (just installed in Step 15) with the original TRMTR circuit pack because the trouble still exists.

Reference: **DLP-514**



**NOTE:**

Assistance is required at the adjacent upstream node to replace a circuit pack in the high-speed shelf.

19. Notify personnel to replace the appropriate TRMTR circuit pack at the upstream node (Figure 2). For example, if Node 2 is reporting an incoming (from OC-48) STS LOP condition on sts-1W-6, then the 1E transmitter at Node 1 may be the cause of the failure condition.

Reference: **DLP-514**

20. After the new TRMTR boots, obtain another Active Alarm and Status Report (use FAULT-Retrieve-Alarms command or F9 key).
21. Does the incoming (from OC-48) STS LOP condition still appear in the Active Alarm and Status Report?

If **NO**, then go to Step 34.

If **YES**, then continue.

22. Notify personnel to replace the TRMTR circuit pack (just installed in Step 19 at the upstream node) with the original TRMTR circuit pack because the trouble still exists.

Reference: **DLP-514**

23. At the CIT, select FAULT-Switched-Line:Reset command and execute to retire the OC-48 Forced Switch.

24. At the CIT, select CONFIGURATION-Retrieve-Crossconnection-All command and execute to obtain the Cross-Connection Map Report for the tributary identified in Step 3.
25. Identify and record the low-speed interface circuit pack used (if any) to **drop the STS signal** indicated by the Source Address in Step 3.



**CAUTION:**

*Removing low-speed interface circuit pack (DS3 or STS1E) may result in loss of traffic if protection circuit pack is not available (slots P1 and P2).*



**CAUTION:**

*Removing an OC-3 circuit pack may result in loss of traffic if a protection circuit pack is not available (slot #B).*



**CAUTION:**

*Removing an OC-12 circuit pack will result in loss of traffic if a protection circuit pack is not available (slot 3B or slot 7B).*



**NOTE:**

Low-speed interface protection switching will be required in the next few steps.

26. From the list of low speed interface circuit packs (Step 25), disengage one of the circuit packs just enough to cause a low-speed protection switch.
27. Obtain another Active Alarm and Status Report (use FAULT-Retrieve-Alarms command or **F9** key).
28. Does the incoming (from OC-48) STS LOP condition still appear in the Active Alarm and Status Report?  
  
If **NO**, then go to Step 31.  
  
If **YES**, then continue.
29. Reengage the low-speed interface circuit pack, and wait for the system to revert from protection to normal operation. Optionally, the FAULT-Switch-Low Speed:Reset command may be used.)
30. Contact your next level of technical support for assistance.
31. Replace the low-speed interface circuit pack.  
  
Reference: **DLP-514**
32. Obtain another Active Alarm and Status Report (use FAULT-Retrieve-Alarms command or **F9** key).



33. Does the incoming (from OC-48) STS LOP condition still appear in the Active Alarm and Status Report?

If **YES**, then go to Step 30.

If **NO**, then continue with Step 35.

34. At the local node, select FAULT-Switch-Line:Reset command and execute to normalize the line identified in Step 3 (original line now active.)
35. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Clear 'LCLAN Failure'

⇒ **NOTE:**  
The LCLAN failure alarm is an indication of a particular hardware failure directly related to the SYSCTL circuit pack.

⚠ **CAUTION:**  
*Do not remove the SYSCTL circuit pack unless instructed to do so by this procedure. If removed, provisioning data (current values) could be lost. Current values were provisioned with the **Set** commands.*

36. At the CIT, select **FAULT -Retrieve-Alarm** to obtain an Active Alarm and Status Report.

37. Do other alarms, in addition to **LCLAN failure**, appear under the Description column of the report?

If **YES**, then go to **TAP-102**.

If **NO**, then continue with Step 3.

⇒ **NOTE:**  
It is necessary to clear any/all additional alarms before continuing with this procedure.

38. Remove the SYSCTL circuit pack and install a replacement circuit pack having the same or a higher series number. For example, an LAA23C with series number **S1:2** is the same series number as **S1:4**. The 2 and 4 refer to minor changes within series 1.

Reference: **DLP-514**

⇒ **NOTE:**  
For circuit pack and software compatibility, refer to Table 7-1 in Section 7, "Circuit Pack Description."

39. Wait 10 minutes or until the **MJ** LED is off at the user panel, whichever comes first.

40. At the CIT, select **FAULT -Retrieve-Alarm** to obtain an Active Alarm and Status Report.

41. Does a **LCLAN failure** appear under the **Description** column of the report?

If **YES**, then go to **TAP-116**.

If **NO**, then continue with Step 7.

42. Is the **NE ACTY** LED off at the user panel?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then go to **Trouble Clearing: IXL-001**.

## Detailed Level Procedures Task Index

---

### **FIND YOUR JOB IN THE LIST BELOW . . . . . THEN GO TO**

---

|                                                                                                                                                 |         |
|-------------------------------------------------------------------------------------------------------------------------------------------------|---------|
| Change, Delete, or Enter a User's Login . . . . .                                                                                               | DLP-522 |
| Clean Optical Fibers and Connectors . . . . .                                                                                                   | DLP-510 |
| Connect and Condition Craft Interface Terminal (CIT) . . . . .                                                                                  | DLP-501 |
| Enter or Change TID String of FT-2000 Network Element . . . . .                                                                                 | DLP-544 |
| Enter/Delete DCC Assignment on OC-3/OC-12 Line . . . . .                                                                                        | DLP-542 |
| Enter/Delete/Edit Limited Granularity STS-1 Cross-Connections in<br>Network Elements for 2-Fiber Ring Network (for Rel. 6 and Earlier). . . . . | DLP-537 |
| Enter/Delete/Edit STS-3 (or STS-1) Cross-Connections at Network<br>Elements for 2-Fiber Ring Network. . . . .                                   | DLP-536 |
| Establish Modem or Datakit <sup>®</sup> Network Access . . . . .                                                                                | DLP-521 |
| Establish or Disconnect Remote Login Using CIT . . . . .                                                                                        | DLP-524 |
| Identify Source of Incoming Signal . . . . .                                                                                                    | DLP-535 |
| Inspect (Repair) Optical and/or Electrical Cable(s) . . . . .                                                                                   | DLP-528 |
| Install and/or Remove Optical Loopback at OC48 REGENR<br>Circuit Pack. . . . .                                                                  | DLP-519 |
| Install/Remove Apparatus (Circuit Pack) Blank. . . . .                                                                                          | DLP-533 |
| Install/Remove Lightguide Buildout. . . . .                                                                                                     | DLP-512 |
| Introduction to the PC . . . . .                                                                                                                | DLP-529 |
| Loop Back DS3 and/or EC1 Circuit(s) for Testing . . . . .                                                                                       | DLP-525 |
| Loop Back OC-12 Line(s) for Testing . . . . .                                                                                                   | DLP-549 |
| Loop Back OC-3 Line(s) for Testing . . . . .                                                                                                    | DLP-538 |
| Make Manual Optical Loopback at FT-2000 Network Element. . . . .                                                                                | DLP-500 |
| Obtain State Report. . . . .                                                                                                                    | DLP-516 |
| Perform Automatic Low-Speed Cabling (Fiber) Test. . . . .                                                                                       | DLP-503 |
| Perform Local Automatic Turnup Test. . . . .                                                                                                    | DLP-502 |
| Perform Provisioning. . . . .                                                                                                                   | DLP-505 |
| Perform Provisioning Test Office Alarm Operation . . . . .                                                                                      | DLP-504 |

|  |                                                                                           |         |
|--|-------------------------------------------------------------------------------------------|---------|
|  | Power Down/Power Up an Optical Network Element . . . . .                                  | DLP-551 |
|  | Properly Terminate DS3 and/or EC1 Outputs . . . . .                                       | DLP-520 |
|  | Provision Low-Speed Slot and Port Parameters . . . . .                                    | DLP-517 |
|  | Provision OC12 Slot for 1-Way Drop Service . . . . .                                      | DLP-547 |
|  | Provision OC3 Slot for 1-Way Drop Service . . . . .                                       | DLP-540 |
|  | Provision STS-1 Path Trace . . . . .                                                      | DLP-531 |
|  | Release Looped Back DS3 and/or EC1 Circuit(s) . . . . .                                   | DLP-523 |
|  | Release Looped Back OC-12 Line(s) . . . . .                                               | DLP-548 |
|  | Release Looped Back OC-3 Line(s) . . . . .                                                | DLP-539 |
|  | Remove and/or Install Circuit Pack . . . . .                                              | DLP-514 |
|  | Remove/Replace Shelf Cover . . . . .                                                      | DLP-511 |
|  | Replace a Defective Power-Line Filter. . . . .                                            | DLP-550 |
|  | Replace Circuit Pack With Different Circuit Pack. . . . .                                 | DLP-527 |
|  | Replace Defective Fan Unit in D-Bay . . . . .                                             | DLP-546 |
|  | Replace Fan Filter (in E-Bay). . . . .                                                    | DLP-526 |
|  | Replace Fan Filter Every Six Months (in D-Bay) . . . . .                                  | DLP-545 |
|  | Replace Fuse in Power Distribution and Fuse Panel or User Panel . . . . .                 | DLP-515 |
|  | STS3 (or STS1) Path Trace Monitor for Non-DS3 Signals . . . . .                           | DLP-543 |
|  | Temporarily Remove High-Speed Optical Loopback(s) at FT-2000<br>Network Element . . . . . | DLP-518 |
|  | Test DRI Cross-Connections for Path Integrity in FT-2000 Ring . . . . .                   | DLP-541 |
|  | Test Fan Assembly (ED-6G992-30). . . . .                                                  | DLP-513 |
|  | Test Miscellaneous Discrete Telemetry . . . . .                                           | DLP-530 |
|  | Test Parallel Telemetry. . . . .                                                          | DLP-508 |
|  | Test Serial Telemetry . . . . .                                                           | DLP-509 |
|  | Turnup Test - Point-to-Point System . . . . .                                             | DLP-507 |
|  | Verify FT-2000 Network Element Is Connected. . . . .                                      | DLP-506 |
|  | Verify STS-1 Path Trace . . . . .                                                         | DLP-532 |
|  | Verify That Low-Speed Ports Are Not NMON . . . . .                                        | DLP-534 |

## Make Manual Optical Loopback at FT-2000 Network Element

**Overview:** This procedure requires the FT-2000 network element to be **out of service**.

**⚠ CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components.*

1. Remove the shelf cover from one high-speed shelf.

Reference: **DLP-511**

2. For each OC-48 line to be looped back, obtain a lightguide jumper (package label FS1E-E-02) and a 20-dB loss lightguide buildout, or equivalents. (It is possible to connect another LBO to an existing LBO to obtain a total of 20-dB provided the LBOs do not physically interfere with the shelf cover when closed.) Table A shows additional buildouts that are available. Table B shows the buildout types that are compatible with different codes of the TRMTR and cks. Figure 1 shows optical line loopback connections.

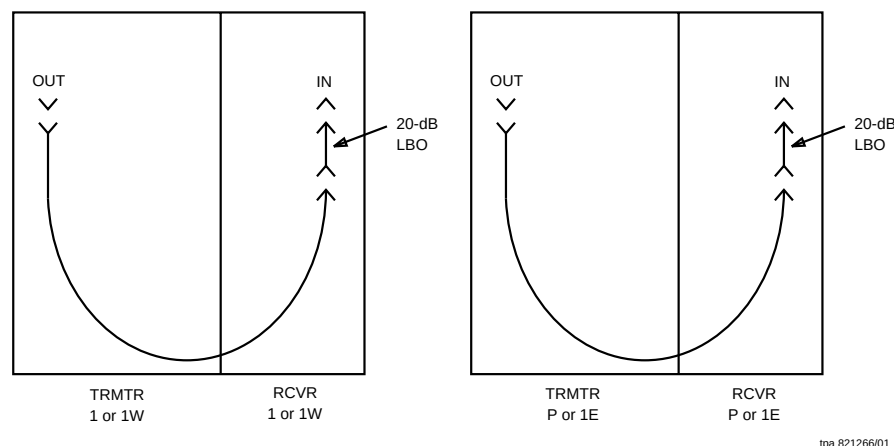


**Figure 1 – Loopback Connections**

**Table A – Lightguide Buildout Values**

| dB Loss | Letter Code |
|---------|-------------|
| 5 ±1    | B           |
| 10 ±1   | D           |
| 15 ±2   | F           |
| 20 ±2   | H           |

**Table B - Lightguide Buildouts and Circuit Pack Compatibility**

| Buildout Type (Code)                       | Circuit Pack                                                                                                                                            |
|--------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------|
| ST® (A3010*), FC(A3030*)                   | TRMTR 739B1<br>TRMTR 739C1<br>RCVR 839B1                                                                                                                |
| ST (A3070*), FC(A3080*),<br>or SC (A3060*) | TRMTR 739B2/B5<br>TRMTR 739C2/C5<br>TRMTR 739J2/J5<br>TRMTR 739J3<br>TRMTR 739P5<br>TRMTR 739R2/R5<br>TRMTR 739S2/S5<br>TRMTR 739E_<br>RCVR 839B2/B4/B5 |

\* See Table A for letter code.



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if a beam is viewed directly or with improper optical instruments. Avoid direct exposure to the beam*



**CAUTION:**

*A wrong connection can be avoided by marking or recording all disconnections in Steps 3, 4, and 5.*

3. Remove the optical OC-48 line connection from the **OUT** connector of the TRMTR circuit pack and slide the protector cap (if available) over the end of the fiber.
4. Remove the optical OC-48 line connection to the **IN** connector of the RCVR circuit pack and slide the protector cap (if available) over the end of the fiber.
5. Remove the lightguide buildout from the **IN** connector of the RCVR circuit pack, if equipped.

Reference: **DLP-512**



6. Remove the protector caps (if equipped) from the ends of the lightguide jumper (loopback cable).
7. Clean the optical buildouts, fiber, and connectors with canned air or a non-toxic gas.

Reference: **DLP-510**

8. Install the 20-dB loss buildout into the **IN** connector of the RCVR circuit pack.

Reference: **DLP-512**

9. Connect the lightguide jumper between the buildout (on RCVR) and the **OUT** connector on the TRMTR circuit pack for the same line.
10. Repeat Steps 3 through 9 for the adjacent high-speed line in the same shelf to be looped back.
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Connect and Condition Craft Interface Terminal (CIT)

**Overview:** This procedure provides instructions to verify the CIT [a personal computer (PC) running FT-2000 CIT-PC software] is properly connected, powered, and has software installed. If you are unfamiliar with the PC, an "Introduction to the PC" is provided in DLP-529.



**NOTE:**

The PC must meet the minimum requirements as stated in the "Craft Interface Terminal" part of Section 6, "Operations Interfaces."

1. Verify the PC is safely positioned on the PC tray (bay shelf) or safely secured on a cart or table.
2. Verify the cable connections at the PC.

Reference: 365-575-104, *FT-2000 OC-48 Lightwave System, Releases 1 and 2, Installation Manual*, and 365-575-115, *FT-2000 OC-48 Lightwave System, Releases 3, 4, 5, 6, and 7, Installation Manual*. To summarize, check the following:

- The appropriate 9-pin or 25-pin cable or equivalent interface cable is connected between the **CIT (DCE)** female connector on the user panel and a serial male connector on the PC.
- If required, a printer with the appropriate printer cable is connected to the CIT per the instructions provided with the printer.
- If required, a modem (internal or external) is connected per the instructions provided with the modem. Many modem configurations exist and operate over the public switched network or a data communications network (*Datakit*®d network).

Reference: **DLP-521** (Establish modem or *Datakit* network access.)

3. Turn the PC power switch to "on."
4. Verify the PC prompt is displayed (typically C:>).

Reference: **DLP-529**

5. Is the FT-2000 CIT-PC software loaded on the PC?

If **YES**, then continue with Step 9.

If **NO**, then go to **Operation: NTP-002**.

If **DON'T KNOW**, then continue with Step 6.

6. If necessary, change your current directory to the directory where the CIT-PC software should be installed.
7. Enter "dir" and press the  key.
8. Was the "CIT EXE" file displayed (also known as CIT.EXE)?

If **YES**, then continue with Step 10.

If **NO**, then go to **Operation: NTP-002** (software is not loaded).

9. If necessary, change your current directory to the directory where the CIT-PC software is installed.
10. Execute the FT-2000 CIT-PC software by entering **cit** or **cit expert** (as your security level allows) and pressing the  key. Then, follow the instructions displayed on the screen to log in to the network element. If you encounter difficulty in logging in or the connection attempt fails, the wrong COM port may have been specified in the SET-UP menu. A CIT tutorial is available in Section 11, "Craft Interface Terminal Usage."

**Nice to Know:** Memory-resident programs (also called TSRs) run in the background and may compete for the same random access memory (RAM) as the CIT-PC program. If this is the situation, disable all memory resident programs currently running in the PC unless modified to run with the CIT-PC program. The easiest way to do this is to replace the AUTOEXEC.BAT and CONFIG.SYS files, with the sample files provided in the directory where the CIT-PC program is loaded. For help, contact your next level of technical support or refer to the appropriate software release description as listed in Operation: NTP-002.

11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Perform Local Automatic Turnup Test

**Overview:** This acceptance procedure is used to verify the internal transmission health of an **out-of-service** FT-2000 network element that was installed by someone else. The FT-2000 network element must be equipped (circuit packs installed) per the engineered configuration.



**CAUTION:**

*Do not remove any circuit packs during testing because erroneous indications can result.*

1. Which FT-2000 network element are you testing?  
If **FT-2000 end terminal**, then continue with Step 2.  
If **FT-2000 repeater bay/shelf**, then continue with Step 4.  
If **FT-2000 add/drop-rings terminal**, then continue with Step 6.
2. At the CIT, select **FAULT - Test - Auto Turnup** (use **CONFIGURATION** in Release 1.1.1) with Mode: **Local** and **All** for the lines. This test can take up to 10 minutes.

Response: Execution in progress (one dot appears about every 2 seconds).



**NOTE:**

The 'Fault-Test-Auto Turnup-Local' test occasionally indicts circuit packs in random slots. Repeat the test with the indicted circuit pack in a different slot. Unless the circuit pack fails in the other slot, it is highly unlikely that the circuit pack is defective and it should not be returned.



**NOTE:**

A "g" is for good or successful test. A "-" is for not equipped, therefore not tested. An "F" is for failed or unsuccessful test.

3. Does the report indicate "g" for good or "-" for not tested for each entry? See Figure 1 for an end terminal example with only a service line. See Figure 2 for an end terminal example with a service and protection line.

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then refer the problem to the group that installed the equipment.

4. At the CIT, select FAULT-Test-Auto Turnup with Mode: Local and All for the lines. This test can take up to 10 minutes.

Response: Execution in progress (one dot appears about every 2 seconds).

5. A "good" is for a successful test. A "-" is for not equipped, therefore not tested. A "FAIL" is for an unsuccessful test.

Does the report indicate "good" or "-" for each entry? See Figure 3 for an FT-2000 repeater bay/shelf example with a service line and a protection line.

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then refer the problem to the group that installed the equipment.

6. At the CIT, select FAULT-Test-Auto Turnup-Local. If Auto Turnup does not appear as a menu item, log out of the CIT by pressing **F5** key and answer "yes" to the confirmation message. You must log into the CIT in the expert mode by entering `cit expert` at the DOS prompt. This test can take up to 10 minutes.

Response: Execution in progress (one dot appears about every

7.



**NOTE:**

A "g" is for good or successful test. A "-" is for not equipped, therefore not tested. An "F" is for failed or unsuccessful test.

Does the report indicate "\f5g\f1" for good or "\f5-\f1" for not tested for each entry? See Figure 4 for an FT-2000 add/drop-rings terminal example with a fiber sharing service and protection.

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then refer the problem to the group that installed the equipment.

```

Lucent Technologies FT-2000 96-11-07 08:30:45 EST
M Test-Auto:All: COMPLD
/* Test Auto Turnup Report - Local Test
=====
Line 1: --TG-- --LSSW--
 TRMTR RCVR 1 2 IN OUT OHCTL LNCTL
 g g g g g g g g

 ----- LS INTFC-----
 1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8 P P
 A B A B A B A B A B A B A B 1 2
 g g g g g g g g g g g g g g g g -

*/
;

```

**Figure 1 – FT-2000 End Terminal Example (Service Line Only)**

```

Lucent Technologies FT-2000 96-11-07 08:30:45 EST
M Test-Auto:All: COMPLD
/* Test Auto Turnup Report - Local Test
=====
Line 1: --TG-- --LSSW--
 TRMTR RCVR 1 2 IN OUT OHCTL LNCTL
 g g g g g g - g

 ----- LS INTFC-----
 1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8 P P
 A B A B A B A B A B A B A B 1 2
 g g g g g g g g g g g g g g g g -

Line P: --TG-- --LSSW--
 TRMTR RCVR 1 2 IN OUT OHCTL LNCTL
 g g g g g g - g

 ----- LS INTFC-----
 1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8 P P
 A B A B A B A B A B A B A B 1 2
 g g g g g g g g g g g g g g g g -

*/
;

```

**Figure 2 – FT-2000 End Terminal Example (Service Line and Protection Line)**

```

Lucent Technologies FT-2000 96-11-07 08:30:45 EST
M Test-Auto:All: COMPLD
/* Test Auto Turnup Report - Local Test
=====
Slot Test
Address Results
=====
regenr-1ew good
regenr-1we good
regenr-pew FAIL
regenr-pwe good

*/
;

```

**Figure 3 – Repeater Bay/Shelf Example (Service Line and Protection Line)**

```

Lucent Technologies FT-2000 96-11-07 08:30:45 EST
M Test-Auto:All: COMPLD
/* Test Auto Turnup Report - Local Test
=====
--TRMTR-- --RCVR-- ---TG--- --LSSW-- --OHCTL--
1W 1E 1W 1E 1 2 IN OUT 1W 1E LNCTL
g g g g g g g g g g g

----- LS INTFC-----
1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8 P P
A B A B A B A B A B A B A B 1 2
g g g g g g g g g g g g g g g -

*/
;

```

**Figure 4 – Add/Drop-Rings Terminal Example (1W and 1E Lines)**



## Perform Automatic Low Speed Cabling (Fiber) Test

**Overview:** This acceptance procedure is used to verify continuity of the low-speed cabling (fiber) between the low-speed cross-connect bay or lightguide cross-connect panel and an FT-2000 network element that was installed by someone else. The particular low-speed cables (fibers) being tested must be **out-of-service**.

⇒ **NOTE:**  
Generally, it is strongly recommended that all cables (fibers) be installed (initially) and tested up front in both directions. This would be in lieu of the steps defined within this DLP.

⚠ **CAUTION:**  
*Do not remove any circuit packs during testing because erroneous indications can result.*

⇒ **NOTE:**  
The circuit being tested may be used for 1-way drop or 1-way add services, in which case there may be an output cable and not an input cable or an input cable and not an output cable.

1. Is the circuit to be tested a 1-way only?  
If **YES**, then continue with Step 2.  
If **NO**, then continue with Step 3.
2. To test a 1-way only circuit, a temporary cable can be installed to provide a return path during the loopback test. After the temporary cable has been installed (see reference), continue with Step 3 as if a 2-way cable.

Reference: **365-575-115**

3.



**NOTE:**

If LAA4 EC-1 circuit packs are equipped for this test, a SONET test set must be used as the source signal. Replace the circuit pack output signal with the signal from the SONET test set. The LAA4 circuit pack will not transition to the 'In-Service' state in the presence of an AIS signal.

Is this test part of the initial acceptance (**Acceptance: NTP-002** or **NTP-006**) or part of the protection access upgrade (**Acceptance: NTP-004**), or part of an added electrical low-speed cable acceptance (**Acceptance: NTP-008**), or part of an added optical low-speed fibers acceptance (**Acceptance: NTP-010**)?

If **initial acceptance** of network element, then continue with Step 4.

If an upgrade to add **protection access**, then continue with Step 14.

If acceptance of **new and added electrical low-speed cables**, then continue with Step 21.

If acceptance of **new and added optical low-speed fibers**, then continue with Step 28.

### Initial Acceptance

4. Temporarily remove any high-speed optical loopbacks on all lines.

Reference: **DLP-518**



**CAUTION:**

*FT-2000 low-speed outputs toward the cross-connect panel or other transmission equipment must be properly terminated. If unterminated, false indications of failures may result.*

5. At the low-speed cross-connect bay (that is, DSX-3, STSX-1, LGX, DACS III-2000, etc.), loop back each low-speed circuit to be tested. Do **not** use the **FAULT-Operate-Loopback** command at the FT-2000 network element.
6. At the CIT, select **FAULT-Retrieve-Alarm** to obtain Active Alarms and Status Report.
7. Referring to the report just obtained, make a note of any conditions other than those listed below that appear in the report. (Your report may not include all of the conditions listed.) If acceptance testing, report these conditions to installation personnel if any failures occur during the following test. If circuit order testing, refer to TAP-102 to clear the trouble condition.

FT-2000 Point-to-Point Network

| Source Address | Description                            |
|----------------|----------------------------------------|
| line-1         | incoming OC-48 LOS                     |
| line-p         | incoming OC-48 LOS                     |
| system         | APS channel failure                    |
| system         | incoming DCC failure (only if enabled) |
| line-1         | site provisioning conflict             |
| line-p         | site provisioning conflict             |
| rcvr-1         | receiver parameter-out-of-range        |
| rcvr-p         | receiver parameter-out-of-range        |

FT-2000 Ring Network

| Source Address | Description          |
|----------------|----------------------|
| line-1e        | incoming OC-48 LOS   |
| line-1w        | incoming OC-48 LOS   |
| system         | ring incomplete/open |
| system         | DS-NE not reachable  |
| system         | squelch map conflict |
| line-1e        | incoming DCC failure |
| line-1w        | incoming DCC failure |

➡ **NOTE:**  
Any alarms, other than those shown in the list, may prohibit continuing with the test until they are cleared.

- At the CIT, select the appropriate command as follows:

FT-2000 End Terminal: select **FAULT-Test-Auto Turnup** with **All** for **Line** and **DSX** for **Mode**.

FT-2000 Add/Drop-Rings Terminal: select **FAULT-Test-Auto Turnup-DSX** with **All** for **Slot**.

Response: **Execution in progress** (one dot appears about every 2 seconds).

➡ **NOTE:**  
A "g" is for good or successful test. A "-" is for not tested. An "F" is for failed or unsuccessful test.

- Does report indicate "g" for good or "-" for not tested for each entry?  
(See Figure 1 for a service line only in a point-to-point network.)  
(See Figure 2 for a service line and a protection line in a point-to-point network.)  
(See Figure 3 for a shared service/protection line in a ring network.)

If **YES**, then continue with Step 10.

If **NO**, then refer problem to the group that installed the equipment.

10. Remove cross-connect loopback(s) established in Step 5.

11. Replace optical loopbacks removed in Step 4.

Reference: **DLP-500**

12. At the CIT, select **CONFIGURATION-Update** and execute. This update is needed because of possible incoming signal failures, error conditions, etc., from the previous loopbacks.

13. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

```
Lucent Technologies FT-2000 96-11-07 08:30:45 EST
M Test-Auto:All: COMPLD
/* Test Auto Turnup Report - Cross-Connect Wiring Test

=====
Line 1: -----LS INTFC-----
 1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8
port A B A B A B A B A B A B A B A B
1 g g g - F - g g F g g g g g - g
2 g g g - - - g g F g g g g g - g
3 g g g - F - g g g g g g g g - g

*/
;
```

**Figure 1 – Example of Test Auto Turnup Report - Cross-Connect Wiring Test (Service Line Only in a Point-to-Point Network)**

```

Lucent Technologies FT-2000 96-11-07 08:30:45 EST
M Test-Auto:All: COMPLD
/* Test Auto Turnup Report - Cross-Connect Wiring Test
=====
Line 1: -----LS INTFC-----
 1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8
port A B A B A B A B A B A B A B A B
1 g g g g - F g g F g g g g g - g
2 g g g g - F g g g g g g g g - g
3 g g g g - F g g g g g g g g - g

Line P: -----LS INTFC-----
 1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8
port A B A B A B A B A B A B A B A B
1 g g g g - g g F g F g g g g - g
2 g g g g - g g F g g g g g g - g
3 g g g g - g g F g g g g g g - g

```

**Figure 2 – Example of Test Auto Turnup Report - Cross-Connect Wiring Test (Service and Protection Lines in a Point-to-Point Network)**

```

Lucent Technologies FT-2000 96-11-07 08:30:45 EST
M Test-Auto:All: COMPLD
/* Test Auto Turnup Report - Cross-Connect Wiring Test
=====
 -----LS INTFC-----
 1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8
port A B A B A B A B A B A B A B A B
1 g g g - F - g g F g g g g g - g
2 g g g - F - g g - g g g g g - g
3 g g g - F - g g - g g g g g - g

*/
;

```

**Figure 3 – Example of Test Auto Turnup Report - Cross-Connect Wiring Test (Shared Service/Protection Line in a Ring Network)**

## Protection Access Upgrade



### CAUTION:

*Care must be used at the cross-connect bay because traffic is being carried on the DSs from the low speed shelf - system controller. At this time, traffic is not on the protection access shelf.*

14. At the low-speed cross-connect bay (that is, DSX-3, STSX-1, DACS III-2000, etc.), loop back only the low-speed circuits that are associated with protection access. Do **not** use the **FAULT-Operate-Loopback** command at the FT-2000 network element. Loopback is toward the FT-2000 network element.

15. Is the **NE ACTY** LED lighted?

If **YES**, then refer to **Trouble Clearing: IXL-001**.

If **NO**, then continue with Step 16.

16. At the CIT, select **FAULT-Test-Auto Turnup** (use **CONFIGURATION** in Release 1.1.1) with **Mode: DSX** and **P** for the line. This test can take up to 5 minutes.

Response: The **ABN** and **NE ACTY** LEDs are lighted.



### NOTE:

A "**g**" is for good or successful test. A "-" is for not tested. An "**F**" is for failed or unsuccessful test.

17. Does report indicate "**g**" for good or "-" for not tested for each entry?

(See Figure 4 for protection line only in a point-to-point network.)

If **YES**, then continue with Step 18.

If **NO**, then refer problem to the group that installed the equipment.

18. Remove cross-connect loopback(s) established in Step 14.

19. At the CIT, select **CONFIGURATION-Update**.

20. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

```

Lucent Technologies FT-2000 96-11-07 08:30:45 EST
M Test-Auto:P: COMPLD
/* Test Auto Turnup Report - Cross-Connect Wiring Test

=====
Line P: -----LS INTFC-----
 1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8
port A B A B A B A B A B A B A B A B
 1 g g g g g F g g F g g g g g g g
 2 g g g g g F g g - g g g g g g g
 3 g g g g g F g g - g g g g g g g

*/
;

```

**Figure 4 – Example of Test Auto Turnup Report - Cross-Connect Wiring Test (Protection Line Only in a Point-to-Point Network)**

## Electrical Low Speed Cables



### CAUTION:

*Care must be used at the cross-connect bay because traffic is being carried on the other low-speed cables that are not being tested.*

21. At the low-speed cross-connect bay (that is, DSX-3, STSX-1, DACS III-2000, etc.), loop back only the low-speed signals being tested. Do **not** use the **FAULT-Operate-Loopback** command at the FT-2000 network element.
22. Is the **NE ACTY** LED lighted?
  - If **YES**, then refer to **Trouble Clearing: IXL-001**.
  - If **NO**, then continue with Step 23.

23. At the CIT, select the appropriate command as follows:



**CAUTION:**

*Do not select "ALL" on an in-service bay"*

FT-2000 End Terminal: Select **FAULT-Test-Auto Turnup** with Mode: **DSX** and for the particular low-speed ports associated with the cables being tested.

FT-2000 Add/Drop-Rings Terminal: Select **FAULT-Test-Auto Turnup-DSX** for the particular low-speed ports associated with the cables being tested.



**NOTE:**

A "g" is for good or successful test. A "-" is for not tested. An "F" is for failed or unsuccessful test.

24. Does report indicate "g" for good or "-" for not tested for each entry?

(See Figure 1 for a service line only in a point-to-point network.)

(See Figure 2 for a service line and a protection line in a point-to-point network.)

(See Figure 3 for a shared service/protection line in a ring network.)

If **YES**, then continue with Step 25.

If **NO**, then refer problem to the group that installed the equipment.

25. Remove cross-connect loopback(s) established in Step 21.

26. At the CIT, select **CONFIGURATION-Update** and execute. This update is needed because of possible incoming signal failures, error conditions, etc., from the previous loopbacks.

27. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

### Optical Low Speed Fibers



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Care must be used at the lightguide cross-connect panel because traffic is being carried on the other low-speed fibers that are not being tested.*



28. At the lightguide cross-connect panel, manually loop back only the low-speed optical signals (OC3/IS3/OC12) being tested. **Do not use** the **FAULT-Operate-Loopback-OC3** or **FAULT-Operate-Loopback-OC12** command at the FT-2000 network element.
29. Is the **NE ACTY** LED lighted?  
If **YES**, then refer to **Trouble Clearing: IXL-001**.  
If **NO**, then continue with Step 30.
30. Log in to the CIT by entering **cit expert** for expert mode.
31. At the CIT, select **FAULT - Test - Auto Turnup - DSX** for the particular low-speed slots associated with the fibers being tested.
- 32.

⇒ **NOTE:**  
A "g" is for good or successful test. A "-" is for not tested. An "F" is for failed or unsuccessful test.

⇒ **NOTE:**  
Only one entry will appear for a slot with an OC3/IS3 circuit pack. See report (Figure 5) for OC3/IS3 circuit packs in slots 1A-2B example.

⇒ **NOTE:**  
Only one entry will appear for a slot with an OC12 circuit pack.

Does report indicate "g" for good or "-" for not tested for each OC3 slot/fiber tested?

If **YES**, then continue with Step 33.

If **NO**, then refer problem to the group that installed the equipment.

33. Remove cross-connect loopback(s) established in Step 28.

**It is recommended that the loopback remain in place for any OC3/IS3/OC12 circuit pack not scheduled to immediately carry traffic.** If the loopback is removed and the OC3/IS3/OC12 circuit pack is not carrying traffic, an **incoming OC3 LOS** or **incoming OC12 LOS** condition will occur for that slot.

34. At the CIT, select **CONFIGURATION - Update** and execute. This update is needed because of possible incoming signal failures, error conditions, etc., from the previous loopbacks.
35. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

```

NODE-0 97-11-17 08:30:45 EST
M Test-Auto Turnup:DSX:ALL: COMPLD
/* Test Auto Turnup Report - Cross-Connect Wiring Test

=====
Line 1: -----LS INTFC-----
 1 1 2 2 3 3 4 4 5 5 6 6 7 7 8 8
port A B A B A B A B A B A B A B A B
1 g g g g g F g g F g g g g g g g
2 g F g g - g g g g g g g
3 g F g g - g g g g g g g

*/
;
```

**Figure 5 – Example of Test Auto Turnup Report - Cross-Connect Wiring Test (Slots 1A-2B Equipped with OC3 Circuit Packs and Remaining Slots Equipped with DS3/STS1E Circuit Packs)**

## Perform Provisioning Test Office Alarm Operation

1. At the CIT, select **FAULT-Test-Alarm** and **Mode: All** with **Repeat: 1**.
2. Verify the **CR**, **MJ**, and **MN** visual LEDs are sequentially *lighted* for 4 seconds and then remain *off* per the sequence chart below. Also, the office audible alarms should be activated during this test.

| Step* | Alarm LEDs at User Panel |           |           |
|-------|--------------------------|-----------|-----------|
|       | CR                       | MJ        | MN        |
| 1     | off                      | off       | off       |
| 2     | <b>on</b>                | off       | off       |
| 3     | off                      | <b>on</b> | off       |
| 4     | off                      | off       | <b>on</b> |
| 5     | off                      | off       | off       |

\* Each step has a 4-second duration.

3. Did the office alarms operate as described?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then refer problem to the group that installed the equipment.



## Perform Provisioning

1.  **NOTE:**  
The FT-2000 does not have any manual or switch-settable options on its circuit packs.

 **NOTE:**  
Information about FT-2000 provisioning is provided in Section 8, "Administration and Provisioning," of this manual. Other helpful information is the CIT tutorial and commands tables (by release), which are located in Section 11 of this manual, and the job aids on provisioning as follows:

- 365-575-121, *FT-2000 OC-48 Repeater Bay/Shelf Provisioning Job Aid (Releases 2, 4, and 6)*
- 365-575-122, *FT-2000 OC-48 Add/Drop-Rings Terminal Provisioning Job Aid (Releases 3, 4, 5, 6, and 7)*
- 365-575-123, *Threshold-Crossing Alert (TCA) Parameters Provisioning Job Aid (Release 4 and Later)*
- 365-575-124, *FT-2000 Cross-Connections Job Aid (Releases 3, 4, 5, 6, and 7)*

At the CIT, select CONFIGURATION, PERFORMANCE, FAULT, or SECURITY category and the appropriate inputs [verb and modifier(s)].

 **NOTE:**  
Table 11-9 lists the reports and how to obtain them. For example, the System Alarm Attributes Report is obtained by using **CONFIGURATION-Retrieve-Attribute-Alarm**. Another example is the Synchronization Report that is obtained by using **CONFIGURATION-Retrieve-Synchronization**.

Did the appropriate report show that provisioning has been done?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then (1) refer the problem to the group that did the original provisioning, and/or (2) compare the report with the original work order and correct the provisioning discrepancy. Provisioning job aids are available; see Step 1 for the specific equipment references.



## Verify FT-2000 Network Element Is Connected

**Overview:** This procedure is used to verify that all the network elements are connected in an FT-2000 network.



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components.*

1. You are located at which FT-2000 network element?

If at **FT-2000 add/drop-rings terminal**, then continue with Step 2.

If at **FT-2000 repeater bay/shelf**, then continue with Step 25.

### FT-2000 OC-48 Add/Drop-Rings Terminal (Figure 1)

2. Remove the front cover from the high-speed shelf.

Reference: **DLP-511**

3. Is there a lightguide jumper equipped with an optical buildout (20 dB for loop-back testing) or equivalent connected between the **OUT** connector of the TRMTR circuit pack and the **IN** connector of RCVR circuit pack for line 1W?

If **NO**, then continue with Step 7.

If **YES**, then continue with Step 4.

4. Remove the loopback lightguide jumper between the optical connectors of the TRMTR and RCVR circuit packs for line 1W.
5. Remove the 20-dB buildout (or equivalent) from the RCVR circuit pack but not the 0-dB buildout from the TRMTR circuit pack.
6. Install protector caps (if available) over the fiber ends of the lightguide jumper that was used in the loopback.

7. Are the transmit and receive lightguide cables connected to the **OUT** connector of the TRMTR circuit pack and to the **IN** connector on the RCVR circuit pack, respectively for line 1W?

If **YES**, then continue with Step 21.

If **NO**, then continue with Step 8.

8. Locate the transmit fiber for line 1W.
9. Remove the protector cap (if equipped) from the end of the transmit fiber.
10. Clean the transmit optical fiber and the 0-dB buildout with canned air or non-toxic gas and install the buildout in the **OUT** connector of the TRMTR circuit pack.

Reference: **DLP-510, DLP-512, and DLP-519**

11. Connect the transmit fiber to the 0-dB buildout (installed in the **OUT** connector on the TRMTR circuit pack).

12. Has the received dB value of the lightguide buildout already been determined?

If **YES**, then continue with Step 15.

If **NO**, then continue with Step 13.



**NOTE:**

Assistance at the remote end of the OC-48 line may be required.

13. Measure the power level on the receive fiber using an optical level meter and/or an optical time domain reflectometer (OTDR) or equivalents per local procedures.



14. Using Table A and the measured received power, determine the correct build-out value.

**Table A - Optical Line Buildout Values (Notes 1 and 2)**

| Measured Received Power in dBm | Use Optical Buildout                                    |
|--------------------------------|---------------------------------------------------------|
| –2.0 to –4.9                   | 15 dB                                                   |
| –5.0 to –9.9                   | 10 dB                                                   |
| –10.0 to –14.9                 | 5 db                                                    |
| –15.0 to –25.5                 | 0 dB                                                    |
| –25.5 dBm or less              | Optical loss out of range.<br>Contact line engineering. |

**Notes:**

1. A 20 dB optical line buildout is typically used for optical loopback testing.
  2. The fiber must also meet the outside plant loss as specified in the optical line loss budget of Section 10 of this manual.
15. Clean the lightguide buildout and the **IN** connector of the RCVR circuit pack with canned air or nontoxic gas.  
Reference: **DLP-510**
  16. Install the buildout to the **IN** connector on the RCVR circuit pack.  
Reference: **DLP-512** and **DLP-519**
  17. Locate the receive fiber for line 1W.
  18. Remove the protector cap (if equipped) from the end of the receive fiber.
  19. Clean the receive optical fiber and the buildout with canned air or nontoxic gas (buildout is installed at the **IN** connector on the RCVR circuit pack).  
Reference: **DLP-510**
  20. Connect the receive fiber to the buildout (inserted at the **IN** connector on the RCVR circuit pack).
  21. Repeat Steps 3 through 20 for line 1E.



**NOTE:**

At this point, the TRMTR and RCVR circuit packs should be connected to their respective lines.



**NOTE:**

A flashing **FAULT** LED on the RCVR circuit pack indicates a loss of signal from the other end.

22. Verify all connectors are secure for the OC-48 lines and the fibers are dressed into their slots.

23. Replace the front shelf cover to the high-speed shelf.

Reference: **DLP-511**

24. Return to Step 1 for any remaining network elements in the FT-2000 network.

**FT-2000 Repeater Bay/Shelf (Figure 2)**

25. Perform Steps 2 through 24 for each repeater in the FT-2000 network, but substitute the REGENR circuit pack in place of the TRMTR and RCVR circuit packs. The REGENR **OUT** connector represents the TRMTR and the **IN** connector represents the RCVR. Also, the OC-48 optical lines use 1 for service and P for protection along with east and west for direction. For example, 1WE means the service line has optical transmission in a west to east direction.

26. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

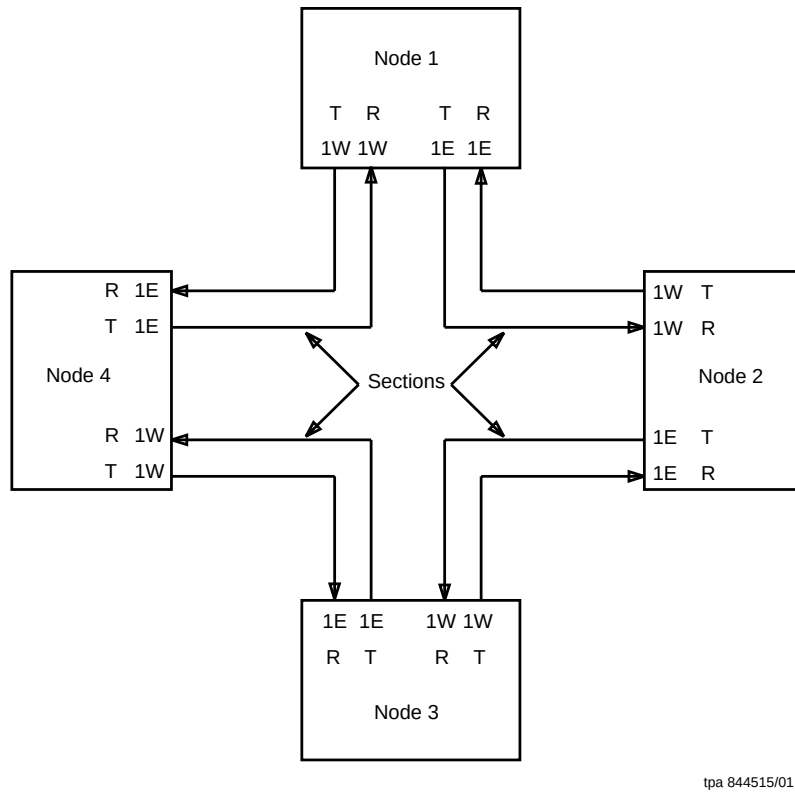


**Figure 1 – Four-Node Ring Network**

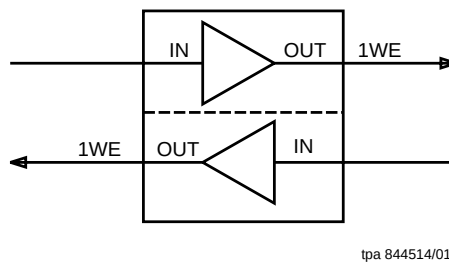


**Figure 2 – Regenerator**



## Turnup Test — Point-to-Point System

**Overview:** This procedure is used to verify end-to-end transmission for an FT-2000 point-to-point network. If you are unable to complete the procedure or the test fails, contact the group who did the installation.

1. Is this test part of the initial acceptance or part of the Protection Access Upgrade?

If **initial acceptance** of network element, then continue with Step 2.

If an upgrade to add **Protection Access**, then continue with Step 4.

2. At the local CIT, select FAULT - Test -Auto Turnup for All lines in the System mode.

3. ➡ **NOTE:**

"g" is for good or successful test. "-" is for not tested. "F" is for failed or unsuccessful test. Test takes 1 minute.

Did the report indicate "g" or "-" for each entry? See Figure 1 for an example.

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then refer the problem (failed system test) to the group that installed the equipment.

```
Lucent Technologies FT-2000 96-11-17 08:30:45 EST
M Test-Auto:All: COMPLD
/* Test-Auto Turnup Report - System Test
=====
Line 1: Transmit Receive
 Direction Direction
 g g

Line P: Transmit Receive
 Direction Direction
 g g
=====
*/
;
```

**Figure 1 – Example of Test Auto Turnup Report - System Test (Service Line and Protection Line)**

## Protection Access Upgrade

4.  **CAUTION:**  
*If this test is performed on a line other than the protection line, then service will be interrupted.*

 **NOTE:**  
This test can take up to 5 minutes.

At the local CIT, select FAULT-Test-Auto Turnup and  
Line: P with Mode: System.

Response: The **ABN** and **NE ACTY** LEDs are lighted.

Did the report indicate "g" or "-" for each entry?  
See Figure 2 for an example.

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then refer to the Installation Manual or contact the group that did the upgrade installation.

```
Lucent Technologies FT-2000 96-11-17 08:30:45 EST
M Test-Auto:P: COMPLD
/* Test-Auto Turnup Report - System Test
=====
Line P: Transmit Receive
 Direction Direction
 g g
*/
;
```

**Figure 2 – Example of Test Auto Turnup Report - System Test (Protection Line Only)**

## Test Parallel Telemetry

**Overview:** This procedure is used to test all the parallel telemetry points for proper operation. This procedure is performed during the acceptance of the FT-2000 network elements when they are out of service and is performed only during this time. For additional information, refer to the "Parallel Telemetry Interface" part in Section 6, "Operations Interfaces."

1. Contact the remote operations center and request their assistance in monitoring the parallel telemetry points during this test.
2. At the remote operations center, disable the alarm threshold level for the telemetry points being tested.
3. At the local CIT, select FAULT-Test-Telemetry-Parallel and Mode: All with Repeat: 1.

Response: All parallel telemetry points are simultaneously turned on for 20 seconds, then off for 20 seconds before reverting to normal operation.

4. Does the test pass as monitored from the remote operations center.

If **YES**, then **STOP. YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then refer to the Installation Manual to correct the problem or contact the group who did the installation.





## Test Serial Telemetry

**Overview:** This procedure is used to test the serial telemetry points. These points transfer alarm, status, and control information to a remote operations center. The transfer via a modem link uses a telemetry byte-oriented serial (TBOS) protocol to a remote maintenance or operations center. This procedure is performed during the acceptance of the FT-2000 network elements when they are out of service and is performed only during this time. For additional information, refer to "Serial Telemetry Interfaces" part in Section 6, "Operations Interfaces."

1. Contact the remote operations center and request their assistance in monitoring the serial telemetry points during this test.
2. At the remote operations center, disable the alarm threshold level for the telemetry points being tested.
3. At the local CIT, select FAULT-TEST-TELEMETRY-SERIAL, SERIAL TELEMETRY LINK: 1, DISPLAY: 1, POINT: even, odd, or all, and Repeat: 1 through 10.

**Response:** Up to 64 serial telemetry points are activated for 20 seconds for the specified display and the specified link before reverting to normal operation.

**Table A - Serial Telemetry Points**

| Display | Shows Alarm, Status, and Control Points |
|---------|-----------------------------------------|
| 1       | For local service line                  |
| 2       | For local protection line               |
| 3       | For remote service line                 |
| 4       | For remote protection line              |
| 5       | Reserved for future use                 |
| 6       | Reserved for future use                 |
| 7       | Reserved for future use                 |
| 8       | Reserved for future use                 |

4. Did the remote operations center receive the correct response during the test?  
If **YES**, then continue with Step 5.  
If **NO**, then refer to the Installation Manual to correct the problem or contact the group who did the installation.
5. Repeat Steps 2 through 4 for the remaining displays on the link.
6. Repeat Steps 3 through 5 for the other link.
7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Clean Optical Fibers and Connectors

1.  **WARNING:**  
*Disconnected or separated optical connectors may emit invisible laser radiation. Do not view the lightwave beam with an optical instrument. Avoid direct exposure to the beam.*

 **NOTE:**  
All optical fiber connectors (*ST*<sup>®</sup>, FC/PC, and SC types), lightguide buildouts, and buildout blocks or equivalents should be cleaned before making initial connections or reconnections per the following instructions. Only the components being assembled at this particular time should be cleaned.

Keep all dust caps and plugs in place until time to make connections.

### Disassemble

2. Remove (if necessary) lightguide connector from buildout.
3. Remove (if necessary) lightguide buildout from buildout block by unlocking tab and pushing in and turning in counterclockwise direction.

Reference: **DLP-512**

### Clean

4. Clean the end and the sides of the connectors, buildouts, and blocks with a lint-free, optical quality tissue dampened with isopropyl alcohol.
5. Carefully wipe the connector, buildouts, and blocks again with a clean, dry, lint-free optical quality tissue.
6. Blow any lint or dust from the connectors, buildouts, and blocks using canned air or a nontoxic gas held 3 inches away.
7. Clean the inside of the block (and 0-dB buildout only) using a pipe cleaner moistened with isopropyl alcohol.
8.  **CAUTION:**  
*Do not use pipe cleaner on attenuated buildouts (5, 10, 15, and 20 dB) as the lens can be damaged.*

Blow any lint or dust from inside the block, using canned air or a nontoxic gas held 3 inches away.

**Assemble**

9. Assemble by inserting the buildout into the block by aligning the buildout with the slot in the block. Push in the buildout and rotate it clockwise until locked into position. Slide the connector onto the buildout and rotate it clockwise until locked into position.

Reference: **DLP-512**

10. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Remove/Replace Shelf Cover

1.



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

Locate the circuit pack shelf cover or interconnection panel cover to be removed or replaced.

- Circuit pack shelf cover dimensions are 13 inches x 21.3 inches.
- Interconnection panel cover dimensions are 7 inches x 21.3 inches.

2. Is the cover being removed or replaced?

If **Removed**, then continue with Step 3.

If **Replaced**, then continue with Step 11.

### Remove Cover

3. At the top of each cover, locate the two •-turn fasteners.
4. Use the appropriate size slotted screwdriver (or a dime) to rotate the •-turn fasteners counterclockwise 90 degrees so slots are positioned vertically (unlocked position).
5. Which cover is being removed?
  - If **Circuit Pack Shelf Cover**, then continue with Step 6.
  - If **Interconnection Panel Cover**, then continue with Step 9.
6. Put a finger in each wire loop and pull the cover forward until it is horizontal (rotated 90 degrees from closed position).
7. Lift the pivot pins located at the bottom of the cover, push back, and lift up on the pins again to remove the cover from the shelf, then lift cover up until it is free from shelf framing.
8. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

9. Put a finger in each wire loop and pull the shelf cover forward nearly 3 inches and lift cover up to free it from the shelf framing.
10. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

#### **Replace Cover**

11. Which cover is being replaced?

If **Circuit Pack Shelf Cover**, then continue with Step 12.

If **Interconnection Panel Cover**, then continue with Step 13.

12. Position the pivot pins located at the bottom of the cover under the flange on the shelf framing, then continue with Step 14.
13. Place bottom of cover over side pivot pins.
14. Close the cover and rotate the two 1/4-turn fasteners 90 degrees clockwise using the screwdriver (or dime) until the fastener slots are horizontal (locked position).
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Install/Remove Lightguide Buildout

### Description

This procedure is used to select the proper value of Line Build Out (LBO) to be used on the circuit pack's faceplate jacks. High Speed links may require an attenuating LBO on the RCVR (IN) jack, and a zero-dB (0dB) LBO on the TRMTR (OUT) jack. As a contrast, Low-Speed links may require an attenuating LBO on the TRMTR (OUT) jack, and a zero-dB (0dB) LBO on the RCVR (IN) jack. In rare instances LBOs can be placed in the LGX, but this is NOT recommended. Typically, although there is not any requirement, the span fibers will connect to an LGX. And, "Fiber Jumpers" are used to connect the LGX's span fibers to the circuit pack faceplate LBOs.

This procedure covers the selection of both single-mode (SM) and multi-mode (MM) LBOs, span fibers, and associated fiber jumpers. In order to determine the correct type of connection devices (SM or MM) you will need to know the type of circuit pack being used and its application. The information contained within Table A should help in this selection.

**Table A – (Some) Optical Link Configurations and Span Component Requirements**

| Type of Circuit Pack       | Circuit Pack Transmitter Device | Type LBO        | Span Fiber | Jumper | Type RCVR          | Circuit Pack RCVR Device |
|----------------------------|---------------------------------|-----------------|------------|--------|--------------------|--------------------------|
| OC-48                      | Laser (SM)                      | SM              | SM         | SM     | OC-48              | Diode (MM)               |
| OC-12 (T939A)*             | Laser (MLM)                     | SM              | SM         | SM     | OC-12 <sup>†</sup> | Diode (MM)               |
| OC-3 (LAA10B) <sup>‡</sup> | Laser (MLM)                     | SM              | SM         | SM     | OC-3*              | Diode (MM)               |
| OC-3                       | Laser (MLM)                     | MM              | MM         | MM     | IS-3*              | Diode (MM)               |
| OC-3                       | Laser (MLM)                     | SM <sup>§</sup> | SM         | SM     | IS-3*              | Diode (MM)               |
| IS-3 (LAA5) <sup>¶</sup>   | LED (MM)                        | MM              | MM         | MM     | OC-3*              | Diode (MM)               |
| IS-3                       | LED (MM)                        | MM              | MM         | MM     | IS-3*              | Diode (MM)               |

\* T939A circuit packs are shipped from the factory with 0dB LBOs on both the IN and OUT jacks.

† If the attenuation LBO is installed at the transmit (OUT) jack—then normally only a 0dB LBO is required at the receive (IN) jack. If an attenuation LBO must be used at the input (IN) jack on the LAA10B or T939A, then a MM LBO must be used.

‡ LAA10B circuit packs are shipped from the factory with a 0dB LBO on the IN jack and a 7.1dB SM LBO on the OUT jack. LAA10B replaces LAA10 circuit packs, which are rated DA.

§ This option uses more expensive components than the previous (above) option.

¶ LAA5 circuit packs are shipped from the factory with 0dB LBOs on both the IN and OUT jacks.

 **WARNING:**  
*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

 **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components.*

1. Is the buildout being installed or removed (see Figure 1)?
  - If **Installed on a High Speed fiber**, then continue with Step 2.
  - If **Installed on a Low Speed fiber**, then continue with Step 16.
  - If **Removed**, then continue with Step 35.

### High Speed Fiber Installation via an LGX

2. While considering the application (Table A), and as appropriate, select the correct jumper from Table B.

 **NOTE:**  
When connecting to an LGX, you will need one jumper for the transmit fiber and an identical jumper for the receive fiber.

**Table B – Single-mode Lightwave Jumpers for Low-Speed and High-Speed Fiber Connection**

| Code       | Comcode     | Length    | Description |
|------------|-------------|-----------|-------------|
| FS1E-E-02  | 105 357 727 | 2 ft.     | ST-ST       |
| FS1E-E-10  | 105 357 768 | 10 ft.    | ST-ST       |
| FS1E-E-25  | 105 357 800 | 25 ft.    | ST-ST       |
| FS1E-E-50  | 105 357 859 | 50 ft.    | ST-ST       |
| FS1E-E-100 | 105 357 875 | 100 ft.   | ST-ST       |
| LS1K-E-10  | 105 713 150 | 10 ft.    | Biconic-ST  |
| LS1K-E-25  | 105 712 939 | 25 ft.    | Biconic-ST  |
| LS1K-E-50  | 105 712 988 | 50 ft.    | Biconic-ST  |
| LS1K-E-100 | 105 712 947 | 100 ft.   | Biconic-ST  |
| LS1FP-FP   | 106 470 446 | As Req'd. | FC/PC-FC/PC |

3. Install/connect the selected jumpers at the LGX.



4. Label the jumpers. Indicate the line (West or East) and direction (TRMTR or RCVR).
5. Route the jumpers from the LGX to the FT-2000 ADR terminal. Jumpers (1 Transmit and 1 Receive) for the West line must be routed down the left side of the bay in the fiber protection channel, jumpers (1 Transmit and 1 Receive) for the East line must be routed down the right side of the bay in the fiber protection channel. Refer to the following Figure 2.
6. The fiber jumpers should hang without any obstructions in the fiber protection channel. The jumpers should be routed out of the side of the protection channel near the bottom so that they can be properly dressed in the Enhanced High Speed Shelf. Make sure the bend radius of the fiber is not exceeded when dressing the fiber. Fibers should never be directly laced in place or cable tied. If secure dressing is not achieved with the steps above then place the fibers in protective tubing and lace the tubing to the protective channel.
7. Fibers enter the Enhanced High Speed Shelf near the bottom front of the shelf and should be routed up to the optical packs through the notches along the bottom edge of the shelf. Do not crimp the fiber and maintain bend radius greater than the minimum.
8. Optical fibers should be left with enough slack next to the TRMTR and RCVR circuit packs slots for later connection. Be sure to tuck fibers into the trough so that attaching and closing the shelf cover does not crimp the fiber.
9. The protective channel will be covered when the stile strips are installed.
10. Remove the protector caps and plugs (if equipped) from the buildout and build-out block and store them in a clean container.



**NOTE:**

Only 0dB, non-attenuating, LBOs are used in most of the TRMTRs. The 739E1 through 739E8 TRMTRs use a non-zero dB attenuator that is installed at the factory. Do not change this attenuator. It is selected to work properly with an Optical Line System (OLS).

11. Connect the transmit fiber jumper to the TRMTR circuit pack.

**LBO Selection**

12. If necessary, measure, and record for office records, the received optical power.

- ⇒ **NOTE:**  
Be sure to (a) connect the RCVR fiber to the power meter, (b) set the optical power meter for the wavelength of the light to be measured, e.g., 1300nm.

13. While considering the application (Table A), and the measured receive power level from Step 12, choose the appropriate LBO from Table C.

**Table C – Optical Line Buildouts**

| Description               | Connection    | Loss (dB) | Comcode   |
|---------------------------|---------------|-----------|-----------|
| A3070 ST 0 dB buildout    | SM-SM & MM-MM | 0         | 106795354 |
| A2060B ST 5 dB buildout   | MM-MM         | 5         | 106795313 |
| A3070B1 ST 5 dB buildout  | SM-SM         | 5         | 107406183 |
| A3070X1 ST 7.1 buildout   | SM-SM         | 7.1       | 107107740 |
| A2070D ST 10dB buildout   | MM-MM         | 10        | 106795321 |
| A3070D1 ST 10 dB buildout | SM-SM         | 10        | 107406191 |
| A2070F ST 15 dB buildout  | MM-MM         | 15        | 106795339 |
| A3070F1 ST 15 dB buildout | SM-SM         | 15        | 107406209 |
| A3070H ST 20 dB buildout  | SM-SM         | 20        | 106795396 |

14. Align the buildout with the slot in the buildout block, push in, and rotate clockwise until locked into position. Refer to the appropriate Figures (3, 4, or 5) for the specific buildout type.
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## **Low Speed Fiber Installation**

- 16.

- ⇒ **NOTE:**  
This procedure is used to select the proper value of LBO to be used on the faceplate of the LAA5/LAA10/T939A (OUT) jack.

The LAA5 supports span lengths up to 2000 feet, when using MM fiber. The LAA10 supports span lengths from 0 to 51 km, when using SM fiber. Also, the T939A supports span lengths from 0 to 51 km, when using SM fiber.

Low speed fibers connect (either directly or through an intermediate LGX) to circuit packs within the Low Speed Shelf. Connectors are located on the faceplate of the LAA5, LAA10, or T939A circuit packs. Connection is made to a removable Optical Line Build-Out (LBO). Only 0dB LBOs are used in the RCV (IN) port, and attenuating LBOs must be used in the TRMT (OUT) port.

Table D lists some of the available multi-mode jumpers for use when connecting a low speed span through an LGX.

**Table D – Multi-mode fiber jumpers**

| Code       | Comcode   | length (feet) |
|------------|-----------|---------------|
| FL1E-E-2   | 105351795 | 2             |
| FL1E-E-10  | 105351837 | 10            |
| FL1E-E-25  | 105351860 | 25            |
| FL1E-E-50  | 105351910 | 50            |
| FL1E-E-100 | 105351936 | 100           |

## Span Lengths

⇒ **NOTE:**  
Single-mode fiber is loss limited, so only the received power needs to be measured. Multi-mode fiber is dispersion limited, not loss limited, thus requiring this additional consideration for the span length. The receive end power must still be measured to ensure that the power level to the receiver is not too high nor too low, however, it can not be assumed that because the power level is within range, that the signal quality is adequate. At the receive end there may be sufficient power, but the signal shape may not be adequate. The length of fiber that is allowed must be determined by the type and manufacturer of the multi-mode fiber.

Table E shows the approximate maximum distances allowable for different fiber bandwidth specifications.

**Table E – Multimode Operation Span Length**

| Fiber Bandwidth Specification | Maximum Span Length (km) | Maximum Span Length (miles) |
|-------------------------------|--------------------------|-----------------------------|
| 1000 MHz-km                   | 6.5 km                   | 4.1 miles                   |
| 800 MHz-km                    | 5.1 km                   | 3.2 miles                   |
| 500MHz-km                     | 3.1 km                   | 1.9 miles                   |
| 300 MHz-km                    | 1.8 km                   | 1.1 miles                   |

⇒ **NOTE:**  
For more detailed information about maximum span lengths for particular fibers, refer to the manufacturers specifications.

While considering the application (Table A), and as appropriate, select the correct jumper from Table B or Table D.

17. If appropriate, install/connect the selected jumpers at the LGX. If keyed biconics are used, align arrows properly.
18. Label the jumpers.
19. Route the optical fibers from the LGX to the protective channels located on each side of the bay. If the fibers are to be connected to OC-3/OC-12 circuit packs located in slots 1A through 4B then route the fibers down the left side of the bay. If the fibers are to be connected to OC-3/OC-12 circuit packs located in slots 5A through 8B then route the fibers down the right side of the bay. Refer to Figure 6.
20. The fiber jumpers should hang without any obstructions in the fiber protection channel. The jumpers should be routed out of the side of the protection channel near the middle so that they can be properly dressed in the Low Speed System Controller Shelf. Make sure the bend radius of the fiber is not exceeded when dressing the fiber. Fibers should never be directly laced in place or cable tied. If secure dressing is not achieved with the steps above then place the fibers in protective tubing and lace the tubing to the protective channel.
21. Fibers enter the Low Speed Shelf near the middle front of the shelf and should be routed up to the optical packs through the notches along the bottom edge of the shelf. Do not crimp the fiber and maintain bend radius greater than the minimum.
22. Optical fibers should be left with enough slack next to other circuit packs slots for later connection. Be sure to tuck fibers into the trough so that attaching and closing the shelf cover does not crimp the fiber.
23. The protective channel will be covered when the stile strips are installed.

#### LBO Selection

24. Does this Low Speed application use LAA10 or T939A Circuit packs?  
If **NO**, then continue with Step 28.  
If **YES**, then continue with Step 25.
25. Measure, and record for office records, the received optical power. Be sure to set the optical power meter for the wavelength of the light to be measured, 1300nm.

 **NOTE:**  
Table F shows the appropriate LBO value to select when the measured

received power is within ranges specified.

#### OC-3/OC-12 LBO Selection

Table F – LBO Selection for LAA10/T939A (MM or SM fibers)

| Measured Received Power in<br>dBm at FT-2000 | Use Optical LBO Value at<br>Transmit |
|----------------------------------------------|--------------------------------------|
| 0 to -20.0 dBm                               | 7.1 dB                               |
| -20.1 to -34.0 dBm                           | 0 dB                                 |

26. Choose the appropriate LBO from Table C.
27. Align the buildout with the slot in the buildout block, push in, and rotate clockwise until locked into position. Refer to the appropriate Figures (3, 4, or 5) for the specific buildout type.
- 28.

#### IS-3 LBO Selection

⇒ **NOTE:**  
For interconnection between an IS-3 (LAA5 or 21D/21D-U/22D-U) and an OC-3 or circuit pack, multi-mode fiber must be used on the IS-3 transmit, and multi-mode or single-mode can be used on the OC-3 transmit as follows:



29. The IS-3 circuit pack uses multi-mode fiber only with a 2000 foot maximum distance. Zero-dB multi-mode attenuators should always be used when connecting IS-3 circuit packs.

30. Measure, and record for office records, the received optical power. Be sure to set the optical power meter for the wavelength of the light to be measured, e.g., 1300nm.
31. Table G shows the appropriate LBO value to select when the measured received power is within ranges specified.

**Table G – LBO Selection for FT-2000 LAA5 (transmit: multi-mode fiber)**

| Measured Received Power in<br>dBm at FT-2000 | Use Optical LBO Value at<br>Transmit |
|----------------------------------------------|--------------------------------------|
| 0 to -14.0 dBm                               | 15dB                                 |
| -14.0 to -30.8 dBm                           | 0 dB                                 |

32. Choose the appropriate LBO from Table C.
33. Align the buildout with the slot in the buildout block, push in, and rotate clockwise until locked into position. Refer to the appropriate Figures (3, 4, or 5) for the specific buildout type.
34. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
- 35.

#### **Remove Buildout**

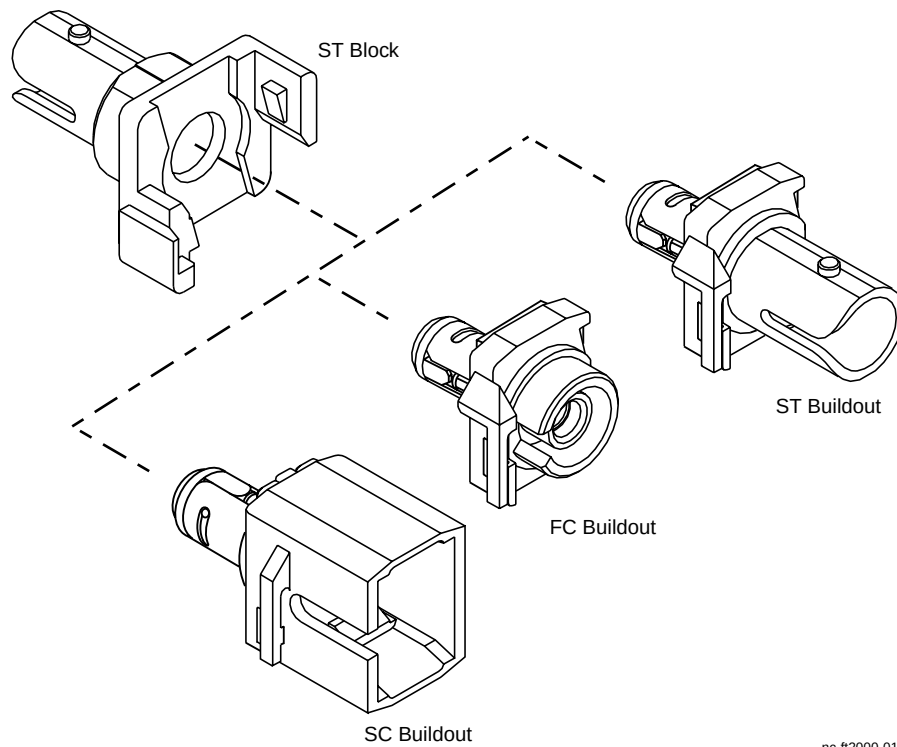


#### **CAUTION:**

*Locking beam must only be pushed along a line perpendicular to buildout body in direction towards the buildout in order to avoid damage to the locking beam.*

Depress the locking beam on the buildout, rotate counterclockwise, and separate from the buildout block by sliding apart. Refer to the appropriate Figures (3, 4, or 5) for the specific buildout type.

36. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



nc-ft2000-015

**Figure 1 – ST<sup>®</sup> Block and Buildouts**

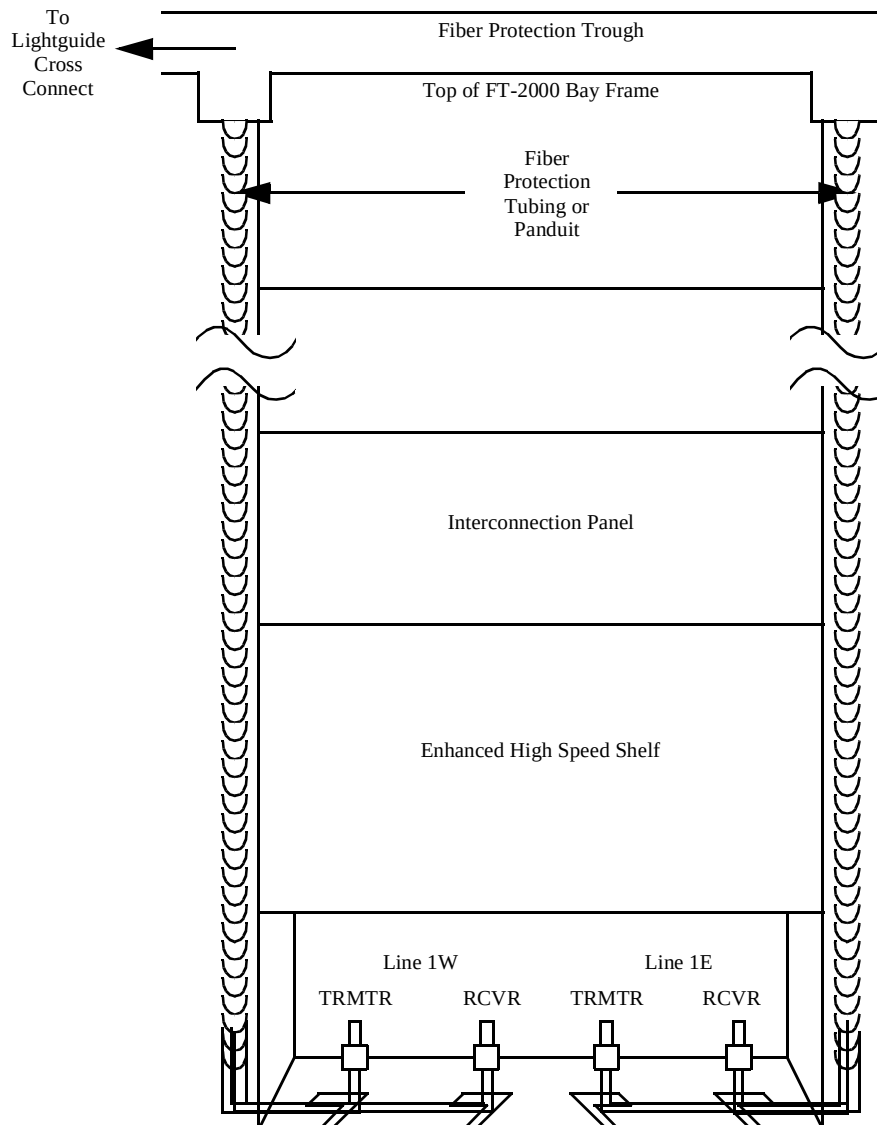


**Figure 2 – FT-2000 OC-48 ADR High-Speed Fiber Routing**



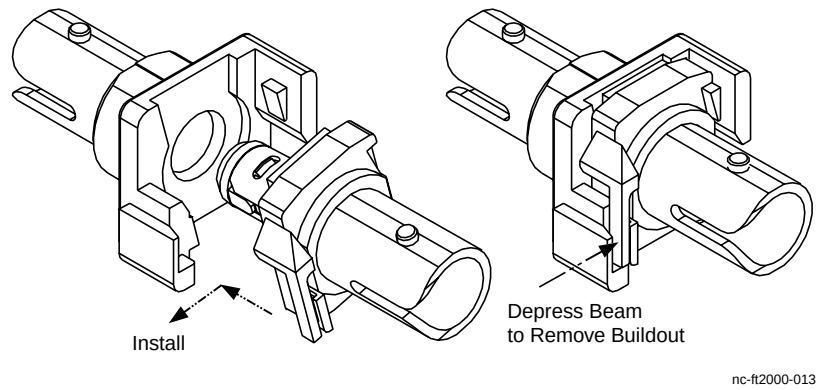


**Figure 3 – ST<sup>®</sup> Block and ST Buildout**

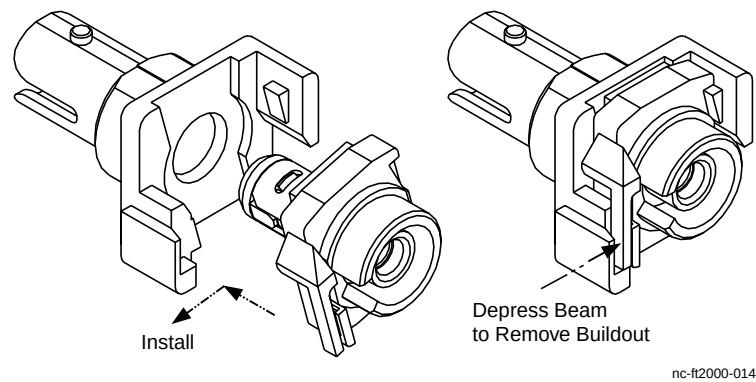


**Figure 4 – ST<sup>®</sup> Block and FC Buildout**

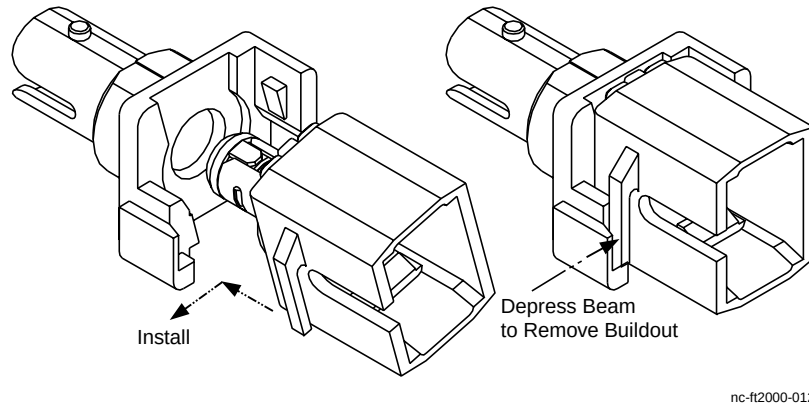


**Figure 5 – ST<sup>®</sup> Block and SC Buildout**

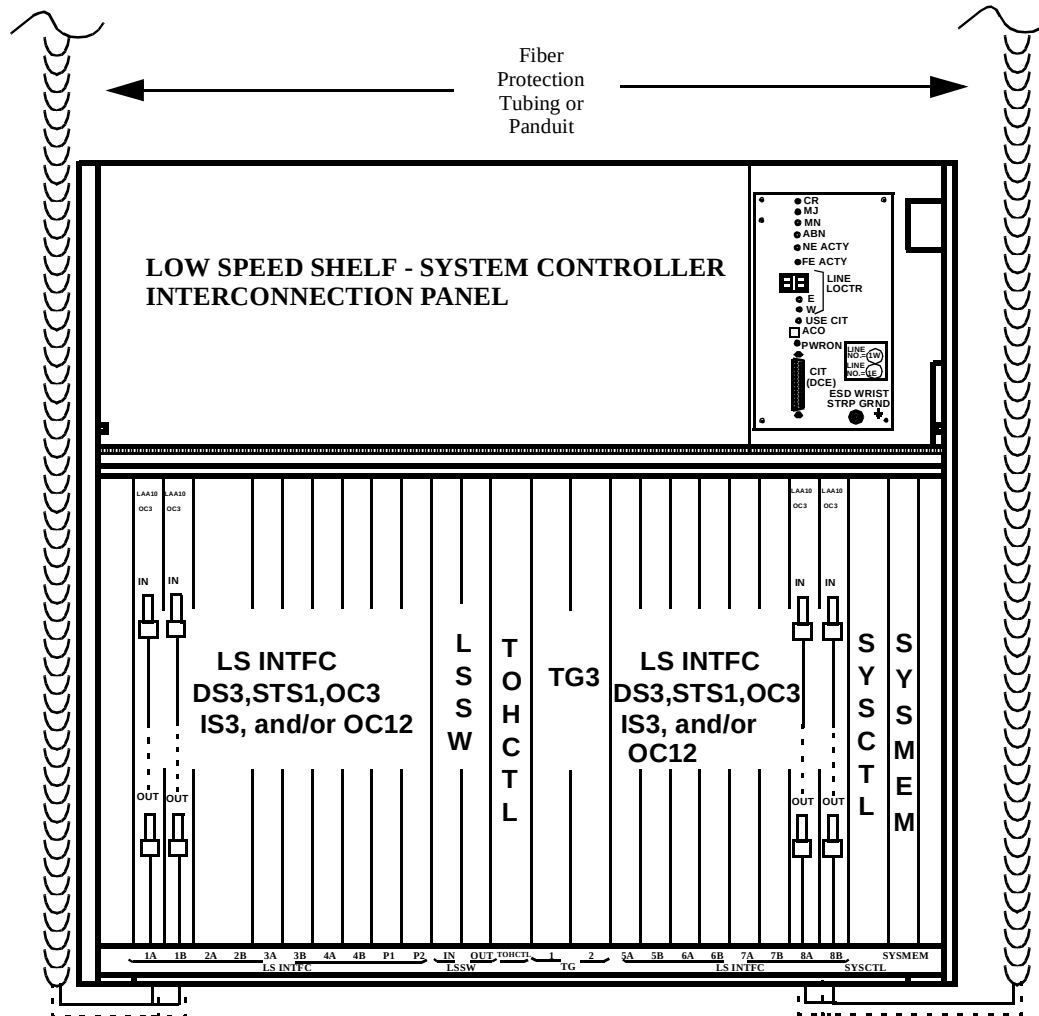


Figure 6 – Low-Speed Shelf Fiber Routing (without cover)



## Test Fan Assembly (ED-6G992-30)

1. Observe that the **POWER ON** LED is lighted and the **ALM** LED is off at the fan assembly. If another condition exists, notify the group that installed the fan assembly.
2. Press and hold in the **ALM TEST** pushbutton for 20 seconds or until the fan speed slows down sufficiently to cause the control board to activate the fan alarm.

Response: **POWER ON** and **ALM** LEDs should be lighted while **ALM TEST** pushbutton is depressed.

3. Release **ALM TEST** pushbutton.

Response: **POWER ON** LED should be lighted and **ALM** LED should be off.

4. At the CIT, select **FAULT-Retrieve-History** to obtain a Maintenance History Report.
5. Are "fan failure" and "fan failure - cleared" conditions listed for the time the fan assembly was tested?

If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

If **NO**, then refer the provisioning discrepancy to the group that did installation of the fan assembly.

Reference: The **CONFIGURATION-Set-Attribute-Environment** command in **Chapter 8** provides information on provisioning the miscellaneous discrete input points #16 as "fan failure."



## Remove and/or Install Circuit Pack

 **CAUTION:**

*A potential service interruption can be avoided for DS3, STS1E, OC3, IS3, OC12, TRMTR, RCVR, or TG3 circuit packs by following this instruction. After a replacement circuit pack has been inserted, the **FAULT** should go out within 35 seconds (optical packs may still FLASH if no signal is present). If the **FAULT** LED remains lighted after the 35 seconds, **immediately remove the replacement circuit pack before 110 seconds elapses.***

 **CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components.*

 **CAUTION:**

*Do not remove the SYSCTL circuit pack unless instructed to by the procedure that sent you here. If the SYSCTL circuit pack is removed, provisioning data could be lost.*

1. Are you to remove or install a circuit pack?  
If to **remove**, then continue with Step 2.  
If to **install**, then continue with Step 17.
2. Is the circuit pack an OC-3, IS-3, OC-12, RCVR, TRMTR, or REGENR?  
If **YES**, then continue with Step 3.  
If **NO**, then continue with Step 6.

 **WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to the beam.*

3. Disconnect the optical fiber(s) from the lightguide buildouts (LBOs) and/or optical connector(s) at the OC-3, IS-3, OC-12, RCVR, TRMTR, or REGENR circuit pack.

4. Remove the optical fiber from the slot guide on the shelf. This is to prevent damage to the fiber when the circuit pack is removed.
5. If required, remove the LBOs from the connector(s) on the OC-3, IS-3, OC-12, RCVR, TRMTR, or REGENR circuit pack.

Reference: **DLP-512**

6. Is the circuit pack a low speed interface (DS3 or STS1E)?

If **YES**, then continue with Step 7.

If **NO**, then continue with Step 10.

7.  **NOTE:**

For DS3 and STS1E circuit packs only: A verification of either an automatic or a CIT provisioned protection switch is made before removing the low speed circuit pack from the shelf.

At the CIT, select FAULT-Retrieve-State:LS INTFC command and obtain a State Report on the circuit pack being replaced.

8. From the report, is the low speed slot (circuit pack to be removed) in a standby state?

If **YES**, then continue with Step 14.

If **NO**, then continue with Step 9.

9. At the CIT, select FAULT-Switch-Low Speed:Slot command for the circuit pack being removed and execute. Use the FAULT-Retrieve-State:LS INTFC command to verify, if desired.

10. Is the circuit pack a TG3 circuit pack?

If **YES**, then continue with Step 11.

If **NO**, then continue with Step 14.

11. **NOTE:**

For TG3 circuit packs only: A verification of a CIT provisioned (manual) protection switch is made before removing the TG3 circuit pack from the shelf.

At the CIT, select FAULT-Retrieve-State:TG command and obtain a State Report on the circuit pack being replaced.

12. From the report, is the TG3 circuit pack to be removed in a standby state with a manual switch?

If **YES**, then continue with Step 14.

If **NO**, then continue with Step 13.



13. At the CIT, select FAULT-Switch-Synchronization-Hardware command for the circuit pack being removed and execute a manual switch. Use the FAULT-Retrieve-State: TG command to verify, if desired.
14. Remove the circuit pack from the shelf as follows:
  1. Push up on the bottom and down on the top metal locking clips to unlock the circuit pack latches.
  2. Unseat the circuit pack by carefully and continuously pulling out equally on the top and bottom latches.
  3. Carefully slide the circuit pack out of the slot guides to remove it from the shelf. DO NOT ROCK THE CIRCUIT PACK BACK AND FORTH.
15. Are you to install another circuit pack in this slot?  
If **YES**, then continue with Step 18.  
If **NO**, then continue with Step 16.
16. At the CIT, select CONFIGURATION-Update command to update the hardware configuration. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
17. If required, remove the apparatus (circuit pack) blank from the slot.  
Reference: **DLP-533**
18. Obtain a **known good** replacement circuit pack with the same or higher series number.
19. Install the circuit pack as follows:
  1. If necessary, close both latches on the circuit pack.
  2. Place the circuit pack into the slot guides and slowly slide it into the shelf until the latches touch the shelf. DO NOT ROCK THE CIRCUIT PACK BACK AND FORTH.
  3. Open the latches and push the circuit pack until it engages the connector pins.
  4. With a thumb on each latch, continue sliding the circuit pack with one firm, continuous motion until the latches are fully engaged (the metal clips are in the locked position).
20. Is the circuit pack just installed an OC-3, IS-3, OC-12, RCVR, TRMTR, or REGENR?  
If **YES**, then continue with Step 21.

If **NO**, then continue with Step 27.

21. If required, install the lightguide buildout to the **IN** connector for the OC-3, IS-3, OC-12, RCVR, or REGENR circuit pack.

Reference: **DLP-512**

22. Clean the optical fiber(s) and connector(s) with canned air or nontoxic gas.

Reference: **DLP-510**

23. Connect the optical fiber(s) to the lightguide buildouts (LBOs) and/or optical connector(s) on the OC-3, IS-3, OC-12, RCVR, TRMTR, or REGENR circuit pack. See table below:

|                    |                                                                                                          |
|--------------------|----------------------------------------------------------------------------------------------------------|
| Receive fiber ==>  | RCVR<br>REGENR (IN Connector)<br>OC-3 (IN Connector)<br>OC-12 (IN Connector)<br>IS-3 (IN Connector)      |
| Transmit fiber ==> | TRMTR<br>REGENR (OUT Connector)<br>OC-3 (OUT Connector)<br>OC-12 (OUT Connector)<br>IS-3 (OUT Connector) |

24. At the CIT, execute the **CONFIGURATION-Retrieve-OC3** or **CONFIGURATION-Retrieve-OC12** command to verify that the input state has transitioned to an IS (in-service) condition on all packs with the fibers connected. If the state of a connected input is not IS, clear the problem before placing the circuits in service.
25. Place the optical fiber(s) in the shelf guide to prevent damaging it when replacing the shelf cover.
26. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
27. Is the circuit pack just installed a TG3?

If **YES**, then continue with Step 28.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

28. At the CIT, select **FAULT-Switch-Synchronization-Hardware** command for the circuit pack just installed and execute a switch reset. Use the **FAULT-Retrieve-State: TG** command to verify, if desired.
29. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Replace Fuse in Power Distribution and Fuse Panel or User Panel

**Overview:** This procedure is used to replace a blown 10A fuse (red lamp is lighted) in the power distribution and fuse panel or in the user panel.

1. Obtain a replacement 10A fuse.
2. Unsnap and lift the latch (from bottom to top) that is around the red lens cap.
3. Push in and down at the top of the red lens cap. Release slowly because the lens cap/fuse should spring forward.
4. Remove the lens cap/fuse from the fuse socket.
5. Remove the blown 10A fuse from the lens cap/fuse and replace with the replacement 10A fuse.
6. Install the lens cap/fuse into the fuse socket by pushing in and up at the bottom of the lens cap.
7. Lower the latch (from top to bottom) around the red lens cap and snap into place.
8. Did the alarm clear in approximately five minutes?  
    If **NO**, perform **DLP-550, Replace a Defective Power-Line Filter**.  
    If **YES**, go to Step 9.
9. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Obtain State Report

**Overview:** This procedure supports obtaining the appropriate report after the provisioning of a DS3, EC1, OC-3, or OC-12 slot or port.

1. At the CIT, select **CONFIGURATION-Retrieve** and the appropriate modifiers as shown in Table A to obtain a State Report based on the previous parameter selections (provisioning).

**Table A - Parameter**

| <b>If Slot and Port Parameter Selections Were</b> | <b>Select These Inputs (Commands) to Obtain Report</b> |
|---------------------------------------------------|--------------------------------------------------------|
| CONFIGURATION-Set-Low Speed                       | CONFIGURATION-Retrieve-Low Speed:All                   |
| CONFIGURATION-Set-T3                              | CONFIGURATION-Retrieve-T3:All                          |
| CONFIGURATION-Set-State-T3                        | CONFIGURATION-Retrieve-T3:All                          |
| CONFIGURATION-Set-EC1                             | CONFIGURATION-Retrieve-EC1:All                         |
| CONFIGURATION-Set-State-EC1                       | CONFIGURATION-Retrieve-EC1:All                         |
| CONFIGURATION-Set-OC3                             | CONFIGURATION-Retrieve-OC3:All                         |
| CONFIGURATION-Set-State-OC3                       | CONFIGURATION-Retrieve-OC3:All                         |
| CONFIGURATION-Set-OC12                            | CONFIGURATION-Retrieve-OC12:All                        |
| CONFIGURATION-Set-state-OC12                      | CONFIGURATION-Retrieve-OC12:All                        |

2. Press function key **F9** to execute and to display the report.
3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Provision Low-Speed Slot and Port Parameters

1. **NOTE:**

Provisioning information is described in Volume 1, Section 8 of this manual. A Job Aid is also available for the FT-2000 add/drop-rings terminal (365-575-122).

Find the slot parameter in Table A to be provisioned or the port parameter in Table B to be provisioned and use the indicated CIT command(s) to provision the desired parameter(s):

**Table A - Slot Parameters**

| Slot for         | Parameter                                                                                                                                                                                                 | Input (Command)                    |
|------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------------------|
| DS-3 and/or EC-1 | Protection Switching Priority                                                                                                                                                                             | CONFIGURATION-Set-Low Speed        |
| OC-3             | Protection Mode (From OC3)<br>Protection Mode (From OC48)<br>Alarm Level<br>Signal Degrade Threshold<br>Output AU4 Mapping                                                                                | CONFIGURATION-Set-OC3              |
|                  | DCC Assignment                                                                                                                                                                                            | CONFIGURATION-Enter-Assignment-DCC |
| OC-12            | Protection Mode (From OC12)<br>Protection Mode (From OC48)<br>Alarm Level (Near End)<br>Alarm Level (Far End)<br>Signal Degrade Threshold<br>Output AU4 Mapping<br>OC12 Timing Direction<br>Sync Messages | CONFIGURATION-Set-OC12             |
|                  | DCC Assignment                                                                                                                                                                                            | CONFIGURATION-Enter-Assignment-DCC |

**Table B - DS-3 and EC-1 Port Parameters**

| <b>Parameter</b>               | <b>DS-3 Input (Command)</b> | <b>EC-1 Input (Command)</b> |
|--------------------------------|-----------------------------|-----------------------------|
| Violation Monitor/Removal Mode | CONFIGURATION-Set-T3        | None                        |
| Alarm Attribute                | CONFIGURATION-Set-T3        | CONFIGURATION-Set-EC1       |
| Signal Degrade Threshold       | CONFIGURATION-Set-T3        | CONFIGURATION-Set-EC1       |
| Primary State                  | CONFIGURATION-Set-State-T3  | CONFIGURATION-Set-State-EC1 |

2. Repeat Step 1 for the next parameter to be provisioned.
3. If desired, verify the parameter (just provisioned) by obtaining a State Report.

Reference: **DLP-516**

4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Temporarily Remove High-Speed Optical Loopback(s) at FT-2000 Network Element

**Overview:** This procedure is used to temporarily remove the high-speed optical loopback(s) during a low-speed cross-connect wiring test.

1.



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if the beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components.*

Remove the lightguide jumper from the **OUT** connector of the TRMTR circuit pack and the **IN** connector of the RCVR circuit pack. Slide a protector cap (if available) over the ends of the lightguide jumper.

2. Repeat Step 1 for an adjacent high-speed line in the same shelf.
3. Repeat Steps 1 and 2 for any remaining high-speed shelves.
4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Install and/or Remove Optical Loopback at OC48 REGENR Circuit Pack

**Overview:** This procedure optically loops each REGENR circuit pack on itself with a fixed line buildout (15 or 20 dB) for testing.

1.  **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 repeater bay/shelf to prevent electrostatic discharge damage to sensitive components.*
-  **WARNING:**  
*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

Remove the shelf cover from the FT-2000 repeater bay/shelf.

Reference: **DLP-511**

2. Is the OC-48 line loopback (Figure 1) being installed or removed?  
To **install loopback**, continue with Step 3.  
To **remove loopback**, continue with Step 13.

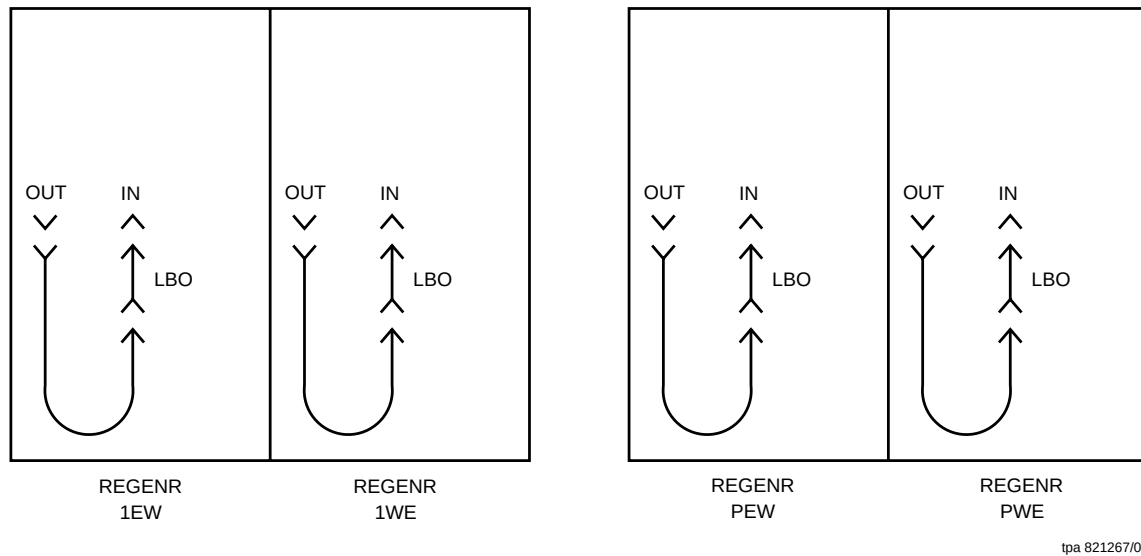


**Figure 1 – Optical Loopbacks at Repeater**

### Install Loopback

3. For each OC-48 line to be manually looped back, obtain a lightguide jumper (package label FS1E-E-02) and a 20-dB loss lightguide buildout, or equivalents. (It is possible to connect another LBO to an existing LBO to obtain a total of 20-dB provided the LBOs do not physically interfere with the shelf cover when closed.) Table A shows additional buildouts that are available. Table B shows the buildout types that are compatible with different codes of the REGENR circuit pack.

**Table A - Lightguide  
Buildout Values**

| dB Loss | Letter<br>Code |
|---------|----------------|
| 0       | None           |
| 5 ±1    | B              |
| 15 ±2   | F              |
| 20 ±2   | H              |

**Table B - Lightguide Buildouts and Circuit Pack  
Compatibility**

| Buildout Type (Code)                        | Circuit Pack                                                                           |
|---------------------------------------------|----------------------------------------------------------------------------------------|
| ST <sup>®</sup> , (A3010*), FC (A3030*)     | REGENR 39B1<br>REGENR 39C1                                                             |
| ST (A3070*), FC (A3080*), or<br>SC (A3060*) | REGENR 39B2<br>REGENR 39C2<br>REGENR 39J2<br>REGENR 39P2<br>REGENR 39R2<br>REGENR 39S2 |

\* See Table A for letter code.



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

4. Remove the OC-48 line optical connection from the **OUT** connector of the OC48 REGENR circuit pack and slide the protector cap (if available) over the end of the fiber.
5. Remove the OC-48 line optical connection to the **IN** connector of the OC48 REGENR circuit pack and slide the protector cap (if available) over the end of the fiber.
6. If equipped, remove the lightguide buildout from the **IN** connector of the OC48 REGENR circuit pack.

Reference: **DLP-512**

7. Remove the protector caps (if equipped) from the ends of the optical loopback cable and store the caps in a clean area (plastic box).
8. Clean the optical buildout, cable fiber ends, and connectors with canned air or nontoxic gas.

Reference: **DLP-510**

9. Install a 20-dB buildout into the **IN** connector of the OC48 REGENR circuit pack.

Reference: **DLP-512**

10. Connect the lightguide jumper between the buildout (at **IN** connector) and the **OUT** connector on the same OC48 REGENR circuit pack (same line.)
11. Repeat Steps 3 through 10 for the adjacent OC48 REGENR circuit pack(s) in the same shelf to be looped back.
12. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

### **Remove Loopback**

- 13.



#### **WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



#### **NOTE:**

Remember that the OC48 REGENR circuit packs are used in the service and protection lines and in both the east and west directions.

Remove the lightguide jumper connected between the **OUT** and **IN** connectors of an OC48 REGENR circuit pack and slide protector caps (if available) over the jumper ends.

14. If equipped, remove the 20-dB buildout (for testing) from the **IN** connector of an OC48 REGENR circuit pack.

Reference: **DLP-512**

15. If equipped, clean the buildout (engineered for that line) that was removed in Step 6 and reinstall it in the **IN** connector of the OC48 REGENR circuit pack.

Reference: **DLP-510**

16. Select the transmit fiber, remove the protector cap (if equipped), and store the cap in a clean area (plastic box).

17. Clean the connector on the transmit fiber and the **OUT** connector of the OC48 REGENR circuit pack with canned air or nontoxic gas.

Reference: **DLP-510**

18. Connect the transmit fiber to the **OUT** connector of the OC48 REGENR circuit pack.
19. Select the receive fiber, remove the protector cap (if equipped), and store the cap.
20. Clean the connector on the receive fiber and the buildout (if equipped) or the **IN** connector of the OC48 REGENR circuit pack, using canned air or nontoxic gas.

Reference: **DLP-510**

21. Connect the receive fiber to the line engineered buildout (if equipped) or the **IN** connector of the OC48 REGENR circuit pack.
22. Repeat Steps 13 through 21 for the next OC48 REGENR circuit pack to have the optical loopback removed.
23. Replace the shelf cover.

Reference: **DLP-511**

24. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Properly Terminate DS3 and/or EC1 Outputs

**Overview:** This procedure is used to properly terminate the DS3 and/or EC1 outputs from the FT-2000 network element. Descriptive information is contained in Section 5 of this manual.



**CAUTION:**

*Do not inadvertently unterminate any DS3 or EC1 outputs that were previously terminated.*



**CAUTION:**

*DS3 and EC1 outputs should always be terminated by one of the following methods to prevent false DS3 or EC1 failure indications.*

1. Are the DS3 and/or EC1 output connected to a cross-connect panel or connected directly to other transmission equipment?  
  
    If to **cross-connect panel**, then continue with Step 2.  
  
    If to **other transmission equipment**, then continue with Step 5.
2. Does the DSX-3 (STXS-1) cross-connect panel provide internal terminations for nonconnected circuits? (cross-connect panels meet this requirement.)  
  
    If **YES**, then continue with Step 4.  
  
    If **NO**, then continue with Step 3.
3. At the DSX-3 (STXS-1) cross-connect panel, install a 75-ohm termination on each nonconnected DS3 and/or EC1 output.
4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
5. Verify the DS3 and/or EC1 outputs (low-speed cable) are connected directly to other working transmission equipment, such as a DACS III-2000, DDM-2000, etc., to provide good low-speed terminations.
6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Establish Modem or *Datakit*<sup>®</sup> Network Access

**Overview:** This procedure is used to access a distant FT-2000 network element (NE) from a local craft interface terminal (CIT). Some of the more common CIT access configurations are shown in Figures 1 through 4. Other configurations exist with access gained through local company procedures. Once access is gained, the FT-2000 network element responds to any CIT selections just as if you were physically connected to it.

1. Are you using the public switched network (via modem) or the *Datakit* network?

If **public switched network**, then continue with Step 2.

If *Datakit* **network**, then continue with Step 25.

### Quick Check for Compatible Modems (Both Locations)

2.



**NOTE:**

Modems at the CIT and FT-2000 NE locations must be compatible. Quick check/setup of the modem parameters and connections are given in this procedure for the FT-2000 NE location first, then for the CIT location.



**NOTE:**

The manufacturer's manuals may be referred to for specific instructions on setting the modem parameters. For example, when using a *Penril*<sup>\*</sup> modem, the speed conversion parameter must be set to off.



**NOTE:**

Additional information on using a modem for remote access is available in Section 6, "Operations Interface." Also, refer to 365-575-115, *FT-2000 OC-48 Lightwave System, Releases 3, 4, and 5, Installation Manual*, or to 365-575-104, *FT-2000 OC-48 Lightwave System, Releases 1 and 2, Installation Manual*.

---

\* Registered trademark of Penril Corporation

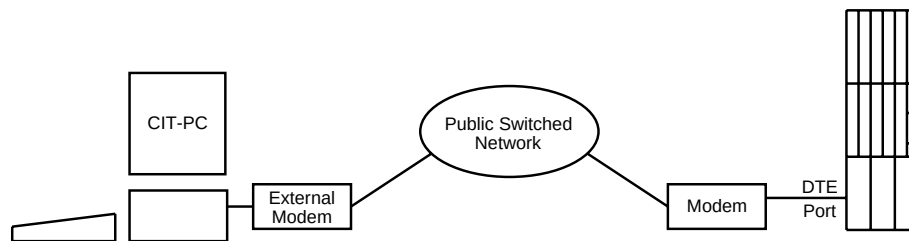


Figure 1 - External Modem Configuration

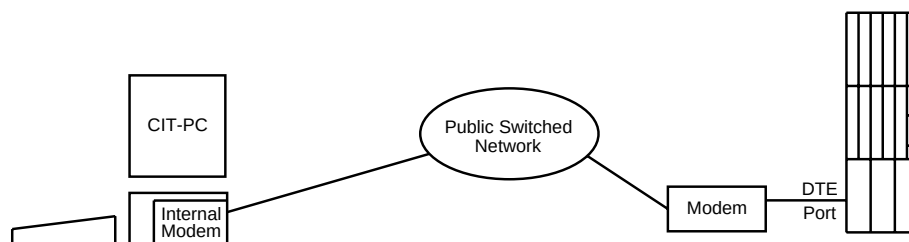


Figure 2 - Internal Modem Configuration

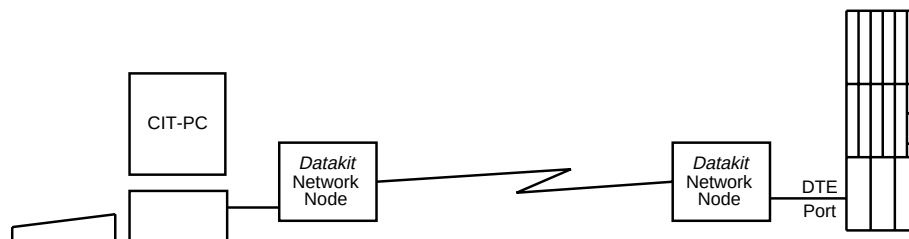


Figure 3 - Datakit Network Configuration

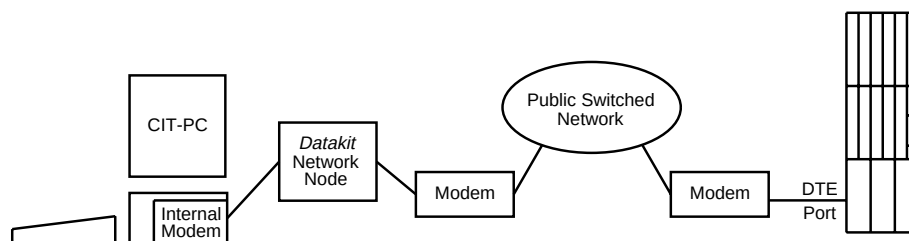


Figure 4 - Hybrid Network Configuration

**Figure 1 – CIT/FT-2000 Network Element Configurations**

Verify the generic parameters of the modem are set to the following:

- Modem baud rate (AT&T 4024 or AT&T 2224 CEO modems at 2400, AT&T 2292 modem at 9600) for CIT (DCE) port. This port and the CIT automatically select the highest usable baud rate up to 9600.
- Full duplex
- 8 data bits
- No parity bits
- 1 start bit
- 1 stop bit
- No flow control.

### FT-2000 Network Element Location

3.



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge Considerations" in **Trouble Clearing: TAD-100**.*

Set the modem to answer incoming calls. For example, if you are using an AT&T 2224 CEO modem, do the following to program the modem:



**NOTE:**

It is assumed the PC and modem ("AT" command compatible) are powered and the PC is loaded with communications software.

- a. Set all front panel switches to DOWN (factory default) for AT command mode.
- b. Connect PC serial port to the modem RS-232C port using modem cable (Table A) or using a null-modem adapter (Figure 5).



**NOTE:**

Black Box model # ME-200 provides these null-modem connections and female-to-female connectors.

- c. Establish communications between the PC and modem using the communications software set for the generic parameters in Step 2.

- d. At the prompt, enter the following "AT" commands.

| <b>PC</b> | <b>Response</b> | <b>Comment</b>                   |
|-----------|-----------------|----------------------------------|
| ATH       | OK              | #hang-up (on-hook)               |
| AT&F      | OK              | #resets to factory defaults      |
| AT&C1     | OK              | #data carrier is present when on |
| ATS0=2    | OK              | answer on 2 rings                |
| AT&W      | OK              | wait for second dial tone        |

- e. Remove the connector at the PC serial port and plug into the CIT (DTE) connector at the interconnection panel (Low Speed Shelf - System Controller) of the FT-2000 network element. Dial-up access to the network element via a 2400 baud modem is now ready.
4. Verify the telephone line is plugged into the modem.
5. This completes the quick check/setup for the modem at the FT-2000 NE location. Continue with the next step (modem at CIT location).

#### **CIT Location**

6. Is the modem installed internally (card) or a stand-alone (external).  
If **internal modem**, then continue with Step 7.  
If **external modem**, then continue with Step 8.
7. Verify the telephone line is plugged into the modem. Continue by going to Step 11.
8. Verify the modem is connected to the CIT, using the appropriate modem cable. Tables B and C list the pins for the DB-25 connector and DB-9 connector, respectively.
9. Verify the modem is powered.
10. This completes the quick check/setup for the modem at the CIT location. Continue with the next step (CIT operation).

#### **Using CIT DIAL Feature**

11. Do you want to use the DIAL feature (function key **F3**) in the FT-2000 CIT-PC software?  
If **YES**, then continue with Step 15.  
If **NO**, then continue with Step 12.
12. Dial the remote FT-2000 network element by entering the telephone number. A personal computer communications software package is very helpful in

accessing the remote FT-2000 network element. Many packages exist, so use the one you are familiar with which may be *ProComm Plus*<sup>\*</sup>, CTRM, QMODEM, SimTerm, *Terranova*<sup>®</sup>, etc.

13. After the remote modem answers, press the  key or space bar until communication between the two is established. Some locations may use a server switch at the FT-2000 network element which requires entering an additional login, password, and a port assignment in order to connect to the FT-2000 network element.
14. Once communication has been established to the remote FT-2000 network element, escape to *MS-DOS*<sup>†</sup> (at the CIT), enter "cit", and press the  key. The FT-2000 CIT-PC software should start and display the logo/copyright screen. Continue with Step 22.
15. At the CIT with an *MS-DOS* prompt, change directory to where the CIT-PC software is installed and enter "cit" without the quotes and press  To clear the Network Systems screen, press .
16. Press the **F6** key for the CIT *terminal* SETUP. Select the desired communication port, baud rate to match the modem, and monitor and press **F6**.
17. Press the **F3** key, labeled **DIAL**.
18. This step is optional and is only used if you wish to change the previous *modem* parameter settings. You may change the parameters by pressing the **F6** key, labeled **SETUP**, then select the desired PORT and BAUD rate and press the  key twice (to SAVE and EXIT).
19. For an external modem, press the  key several times until the dial response appears on the screen. For an internal modem, enter the dial string "atdt xxx-xxxxx" (phone number of the FT-2000 network element) and press the  key.
20. Type the desired telephone number and press the  key.
21. After the connection is confirmed, press function key **F9**, labeled **CONT** (to continue).
22. Press the  key to select **FT-2000**, then type your login and password.
23. After completing your work, log out of the FT-2000 network element by pressing function key **F4**, labeled **LOGOUT**, or exit the software by pressing function key **F5**, labeled **EXIT**. At this time, you may press **F3** again to establish a new connection to a different FT-2000 network element.

---

\* Registered trademark of Datastrom Technologies, Inc.

† Registered trademark of Microsoft Corporation.

24. **STOP. YOU HAVE COMPLETED THIS PROCEDURE.**

**Quick Check for Typical Datakit Network**

25.

⇒ **NOTE:**  
The *Datakit* network supports many configuration parameters. Some parameters are changeable by the user; others are set by the network administrator. The user should consult local procedures in determining how to configure their network. The manufacturer's manuals may be referred to for specific instructions on setting the parameters.

⇒ **NOTE:**  
The provisioned state (parameter settings) of the *Datakit* port to which the user is connected may interfere with the successful interaction of the CIT PC and the FT-2000 NE. This procedure allows the connection to be made independently of the provisioned state of the *Datakit* port.

Press the  key and momentarily wait for the response **DESTINATION:**.

26. Did you get the **DESTINATION:** prompt?

If **YES**, then continue with Step 34.

If **NO**, then continue with Step 27.

27.

⇒ **NOTE:**  
The *Datakit* port (to which the PC is connected) is NOT supporting the *Datakit* command mode. Perform the following steps to allow the connection to be made independently of the provisioned state of the *Datakit* port. This allows a method to connect to the CIT (DTE) port of an FT-2000 NE via a *Datakit* network.

Put the PC into a terminal emulation mode and send the "attention sequence" which is usually provisioned to be two BREAK characters. [Check with the *Datakit* network administrator for the local attention sequence character(s).]

28. Did the *Datakit* port respond with **Command:?**

If **YES**, then continue with Step 31.

If **NO**, then continue with Step 29.



29.



**NOTE:**

Assistance is required from your *Datakit* network administrator. The administrator must verify or provision the following parameters for the *Datakit* port you're using.

Verify the generic parameters of the *Datakit* network port are set to the following:

- Full duplex, asynchronous
- 8 data bits per serial data byte with 1 start bit and 1 stop bit
- No parity bits
- Attention sequence is 2 breaks initially; however, zero breaks may be required for proper operation
- No flow control by the network and no flow control by the device.

30. After verifying or provisioning the parameters, continue with Step 34.

31. At the **Command:** prompt, type SET ATTN NONE and press the **ENTER** key.

32. Type CONT and press the **ENTER** key. (The **Destination:** prompt should appear on the screen.)

33. This completes the provisioning state of the *Datakit* port. Continue with the next step.

### Using Datakit Network

34. Type the desired *Datakit* network number and press the **ENTER** key.

35. Press the **F4** key, labeled **CHECK**, to verify connectivity between the CIT and the network element. A notice frame appears on the screen.

36. What does the notice frame indicate?

If the **NE is communicating**, then continue with Step 37.

If the **NE is not responding**, then you may wish to (a) double check the *Datakit* port parameters (Step 29) and (b) seek the next level of *Datakit* network help.

37. Press the **ESC** key to clear the notice frame.

38. Press the **F9** key, labeled **CONT**, to continue the CIT-PC software program.

39. Press the **ENTER** key to select FT-2000, then type your login and password.

40. After completing your work, log out of the FT-2000 network element by pressing function key **F4**, labeled **LOGOUT**, or exit the software by pressing function key **F5**, labeled **EXIT**. At this time, you may press **F3** again to establish a new connection to a different FT-2000 network element.
41. **STOP. YOU HAVE COMPLETED THIS PROCEDURE.**

**Table A. Pin Connections at CIT (DTE) Connector**

| <b>CIT (DTE) Connector (Note)</b>    | <b>FT-2000 Network Element</b>                                                                    |
|--------------------------------------|---------------------------------------------------------------------------------------------------|
| Pin 1 - Protective Ground            | Protective frame ground.                                                                          |
| Pin 2 - Transmit Data                | Transmits data to the modem.                                                                      |
| Pin 3 - Receive Data                 | Receives data from the modem.                                                                     |
| Pin 4 - Request to Send              | FT-2000 network element notifies the modem that it is ready to send data.                         |
| Pin 5 - Clear to Send                | Modem notifies the FT-2000 network element that it is clear to send its data                      |
| Pin 6 - Data Set Ready               | Modem notifies the FT-2000 network element that the modem is connected and ready to receive data. |
| Pin 7 - Signal Ground                | Signal ground.                                                                                    |
| Pin 8 - Receive Line Signal Detected | This modem has detected carrier from the remote modem.                                            |
| Pin 20 - Data Terminal Ready         | FT-2000 network element notifies the modem that it is connected.                                  |

**Note:** All remaining pins are not connected.

---

**Table B. Pin Connections for Modem Using DB-25 Cable**

| <b>PC DB-25 Connector<br/>(Note)</b> | <b>CIT</b>                                                      |
|--------------------------------------|-----------------------------------------------------------------|
| Pin 1 - Protective Ground            | Protective frame ground.                                        |
| Pin 2 - Transmit Data                | Transmits data to the modem                                     |
| Pin 3 - Receive Data                 | Receives data from modem                                        |
| Pin 4 - Request to Send              | CIT is requesting clearance from the modem so it can send data. |
| Pin 5 - Clear to Send                | Modem notifies the CIT that it is clear to send data.           |
| Pin 6 - Data Set Ready               | Modem notifies the CIT that the modem is connected.             |
| Pin 7 - Signal Ground                | Signal ground                                                   |
| Pin 8 - Carrier Detect               | This modem detects carrier from the remote modem.               |
| Pin 20 - Data Terminal Ready         | CIT notifies the modem that it is connected.                    |

**Note:** All remaining pins are not connected.

**Table C. Pin Connections for Modem Using DB-9 Cable**

| <b>PC DB-9 Connector</b>    | <b>CIT</b>                                                                                   |
|-----------------------------|----------------------------------------------------------------------------------------------|
| Pin 1 - Carrier Detect      | This modem detects carrier from the remote modem.                                            |
| Pin 2 - Receive Data        | Receives data from modem.                                                                    |
| Pin 3 - Transmit Data       | Transmits data to the modem.                                                                 |
| Pin 4 - Data Terminal Ready | CIT notifies the modem that it is connected.                                                 |
| Pin 5 - Signal ground       | Signal ground                                                                                |
| Pin 6 - Data Set Ready      | Modem notifies the CIT that the modem is connected and ready to receive data.                |
| Pin 7 - Request to Send     | CIT is requesting clearance from the modem so it can send data.                              |
| Pin 8 - Clear to Send       | Modem notifies the CIT that it is clear to send.                                             |
| Pin 9 - Ring indicator      | Modem notifies the CIT that ring signal is present when the modem is in an auto-answer mode. |

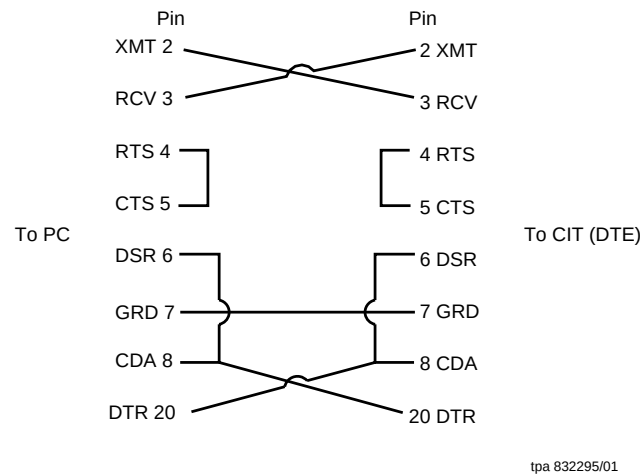


**Figure 5 - Null-Modem Internal Wiring Connections**

## Change, Delete, or Enter a User's Login

**Overview:** This procedure covers login aging and password aging in addition to changing, deleting, or entering a user's login.



**CAUTION:**

*These tasks can only be performed by one of two privileged users. Privileged users cannot change their own login.*



**CAUTION:**

*Additional information about security and aging is provided in Section 8, "Administration and Provisioning."*

1. If required, connect and condition the craft interface terminal (CIT).

Reference: **DLP-501**

2. What do you want to do to a user's login?

If to **change a user's login**, continue with Step 3.

If to **delete a user's login**, continue with Step 7.

If to **enter a new user's login**, continue with Step 10.

If to **enable/disable login aging**, continue with Step 16.

If to **enable/disable password aging**, continue with Step 16.

### Change Login

3. At the CIT, select **SECURITY-Set-Login-Change:** for the specified user's login to be changed.
4. Select the appropriate input parameter (new login, password, verify password, user privilege, and status), make the change, and press function key **F9** to execute the change(s).
5. At the CIT, select **SECURITY-Retrieve-Login:** to obtain the Login Provisioning Report and verify that the user's login has been changed.
6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Delete Login

7. At the CIT, select **SECURITY-Set-Login-Delete:** for the specified user's login to be deleted and press function key **F9** to execute the deletion.
8. At the CIT, select **SECURITY-Retrieve-Login:** to obtain the Login Provisioning Report and verify that the user's login has been deleted.
9. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Enter Login



**NOTE:**

Each FT-2000 network element may have up to 10 nonprivileged users with software Releases 4.0 and lower and up to 98 nonprivileged users with software Releases 4.1 and later.

10. At the CIT, select **SECURITY-Set-Login-Enter** and enter the new user login (up to 10 alphanumeric characters) and an initial password (from 6 to 10 characters with 1 number and with 1 symbolic character except the @, #, and =).
11. When prompted, reenter the initial password to verify your intended password. Passwords are not displayed.
12. Assign either security level (Reports Only or General) to the user's login and press function key **F9** to execute the assignment.
13. At the CIT, select **SECURITY-Retrieve-Login:** to obtain the Login Provisioning Report and verify that the user's login has been entered.
14. The login and initial password may be given to the person working on the FT-2000 network element.
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Enable/Disable Login Aging and/or Password Aging

16. At the CIT, select the **SECURITY-Set-Security-Network Element:** command to gain access to the global security parameters for the system.
17. Select the appropriate input parameter, enter a value, and press function key **F9**.

**Logins Allowed=** <Yes, No> If No, all nonPrivileged users will be immediately disconnected.

**Password Aging=** <0, 7 through 999> A 0 (zero) will disable.

**Login Aging=** <0, 7 through 999> A 0 (zero) will disable.

18. At the CIT, select **SECURITY-Retrieve-Security-Network Element :** to obtain the Access Security Global Configuration Report and verify the correct parameter value(s).
19. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Release Looped Back DS3 and/or EC1 Circuit(s)



**CAUTION:**

*Traffic and service interruption can occur to other DS3 or EC1 circuits if they are accidentally looped back. Recheck the address of the port(s) being released from loopback. Release of loopback occurs at the DS-3/EC-1 level.*



**NOTE:**

A DS3 (LAA2/LAA2B) or STS1E (LAA4) circuit pack has three ports.

1. Obtain the operation instructions to release the looped back DS3 or EC1 port(s) and note the assigned address.
2. If required, connect and condition the craft interface terminal (CIT).  
Reference: **DLP-501**
3. At the CIT, select **FAULT - Release - Loopback - T3** (for DS3 circuits) or **FAULT - Release - Loopback - EC1** (for EC1 circuits) for the first of the specified port(s).
4. Repeat Step 3 for any remaining port(s).
5. The port(s) is released from the looped back condition. If you wish to verify, select **FAULT - Retrieve - Alarm** for an updated Active Alarms and Status Report. Section 11 contains an example of this report.
6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Establish or Disconnect Remote Login Using CIT

**Overview:** This procedure is used to gain access to a remote FT-2000 or DDM-2000 network element (NE) over the data communications channel (DCC) but not to an FT-2000 repeater bay/shelf.

1. Are you to establish remote login or disconnect from remote login?

To **establish**, then continue with Step 2.

To **disconnect**, then continue with Step 8.

### Establish Remote Login

2. If required, connect and condition the craft interface terminal (CIT).

Reference: **DLP-501**

- 3.



**NOTE:**

The CIT connection state is indicated in the upper right-hand corner of the FT-2000 screen only as **LOCAL ONLY**, **REMOTE**, or **LOCAL**.

**LOCAL ONLY** - CIT is only connected to local network element.

**REMOTE** - CIT is connected to remote network element from local network element.

**LOCAL** - CIT is connected to remote network element but it is toggled back to local network element.

Press the **F2** function key labeled **REMOTE** followed by the enter or return key to display the **Destination:** prompt window.

- 4.



**NOTE:**

With Release 4.1 and later software, typing a question mark (?) in response to the TID prompt will display a pick list of available addresses in the network(s).

Enter the target identification (TID) obtained from the office records (or report) for the remote FT-2000 or DDM-2000 NE.

5.

⇒ **NOTE:**  
For FT-2000 only. If your login on the local network is also a valid login on the remote network element, you will be logged in automatically. Otherwise, you must log in at this time.

⇒ **NOTE:**  
For DDM-2000 only. A small box will be displayed on your terminal monitor after a successful login. Please note the software version listed in the box for possible use at a later time.

If necessary, enter the login and password for the remote FT-2000 or DDM-2000 NE. If unable to log in to the network element, check your login and password with the office records and/or supervisor.

6.

⇒ **NOTE:**  
If remotely logged in to an FT-2000, **REMOTE** is now displayed in the upper right-hand corner.

⇒ **NOTE:**  
The remote FT-2000 or DDM-2000 NE will now respond to any inputs from the CIT just as if you were physically connected to it.

⇒ **NOTE:**  
If you have remotely logged in to an **FT-2000**, then **F2** is now labeled **LOCAL** and acts as a toggle back to the local FT-2000. If you have remotely logged in to a **DDM-2000**, then **<control> T** allows you to toggle back to the local network element and **F2** on the FT-2000 screen allows you to toggle back to the DDM-2000.

Perform any required work such as obtaining reports or provisioning.

7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

**Disconnect Remote Login**

8. Is **LOCAL** or **REMOTE** displayed in the upper right-hand corner of the CIT screen or are you currently displaying DDM-2000 screen?  
If **LOCAL**, then continue with Step 9.  
If **REMOTE**, then continue with Step 10.  
If **DDM-2000**, then continue with Step 13.
9. You should be logged in to the remote network element. If the remote is an FT-2000, **REMOTE** will be displayed in the upper right-hand corner.
- 10.



**NOTE:**

If **LOGOUT** is selected while connected to the remote FT-2000, the remote connection is dropped, but the CIT continues to operate with the local FT-2000 NE. If **LOGOUT** is selected at the local FT-2000 NE, both the remote and local connections are dropped.

Are you now remotely logged in to an FT-2000 NE or a DDM-2000 NE?

- If **FT-2000**, then continue with Step 11.
- If **DDM-2000**, then continue with Step 13.
11. Press **F4** function key, labeled **LOGOUT**, to log out of the remote FT-2000. The CIT is still logged into the local FT-2000 NE.
12. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
13. Enter **lo;** at the DDM-2000 prompt. This will log you out of the DDM-2000 NE and return you to **LOCAL ONLY** mode on the FT-2000 NE.
14. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Loop Back DS3 and/or EC1 Circuit(s) for Testing



**CAUTION:**

*Traffic and service interruption can occur to the other DS3 or EC1 circuits if they are accidentally looped back. Recheck the address of the port(s) being looped back. Loopback occurs at the DS3 or EC1 level towards the OC-48 line(s).*



**NOTE:**

A logical loopback will be completed and indicated whether or not a compatible circuit pack is present. A physical loopback will occur only when a compatible circuit pack is present. For example, if an **Operate-Loopback-T3** command is executed to an address of a slot containing an STS1E circuit pack, the logical loopback will be "COMPLD" but no physical loopback will occur.



**NOTE:**

A DS3 or STS1E circuit pack has three ports.

1. Obtain the operation instructions to loop back the DS3 or EC1 circuit(s) for testing and note the assigned address.
2. If required, connect and condition the craft interface terminal (CIT).

**Reference: DLP-501**

3.



**NOTE:**

A single port may be looped back towards the OC-48 line(s) and will remain looped until deactivated by the **Release-Loopback-T3** or **Release-Loopback-EC1** selections.

At the CIT, select **FAULT-Operate-Loopback-T3** (for DS3 circuits) or **FAULT-Operate-Loopback-EC1** (for EC1 circuits) for the first of the specified port(s).

4. Repeat Step 3 for any remaining port(s).
5. Verify the **ABN**, **NE ACTY**, **USE CIT**, and **PWR ON** LEDs are lighted at the user panel.

6. The port(s) is looped back toward the OC-48 line(s). If you wish to verify, select **Retrieve-Alarm** for an updated Active Alarms and Status Report. Section 11 contains an example of this report.
7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Replace Fan Filter (in E-Bay)

**Overview:** This procedure disconnects the DC power to the fan assembly before the air filter is replaced to prevent dust contamination to the FT-2000 network element. This procedure should be completed without any interruptions.



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

1. Locate the Power Distribution and Fuse Panel at the top of the bay.
2. Remove the front cover of the panel by using a 7mm or adjustable wrench to loosen (about three turns) the two hex-head screws (one located on each side of the cover). If the cover is secured with slotted pan-head screws, these must be removed completely by using a slotted screwdriver (straight shaft or offset).
3. Locate the two **FAN** power connectors/plugs, labeled **FAN, -48A** and **FAN, -48B** at the upper left of the connectorized panel.



**CAUTION:**

*Do not pull on the power cables to remove the plugs from the connectors.*

4. Remove the two FAN power plugs from the connectors (squeeze on the sides of the plug and pull it straight out from the connector).  
Response: The **POWER ON** LED at the fan assembly is off.
5. Obtain a replacement (disposable) air filter.



**NOTE:**

Filters may be ordered as **sub List 200** from either the FT-2000 J68974AK-1 or J68974EK-1 (circuit packs, miscellaneous hardware, and spare parts shipped separately from the equipment) drawing.

6. About 32 inches from the floor, locate two 1/4-turn fasteners on the PC tray screen. See Figure 1.
7. Use a slotted screwdriver (or a dime) to rotate the 1/4-turn fasteners counterclockwise 90 degrees.
8. Put a finger in each wire loop and pull to open the screen.

9. Remove the dirty air filter located right above the PC tray by sliding it toward you.
10. Install the replacement filter by sliding it into the bay within the filter guides, with the arrows on the filter pointing up.



**NOTE:**

Air flow in the bay is from bottom to top (up).

11. Dispose of the dirty air filter per the local operating instructions.
12. After the filter replacement has been completed, close the screen and rotate the 1/4-turn fasteners 90 degrees clockwise until each screw slot is horizontal.
13. Insert the two **FAN** power plugs into their respective connectors ("A" plug has red/black wires and "B" plug has slate with black strip/slate wires). The two **FAN** power plugs are keyed and when connected properly, the "A" plug has a red wire on the left and the "B" plug has a slate wire with a black strip on the left.

Response: The **POWER ON** LED at the fan assembly is lighted.

14. Reposition the front cover of the Power Distribution and Fuse Panel and secure it using the two screws.
15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

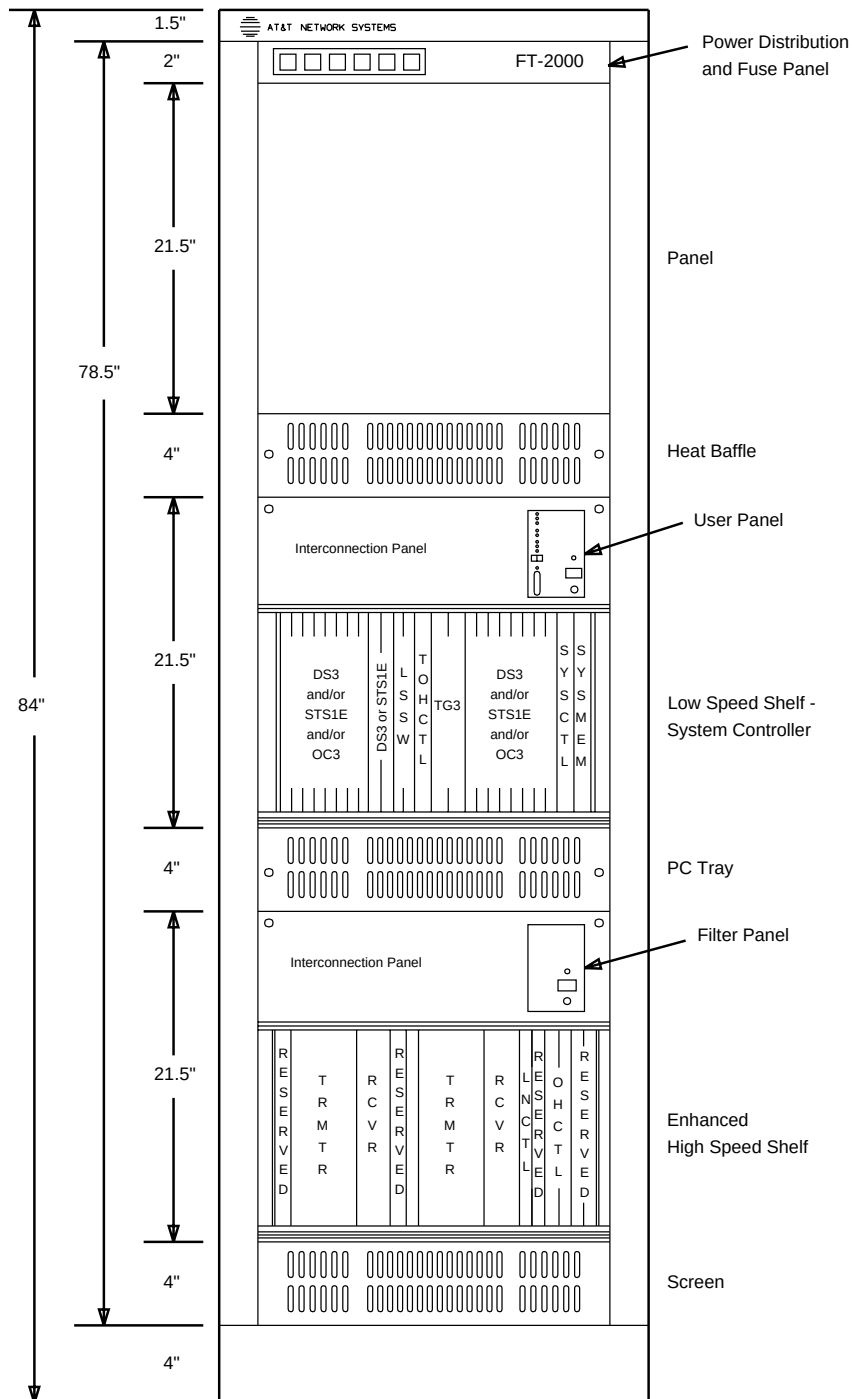


**Figure 1 – FT-2000 OC-48 Add/Drop-Rings Terminal for 2-Fiber Rings**



## Replace Circuit Pack with Different Circuit Pack Code

**Overview:** This procedure is used to replace an existing circuit pack with one of a different code type or version; for example, replacing circuit pack LAA17 with circuit pack LAA18, or 739B2 with 739B5.

 **WARNING:**  
*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*

 **CAUTION:**  
*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components.*

 **CAUTION:**  
*Do not remove the SYSCTL circuit pack unless instructed to by a procedure that sent you here. If the SYSCTL circuit pack is removed, provisioning data could be lost.*

1. Identify the appropriate shelf, using Table A.

**Table A - Shelf and Associated Circuit Packs**

| Circuit Pack                                                | Shelf                                                                                                    |
|-------------------------------------------------------------|----------------------------------------------------------------------------------------------------------|
| DS3<br>LSSW<br>OC3<br>IS3<br>OC12<br>STS1E<br>TG3<br>TOHCTL | Low-Speed Shelf - Complementary<br>or<br>Low-Speed Shelf - System Controller                             |
| LNCTL<br>OHCTL<br>RCVR<br>TRMTR<br>REGENR                   | High-Speed Shelf,<br>Enhanced High-Speed Shelf<br>or<br>Condensed High-Speed Shelf<br>Repeater Bay/Shelf |

2. Remove the appropriate shelf cover.

Reference: **DLP-511**

- 3.



**CAUTION:**

*Before replacing a circuit pack, the corresponding low-speed slot **MUST** be taken out of service to prevent a service outage.*

Is the current circuit pack, that is to be replaced, a DS3 or STS1E low-speed interface?

If **YES**, then continue with Step 4.

If **NO**, then continue with Step 10.



**NOTE:**

If the circuit pack being removed is an electrical DS-3 or STS-1E pack, then a low-speed switch will automatically occur when the circuit pack is removed from the shelf, and will retire when the Update command is executed.



**NOTE:**

To replace a circuit pack with a different code, you must verify that the software running in the bay is compatible with the new circuit pack codes. Refer to Table 7-1 in Section 7, "Circuit Pack descriptions."

## **DS3/STS1E**

4. At the CIT, select **FAULT-Retrieve-State:LS INTFC** command and obtain a State Report on the (slot) circuit pack being replaced.



**NOTE:**

For DS3 and STS1E circuit packs only: Verify that an automatic switch condition does not already exist or that a CIT provisioned protection switch has been made before removing the circuit pack from the shelf.

5. From the report, is the low-speed slot for the circuit pack that is to be removed in a standby state?  
If **YES**, then go to Step 7.  
If **NO**, then continue with Step 6.
6. At the CIT, select **FAULT-Switch-Low Speed:Slot** command for the circuit pack being removed and execute. Use the **FAULT-Retrieve-State:LS INTFC** command to verify, if desired.
7. Remove the existing DS3/STS1E circuit pack from the slot.
8. At the CIT, select **CONFIGURATION-Update** and execute. Go to Step 35.
9. Install the replacement low-speed circuit pack. As appropriate, connect any low-speed cables or optical fibers and provision the slot as required for the application.

### Low-Speed Optical

10. Is the current circuit pack, that is to be replaced, a low-speed optical interface?  
If **NO**, then go to Step 21.  
If **YES**, then continue with Step 11.
11. At the CIT, select **CONFIGURATION-Retrieve-OC3/12:All** command to determine the operating mode of the slot.
12. Is the slot operating in 1+1 mode?  
If **YES**, then go to Step 14.  
If **NO**, then continue with Step 13.
- 13.



**CAUTION:**

*The circuit pack is operating in 0x1 mode. In order to prevent a service outage, it will be necessary to remove service from the slot before replacing the circuit pack. If service is not moved before you replace the circuit pack, there will be a service outage.*

After removing service from the slot, continue with Step 16.

14. If an OC3 circuit pack is to be replaced with an IS3 circuit pack or an IS3 circuit pack is to be replaced with an OC3 circuit pack, go to Step 16.

If an OC3 or OC12 circuit pack is provisioned as 1+1 protected and is to be replaced by a DS3 or STS1E circuit pack, go to Step 15.

15. If the circuit pack to be replaced is a 1+1 protected OC3, use NTP-016 to change the OC-3/IS-3 protection mode from 1+1 to 0x1. After completing NTP-016, return to **DLP-527** Step 16.

If the circuit pack to be replaced is a 1+1 protected OC12, use NTP-017 to change the protection mode from 1+1 to 0x1. After completing NTP-017, return to **DLP-527** Step 16.

Reference: Circuit Order: NTP-016, "Change an OC-3/IS-3 Link From 1+1 to 0x1 Protection Mode"

Reference: Circuit Order NTP-017, "Change an OC-12 Link From 1+1 to 0x1 Protection Mode"

Reference: DLP-517, "Provision Low-Speed Slot and Port Parameter"

16. Disconnect the optical fibers from the existing optical circuit pack and subsequently remove the circuit pack from the slot.
17. Execute the **CONFIGURATION - Update** command.
18. Install the replacement low-speed circuit pack. Provision the slot as required for the application and then, as appropriate, connect any optical fibers.
19. Execute the **CONFIGURATION - Update** command.
20. Is there a need to replace another low-speed optical circuit pack?
  - If **YES**, then continue with Step 11.
  - If **NO**, then go to Step 35.

## **High-Speed Optical**

21. Before removing the optical fibers or removing the circuit packs, determine if you want the automatic protection switch to make the switch or if you want to make a manual switch.
  - If auto switch, go to Step 25.
  - If manual switch then continue with Step 22.



22. At the CIT, select **FAULT-Retrieve-State:TRMTR or RCVR** command and obtain a State Report on the (slot) circuit pack being replaced.
23. From the report, is the slot for the appropriate circuit pack that is to be removed in a standby state?
  - If **YES**, then go to Step 25.
  - If **NO**, then continue with Step 24.
24. At the CIT, select **FAULT-Switch-Line:TRMTR or RCVR** command for the circuit pack being removed and execute. Use the **FAULT-Retrieve-State:TRMTR or RCVR** command to verify, if desired.
25. Remove the OC48 optical fibers on the appropriate TRMTR and/or RCVR.

 **CAUTION:**  
*Only remove the fibers on the 1E side OR the 1W side at a time. Removing fibers on both sides will isolate the node and will result in lost service for all adds and drops at the local node.*
26. Install the new TRMTR and/or the RCVR. Clean and reconnect the OC48 optical fibers.
27. At the CIT, select **CONFIGURATION-Update** and execute.
28. Wait for the OC48 automatic protection switch to retire. At the CIT select, **FAULT-Retrieve-Alarms** command to verify that the automatic protection switch has cleared.
29. Is there a need to replace another high-speed optical circuit pack?
  - If **YES**, then continue with Step 21.
  - If **NO**, then go to Step 35.

### Repeater REGENR

30. Determine, from the work order, which REGENR is to be replaced first. Disconnect the OC48 optical fibers on the appropriate identified REGENR.

 **CAUTION:**  
*Only remove the fibers on one REGENR at a time.*

 **CAUTION:**  
*Install the new REGENR. Clean and reconnect the OC48 optical fibers.*

31. Is there a need to replace another REGENR circuit pack?

If **YES**, then continue with Step 30.

If **NO**, then go to Step 35.

### **All Other Types**

32. Identify the slot for the subject circuit pack. Remove the existing circuit pack from the slot.

33. Install the replacement circuit pack.

34. At the CIT, select **CONFIGURATION-Update** and execute.

35. Wait the minimum time, indicated in Table B, for the circuit pack to reset.

Table B - Mini Reset Waiting Times

| CP Inserted | Waiting Time |
|-------------|--------------|
| LNCTL *     | 10 minutes   |
| OHCTL       | 5 minutes    |
| TG3 †       | 5 minutes    |
| TOHCTL      | 5 minutes    |
| All Others  | 35 seconds   |

\* Inserting an LNCTL circuit pack starts a mini reset that can take up to 10 minutes to complete. During this time, LEDs on other circuit packs will be lighted.

† When a TG3 circuit pack is first inserted, it will be switched out of service until it warms up. This can take as long as 5 minutes. You can confirm that it is in the standby state by using the FAULT-Retrieve-State command for the TG slot.

36. Is the **NE ACTY** LED lighted at the user panel?

If **YES**, then go to **Trouble Clearing: TAP-103**.

If **NO**, then continue with Step 37.

37. Replace the shelf cover.

Reference: **DLP-511**

38. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Inspect (Repair) Optical and/or Electrical Cable(s)

**Overview:** You were sent here from a trouble clearing procedure. This procedure is used to correct an input or output cable problem such as a damaged or disconnected cable. This procedure uses cable in a general sense to refer to either the electrical coax cable or to the optical fibers, as appropriate.



**CAUTION:**

*A prolonged and significant disruption in the flow of ventilation air within the D-Bay can result in the buildup of excessive heat. Therefore, if it becomes necessary to remove one of the front covers or open one of the sliding doors, care should be exercised to limit this time and quickly close the opening.*



**CAUTION:**

1. Remove the appropriate cover as follows:
  - High speed shelf cover for OC-48 cables
  - Interconnection panel cover for low-speed (DS3 or EC1), sync reference cables, or OS cables.
  - Low speed shelf cover for OC-3 cables
  - Low speed shelf cover for IS-3 cables
  - Low speed shelf cover for OC-12 cables.
2. Verify that the cable(s) are properly connected.
3. Visually inspect the cable, starting at the connector on the local network element and going as far as practical (usually to an interconnection bay or to a cross-connect panel).
4. Correct any problems with the cable, following local procedures.
5. Replace the cover that was removed in Step 1.
6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Introduction to the PC

**Overview:** This DLP provides a brief introduction to the PC. It includes proper handling and storage of diskettes and some of the important procedures you should follow when working with your PC. This DLP is a brief review of PC concepts. Refer to your PC user's guide for a complete description.

### Parts of a PC

PCs vary in physical appearance and size. Most *IBM*<sup>\*</sup> compatible PCs consist of three basic parts — a main unit, a monitor, and a keyboard. Note that some PCs, particularly portables, integrate the keyboard, monitor, and main unit into a briefcase-size package. The FT-2000 generic program may be installed with either a desktop PC or a portable PC. The diskettes may be either 3-1/2 inch floppy disks or 5-1/4 inch floppy disks.

### Main Unit

The main unit of the PC contains the central processing unit (CPU), disk drive(s), and memory, among other things. All main units have some common features which include:

- **ON/OFF Switch** — This switch is used to switch the PC **ON** or **OFF**. Newer PCs have the switch located on the front. In older models, the switch is located on the back of the PC, near where the AC power cord plugs in to the PC.
- **Serial Port** — A port which the PC uses to communicate with the customer equipment. When using the PC to install a new generic program, either COM1 or COM2 port must be used. Some PCs have both COM ports. Refer to your PC documentation to determine which port is being used. There are two types of serial port connectors:
  - 25-pin RS-232 connector (male)
  - 9-pin connector (male).

If your PC has the 9-pin connector, you will need a serial port adapter which converts the 9-pin connector to a 25-pin connector. This is a cable which has a 9-pin connector on one end and a 25-pin male connector on the other end.

- **Parallel Port** — This port is used to connect to some printers.

---

\* Registered Trademark of International Business Machines Corporation

- Disk Drives — PCs come in several basic types, such as:
  - Single floppy disk, no hard disk — This type PC is the most basic configuration. The PC has a single floppy disk drive, with no hard (fixed) disk. The floppy drive is referred to as drive **A:**.
  - Dual floppy disk, no hard disk — This type PC is identified by the two floppy disk drives installed. One floppy drive is referred to as drive **A:**. The second is referred to as drive **B:**.
  - Single floppy disk with hard disk — This type PC has a single floppy disk drive and a hard disk, which can typically store 10 or more megabytes of data. The single floppy disk drive is referred to as drive **A:**, and the hard disk is usually referred to as drive **C:**.

Other combinations of floppy and hard drives are possible, but all that is required is a single floppy drive as a minimum. The procedures in this manual for installing a new generic program indicate using drive **A**.

## **Monitor**

The monitor, sometimes called a display screen, may be either monochrome or color. The higher the resolution, the better the monitor.

## **Keyboard**

The keyboard is used to enter commands. This section reviews some of the features of the keyboard and points out some of the special keys.

The keyboard is divided into three different sections:

- *Typewriter section* — This area has the alphanumeric keys that resemble the layout of a typewriter. In particular, you should locate the **ENTER** key (sometimes referred to as **RETURN**). This key is located on the right side of the typewriter section. It may have the word **ENTER** or **RETURN** printed on the key or a bent arrow. When you are asked to select **<ENTER>**, you should momentarily depress that key, or equivalent.
- *Function Keys* — The keyboard normally has 10 or 12 function keys. These keys can be set up (programmed) to perform different functions. Refer to your PC documentation to determine how to setup these keys.
- *Special Keys* — The keyboard has many special keys which are interpreted differently by various software packages. Locate the **CANCEL** or **DELETE** key.

## Rebooting the PC

Occasionally, you may have to reboot the PC. There are three ways to accomplish this:

- Power **ON/OFF** switch ###V-EM-V### If the PC is **OFF**, you should set the **ON/OFF** switch to the **ON** position. If the PC is already **ON**, it is preferable to reset it using one of the following two methods:
- **RESET** button ###V-EM-V### Not all PCs have a **RESET** button, but if yours does, it provides an easy way to reset the PC (also known as a hard reset). Alternatively, the following method may work.
- Hold down the **CTRL**, **ALT**, and **DELETE** keys all at the same time (also known as a soft reset).

If **RESET** or **CTRL**, **ALT**, and **DEL** keys are not effective, turn **OFF** the PC, wait a few seconds, and turn it back **ON**. Rebooting the PC clears all data you had in random access memory. If the time of day clock is not indicating the current time and date, set the time of day clock.

## Diskettes — Care and Handling

Diskettes are sometimes called floppy disks, or floppies for short. Diskettes are similar to cassette tapes—magnetically encoded information is stored on the diskette for later retrieval. By adhering to the following guidelines you will increase the useful life of your diskettes:

### DO:

- Keep the diskette in a protective envelope when not in use.
- Keep the diskette in a dry place.
- Keep the diskettes away from magnetic fields or objects (for example, magnetic screwdriver.)

### DO NOT:

- Touch any of the exposed surfaces of the diskette itself; handle it by the edge of the jacket. Very tiny scratches, fingerprints, or dust can ruin a diskette.
- Bend a diskette.
- Write on a diskette label with a ball point pen or pencil. Use a felt-tipped pen instead. Write the label before applying it to the diskette if possible.
- Erase the diskette label with a pencil eraser. The dust may destroy the diskette. Instead, place a new label over the old one.

## **Inserting Diskettes**

You should never remove or insert a diskette while the disk drive indicator light is on. (The computer turns the indicator light on to let you know that it is reading or writing from the disk.) Always wait for the indicator light to go off before opening the disk drive door. Failure to do this may result in damaging the diskette or loss of data on the diskette. Insert a diskette into a disk drive as follows:

1. Open the diskette drive door.
2. Gently insert the diskette with the label facing up and the notch to the left, if provided.



### **CAUTION:**

*Do not attempt to force the diskette into the drive.*

3. Slide the diskette in as far as it will go, but do not force it. You may hear a click when the diskette is all the way in.

If resistance is felt or it does not slide in easily, remove the diskette and insert it again.

4. Close (latch) the disk drive door.

When you are instructed to insert a diskette into a drive, it is implied that you should open the door, slide the diskette in all the way, and close (latch) the door.

## **Removing Diskettes**

Wait for the indicator light on the disk drive to go off. Open the disk drive door. The diskette may pop out for easy removal. (Opening up a disk drive door is similar to ejecting a cassette from a cassette recorder.) After the diskette has been removed, place it into its protective envelope or storage facility.



## Test Miscellaneous Discrete Telemetry

**Overview:** This procedure tests the user settable miscellaneous discrete environmental (input) points and control (output) points.

1. Contact the remote operations center and request their help to observe the environmental points and to activate the control points during this test.
2. At the remote operations center, disable, if desired, the alarm threshold level for the points being tested.
3. Operate the first external device connected to the FT-2000 network element in such a way as to cause a closed contact to appear at the network element environmental input.



**NOTE:**

When an external device connected to the FT-2000 network element causes a contact closure to appear as an environmental input to the network element, a corresponding environmental point is activated at the remote operations center.

4. Was the corresponding environmental point activated at the remote operations center?

If **YES**, then continue with Step 5.

If **NO**, then report the trouble to the installation personnel.

5. Operate the first external device connected to the FT-2000 network element in such a way as to cause an open contact to appear at the network element environmental input.

6. Was the corresponding environmental point deactivated at the remote operations center?

If **YES**, then continue with Step 7.

If **NO**, then report the trouble to the installation personnel.

7. Repeat Steps 3 through 6 for any remaining environmental points to be tested.

8. Ask the personnel at the remote operations center to activate the first control point being tested.



**NOTE:**

When a control point is activated at the remote operations center, it results in a contact closure at the corresponding control output of the FT-2000 network element which, in turn, can operate an external device connected to the network element.

9. Was the corresponding external device connected to the network element operated correctly? (For example, was it turned on?)

If **YES**, then continue with Step 10.

If **NO**, then report the trouble to the installation personnel.

10. Ask the personnel at the remote operations center to deactivate the first control point being tested.

11. Was the corresponding external device connected to the network element operated correctly? (For example, was it shut off?)

If **YES**, then continue with Step 12.

If **NO**, then report the trouble to the installation personnel.

12. Repeat Steps 8 through 11 for any remaining control points to be tested.
13. At the remote operations center, enable the alarm threshold level for the points being tested, if disabled in Step 2.
14. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Provision STS-1 Path Trace

**Overview:** This procedure is used to provision the transmit and receive path trace fields of one or more 1 way STS-1 signals. Path trace is only applicable to a DS3 circuit pack because it is an STS-1 terminating device, whereas an STS-1E circuit pack is not an STS-1 terminating device. This procedure can be performed from any network element in an FT-2000 network.

1. If required, condition the craft interface terminal (CIT) and connect it to the local FT-2000 network element.

Reference: **DLP-501**

2. From office records, determine which FT-2000 network elements are the source network element and the destination network element for the STS-1 path(s) to be provisioned.
3. From office records, determine the path trace character string for each STS-1 path to be provisioned (path is between the source and destination network elements).
4. Establish a login to the **source** network element for the first 1 way STS-1 path to be provisioned. (To establish a remote login, refer to **DLP-524**.)
5. At the CIT, select **CONFIGURATION-Set-Path-Trace** command to set the transmit path trace (Xmit Path Trace) for all STS-1 paths to be provisioned that originate at this network element. (For example, the Xmit Path Trace could be "transmitting from node 3 at Boston.")



**NOTE:**

The path trace string may be up to 64-characters long. The transmit and receive strings should be different. Additional information is in Section 8, "Administration and Provisioning" or by pressing ? (help).

6. Repeat Step 5 for any additional STS-1 paths to be provisioned.
7. Discontinue the login at the source network element. (To discontinue a remote login, refer to **DLP-524**.)
8. Establish a login to the **destination** network element for the first STS-1 path to be provisioned. (To establish a remote login, refer to **DLP-524**.)

9. At the CIT, select the **CONFIGURATION-Set-Path-Trace** command to set the expected Rcv Path Trace for all STS-1 paths to be provisioned that terminate at this network element. (To continue with the same example as in Step 5, the Rcv Path Trace must be "transmitting from node 3 at Boston.")



**NOTE:**

The path trace string may be up to 64-characters long. Additional information is in Section 8, "Administration and Provisioning" or by pressing ? (help).

10. Repeat Step 9 for any additional STS-1 paths to be provisioned.
11. Discontinue the login at the destination network element. (To discontinue a remote login, refer to **DLP-524**.)
12. Verify the provisioning just done by using the **CONFIGURATION-Retrieve-Path-Trace** command.

Reference: **DLP-532**

13. This step is optional. If you desire to clear the path trace fields (fields set to all nulls), enter spaces (nothing else) in place of the characters in the **PrvTx** and the **PrvRx** fields.
14. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Verify STS-1 Path Trace

**Overview:** This procedure is used to verify the provisioned values of the transmit and receive path trace fields for one or more 1 way STS-1 signals of a DS3 circuit pack. It is also used to verify that the actual receive path trace agrees with the provisioned receive path trace. This procedure can be performed from any network element in an FT-2000 network. For non-DS3 signals, a monitoring arrangement is described in **DLP-543**.

1. If required, condition the craft interface terminal (CIT) and connect it to the local FT-2000 network element.

Reference: **DLP-501**

2. Do you wish to verify the provisioned values of the transmit and receive path trace fields for one or more STS-1 signals?

If **YES**, then continue with Step 3.

If **NO**, then continue with Step 17.

### Verify Provisioned Values

3. From office records, determine which FT-2000 network elements are the source network element and the destination network element for the STS-1 path(s) to be verified.
4. From office records, determine the path trace character string to be observed for each 1 way STS-1 path to be verified. (Path is between the source and destination network elements.)
5. Establish a login to the **source network element** for the first STS-1 path to be verified. (To establish a remote login, refer to **DLP-524**.)
6. At the CIT, select **CONFIGURATION-Retrieve-Path-Trace** command and execute to obtain a Path Trace Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual. This report shows the STS-1 path(s) that originates at this network element.
7. Compare the provisioned transmit (PrvTx) character string(s) in the Path Trace Report with the values determined from office records (Step 4) and make a note of any differences. (To continue with the same example as in DLP-531, the PrvRx and the PrvTx strings should match with "transmitting from node 3 at Boston.")

8. If required to correct any discrepancies, use the **CONFIGURATION-Set-Path-Trace** command.  
Reference: **DLP-531**
9. Repeat Steps 6 through 8 for any additional STS-1 paths to be verified.
10. Discontinue the login at the source network element. (To discontinue a remote login, refer to **DLP-524**.)
11. Establish a login to the **destination network element** for the first STS-1 path to be verified. (To establish a remote login, refer to **DLP-524**.)
12. At the CIT, select **CONFIGURATION-Retrieve-Path-Trace** command and execute to obtain a Path Trace Report. Section 11 shows example of report. This report shows the STS-1 paths that originate or terminate at this network element.
13. Compare the provisioned receive (PrvRx) character string(s) in the Path Trace Report with the values determined from office records (Step 4) and make a note of any differences. (To continue with the same example as in DLP-531, the PrvRx and the PrvTx strings should match with "transmitting from node 3 at Boston.")



**NOTE:**

A path trace mismatch will not cause an alarm or status condition. A mismatch could be caused by missing cross-connections.

14. If required to correct any discrepancies, use the **CONFIGURATION-Set-Path-Trace** command.  
Reference: **DLP-531**
15. Repeat Steps 12 through 14 for any additional STS-1 paths to be verified.
16. Discontinue the login at the destination network element. (To discontinue a remote login, refer to **DLP-524**.)

**Verify Actual Receive Path Trace**

17. Do you wish to verify that the actual receive path trace agrees with the provisioned receive path trace for one or more STS-1 signals?  
If **YES**, then continue with Step 18.  
If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
18. From office records, determine which FT-2000 network element is the destination network element for the STS-1 path(s) to be verified.
19. Establish a login to the destination network element for the first STS-1 path to be verified. (To establish a remote login, refer to **DLP-524**.)

20. At the CIT, select **CONFIGURATION-Retrieve-Path-Trace** command and execute to obtain a Path Trace Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual. This report shows the STS-1 path(s) that originates or terminates at this network element.
21. Does the Status field for each STS-1 path to be verified show the value "good"?



**NOTE:**

The Status field in the report for each STS-1 path to be verified will show one of the following values:

**good** means the actual receive path trace is the same as the provisioned Rcv Path Trace.

**MISMATCH** means that the actual receive path trace is different from the provisioned Rcv Path Trace and is only detectable by using the **CONFIGURATION-Retrieve-Path Trace** command.

**NOT APPLICABLE** means that the addressed slot is not provisioned for DS3 operation.

**UNAVAILABLE** means that an incoming signal failure, a hardware failure, or a missing cross-connection is preventing the observation of the actual receive path trace.

If **YES**, then continue with Step 25.

If **NO**, then continue with Step 22.

22. Make a note of the value in the Status field for each STS-1 path with a Status other than "good". Also, record the actual receive path trace (ActRx) and the provisioned receive path trace (PrvRx) for each STS-1 path showing a Status of "MISMATCH."



**NOTE:**

At this time, you must determine from office records whether the mismatch is due to an incorrect actual receive path trace or to an incorrect provisioned receive path trace.

23. Is the incorrect path trace the actual receive path trace or the provisioned receive path trace?

If **provisioned receive path trace**, then continue with Step 24.

If **actual receive path trace**, then continue with Step 26.

24. Provision the correct provisioned receive path trace using the **CONFIGURATION-Set-Path-Trace** command or be sure cross-connections are correct in the network.

Reference: **DLP-531**

25. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

26. Determine via a remote login, or by contacting the personnel at the source of the DS3 path, if the actual receive path trace you are seeing is the **same** as the provisioned transmit path trace at the source of the DS3 path?

 **NOTE:**

There is a remote possibility that the actual receive path trace is NOT the same as the provisioned transmit path trace at the source of the DS3 path.

If **the same**, then continue with Step 27.

If **different**, then continue with Step 29.

27. At the source of the DS3 path, provision the correct provisioned transmit path trace using the **CONFIGURATION-Set-Path-Trace** command or be sure cross-connections are correct in the network.

Reference: **DLP-531**

28. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

29. What software release is running at the source of the DS3 path?

If **Release 4.0 or earlier**, then continue with Step 30.

If **Release 4.1 or later**, then continue with Step 34.

 **CAUTION:**

*Service interruption may occur if the protections DS3 circuit pack is not ready.*

30. At the source of the DS3 path, unseat, and then reseat, the DS3 circuit pack that terminates the path.
31. Is the path trace mismatch still observed at the original network element?
- If **YES**, then continue with Step 32.
- If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
32. Consult the appropriate maintenance support organization for further technical assistance.
33. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



34. At the source of the DS3 path, use the CIT's **CONFIGURATION-Switch-Low Speed** command to make and then reset a low-speed protection switch of the DS3 circuit pack that terminates the DS3 path.
35. Is the path trace mismatch still observed at the original network element?
  - If **YES**, then continue with Step 36.
  - If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
36. Consult the appropriate maintenance support organization for further technical assistance.
37. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Install/Remove Apparatus (Circuit Pack) Blank



**CAUTION:**

*A prolonged and significant disruption in the flow of ventilation air within the D-Bay can result in the buildup of excessive heat. Therefore, if it becomes necessary to remove one of the front covers or open one of the sliding doors, care should be exercised to limit this time and quickly close the opening.*

1. Is an apparatus blank to be installed or removed?  
To be **installed**, then continue with Step 2.  
To be **removed**, then continue with Step 7.
2. Position the apparatus blank vertically with its white painted surface towards you and its two extended protrusions away from you and on the right side.
3. Insert the apparatus blank bottom extended protrusion into the bottom of the circuit pack slot and snap its spring-clip notch over the bottom shelf cross-support channel.
4. Carefully bend the apparatus blank so that it is slightly bowed towards you in the middle, insert its top protrusion into the top of the circuit pack slot, and snap its spring-clip notch over the top shelf cross-support channel.
5. Push the center of the apparatus blank inwards to fully seat it and to eliminate any residual bowing.
6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
7. Is there a vacant slot adjacent to the apparatus blank to be removed?  
If **YES**, then continue with Step 8.  
If **NO**, then continue with Step 12.
8. Carefully bend the apparatus blank so that it is slightly bowed towards you in the middle, unsnap its spring-clip notch from the top shelf cross-support channel, and remove its top protrusion from the top of the circuit pack slot.
9. Unsnap the apparatus blank spring-clip notch from the bottom shelf cross-support channel and remove its bottom extended protrusion from the bottom of the circuit pack slot.
10. Lift the apparatus blank from the slot.
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

12. Use a flat-bladed screwdriver against the bottom shelf cross-support channel to pry the apparatus blank bottom edge upward and forward.
13. Lift the apparatus blank from the slot.
14. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Verify That Low-Speed Ports Are Not NMON

**Overview:** This procedure is used to verify that one or more low speed ports are not in the NMON state and to set them to the AUTO state, if necessary.

1. At the CIT, select **FAULT-Retrieve-State** to obtain a State Report for the **LS INTFC** slot(s) associated with the electrical low-speed ports to be verified.
2. Referring to the Port State column for the **LS INTFC** slot(s) associated with the electrical and optical low-speed ports to be verified, are any of the ports in the NMON state as indicated by the letter "n"?

If **YES**, then continue with Step 3.

If **NO**, then continue with Step 5.

3. At the CIT, select **CONFIGURATION-Set-State-T3** (for DS3 ports) or **CONFIGURATION-Set-State-EC1** (for EC1 ports) or **CONFIGURATION-Set-State-OC3** or **CONFIGURATION-Set-State-OC12** with a Primary State of AUTO for the ports identified in Step 2.
4. You can use the **FAULT-Retrieve-State** command again (for the State Report) to verify that none of the affected low-speed ports are in the NMON state. All of the affected ports should be shown with an "a" (indicating the AUTO state) or an "i" (indicating the IS state).  
 **NOTE:**  
Some of the ports just CIT provisioned to the AUTO state may have been immediately auto-provisioned to the IS state.
5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Identify Source of Incoming Signal

**Overview:** You were sent to this procedure to identify the signal source associated with an incoming signal condition so that you can issue a Trouble Report to the personnel at that location.

1. Find the condition to be reported in the table below and continue with the indicated step.

| For This Condition              | Go To   |
|---------------------------------|---------|
| incoming DCC failure            | Step 2  |
| inc. (from DS1) sync ref fail   | Step 4  |
| inc (from OC48) path RFI/yellow | Step 12 |
| inc (from OC-48) sync ref fail  | Step 2  |
| inc (from STSX) line RFI/yellow | Step 12 |
| incoming (from DSX) DS3 LOS     | Step 12 |
| incoming (from DSX) DS3 SD      | Step 12 |
| incoming (from OC-3) STS LOP    | Step 16 |
| incoming (from OC-12) STS LOP   | Step 16 |
| incoming (from OC-48) DS3 AIS   | Step 12 |
| incoming (from OC-48) DS3 LOF   | Step 10 |
| incoming (from OC-48) path AIS  | Step 14 |
| incoming (from OC-48) STS LOP   | Step 10 |
| incoming (from STSX) EC1 LOF    | Step 12 |
| incoming (from STSX) EC1 LOP    | Step 12 |
| incoming (from STSX) EC1 LOS    | Step 12 |
| incoming (from STSX) EC1 SD     | Step 12 |
| incoming (from STSX) line AIS   | Step 12 |
| incoming OC-3 AIS               | Step 18 |
| incoming OC-12 AIS              | Step 18 |
| incoming OC-48 AIS              | Step 8  |
| incoming OC-3 APS byte fail     | Step 18 |
| incoming OC-12 APS byte fail    | Step 18 |
| incoming OC-48 APS byte fail    | Step 2  |

| <b>For This Condition</b>      | <b>Go To</b> |
|--------------------------------|--------------|
| incoming OC48 B1 parity error  | Step 6       |
| incoming OC-48 invalid F byte  | Step 8       |
| incoming OC-3 line RFI/yellow  | Step 6       |
| incoming OC-12 line RFI/yellow | Step 6       |
| incoming OC-48 line RFI/yellow | Step 6       |
| incoming OC-3 LOF              | Step 6       |
| incoming OC-12 LOF             | Step 6       |
| incoming OC-48 LOF             | Step 6       |
| incoming OC-3 LOS              | Step 6       |
| incoming OC-12 LOS             | Step 6       |
| incoming OC-48 LOS             | Step 6       |
| incoming OC-48 LOS/LOF         | Step 6       |
| incoming OC-3 SD               | Step 6       |
| incoming OC-12 SD              | Step 6       |
| incoming OC-48 SD              | Step 6       |
| incoming OC-3 SF               | Step 6       |
| incoming OC-12 SF              | Step 6       |
| incoming OC-48 SF              | Step 6       |
| incoming sync ref failure      | Step 4       |
| incoming OC-48 LOS             | Step 6       |
| incoming OC-48 LOS/LOF         | Step 6       |
| incoming OC-3 SD               | Step 6       |
| incoming OC-12 SD              | Step 6       |
| incoming OC-48 SD              | Step 6       |
| incoming OC-3 SF               | Step 6       |
| incoming OC-12 SF              | Step 6       |
| incoming OC-48 SF              | Step 6       |
| incoming sync ref failure      | Step 4       |

2. From office records, determine the location of the next ring node and send a Trouble Report to the personnel responsible for maintaining that site.



**NOTE:**

The source of the incoming signal condition is the next ring node in the direction of the incoming signal (or the other end terminal if this is a point-to-point network).



3. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
4. From office records, determine the location of the synchronization reference source and send a Trouble Report to the personnel responsible for maintaining that site.  
  
 **NOTE:**  
The source of the incoming signal condition is the external synchronization reference source.
5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

6. From office records, determine the location of the next network element and send a Trouble Report to the personnel responsible for maintaining that site.



**NOTE:**

The source of the incoming signal condition is the next network element in the direction of the incoming signal.

7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

8. From office records, determine the location of the next Regenerator Bay/Shelf and send a Trouble Report to the personnel responsible for maintaining that site.



**NOTE:**

The source of the incoming signal condition is the next Regenerator Bay/Shelf in the direction of the incoming signal.

9. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

10. From office records, determine the location where the DS3 signal enters (or is supposed to enter) the SDH network and send a Trouble Report to the personnel responsible for maintaining that site.



**NOTE:**

The source of the incoming signal condition is the location where the DS3 signal enters the Synchronous Digital Hierarchical (SDH) network. This is either a DS3 circuit pack (CP) in an FT-2000 network element (NE) or a non-FT-2000 NE external to the FT-2000 network.



**NOTE:**

Another possible cause of the incoming signal condition is that the signal originates at a non-DS3 CP in an FT-2000 network element at the source location.

11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

12. From office records, determine the location where the DS3 or EC1 signal enters the SDH network and send a Trouble Report to the personnel responsible for maintaining that site.



**NOTE:**

The source of the incoming signal condition is the location where the DS3 or EC1 signal enters the SDH network. For a DS3 signal, this is either a DS3 CP in an FT-2000 NE or a non-FT-2000 NE external to the FT-2000 network.

13. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

14. If there are no other active conditions at any other network elements in the SDH network, then refer the trouble to the next level of technical support.



**NOTE:**

Path AIS can be inserted into the OC-48 signal at any of a number of locations. The best approach is to clear all other reported conditions at all other FT-2000 network elements in the network. In all likelihood that will also clear the incoming (from OC-48) path AIS condition.

15. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

16. From office records, determine the location where the OC-3 signal enters the SDH network and send a Trouble Report to the personnel responsible for maintaining that site.



**NOTE:**

The source of the incoming signal condition is the location where the OC-3 signal enters the SDH network. For an OC-3 signal, this is either an OC-3 CP in an FT-2000 NE or non-FT-2000 external equipment to the FT-2000 network.

17. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

18. From office records, determine the location of the next network element and send a Trouble Report to the personnel responsible for maintaining that site.



**NOTE:**

The source of the incoming signal condition is the next network element in the direction of the incoming signal.

19. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Enter/Delete/Edit STS-3 (or STS-1) Cross-Connections at Network Elements for 2-Fiber Ring Network

**Overview:** This procedure for the FT-2000 2-fiber ring network can be performed on an **in-service** or **out-of-service** basis. Cross-connections are made at one network element or node in the ring. Then, a remote login may be established to the other nodes in the ring to make cross connections for a complete circuit.



**CAUTION:**

*Cross-connections made to the wrong points can cause service interruptions. Care must be exercised to closely follow the "Cross-Connect Rules" — which are specified in Volume 1, Section 8.*



**CAUTION:**

*STS-1 traffic may be interrupted if making STS-3 cross-connections for the same slot.*



**NOTE:**

A DS3 (LAA2) or STS1E (LAA4) circuit pack has three ports. For STS-3 cross-connections, all three ports associated with the slot will be affected by a slot-to-tributary or a tributary-to-slot cross-connection. This is also true for an OC3 (LAA10B) circuit pack which has three STS1s. An OC12 (T939A) circuit pack has 12 ports which are arranged as four groups of three ports each. For STS-1 cross-connections, each of the 12 ports can be cross-connected to any tributary. For STS-12c cross-connections, all 12 ports must be cross-connected to four consecutive slots.



**NOTE:**

A 1-way cross-connection means that the connection is made in **either** the transmit or the receive direction. A 2-way cross-connection means that the connection is made in **both** the transmit and receive directions.



**NOTE:**

This procedure requires that an LNCTL circuit pack be installed and operating (not removed or failed).

1. Obtain the instructions (work order) that contain the cross-connect information. Form 1 (at the end of this procedure) may be used to track the cross-connections.
2. If required, connect the craft interface terminal (CIT) to the local FT-2000 network element and condition it.

Reference: **DLP-501**

3. At the CIT (local network element), select **CONFIGURATION-Retrieve-Crossconnection-All** and execute to obtain a Cross-Connection Map Report. Examples of a Cross-Connection Map Report with a description of the output parameters are provided in Volume 1, Section 11 of this manual.



**CAUTION:**

*Incorrectly provisioned source node TID and/or destination node TID can result in a service outage under certain multiple failure conditions.*



**NOTE:**

To edit a cross-connection simply means to change the Source Node TID and/or Destination Node TID associated with an existing cross-connection. This is done by re-entering the same cross-connection right on top of the existing one but with the new TID(s). The source node TID is the TID of the node where the signal is added, whereas, the destination node TID is the TID of the node where the signal is dropped. See Examples 1 and 2.

4. Compare the report with the instructions (work order). Are any cross-connections to be added, edited, and/or deleted at the local network element?

If **YES**, then continue with Step 5.

If **NO**, then continue with Step 14.



**NOTE:**

Assistance in selecting the appropriate parameters for the **CONFIGURATION-Delete-Crossconnection-STS3 (or STS1)** command is provided by the **F1** help key function.



**NOTE:**

Descriptive information and examples on cross-connections are given in Volume 1, Section 8, "Administration and Provisioning."

5. Determine from the instructions (work order) if any existing cross-connections are to be deleted and delete only those cross-connections. If you have any doubt about the instruction, please verify with your supervisor. Use the **CONFIGURATION-Delete-Crossconnection-STS3 (or STS1)** command to delete a cross-connection.



**NOTE:**

If the cross-connection just deleted were to all ports of an OC-3/OC-12 circuit pack, they may have had DCC assignments that should also be deleted now.

6. You may delete a DCC assignment on an OC-3/OC-12 line.

Reference: **DLP-542**

7. At the CIT (local network element), select **CONFIGURATION-Retrieve-Crossconnection-All** and execute to obtain a Cross-Connection Map Report (examples are shown in Volume 1, Section 11).



**CAUTION:**

*Incorrectly provisioned source node TID and/or destination node TID can result in a service outage under certain multiple failure conditions.*



**NOTE:**

Assistance in selecting the appropriate parameters for the **CONFIGURATION-Enter-Crossconnection-STS3 (or STS1)** command is provided by the **F1** help key function.



**NOTE:**

If a tributary-to-slot (drop) cross-connection is made for a DS3 circuit pack, a **DS3 path unequipped** condition may exist until all cross-connections for the entire circuit have been made and if one of the low-speed ports is in the IS state.



**NOTE:**

A drop cross-connection is always looking for its source. If it does not find a source, a **path integrity failure** condition is generated at the node that has the drop. This condition will remain until the entire path in the ring is cross-connected.

8. Compare the report with the instructions. Add and/or edit the cross-connections per the instructions. This can be a slot-to-tributary, tributary-to-slot, or a tributary-to-tributary cross-connection. Use the **CONFIGURATION-Enter-Crossconnection-STS3 (or STS1)** command to add a

cross-connection. The source node TID is the TID of the node where the signal is added, whereas, the destination node TID is the TID of the node where the signal is dropped. See Examples 1 and 2.



**NOTE:**

If the cross-connection just made was to a port(s) of an OC-3/OC-12 circuit pack, you may want to provision a DCC assignment to establish DCC connectivity with the network element at the other end of the OC-3/OC-12 line.

9. You may enter a DCC assignment on an OC-3/OC-12 line.

Reference: **DLP-542**

10. At the CIT (local network element), select **CONFIGURATION-Retrieve-Crossconnection-All** and execute to obtain a Cross-Connection Map Report (examples are shown in Volume 1, Section 11).
11. Verify that the cross-connections in the report match the instructions.
12. Steps 12 through 16 are used to verify that the source and destination node TID provisioning has been done correctly.

Wait 2 minutes and verify that a **site provisioning conflict** IS NOT reported at either of the two end NEs or any of the intermediate NE(s). It will be necessary to individually check each NE, that is involved in the crossconnection. At the CIT, use the **FAULT-Retrieve-Alarms** command.



**NOTE:**

Every possible combination of DRI manual switches in the path being established should be checked. See Example 2 where a DRI manual switch at node B would result in a new path coming from node C.

13. Make the first possible DRI manual switch in the path by using the **FAULT-Switch-Path** command.
14. Wait 2 minutes and verify that a **site provisioning conflict** IS NOT reported at either of the two end NEs or any of the intermediate NE(s). It will be necessary to individually check each NE, that is involved in the crossconnection. At the CIT, use the **FAULT-Retrieve-Alarms** command.
15. Repeat Steps 12 through 14 for each different remaining path.
16. Reset all DRI manual switches using the **FAULT-Switch-Path:Reset** command.



17. Do you have instructions pertaining to cross-connections at other network elements?

If **YES**, then continue with Step 18.

If **NO**, then continue with Step 22.

18. Establish a remote login to one of the other network elements in the FT-2000 ring network.

Reference: **DLP-524**

19. Repeat Steps 3 through 22 for the appropriate cross-connections. Do not forget that the remote node has now become the local node.

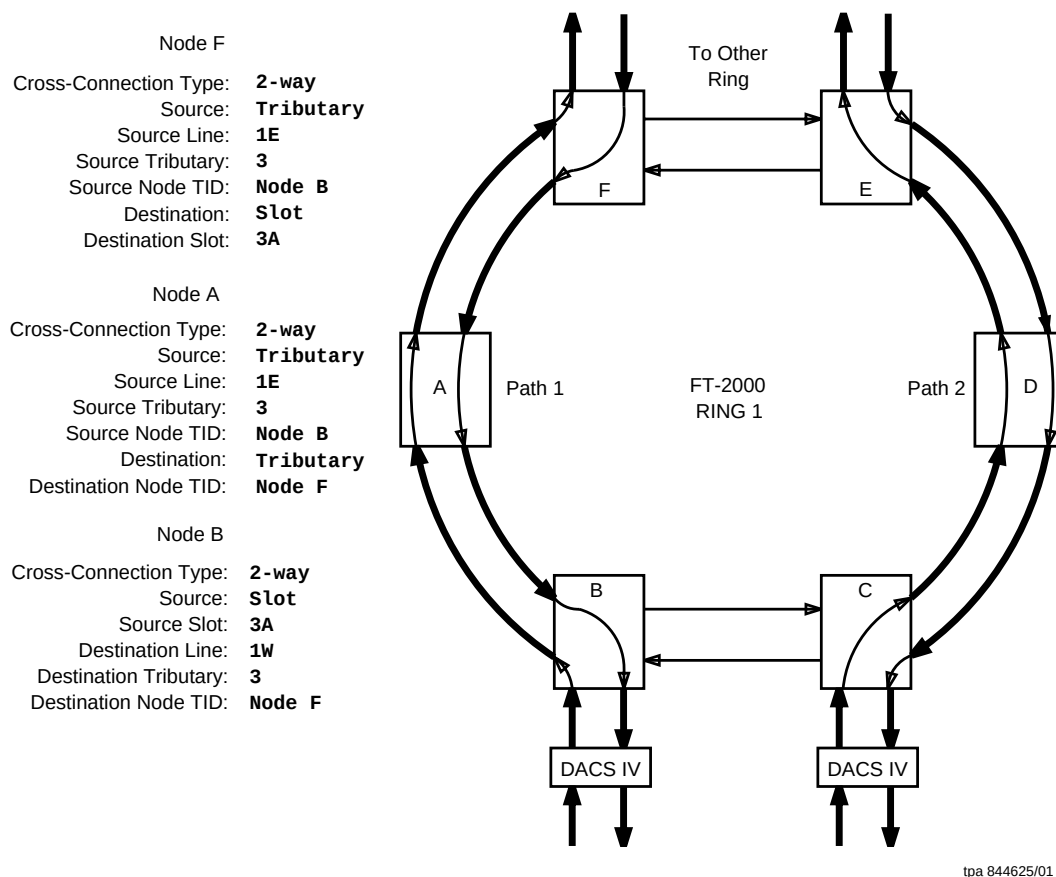
20. Discontinue the remote login.

Reference: **DLP-524**

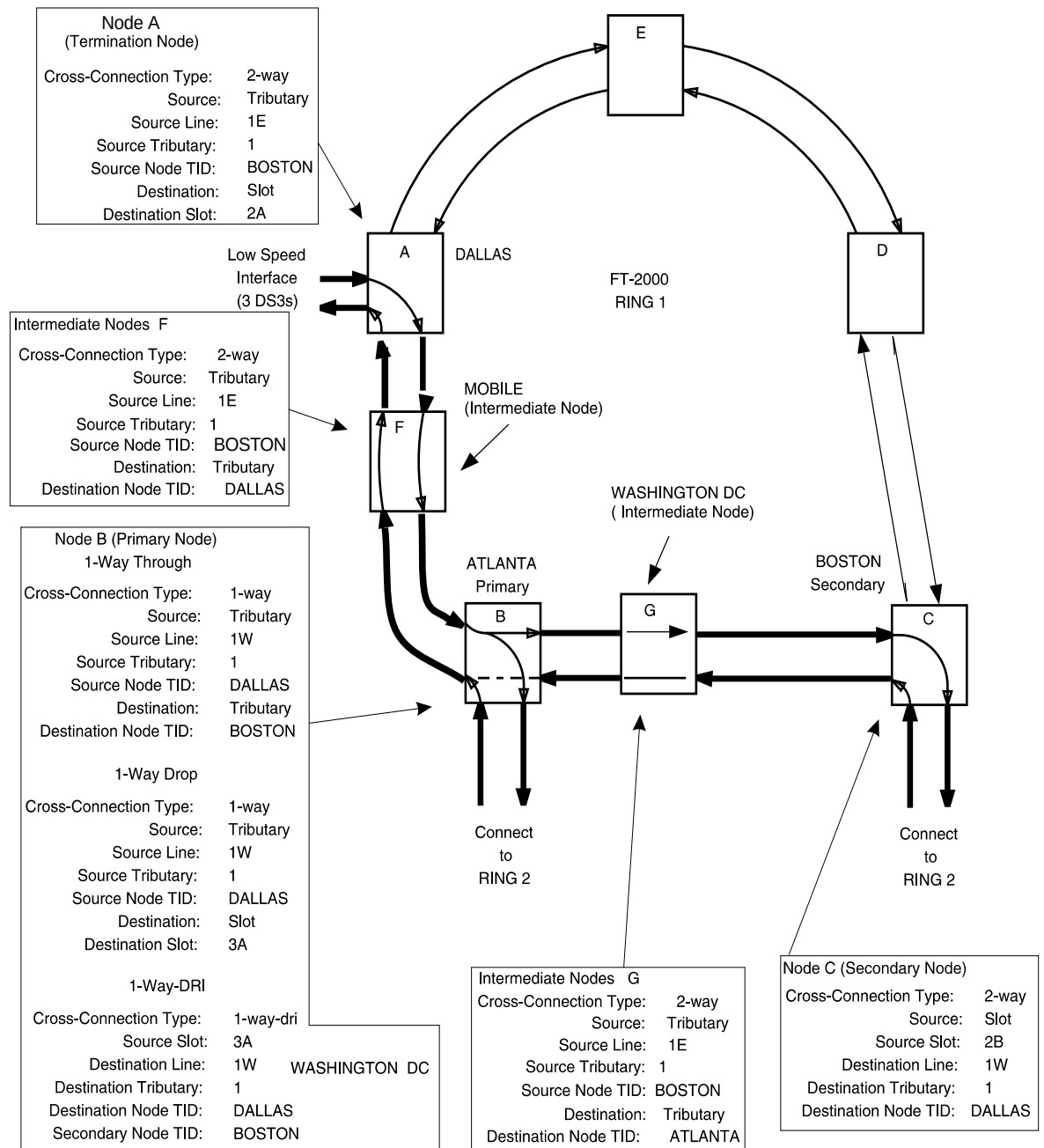
21. Repeat Steps 18 through 20 for each of the remaining network elements in the ring network that requires cross-connections per the instructions.

22. All cross-connections have been made and the FT-2000 ring network is now ready for traffic and/or testing.

23. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



**Example 1 - Cross-Connections (Dual Wire Center)**



Dri-cross-conn

Example 2 - Cross-Connections (DRI Ring)

|    | W           | E           | W            | E           | W            | E           | W            | E           |
|----|-------------|-------------|--------------|-------------|--------------|-------------|--------------|-------------|
| 1  | 1<br>2<br>3 | 1<br>2<br>3 | 1<br>2<br>3  | 1<br>2<br>3 | 1<br>2<br>3  | 1<br>2<br>3 | 1<br>2<br>3  | 1<br>2<br>3 |
| 2  | 1<br>2<br>3 | 1<br>2<br>3 | 2<br>2<br>3  | 1<br>2<br>3 | 2<br>2<br>3  | 1<br>2<br>3 | 2<br>2<br>3  | 1<br>2<br>3 |
| 3  | 1<br>2<br>3 | 1<br>2<br>3 | 3<br>2<br>3  | 1<br>2<br>3 | 3<br>2<br>3  | 1<br>2<br>3 | 3<br>2<br>3  | 1<br>2<br>3 |
| 4  | 1<br>2<br>3 | 1<br>2<br>3 | 4<br>2<br>3  | 1<br>2<br>3 | 4<br>2<br>3  | 1<br>2<br>3 | 4<br>2<br>3  | 1<br>2<br>3 |
| 5  | 1<br>2<br>3 | 1<br>2<br>3 | 5<br>2<br>3  | 1<br>2<br>3 | 5<br>2<br>3  | 1<br>2<br>3 | 5<br>2<br>3  | 1<br>2<br>3 |
| 6  | 1<br>2<br>3 | 1<br>2<br>3 | 6<br>2<br>3  | 1<br>2<br>3 | 6<br>2<br>3  | 1<br>2<br>3 | 6<br>2<br>3  | 1<br>2<br>3 |
| 7  | 1<br>2<br>3 | 1<br>2<br>3 | 7<br>2<br>3  | 1<br>2<br>3 | 7<br>2<br>3  | 1<br>2<br>3 | 7<br>2<br>3  | 1<br>2<br>3 |
| 8  | 1<br>2<br>3 | 1<br>2<br>3 | 8<br>2<br>3  | 1<br>2<br>3 | 8<br>2<br>3  | 1<br>2<br>3 | 8<br>2<br>3  | 1<br>2<br>3 |
| 9  | 1<br>2<br>3 | 1<br>2<br>3 | 9<br>2<br>3  | 1<br>2<br>3 | 9<br>2<br>3  | 1<br>2<br>3 | 9<br>2<br>3  | 1<br>2<br>3 |
| 10 | 1<br>2<br>3 | 1<br>2<br>3 | 10<br>2<br>3 | 1<br>2<br>3 | 10<br>2<br>3 | 1<br>2<br>3 | 10<br>2<br>3 | 1<br>2<br>3 |
| 11 | 1<br>2<br>3 | 1<br>2<br>3 | 11<br>2<br>3 | 1<br>2<br>3 | 11<br>2<br>3 | 1<br>2<br>3 | 11<br>2<br>3 | 1<br>2<br>3 |
| 12 | 1<br>2<br>3 | 1<br>2<br>3 | 12<br>2<br>3 | 1<br>2<br>3 | 12<br>2<br>3 | 1<br>2<br>3 | 12<br>2<br>3 | 1<br>2<br>3 |
| 13 | 1<br>2<br>3 | 1<br>2<br>3 | 13<br>2<br>3 | 1<br>2<br>3 | 13<br>2<br>3 | 1<br>2<br>3 | 13<br>2<br>3 | 1<br>2<br>3 |
| 14 | 1<br>2<br>3 | 1<br>2<br>3 | 14<br>2<br>3 | 1<br>2<br>3 | 14<br>2<br>3 | 1<br>2<br>3 | 14<br>2<br>3 | 1<br>2<br>3 |
| 15 | 1<br>2<br>3 | 1<br>2<br>3 | 15<br>2<br>3 | 1<br>2<br>3 | 15<br>2<br>3 | 1<br>2<br>3 | 15<br>2<br>3 | 1<br>2<br>3 |
| 16 | 1<br>2<br>3 | 1<br>2<br>3 | 16<br>2<br>3 | 1<br>2<br>3 | 16<br>2<br>3 | 1<br>2<br>3 | 16<br>2<br>3 | 1<br>2<br>3 |

TID \_\_\_\_\_ TID \_\_\_\_\_ TID \_\_\_\_\_ TID \_\_\_\_\_

Form 1 - Worksheet for Cross-Connections

## Enter/Delete/Edit Limited Granularity STS-1 Cross-Connections in Network Elements for 2-Fiber Ring Network (for Rel. 6 and Earlier)

**Overview:** This procedure for the FT-2000 2-fiber ring network can be performed on an **in-service** or **out-of-service** basis. For Release 6 and earlier, STS-1 cross-connections are made with external loopback cables at one network element or node on the ring network; then a remote login is established to other network elements on the network where the reciprocal STS-1 cross-connections exist. For Release 7 and higher, refer to the associated STS-3 (or STS-1) cross-connections covered in **DLP-536**. Section 8 has additional information on STS-1 granularity.



**CAUTION:**

*Cross-connections made to the wrong points can cause service interruptions.*



**CAUTION:**

*STS-3 (or STS-1 for Release 7 and higher) cross-connections are covered in DLP-536. Refer to DLP-536 to add/drop/edit STS-3 (or STS-1) cross-connections.*

1. Obtain the instructions (work order) that contain the STS-1 cross-connect information.



**NOTE:**

This procedure requires that a LNCTL circuit pack be installed and operating (not removed or failed).



**NOTE:**

A DS3 (LAA2) or STS1E (LAA4) circuit pack has three ports. STS-1 granularity allows the individual STS-1 signals (DS3 or EC1 ports) associated with a slot to be added, dropped, or cabled through.



**NOTE:**

A 1-way cross-connection (Release 4.1 and later) means that the connection is made in **either** the transmit or the receive direction. A 2-way cross-connection (Release 4.0 and earlier) means that the connection is made in **both** the transmit and receive directions.

2. If required, connect the craft interface terminal (CIT) to the local FT-2000 network element and condition it.

Reference: **DLP-501**

3. At the CIT (local network element), select **CONFIGURATION-Retrieve-Squelch Map-STS1** and enter to obtain an STS-1 Squelch Map Report. An example of an STS-1 Squelch Map Report with a description of the output parameters is provided in Section 11 of this manual.
4. At the CIT (local network element), select **CONFIGURATION-Enter-Squelch Map-STS1** and enter to provision the source node TID and the destination node TID of the STS-1 squelch map.



**NOTE:**

Three STS-1 signals are included in an STS-3 signal. Therefore, both cross-connections (STS1 and STS3) are necessary.

5. For DS3 port only. At the CIT (local network element), select **CONFIGURATION-Set-T3** and enter to provision the VMR mode parameter for DS3s being added, dropped, and through cabled.

|                                  |          |          |          |            |
|----------------------------------|----------|----------|----------|------------|
| VMR Mode at terminating nodes    | VMR      | VM       | CC w/AIS | CC w/o AIS |
| VMR Mode at through cabled nodes | CC w/AIS | CC w/AIS | CC w/AIS | CC w/o AIS |

6. At the low-speed electrical cross-connect panel (DSX-3 and/or STSX-1) and using the appropriate cables, plug in a cable for each STS-1 port (DS3 or EC1).



**NOTE:**

Use Figure 1 as a guide to understand the STS-1 cross-connections using external loopback cables. Port 1 must correspond to port 1, port 2 to port 2, and port 3 to port 3.



**NOTE:**

The STS1 cross-connections cannot be rearranged in a through STS3 cross-connection. The same time slot assignment must be maintained on both sides of the FT-2000 add/drop-rings terminal.

7. At the CIT (local network element), select **CONFIGURATION-Retrieve-Squelch Map-STS1** and enter to obtain an STS-1 Squelch Map Report.

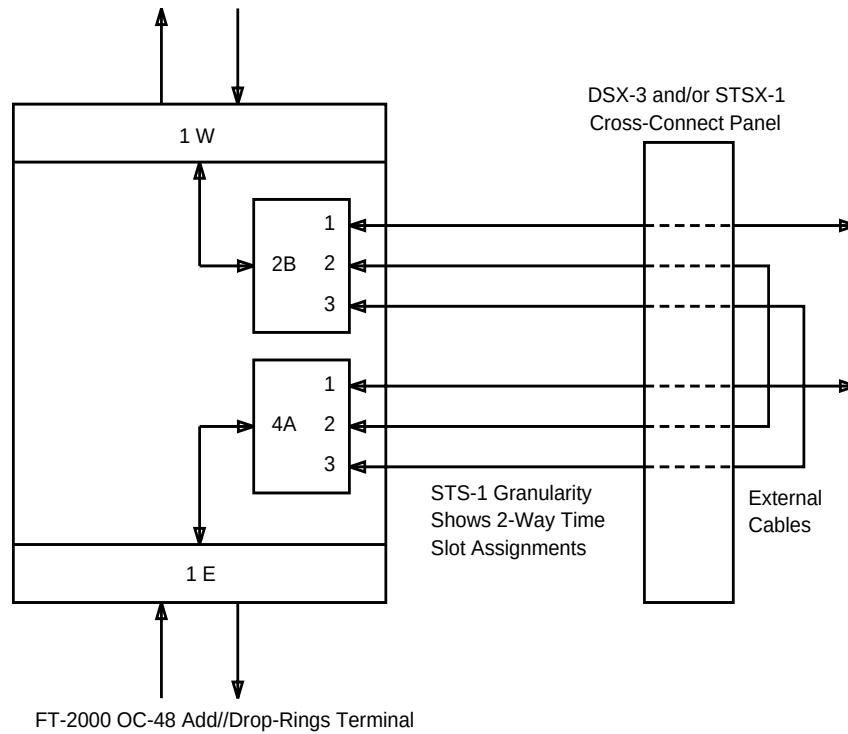
8. Verify that the information in the report matches the instructions.



**CAUTION:**

*Service or traffic interruption can occur by deleting an existing cross-connection by mistake.*

9. Are any existing STS-1 cross-connections to be deleted?  
If **YES**, then continue with Step 10.  
If **NO**, then continue with Step 12.
10. At the CIT, select **CONFIGURATION-Delete-Squelch Map-STs1** command to delete an STS-1 cross-connection. (This deletes an STS-1 squelch map entry at this node.)
11. At the DSX-3 and/or STSX-1 cross-connect panel, rearrange the low-speed cables to agree with Step 10 (see Figure 1).
12. Do you have instructions pertaining to STS-1 cross-connections at other network elements?  
If **YES**, then continue with Step 13.  
If **NO**, then continue with Step 17.
13. Establish a remote login to one of the other network elements in the FT-2000 ring network.  
Reference: **DLP-524**
14. Repeat Steps 3 through 11 for the appropriate STS-1 cross-connections. [Assistance will be required at the other network element to rearrange the low-speed cables at the DSX-3 and/or STSX-1 cross-connect panel(s).]
15. Discontinue the remote login.  
Reference: **DLP-524**
16. Repeat Steps 12 through 15 for each of the remaining network elements in the ring network that requires STS-1 cross-connections per the instructions.
17. All STS-1 cross-connections have been made and are now ready for traffic.
18. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



tpa 832887/01

**Figure 1 – Example of STS-1 Cross-Connections (Using External Cables)**



## Loop Back OC-3 Line(s) for Testing



### CAUTION:

*Traffic and service interruption can occur to other OC3 lines if they are accidentally looped back. Recheck the address of the line(s) being looped back.*

1. Obtain the operation instructions to loop back the OC3 line(s) for testing and note the assigned address.



### NOTE:

Loopback occurs at the OC3 slot in either the multiplex direction (towards the OC-48 line) or the demultiplex direction (towards the lightguide cross-connect panel). Simultaneous loopbacks in both directions are not possible.



### NOTE:

The OC3 slot must be cross-connected to a tributary for a valid loopback.

2. If required, connect and condition the craft interface terminal (CIT).

Reference: **DLP-501**

3. At the CIT, select **FAULT - Operate - Loopback - OC3** for the first specified slot(s) and the direction (either **From OC48** or **From OC3**).



### NOTE:

A loopback will remain in effect until deactivated by the **Release - Loopback - OC3** command.

4. Repeat Step 3 for any remaining slot(s).
5. Verify the **ABN**, **NE ACTY**, **USE CIT**, and **PWR ON** LEDs are lighted at the user panel.
6. The slot(s) is now looped back in the direction specified in Step 3. If you wish to verify, select **Retrieve-Alarm** for an updated Active Alarms and Status Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Release Looped Back OC-3 Line(s)



**CAUTION:**

*Traffic and service interruption can occur to other OC3 lines if they are accidentally looped back. Recheck the address of the slot(s) being released from loopback.*

1. Obtain the operation instructions to release the looped back OC3 slot(s) and note the assigned address.



**NOTE:**

Loopback occurred at the OC3 slot in either the multiplex direction (towards the OC-48 line) or the demultiplex direction (towards the lightguide cross-connect panel). Simultaneous loopbacks in both directions were not possible.

2. If required, connect and condition the craft interface terminal (CIT).  
Reference: **DLP-501**
3. At the CIT, select **FAULT-Release-Loopback-OC3** for the first specified slot and the direction (either **From OC48** or **From OC3**).
4. Repeat Step 3 for any remaining slot(s).
5. The slot(s) is released from the looped back condition. If you wish to verify, select **FAULT-Retrieve-Alarm** for an updated Active Alarms and Status Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Provision OC3 Slot for 1-Way Drop Service

**Overview:** This procedure is used only with Release 6.0 software to silence an alarm condition. The condition is associated with a nonexistent incoming OC-3 low-speed signal (towards the OC-48 line) and associated with a valid outgoing signal.

1. Determine the OC3 slot address from the work order (engineering instructions).
2. At the CIT, select **CONFIGURATION-Retrieve-Crossconnection** command and determine what type of cross-connection(s) (if any) is already at that slot.
3. Is the OC3 slot cross-connected for a 1-way drop?

If **YES**, then continue with Step 4.

If **NO**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

4. At the CIT, select **CONFIGURATION-Set-OC3** command and provision the Alarm Level to NO to prevent the reporting or alarming of any future condition.
5. At the CIT, select **CONFIGURATION-Retrieve-OC3** command and verify the **Alarm Level** is provisioned correctly.

**Supplemental Information:** If an **incoming OC3 LOS** condition exists prior to setting the Alarm Level to NO, this condition **will not disappear** after the Alarm Level is set to NO. Only subsequent conditions will be subdued (not reported). The user may clear the condition by temporarily providing a good input signal until the condition clears or by reseating the OC3 circuit pack. Only reseat the circuit pack if you are sure there is no live traffic on the outgoing fiber, otherwise service interruption will occur.

6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Test DRI Cross-Connections for Path Integrity in FT-2000 Ring

**Overview:** This procedure is used to test path integrity through the DRI cross-connections of an FT-2000 ring (Figures 1 and 2) using external test sets.

### 1. Required Test Equipment:

- Craft Interface Terminal (CIT)
- Wrist Strap
- Microwave Logic's ST-112 SONETest Transmission Analyzer or equivalent



**WARNING:**

*Unterminated optical connectors may emit invisible laser radiation. Eye damage may occur if beam is viewed directly or with improper optical instruments. Avoid direct exposure to beam.*



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 add/drop-rings terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

Referring to Figure 1 and Figure 2, which figure most closely represents your configuration?

If **Figure 1**, then continue with Step 2.

If **Figure 2**, then continue with Step 12.



**NOTE:**

Assistance is required at the source and destination test sets.



**NOTE:**

Path testing should be accomplished by using an end-to-end arrangement as shown in Figure 3. However, due to equipment availability, another possibility includes a loopback arrangement (Figure 4).

2. Verify that there are no alarm or status conditions in the ring.



**NOTE:**

The local FT-2000 ring has one DRI cross-connection to the outside. Both DRI cross-connections and local source/destination cross-connections terminate directly to test sets.

3. At the transmit test set, insert a verifiable signal for each path being tested.
4. At the receive test set, verify the detection of the inserted signal on each path being tested.
5. To test path integrity for 2-way paths, repeat Steps 2 and 3 from the opposite end for each path.
6. At the FT-2000 primary node P, execute a manual DRI switch for each path being tested. Use the **FAULT-Switch-Path-STS3-Manual** command for the appropriate line(s) and tributary(s).
7. Repeat Steps 2 through 4 for each path being tested through the secondary node S.
8. At the FT-2000 primary node P, reset the manual DRI switch for each path being tested. Use the **FAULT-Switch-Path-STS3-Reset** command for the appropriate line(s) and tributary(s).
9. Verify that there are no alarm or status conditions in the ring.
10. This completes testing path integrity.
11. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
12. Verify that there are no alarm or status conditions in the ring (Figure 2).



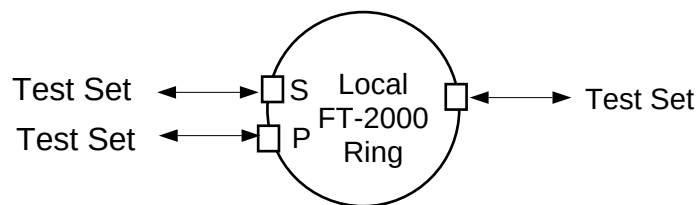
**NOTE:**

The local FT-2000 ring has two DRI cross-connections to the outside. Both DRI cross-connections terminate directly to test sets.

13. At the transmit test set, insert a verifiable signal for each path being tested.
14. At the receive test set, verify the detection of the inserted signal on each path being tested.
15. To test path integrity for 2-way paths, repeat Steps 13 and 14 from the opposite end for each path.
16. At the FT-2000 primary node P, execute a manual DRI switch for each path being tested. Use the **FAULT-Switch-Path-STS3-Manual** command for the appropriate line(s) and tributary(s).
17. Repeat Steps 13 through 15 for each path being tested.

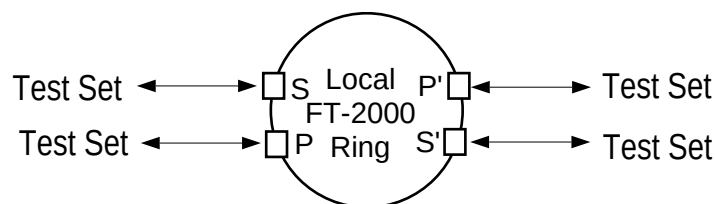


18. DO NOT RESET the DRI switch at node P at this time. At FT-2000 primary node P' (P prime), execute a manual DRI switch for each path being tested. Use the **FAULT-Switch-Path-STS3-Manual** command for the appropriate line(s) and tributary(s).
19. Repeat Steps 13 through 15 for each path being tested.
20. At the FT-2000 primary node P, reset the manual DRI switch for each path being tested. Use the **FAULT-Switch-Path-STS3-Reset** command for the appropriate line(s) and tributary(s).
21. Repeat Steps 13 through 15 for each path being tested.
22. At the FT-2000 primary node P', reset the manual DRI switch for each path being tested. Use the **FAULT-Switch-Path-STS3-Reset** command for the appropriate line(s) and tributary(s).
23. Verify that there are no alarm or status conditions in the ring.
24. This completes testing path integrity (Figure 2).



Note: One DRI cross-connection to the outside. Both DRI cross-connections and local source/destination cross-connections terminate directly to test sets.

**Figure 1 – Testing Local FT-2000 Ring (Three Nodes)**



Note: Two DRI cross-connections to the outside. Both DRI cross-connections terminate directly to test sets.

**Figure 2 – Testing Local FT-2000 Ring (Four Nodes)**

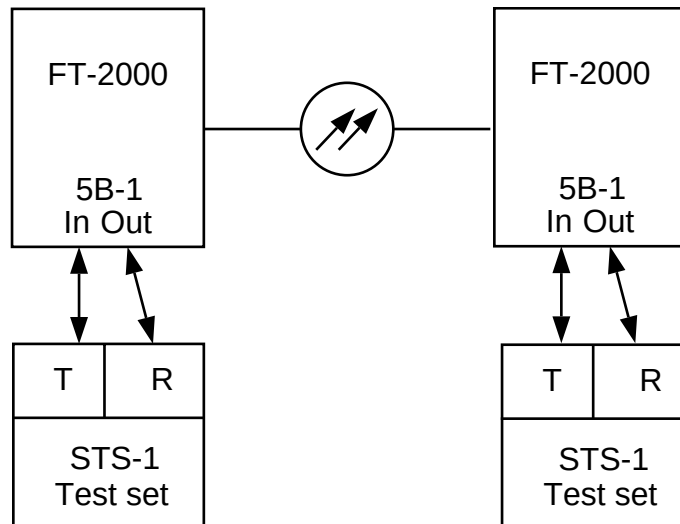


**Figure 3 – End-to-End Test Arrangement (for Slot 5B)**

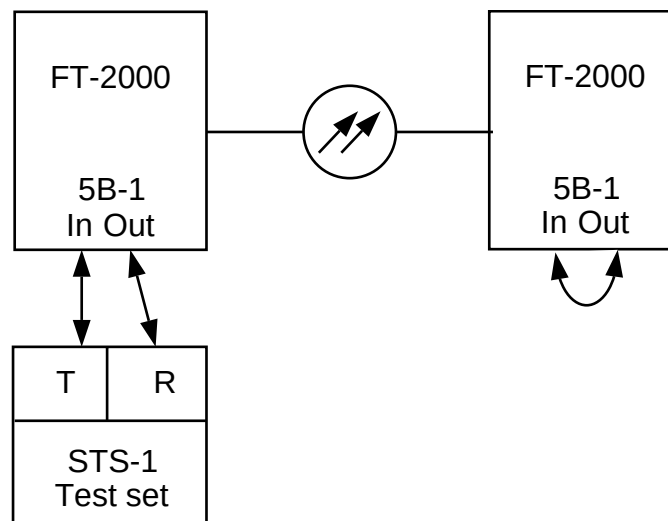


**Figure 4 – Loopback Test Arrangement (for Slot 5B)**

## Enter/Delete DCC Assignment on OC-3/OC-12 Line

**Overview:** This procedure is used to enter, delete, or change the assignment of one of eight Data Communications Channels (DCC) to a particular LS INTFC slot or pair of slots. Also at this time, the orientation of the DCC may be provisioned to "User" or "Network".

1. Do you want to **enter, delete, or change** a DCC assignment?

To **enter new DCC assignment**, then continue with Step 2.

To **delete DCC assignment**, then continue with Step 7.

To **change an existing DCC assignment/orientation**, then continue with Step 11.

### Enter DCC Assignment

2. Determine from office records the DCC assignment (1-8) and the orientation (network/user).



**NOTE:**

If you are using Release 6.0 or later software, a DCC assignment should not be made to an LS INTFC slot that does not already contain a good OC3 circuit pack.

3. At the CIT, select **CONFIGURATION-Retrieve-Equipment** command to verify the slot contains an OC3/OC-12 circuit pack.



**NOTE:**

The orientation of the OC-3/OC-12 circuit pack is provisioned "network" at one end and "user" at the other end. Both ends of the OC-3/OC-12 line cannot be the same, and it makes no difference which end is "network."

4. At the CIT, select **CONFIGURATION-Enter-Assignment-DCC** to make the DCC assignment and set the orientation.



**NOTE:**

If you are establishing DCC connectivity for the first time, you may have additional alarms, for example, **multiple DSNEs defined** and/or remote alarms. All alarms should clear after the DCC stabilizes, but if they do not, refer to the trouble clearing tab of this manual.

5. This step is optional. At the CIT, select **CONFIGURATION-Retrieve-Assignment-DCC** command and verify the entries are correct.
6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

### Delete DCC Assignment

7. Determine from office records the DCC assignment (1-8) to be deleted.
8. At the CIT, select **CONFIGURATION-Delete-Assignment-DCC** to delete the DCC assignment.



**NOTE:**  
It is possible a **DSNE not reachable** and/or **NE status comm failure** condition(s) may be generated if rings are being separated.

9. This step is optional. At the CIT, select **CONFIGURATION-Retrieve-Assignment-DCC** command and verify the entry is correct.
10. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

### Change DCC Assignment/Orientation

11. Determine from office records the required orientation (network/user) of the existing DCC assignment.



**NOTE:**  
The orientation of the OC-3/OC-12 circuit pack is provisioned "network" at one end and "user" at the other end. Both ends of the OC-3/OC-12 line cannot be the same, and it makes no difference which end is "network."



**NOTE:**  
If you are establishing DCC connectivity for the first time, you may have additional alarms, for example, **multiple DSNEs defined** and/or remote alarms. All alarms should clear after the DCC stabilizes, but if they do not, refer to the trouble clearing tab of this manual.

12. At the CIT, select **CONFIGURATION-Enter-Assignment-DCC** to make the same DCC assignment but with a different orientation.
13. This step is optional. At the CIT, select **CONFIGURATION-Retrieve-Assignment-DCC** command and verify the entries are correct.
14. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## STS3 (or STS1) Path Trace Monitor for Non-DS3 Signals

**Overview:** This procedure is used to monitor non-DS3 signals at the destination node and at any intermediate node(s). [The path trace is a "tag" on a path in the network.] The non-DS3 signals could be an STS1, OC3, OC3c, OC12, OC12c, STS-12, STS-12c, or any other SONET signal. The monitor arrangement accesses the path trace byte (J1) of the SONET signal. The SONET signal is provisioned at its source (network element) and may be either inside an FT-2000 ring or outside an FT-2000 ring. This procedure can be performed from any network element in an FT-2000 network.

1. Do you wish to verify the path trace byte for a DS3 or non-DS3 signal?

For a **DS3 signal**, then go to **DLP-532**.

If a **non-DS3 signal**, then continue with Step 2.

2. If required, condition the craft interface terminal (CIT) and connect it to the local FT-2000 network element.

Reference: **DLP-501**

3. From office records or work instructions, determine the path(s) to be monitored.
4. Establish a login to the network element, if required, for the first path to be verified. (To establish a remote login, refer to **DLP-524**.)



**NOTE:**

A "tag" or path trace may be verified at one or at all of the intermediate network elements and/or at the destination network element.

5. From office records, identify a DS3 circuit pack that is ready for service BUT IS NOT carrying service (AUTO state). This slot (STS3) or port (STS1) and circuit pack will be used as an STS3 (or STS1) path trace monitor. To verify the slot or port state, you can use the **FAULT-Retrieve-State-LS** command.
6. At the CIT, select **CONFIGURATION-Enter-Crossconnection-STs3 (or STS1)** command and enter a drop cross-connection (tributary-to-slot). The drop must be from the incoming tributary of the 1-way circuit. Another DS3 circuit pack would be required if monitoring a 2-way circuit.

7. At the CIT, select **CONFIGURATION-Retrieve-Path-Trace** command and execute to obtain a Path Trace Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.

⇒ **NOTE:**  
When monitoring an OC3 or three STS1 signals, the report shows the path trace status for all three ports of the DS3 circuit pack.

⇒ **NOTE:**  
When monitoring an OC3c signal, the report shows the path trace status for only port 1 of the DS3 circuit pack.

⇒ **NOTE:**  
Optional and only if the signal has been provisioned at its source. You can provision the PrvRx field using the **CONFIGURATION-Set-Path-Trace** command to match what was provisioned at its source. If the PrvRx field matches the ActRx field, then the report shows the path trace status as "good" at the DS3 circuit pack.

8. Verify the expected or embedded path trace byte or "tag" of the SONET signal for this path.

⇒ **NOTE:**  
The STS1E and the OC3 circuit packs do not create new signals, they simply pass the existing SONET signal on through at the intermediate node(s).

9. Repeat Steps 3 through 8 for any additional path(s) to be monitored at this network element.
10. Discontinue the login to this network element. (To discontinue a remote login, refer to **DLP-524**.)
11. Repeat Steps 3 through 10 for any additional path(s) to be monitored at the next network element, if required.
12. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Enter or Change TID String of FT-2000 Network Element

**Overview:** This procedure is used to enter or change the Target Identifier (TID) string of an FT-2000 network element. A TID change is the same as entering a TID. Additional information on the SECURITY-Set-Network Element command is in Chapter 8 of this manual.

1. At the CIT, select **CONFIGURATION-Retrieve-Map Ring** command and execute to obtain Ring Map Report.
2. Verify that the DCC Status of the ring is **good**. If the DCC Status column indicates a **FAIL**, this must be cleared before continuing with this procedure.
3. Determine from office records the unique TID string (up to 20 characters).
4. At the CIT, select **CONFIGURATION-Set-Network Element** command and enter or change the TID string, but DO NOT press the ENTER key at this time.



**NOTE:**

Changing the value of the local TID will result in all incoming remote TL1 sessions being disconnected. This means the local network element will terminate all login sessions that were requested from a TL1 gateway network element.

5. Verify the TID string with office records.
6. If necessary, enter or change any of the other parameters for this command, for example, the Directory Service NE, the Alarm Group, etc., then execute by pressing the ENTER key.
7. Wait at least 2 minutes for the TID to be mapped into all nodes on the ring.
8. This step is optional. At the CIT, select **CONFIGURATION-Retrieve-Map Ring** command and verify all entries are consistent with the work instructions.
9. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Replace Fan Filter Every Six Months (in D-Bay)

**Overview:** The equipment manufacturer (Lucent Technologies) suggests that the fan filter be changed every six-months. Due to the amount of heat generated within the D-Bay it is necessary that the fan remain powered and continuously circulate air even while the filter is being changed. Please note, this is a different procedure than required for the E-Bay. Once the old fan filter has been removed the new fan filter should be inserted without an interruption.



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*

1. The filter is accessible from the front of the bay without opening any doors or removing any covers. It is located approximately 5-inches up from the floor. There are two plastic loops (one on the right side and one on the left side) available to pull the filter out of the bay.
2. Place one finger in both plastic loops and pull the old filter from the bay.
3. Obtain the new (replacement) filter and make sure that the arrows, which are located on the front edge of the filter, are pointing up. Apply equal pressure to both sides of the filter and gently slide it, within the filter guides, until it is flush with the front of the bay.



**NOTE:**

Air flow in the bay is from bottom to top (up).

4. Dispose of the dirty air filter per the local operating instructions.
5. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Replace Defective Fan Unit in D-Bay

**Overview:** Replacing a defective fan unit is a simple procedure which can and, once started, must be completed in a brief time interval. A fan unit alarm is typically the first and only sign indicating a problem with the fan unit. Within the fan unit there are two conditions that may raise an alarm. These are:

- a. Loss of power to the fan unit.
- b. Lack of sufficient air flow (e.g., one of the fans has stopped turning).



**NOTE:**

The fan unit alarm LED, which is normally not lit, is red in color. The power LED, which is normally lit, is green in color.



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 terminal to prevent electrostatic discharge damage to sensitive components. See "Electrostatic Discharge (ESD) Considerations" in **Trouble Clearing: TAD-100**.*



**CAUTION:**

*Due to the amount of heat generated within the D-Bay, it is necessary to minimize the time interval wherein air is not being circulated.*

1. Unpack the new fan unit and place near the D-Bay for easy and quick access.
2. At the D-Bay, locate and disconnect the fan unit alarm leads. This is the squeeze type connector on the left side of the fan unit.
3. At the D-bay, locate and disconnect the fan unit power leads. This is the squeeze type connector on the right side of the fan unit.
4. There are two screws (one on the left front and the other on the right front) that hold the fan unit to the D-Bay frame. Obtain a screw-driver and remove both screws.



**CAUTION:**

*Verify that the fan blades are not spinning. Then, pull the defective fan unit forward and out of the front of the D-Bay frame.*

5. Place the defective fan unit aside and carefully, but quickly, insert the new fan unit.
6. Insert both left and right screws and tighten each securely.

7. Obtain the fan unit power lead and connect it to the fan unit.
8. Visually, verify that the fan unit "power" LED, which is located on the fan unit, is lit and that each fan is turning.
9. Obtain the fan unit alarm lead and connect to the alarm receptacle on the fan unit.
10. Visually, verify that the fan unit "alarm" LED, which is located on the fan unit, is not lit.



**NOTE:**

Once the new fan unit is installed, the Red alarm LED should not be lit. Generally, this alone will be sufficient indication that the problem has been corrected. However, if this is an unstaffed site, it is recommended that a check be made, via the CIT, to determine for certain that the "remote" alarm has been cleared.

11. If you wish to verify that the remote alarm has been cleared, at the CIT select **Retrieve-Alarm** for an updated Active Alarms and Status Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
12. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Provision OC12 Slot for 1-Way Drop Service

### Overview:

1. Determine the OC12 slot address from the work order (engineering instructions).
2. At the CIT, select **CONFIGURATION-Retrieve-Crossconnection** command and determine what type of cross-connection(s) (if any) is already at that slot.
3. Is the OC12 slot cross-connected for a 1-way drop?  
If **NO**, then continue with Step 4.  
If **YES**, then **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**
4. At the CIT, select **CONFIGURATION-Set-OC12** command and provision the Alarm Level to NO to prevent the reporting or alarming of any future condition.
5. At the CIT, select **CONFIGURATION-Retrieve-OC12** command and verify the **Alarm Level** is provisioned correctly.

**Supplemental Information:** If an **incoming OC12 LOS** condition exists prior to setting the Alarm Level to NO, this condition **will not disappear** after the Alarm Level is set to NO. Only subsequent conditions will be subdued (not reported). The user may clear the condition by temporarily providing a good input signal until the condition clears or by reseating the OC12 circuit pack. Only reseal the circuit pack if you are sure there is no live traffic on the outgoing fiber, otherwise service interruption will occur.

6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Release Looped Back OC-12 Line(s)



**CAUTION:**

*Traffic and service interruption can occur to other OC12 lines if they are accidentally looped back. Recheck the address of the slot(s) being released from loopback.*



**NOTE:**

Loopback occurred at the OC12 slot in either the multiplex direction (towards the OC-48 line) or the demultiplex direction (towards the lightguide cross-connect panel). Simultaneous loopbacks in both directions were not possible.

1. Obtain the operation instructions to release the looped back OC12 slot(s) and note the assigned address.
2. If required, connect and condition the craft interface terminal (CIT).

Reference: **DLP-501**

3. At the CIT, select **FAULT-Release-Loopback-OC12** for the first specified slot and the direction (either **From OC48** or **From OC12**).
4. Repeat Step 3 for any remaining slot(s).
5. The slot(s) is released from the looped back condition. If you wish to verify, select **FAULT-Retrieve-Alarm** for an updated Active Alarms and Status Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
6. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**





## Loop Back OC-12 Line(s) for Testing



**CAUTION:**

*Traffic and service interruption can occur to other OC12 lines if they are accidentally looped back. Recheck the address of the line(s) being looped back.*

1. Obtain the operation instructions to loop back the OC12 line(s) for testing and note the assigned address.



**NOTE:**

Loopback occurs at the OC12 slot in either the multiplex direction (towards the OC-48 line) or the demultiplex direction (towards the lightguide cross-connect panel). Simultaneous loopbacks in both directions are not possible.



**NOTE:**

The OC12 slot must be cross-connected to a tributary for a valid loopback.

2. If required, connect and condition the craft interface terminal (CIT).

Reference: **DLP-501**

3. At the CIT, select **FAULT - Operate - Loopback - OC12** for the first specified slot(s) and the direction (either **From OC48** or **From OC12**).



**NOTE:**

A loopback will remain in effect until deactivated by the **Release - Loopback - OC12** command.

4. Repeat Step 3 for any remaining slot(s).
5. Verify the **ABN**, **NE ACTY**, **USE CIT**, and **PWR ON** LEDs are lighted at the user panel.
6. The slot(s) is now looped back in the direction specified in Step 3. If you wish to verify, select **Retrieve - Alarm** for an updated Active Alarms and Status Report. An example of this report with a description of the output parameters is provided in Section 11 of this manual.
7. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



## Replace a Defective Power Line Filter

**Overview:** Use this procedure to replace defective power line filters in an FT-2000 OC-48 Lightwave System if you have used **DLP-515, Replace Fuse in Power Distribution and Fuse Panel or User Panel** to replace the fuse to the affected shelf and feeder and the alarm does not clear in approximately five minutes.

You can use this procedure to replace power line filters on FT-2000 OC-48 Lightwave System A-Bays, D-Bays, E-Bays, and R-Bays. These filters include:

- Power line filter assembly
  - For each feeder there is one circuitry unit with two separate capacitors.

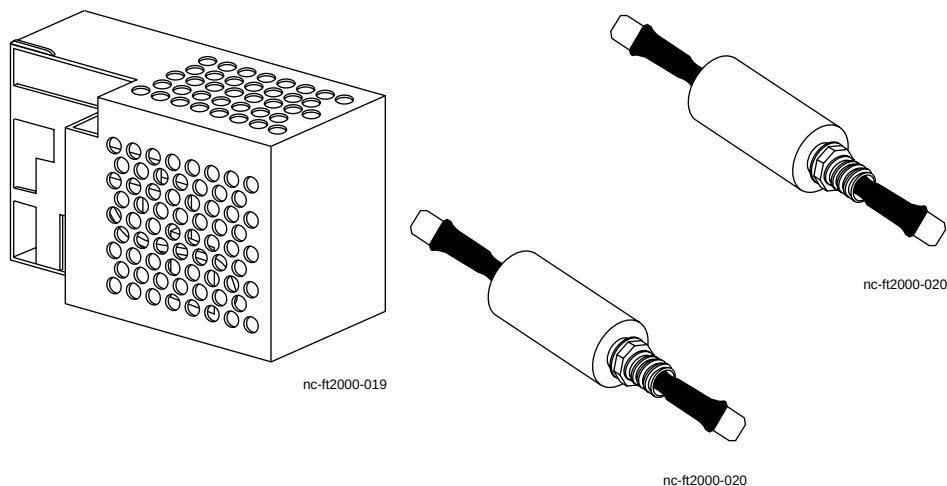


**Figure 1 – Power Line Filter Assembly**

- Power line filter module
  - For each feeder there is one module containing all of the filter components as captive parts of the module.

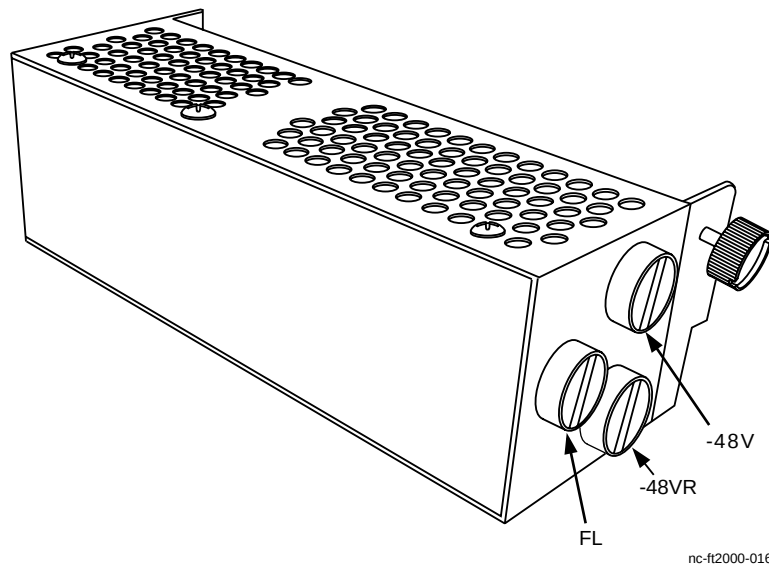


**Figure 2 – Power Line Filter Module**

⇒ **NOTE:**  
Some modules have a fourth connector for a sensor lead. All modules have the three connectors shown above.

⇒ **NOTE:**  
A-Bays, E-Bays, and R-Bays almost always have power line filter assemblies (Figure 1) and D-Bays almost always have power line filter modules (Figure 2).

**Equipment required:**

- Correct replacement power line filter for the affected shelf.
- Static ground wrist strap.
- Phillips-head or flat-blade screw driver, depending on the type of screws on the User Panel.
- Pliers or other appropriate tool to remove power leads. Ratchet with 3/8" deep socket recommended to loosen the nuts on the capacitors.
- Flashlight.

### First Step: Determine the Bay Type

Determine which type of bay has the **fuse/power failure** alarm. An equipment package code, such as **J68974E**, appears on the Fuse Panel at the top of the bay. The last letter of this code indicates the bay type.

If it is an **A-Bay**, **E-Bay**, or **R-Bay** then go to “Option 1: Replace the Capacitors of a Power Filter Assembly” on page 4.

If it is a **D-Bay** then go to “Option 3: Replace a Power Line Filter Module” on page 12.

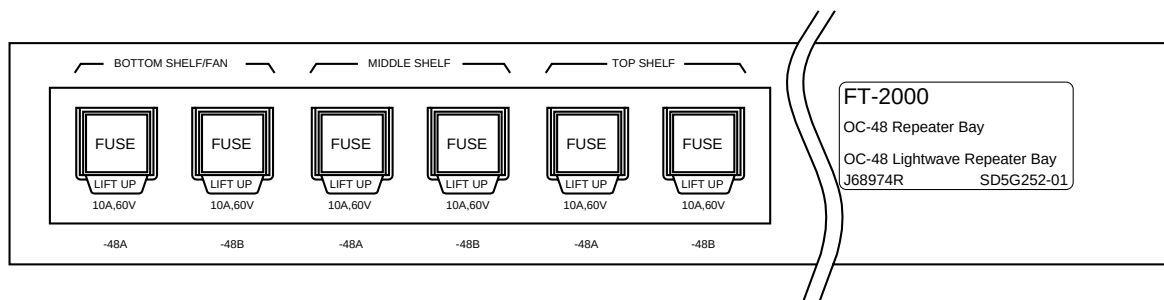


**Figure 3 – Example of Fuse Panel with Bay Type Label**



**NOTE:**

The Fuse Panels of other bay types look very similar, except for the wording on the bay type label.

### Option 1: Replace the Capacitors of a Power Filter Assembly



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components.*



**CAUTION:**

**DO NOT** remove the fuse for more than one feeder at a time or the shelf will lose service.

1. Verify that there is a **fuse/power failure** alarm on the appropriate shelf and take note of whether the alarm indicates feeder **A** or **B**.



**NOTE:**

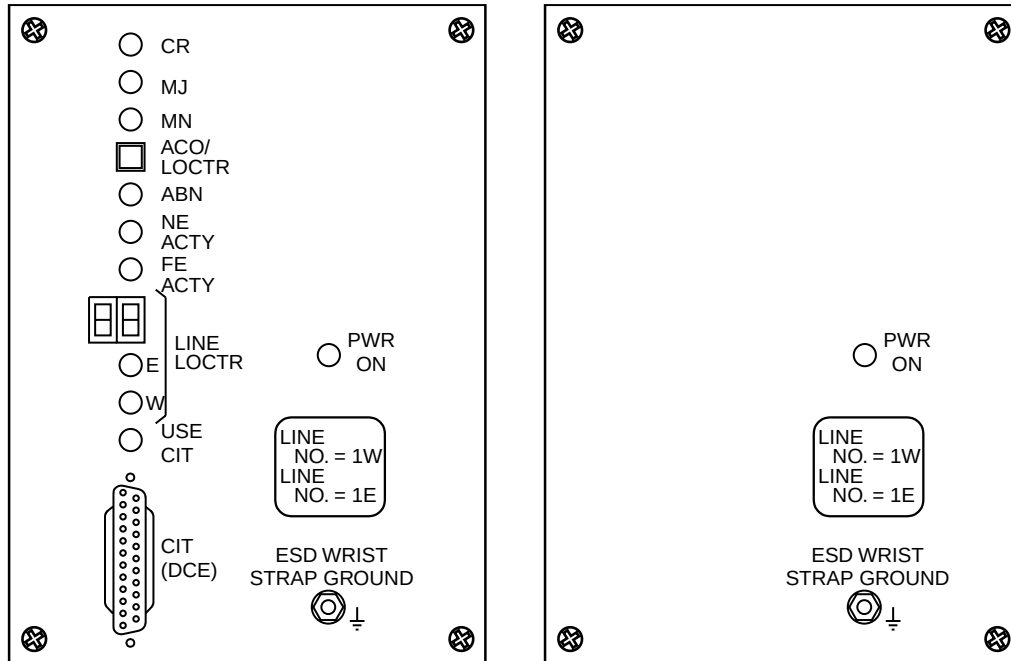
In the alarm message, **ls** indicates low-speed shelf and **hs** indicates high-speed shelf. For example, if the alarm is **hs fuse/power failure A** then the filter assembly for the A feeder for the high speed shelf needs to be replaced.

2. Remove the fuse for the affected shelf **and feeder**.

Reference: **DLP-515, Replace Fuse in Power Distribution and Fuse Panel or User Panel** steps 2 through 4.

3. Locate the Interconnection Panel for the affected shelf. (It's the part of the shelf above the circuit packs.)
4. Turn the screws on the Interconnection Panel cover so that they are in the vertical (unlocked) position.
5. Use the wire handles that are mounted to the screws to pull the top of the cover away from the Interconnection Panel, then lift the cover off of the hinges.

6. Notice that this exposes the four screws on the corners of the User Panel, similar to one of the panels depicted in the following figure.



nc-ft2000-028

- 7.



**CAUTION:**

*DO NOT ALLOW the screws to fall into the bay.*

Remove the four corner screws from the User Panel.

8. Set the User Panel aside, being careful not to short out any of the electrical connections on the back of the panel.

9. Locate the affected power filter capacitors in the back right corner of the Interconnection Panel. Feeder A has one capacitor with a red lead and one with a black lead. Feeder B has one capacitor with a grey lead and one with a grey with black stripe lead.

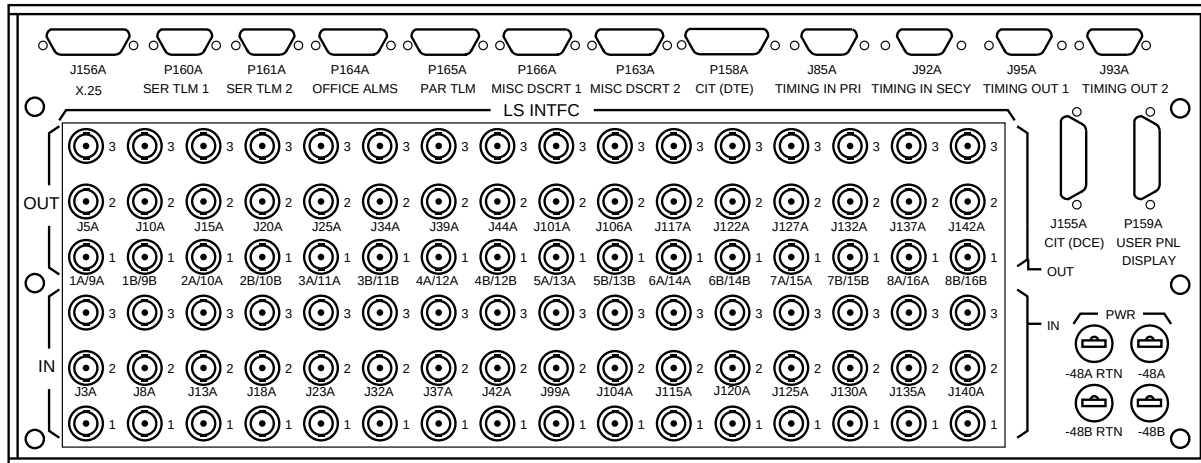


**Figure 4 – Example Interconnection Panel**



**NOTE:**

The Interconnection Panels for other shelf types have the same power filter connectors, but the other connectors may vary.

10. Go to the back of the bay and remove the top or bottom cover, depending on which shelf is affected. (Remove all screws and lift the cover off.)
11. Locate the capacitors mounted below the PWR label on the left side of the backplane. The label for each connector indicates whether it belongs to the A or B feeder.



**NOTE:**

It is highly unlikely that both the -48 and -48 RTN capacitors are defective. Therefore it is recommended to replace one at a time (Steps 12-25). If replacing the first one doesn't clear the alarm then remove the fuse and repeat Steps 12-25 for the other capacitor.

12. Remove the leads to the affected feeder capacitors.



13. Use a ratchet with a 3/8" deep socket or some other tool that can fit over the connector to unscrew the nut that holds each capacitor in place.



**CAUTION:**

*DO NOT ALLOW the nuts or washers to fall into the bay.*



**NOTE:**

The nut that secures the capacitor has slots on it that allow it to slide over the spade wings on the capacitor lead.

14. Guide the nuts and lock washers off of the connectors.
15. Go to the front of the shelf and locate the loosened capacitors.
16. Pull the capacitors out to where you can work with the remaining leads. You may need to pull the leads through a cable tie in order to give yourself enough slack.



**CAUTION:**

*To remove a lead, grip it by the neck of the lead's short plastic sleeve and not by the lead itself.*

17. Remove the remaining leads from the capacitors. You may want to grip the sleeves with pliers.
18. Attach the leads to the new capacitors, if possible. If not, attach them after mounting the capacitors to the backplane.
19. Place the capacitors back into position in the shelf.
20. Go to the back of the bay and replace the lock washers and nuts onto the capacitors.



**NOTE:**

The nut that secures the capacitor has slots on it that allow it to slide over the spade wings on the capacitor lead.

21. Attach the leads in the back. Red is for -48A, black is for -48ARTN, grey is for -48B, grey with black stripe is for -48BRTN.
22. Replace the back cover.
23. If you did not attach the leads in front in Step 18, then attach them now.

24. Reinstall the fuse.

Reference: **DLP-515, Replace Fuse in Power Distribution and Fuse Panel or User Panel Steps 6 and 7.**

25. Did the alarm clear in approximately five minutes?

If **NO**, go to "Option 2: Replace the Circuitry Unit of a Power Line Filter Assembly" on page 9.

If **YES**, ***STOP! YOU HAVE COMPLETED THIS PROCEDURE.***

## Option 2: Replace the Circuitry Unit of a Power Line Filter Assembly



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components.*



**CAUTION:**

**DO NOT** remove the fuse for more than one feeder at a time or the shelf will lose service.

1. Verify that there is a **fuse/power failure** alarm on the appropriate shelf and take note of whether the alarm indicates feeder **A** or **B**.



**NOTE:**

In the alarm message, **ls** indicates low-speed shelf and **hs** indicates high-speed shelf. For example, if the alarm is **hs fuse/power failure A** then the filter assembly for the A feeder for the high speed shelf needs to be replaced.

2. Remove the fuse for the affected shelf **and feeder**.

Reference: **DLP-515, Replace Fuse in Power Distribution and Fuse Panel or User Panel** steps 2 through 4.

- 3.



**CAUTION:**

**DO NOT** replace the power line filters for Feeder A and Feeder B at the same time, or the shelf will lose service.

Use the color coding of the power leads to locate the defective power line filter circuitry unit for the shelf **and feeder**.

Reference: Figure 5 on page 11.

4. The circuitry unit is held in place by a set screw. Loosen the set screw. The set screw is captive, so it will not fall off.

Reference: Figure 5 on page 11.

5. Once the set screw is loose, the circuitry unit is free to slide off of the two standoffs that are mounted to the right side of the shelf. Work the circuitry unit off of the standoffs.
6. Extract the circuitry unit until it is out where you can work with the leads.

7.



**CAUTION:**

*To remove a lead, grip it by the neck of the lead's short plastic sleeve and not by the lead itself.*

Remove all leads from the defective module. The thinner leads can be pulled straight out easily. The thicker power leads are more difficult — work them side to side while pulling outward.

8. Separate the circuitry unit cover from the circuit board as follows. Hold the cover with one hand and grip the circuit board at the end with the connectors. Pull the circuit board away from the cover until the two pieces are completely separate.
9. Fit the new circuit board to the cover and screw the set screw in enough to hold the two pieces together.
10. Notice the two holes in the circuit board. Insert the circuitry unit back into place and guide the two holes onto the two standoffs.
11. Screw the unit in place. If the screw has tilted you may have to extract the unit and screw the cover onto the circuit board again.
12. Plug each lead back into the new circuitry unit. Note that the E10-E9 pair is bundled with the thin black lead that goes to P2. Plug the thin leads in with the wire closer to the P1 or P2 label. You can also use Figure 5 on page 11 as a guide.
- 13.



**CAUTION:**

*DO NOT ALLOW the screws to fall into the bay. Also be careful to guide the User Panel over the "PWR ON" LED.*

Put the User Panel back on and replace the screws.

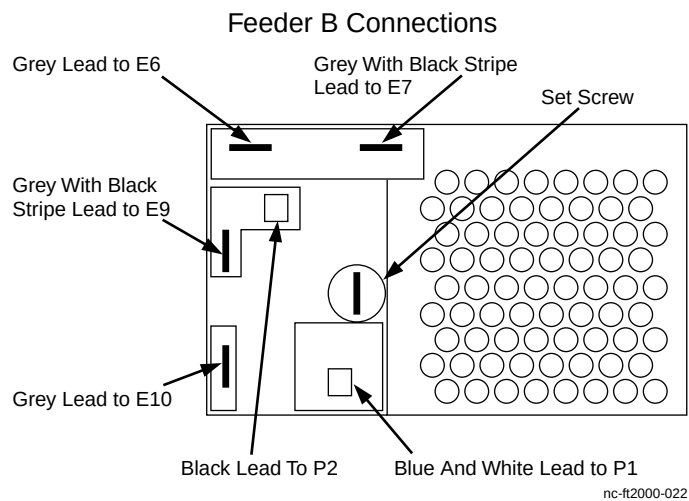
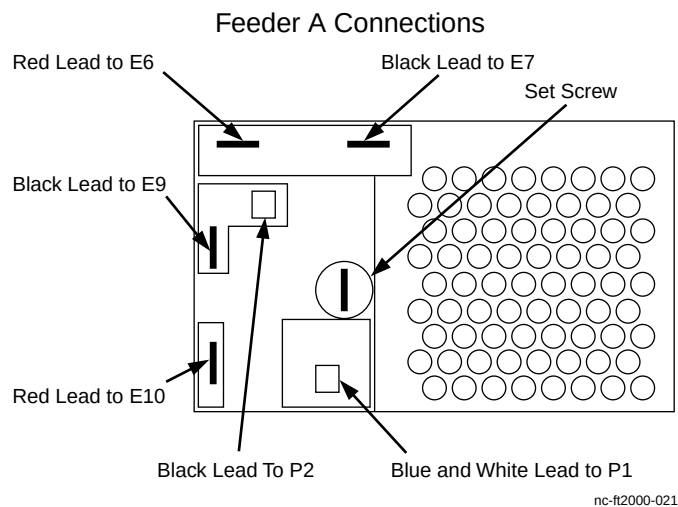
14. Guide the Interconnection Panel cover back onto its hinges, push the top closed, and lock the screws by turning them so that the slots are horizontal.
15. Reinstall the fuse.

Reference: **DLP-515, Replace Fuse in Power Distribution and Fuse Panel or User Panel Steps 6 and 7.**

16. Did the alarm clear in approximately five minutes?

If **NO**, call your next level of support.

If **YES**, **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



➡ **NOTE:**  
The E10-E9 pair is bundled with the thin black lead that goes to P2.

**Figure 5 – Power Connections for Power Line Filter Assembly**

### Option 3: Replace a Power Line Filter Module



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on an FT-2000 network element to prevent electrostatic discharge damage to sensitive components.*



**CAUTION:**

**DO NOT** remove the fuse for more than one feeder at a time or the shelf will lose service.

1. Verify that there is a **fuse/power failure** alarm on the appropriate shelf and take note of whether the alarm indicates feeder **A** or **B**.



**NOTE:**

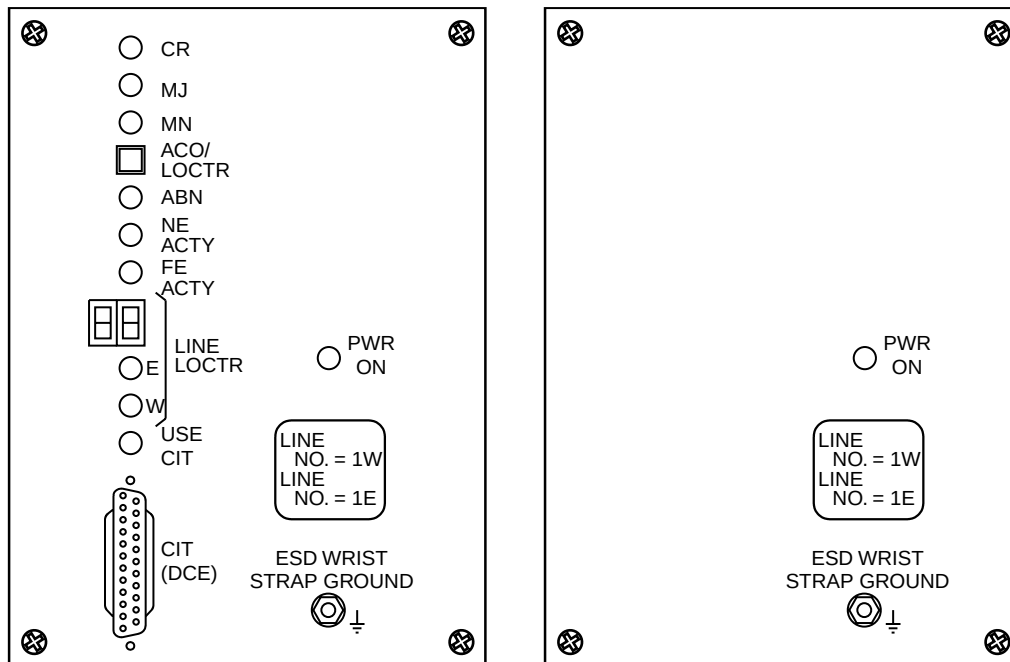
In the alarm message, **ls** indicates low-speed shelf and **hs** indicates high-speed shelf. For example, if the alarm is **hs fuse/power failure A** then the filter assembly for the A feeder for the high speed shelf needs to be replaced.

2. Remove the fuse for the affected shelf **and feeder**.

Reference: **DLP-515, Replace Fuse in Power Distribution and Fuse Panel or User Panel** steps 2 through 4.

3. Locate the Interconnection Panel for the affected shelf. (It's the part of the shelf above the circuit packs.)
4. Turn the screws on the Interconnection Panel cover so that they are in the vertical (unlocked) position.
5. Use the wire handles that are mounted to the screws to pull the top of the cover away from the Interconnection Panel, then lift the cover off of the hinges.

6. Notice that this exposes the four screws on the corners of the User Panel, similar to one of the panels depicted in the following figure.



nc-ft2000-028

7.



**CAUTION:**

*DO NOT ALLOW the screws to fall into the bay.*

Remove the four corner screws from the User Panel.

8. Set the User Panel aside, being careful not to short out any of the electrical connections on the back of the panel.

9.



**CAUTION:**

*To avoid service interruption, it is essential to identify which module is defective.*

Use the color coding of the power leads to locate the defective power line filter module for the shelf **and feeder**.

Reference: Figure 6 on page 15.

10.



**CAUTION:**

*To remove a lead, grip it by the neck of the lead's short plastic sleeve and not by the lead itself.*

Remove the power leads from the defective module.

11. Loosen the large set screw that is to the right of the modular unit while holding the filter in place. The screw is captive, so it will not fall off.
12. Gently pull on the defective power line filter module or power-filter assembly, and slide it out.
13. Orient and plug in the new power line filter module.
14. Tighten the large set screw that is to the right of the modular unit while holding the filter in place.
15. Plug each lead back into the appropriate connector.

Reference: Figure 6 on page 15.

16.



**CAUTION:**

*DO NOT ALLOW the screws to fall into the bay.*

Put the User Panel back on and screw in the User Panel screws.

17. Reinstall the fuse.

Reference: **DLP-515, Replace Fuse in Power Distribution and Fuse Panel or User Panel** Steps 6 and 7.

18. Did the alarm clear in approximately five minutes?

If **NO**, call your next level of support.

If **YES**, **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**



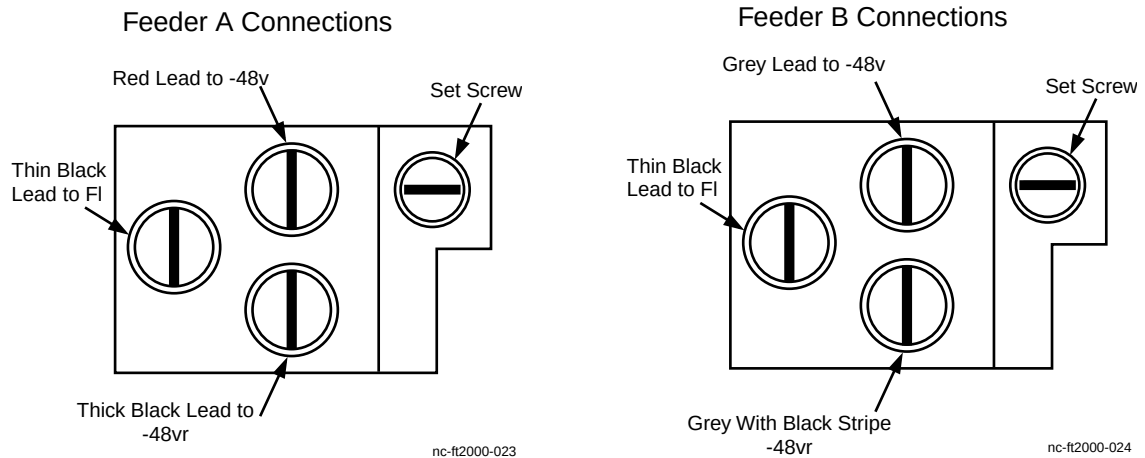


**Figure 6 – Power Connections for Power Line Filter Module**



## Power Down/Power Up an Optical Network Element (NE)

**Overview:** This procedure is designed to help Lucent customers prepare for a planned loss of power to all or part of a central office. The loss of power to any NE will cause a loss of traffic on that NE, so traffic must be rerouted around the NE. Because there are so many variations from product to product, this procedure is written at a high level. See the specific product documentation if you need more details for any step.

⇒ **NOTE:**  
The personnel performing this procedure should have knowledge of the specific product being powered down and/or powered up and should have access to specific product documentation so that they can determine whether the NE is behaving properly and address any problems that may occur.

⇒ **NOTE:**  
Any shelf that receives electrical power has either two (A and B) circuit breakers or two (A and B) fuses. Most NE shelves do receive electrical power. The circuit breakers or fuses may be located on the shelf itself or at the top of the bay frame.

## Power Down an Optical Network Element



**CAUTION:**

*Use a static ground wrist strap whenever handling circuit packs or working on a network element to prevent electrostatic discharge (ESD) damage to sensitive components.*

1. Reroute traffic around the NE.
2. Power down each shelf in one of the following ways:
  - Shut off both A and B circuit breakers by inserting an object such as a thin flathead screwdriver into the slot on the “off” side of each circuit breaker.
  - Remove both A and B fuses by lifting the cover, then pressing in and down, then releasing to allow the fuse to spring outward.
3. Shut off power at the Battery Distribution Feeder Bay (BDFB).
4. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

## Power Up an Optical Network Element



### CAUTION:

*Use a static ground wrist strap whenever handling circuit packs or working on a network element to prevent electrostatic discharge (ESD) damage to sensitive components.*

1. Make sure the circuit breakers are off or the fuses are removed from the NE.

**Result:** This eliminates any chance of one person powering up at the Battery Distribution Feeder Bay (BDFB) while someone else could be exposed to electrical power at the NE.

2. Turn on the power supply to this NE at the BDFB.
3. Continue with the next step for the shelf containing the system controller circuit pack.
4. Power up the shelf and verify that both A and B power supplies are good as follows:

- A. Turn on circuit breaker A or insert fuse A and press inward and upward.



### NOTE:

If the green power on the LED is not lit, check for a reversal on the power feeder and verify that there is voltage at the connector.

- B. Turn off circuit breaker A or remove fuse A.

- C. Turn on circuit breaker B or insert fuse B and press inward and upward.



### NOTE:

If the green power on the LED is not lit, check for a reversal on the power feeder and verify that there is voltage at the connector.

- D. Turn on circuit breaker A or insert fuse A and press inward and upward.

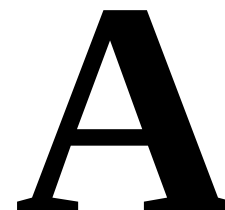
**Result:** You have verified that both A and B power supplies are good and are now turned on.

5. Wait for all of the circuit packs on this shelf to finish booting, which will be indicated by the following:
  - All red LEDs are off
  - For packs with green LEDs, they are on steadily
6. Repeat Steps 3 and 4 for all other shelves that contain controller circuit packs.
7. Repeat Step 3 for all shelves that do not contain any controller circuit packs.
8. Log into the NE with a craft interface terminal and check alarms.

9. If any alarms appear, use the product documentation to clear them. For most products, the appropriate document will be an *Alarm Message and Trouble Clearing Guide* or a *User/Service Manual*.
10. If this NE allows cross-connection, verify that the cross-connections are correct. Refer to the appropriate product documentation, usually a *User Operations Guide* or a *User/Service Manual*.
11. Reroute traffic back onto this NE.
12. **STOP! YOU HAVE COMPLETED THIS PROCEDURE.**

---

# A SONET Overview

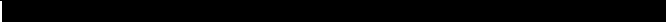


---

## Table of Contents

|                                      |             |
|--------------------------------------|-------------|
| <b>Overview</b>                      | <b>A-1</b>  |
| <b>History</b>                       | <b>A-1</b>  |
| <b>Basic Purpose</b>                 | <b>A-2</b>  |
| <b>Technical Overview</b>            | <b>A-2</b>  |
| ■ SONET Signal Hierarchy             | A-2         |
| ■ SONET Layers                       | A-4         |
| ■ SONET Frame Structure              | A-6         |
| ■ Section Overhead                   | A-6         |
| ■ Line Overhead                      | A-7         |
| ■ Path Overhead                      | A-8         |
| STS-1 Path Overhead                  | A-8         |
| VT Path Overhead                     | A-9         |
| ■ SONET Multiplexing Procedure       | A-10        |
| ■ SONET Demultiplexing Procedure     | A-12        |
| ■ SONET Digital Multiplexing Schemes | A-14        |
| Asynchronous Multiplexing            | A-14        |
| Synchronous Multiplexing             | A-15        |
| ■ Virtual Tributary Signals          | A-15        |
| ■ Concatenated Mode                  | A-16        |
| <b>SONET Interface</b>               | <b>A-17</b> |
| ■ SONET Payloads                     | A-18        |
| ■ Higher Rate Transport              | A-19        |
| <b>Conclusion</b>                    | <b>A-19</b> |

---

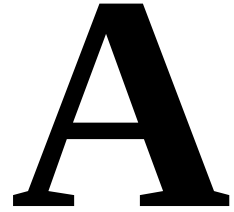


## Table of Contents



---

# A SONET Overview



---

## Overview

This section briefly describes the Synchronous Optical Network (SONET).

---

## History

In the early 1980's, the American National Standards Institute (ANSI) recognized the need for an optical signal standard for future broadband transmission. The ANSI T1X1 subcommittee began working on optical signal and interface standards in 1984. In 1985, Bellcore proposed a network approach to fiber system standardization to T1X1. The proposal suggested a hierarchical family of signals whose rates would be **integer multiples** of a basic modular signal. The proposal further suggested a synchronous multiplexing technique, leading to the coining of the term *Synchronous Optical Network* (SONET).

The International Telephone and Telegraph Consultative Committee (CCITT) first showed interest in 1986. Conferences held through 1987 and 1988 resulted in coordinated specifications for both the American National Standard (SONET) and the CCITT-International Standard, Synchronous Digital Hierarchy (SDH). Approval of both sets of standards occurred in late 1988.

## Basic Purpose

---

The basic purpose of SONET is to provide a standard synchronous optical hierarchy with sufficient flexibility to accommodate digital signals that currently exist in today's network as well as those planned for the future.

SONET currently defines standard rates and formats and optical interfaces. These and other related issues continue to evolve through the ANSI committees. SONET ultimately will permit an optical midspan meet in a multivendor environment.

The American National Standard defines the following:

- Optical parameters (*ANSI*\* T1.106-1988)
- Electrical parameters (*ANSI* T1.102-1993 Draft)
- Multiplexing schemes to map existing digital signals (for example, DS1, DS2, and DS3) into SONET payload signals (*ANSI* T1.105-1991)
- Criteria for optical line automatic protection switch (APS) (*ANSI* T1.105-1991)
- Overhead channels to support standard operation, administration, maintenance, and provisioning (OAM&P) functions (*ANSI* T1.105-1991).

\*

## Technical Overview

---

### SONET Signal Hierarchy

---

The SONET signal hierarchy is based on a basic "building block" frame called the synchronous transport signal - level 1 (STS-1), as shown in Figure A-1 on the following page. The STS-1 frame has a reoccurring rate of 8000 frames per second. Each frame is 125 microseconds.

The STS-1 frame consists of:

- 90 columns (each column is an 8-bit byte)
- 9 rows.

---

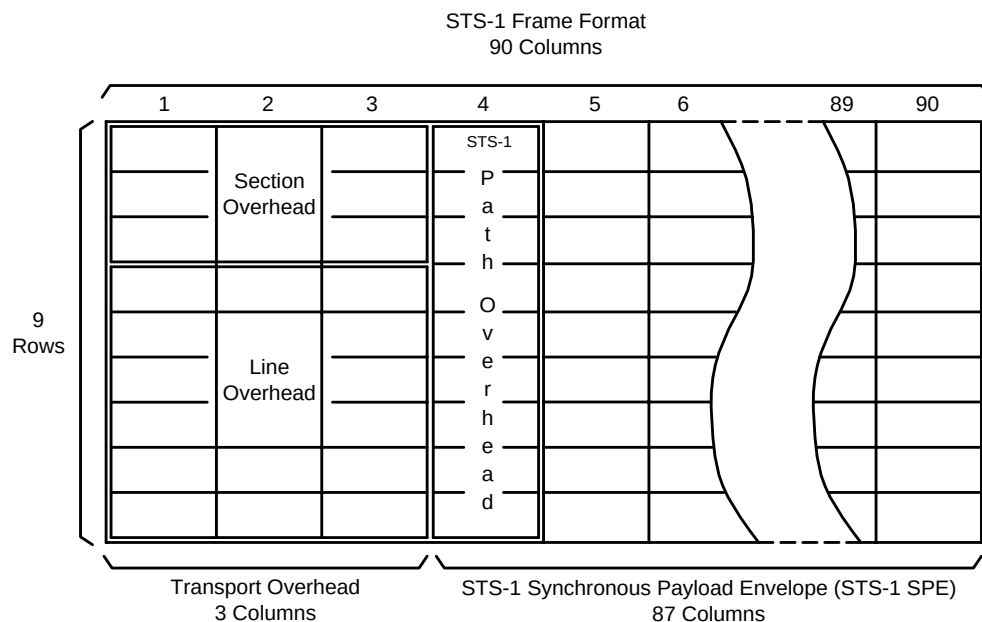
\* Registered trademark of America National Standards Institute.

The STS-1 frame is transmitted serially starting from the left with row 1 column 1 on through column 90, then row 2 column 1 through 90, continuing on, row-by-row, until all 810 bytes (9 X 90) of the STS-1 frame have been transmitted.

Since each STS-1 frame consists of 810 bytes and each byte has 8 bits, the frame contains 6480 bits a frame. There are 8000 STS-1 frames per second, at the STS-1 signal rate of 51,840,000 (6480 X 8000) bits a second.

The first three columns in each of the nine rows carry the SECTION and LINE overhead bytes. Collectively, these 27 bytes are referred to as transport overhead.

The remainder of the frame, columns 4 through 90, is reserved for payload signals (for example, DS1, DS3, and path overhead) and is referred to as the STS-1 synchronous payload envelope (STS-1 SPE). The optical counterpart of the STS-1 is the optical carrier level 1 signal (OC-1), which is the result of a direct optical conversion. The electrical counterpart of the STS-1 is the electrical carrier level 1 signal (EC-1).



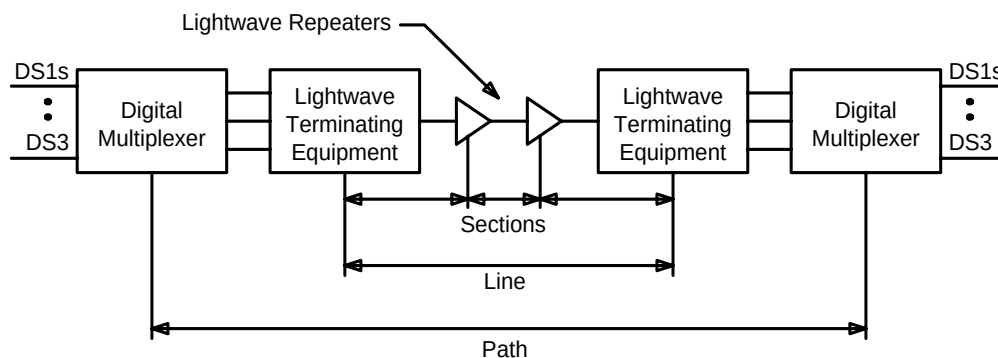
## SONET Layers

---

SONET divides its processing functions into three layers. These three layers are associated with equipment that reflects the natural divisions in network spans.

Figure A-2 shows these defined layers in a signal path. They include:

- **SECTION** and **Section Terminating Equipment** - the transmission spans between lightwave terminating equipment and the regenerators. The spans between the regenerators are also considered sections. Section terminating equipment provides regenerator functions and terminates the section overhead to provide single-ended operations and section performance monitoring.
- **LINE** and **Line Terminating Equipment** - the transmission span between terminating equipment (STS-1 cross-connections) that provides line performance monitoring. If there are no intervening repeaters, the line terminating equipment also functions as section terminating equipment.
- **STS-1 and VT Path** and **Path Terminating Equipment** - the transmission span for an end-to-end tributary (DS1 or DS3) signal that provides functions including signal labeling and path performance monitoring for signals as they are transported through a SONET network. STS-1 path terminating equipment can also provide cross-connections for lower rate (that is, DS1) signals. A virtual tributary (VT) is a sub-DS3 payload and is described later in more detail.



---

**Figure A-2. Section, Line, and Path Definitions**

Each SONET layer has a set of overhead bytes as shown in Figure A-3. These bytes carry information used by various network elements.

- **Section Overhead** contains information that is used by all SONET equipment including repeaters.
- **Line Overhead** is used by all SONET equipment except repeaters.
- **Path Overhead** is carried within the payload envelope.
  - **STS-1 path overhead** remains with the STS-1 SPE until its asynchronous signal is extracted (for example, DS-3) or until its individual VT1.5 signals are demultiplexed.
  - **VTN (N= 1.5, 2, 3, or 6) path overhead** remains with the VTN until its asynchronous signal is extracted.

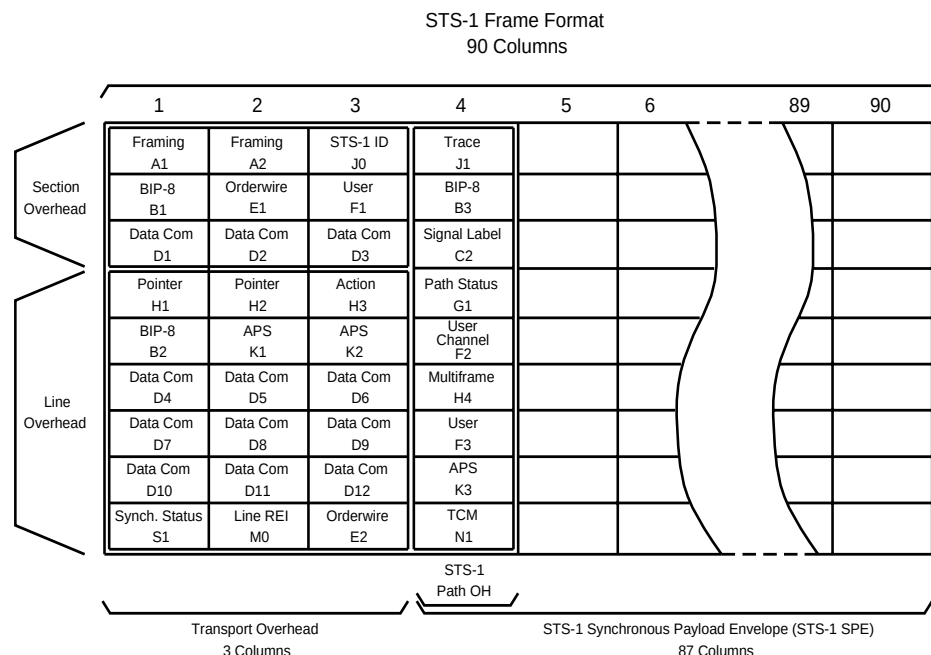


**Figure A-3. SONET Frame Format**

## **SONET Frame Structure**

---

The following pages provide more detailed information on the function of various overhead bytes for each SONET layer.

### **Section Overhead**

---

- Framing (A1, A2)
  - Provides framing for each STS-1.
- STS-1 ID (J0)
  - Provides the order of appearance in a byte-interleaved STS-*N* frame; for example, STS-1 #1, STS-1 #2.....STS-1 #48. In future applications, this byte will provide a section trace function. For information on STS-*N* signals, see the "Higher Rate Transport" part of this section.
- Section Bit-Interleaved Parity (BIP-8) (B1)
  - Provides SECTION performance monitoring and is calculated over all bits of the previous STS-*N* frame. Defined only for STS-1 #1 of an STS-*N* signal.
- Section Order Wire (E1)
  - Provides a local order wire for voice communication channel between section terminating network elements, such as repeaters. Defined only for STS-1 #1 of an STS-*N* signal.
- Section User Channel (F1)
  - Set aside for the user's purpose. Defined only for STS-1 #1 of an STS-*N* signal.
- Section Data Communications Channel (D1, D2, D3)
  - Is a 192 kb/s message-based channel. Used for alarms, maintenance, control, monitoring, and other communication needs between section terminating equipment. Defined only for STS-1 #1 of an STS-*N* signal.

## **Line Overhead**

---

- Line Pointer (H1, H2)
  - Two bytes indicate the offset in bytes between the pointer action byte (H3) and the first byte (J1) of the STS-1 synchronous payload envelope (SPE).
- Pointer Action (H3)
  - One byte is allocated for frequency justification.
- Line Bit-Interleaved Parity (BIP-8) (B2)
  - This byte is for line performance monitoring. This byte is provided in all STS-1 signals within an STS-*N* signal.
- Line Automatic Protection Switching (APS) (K1, K2)
  - Two bytes used for APS signaling between line level entities. In addition, bits 6, 7, and 8 of K2 are used for line alarm indication signal (AIS) and line far-end receive failure (FERF). Defined only for STS-1 #1 of an STS-*N* signal.
- Line Data Communications Channel (D4 - D12)
  - Is a 576 kb/s message-based channel.
- Synch. Status (S1)
  - In STS-1 #1, the S1 byte is for synchronization status messages, and only bits 5 through 8 are used.
- Line REI (M0)
  - The M0 byte is for STS-1 line far-end block error (FEBE), and only bits 5 through 8 are used.
- Line Orderwire (E2). Defined only for STS-1 #1 of an STS-*N* signal.
  - One byte is allocated to be used as an express orderwire between line terminating equipment.

## **Path Overhead**

---

There are two types of path overheads:

- STS-1 path overhead
- VT path overhead.

### **STS-1 Path Overhead**

The STS-1 path overhead is assigned to and remains with the STS-1 SPE until the payload is extracted and is used for functions that are necessary to transport all synchronous payload envelopes.

- STS-1 Path Trace (J1)
  - Repetitively transmits a 64 byte, fixed length, string so that an STS-1 path receiving terminal can verify its continued connection to the intended transmitter.
- STS-1 Path Bit-Interleaved Parity (BIP-8) (B3)
  - Provides each STS-1 path performance monitoring. This byte is calculated over all bits of the previous STS-1 SPE before scrambling.
- STS-1 Path Signal Labels (C2)
  - Indicates the construction of the STS-1 SPE. A value of 00000000 indicates an unequipped STS-1 SPE. Values for various payload mappings are defined in TR-NWT-000253, Issue 2.
- STS-1 Path Status (G1)
  - Conveys the STS-1 path terminating status, far end block errors (FEBE), and yellow alarm signal conditions back to an originating STS-1 path terminating equipment.
- STS-1 Path User Channel (F2, F3)
  - User communication channel between Path elements.
- VT Multiframe Indicator (H4)
  - Provides a general multiframe indicator for VT-structured payloads.
- STS-1 Path Automatic Protection Switching (K3)
  - Path Automatic Protection Switching
- TCM - Tandem Connection Maintenance (N1)
  - Bits 1-4 used for incoming error monitoring. Bits 5-8 used as communications channel.



## VT Path Overhead

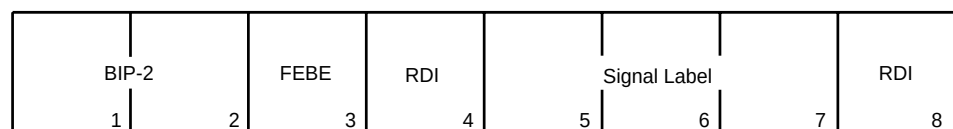
There is one byte of VT path overhead called V5. It occurs on every fourth frame; that is, 2000 times a second.

This byte provides for VT paths the same functions that B3, C2, and G1 provide for STS paths, namely:

- Error checking
- Signal label
- Path status.

The bit assignments of the VT path overhead are specified in the following list and are illustrated in Figure A-4:

- Bits 1 and 2 are used for error performance monitoring (BIP-2).
- Bit 3 is a VT path far-end-block-error (FEBE) indication that is sent back toward an originating VT PTE when errors are detected by the BIP-2.
- Bit 4 and Bit 8 are used for remote defect indication (RDI)
- Bits 5 through 7 provide a VT signal label.



VT Path Signal Label Coding:

|       |                      |
|-------|----------------------|
| 0 0 0 | Unequipped           |
| 0 0 1 | Equipped-Nonspecific |

---

**Figure A-4. VT Path Overhead Byte**

## **SONET Multiplexing Procedure**

---

SONET has provisions for multiplexing asynchronous DS1s, synchronous DS1s, and asynchronous DS3s. Refer to Figure A-5 and Figure A-6.

The first stage in multiplexing is mapping the input DS1 or DS3 tributary. In the case of DS1 inputs, three time slots (DS0s) are added to the incoming signal thus becoming a VT1.5. An asynchronous DS1 that fully meets the specified rate is mapped into the VT1.5 SPE as clear channel input since no framing is needed.

- Each VT1.5 carries a single DS1 payload.
- Four VT1.5s are bundled into a VT group (VT-G).
- Seven VT-Gs are byte-interleaved into an STS-1 frame.

The VT-G to-STS-1 multiplex is a simple byte-interleaving process, so individual VT signals are easily observable within the STS-1. Thus, cross-connections and add/drop can be accomplished without the back-to-back multiplexing/multiplexing steps required by asynchronous signal formats. The structured VTs are now multiplexed into the STS-1 SPE, and the path, line, and section overhead are added. The final multiplexing, as shown in Figure A-5, provides the scrambled STS-*N* signal to the optical conversion stage.

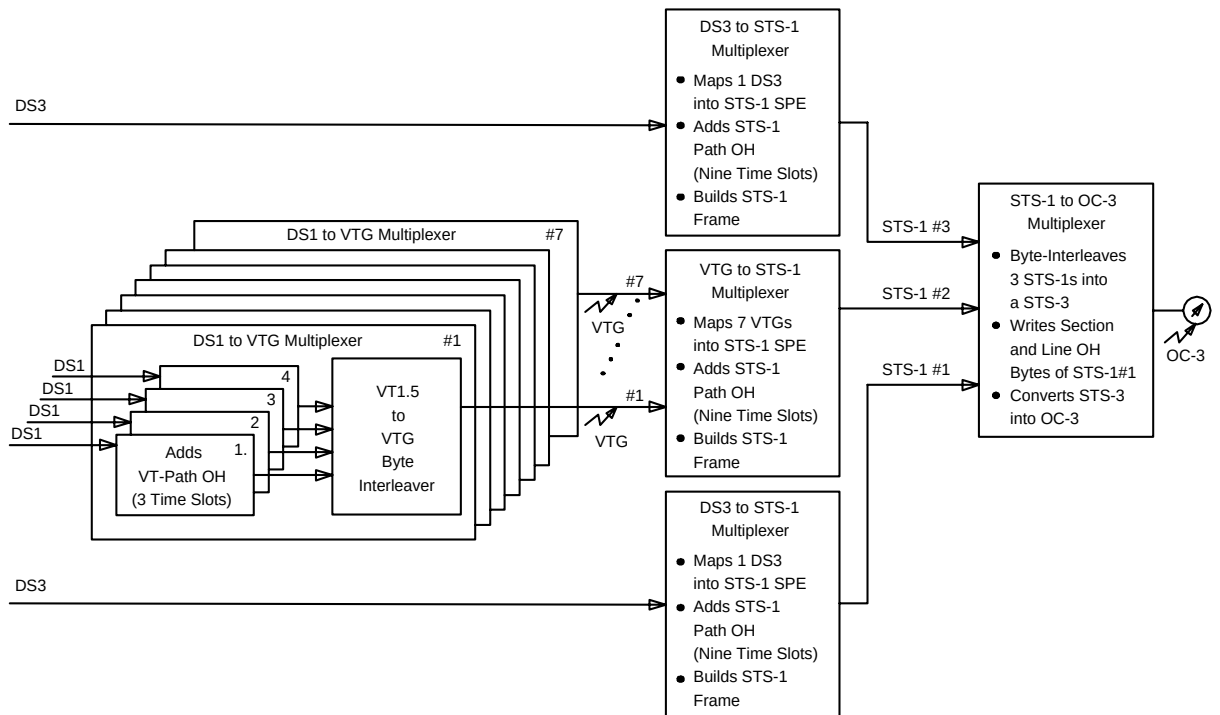


**Figure A-5. SONET Multiplexing Procedure**

## SONET Demultiplexing Procedure

As shown in Figure A-6, demultiplexing is the inverse of multiplexing. The unscrambled STS-1 signal from the optical conversion stages is processed to extract the section and line overhead and accurately locate the SPE. The next stage processes the path overhead and demultiplexes the VTs. A standard DS3 signal will be provided to the asynchronous network after path overhead processing. For DS1 signals, the individual DS1 VTs are then processed to extract VT overhead and, via the VT pointer, accurately locate the DS1 SPE. Finally, desynchronization of the DS1 SPE provides a standard DS1 signal to the asynchronous network.

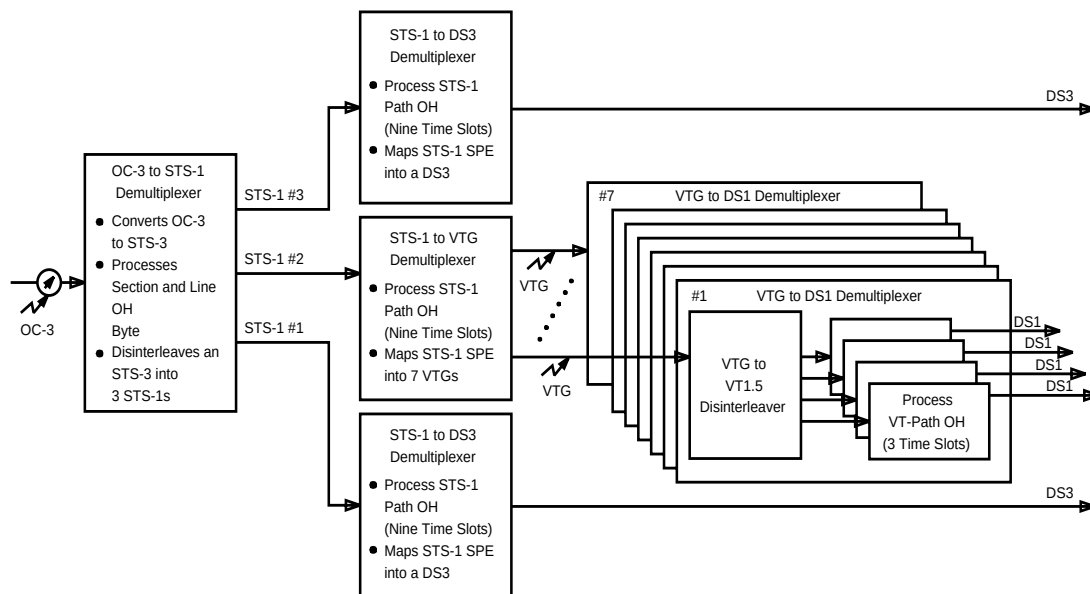


**Figure A-6. SONET Demultiplexing Procedure**

Two key points should be noted at this time. First, the SONET frame is a fixed time (125  $\mu$ s) and no bit-stuffing is used. Second, as shown in Figure A-7, the synchronous payload envelope can *float* within the frame using byte-stuffing. This is to permit compensation for small variations in frequency between the clocks of the two systems that may occur if the systems are independently timed (plesiochronous timing). The SPE can also drift across the 125- $\mu$ s frame boundary. SONET STS pointers are used to locate the SPE relative to the transport overhead.

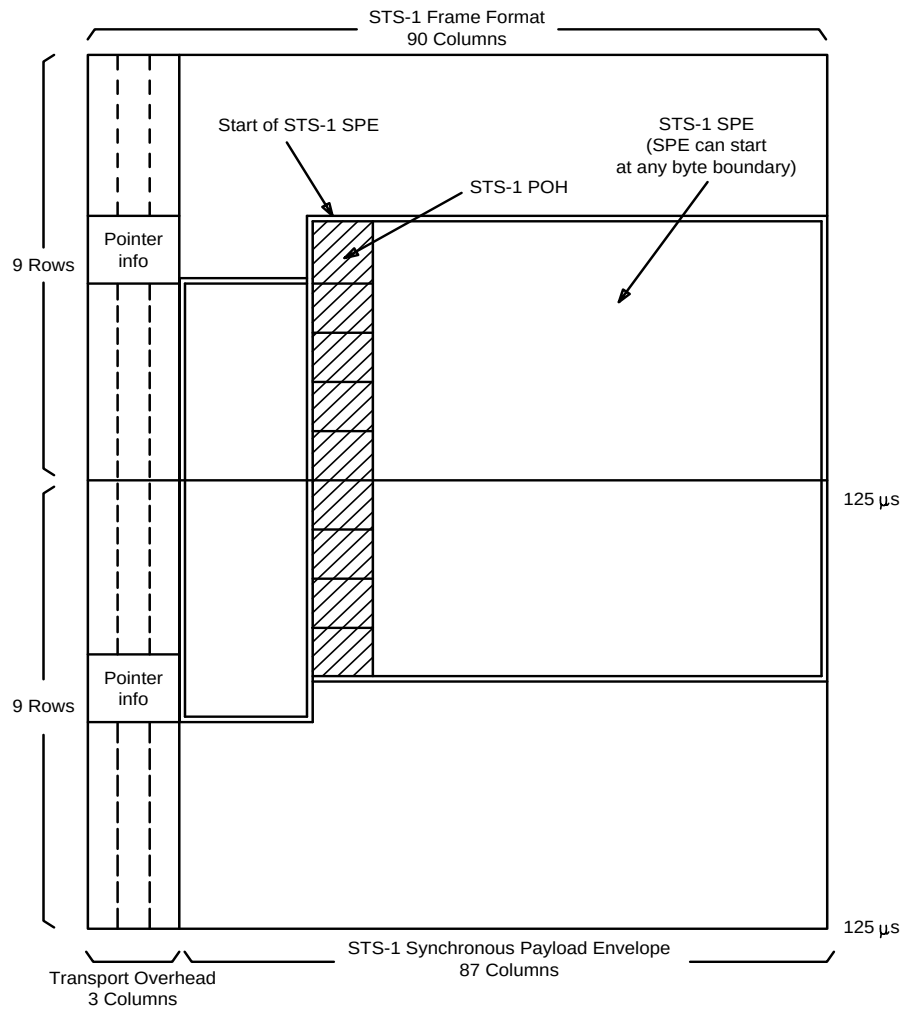


**Figure A-7. STS-1 Synchronous Payload Envelope in Interior of STS-1 Frame**

## SONET Digital Multiplexing Schemes

---

### Asynchronous Multiplexing

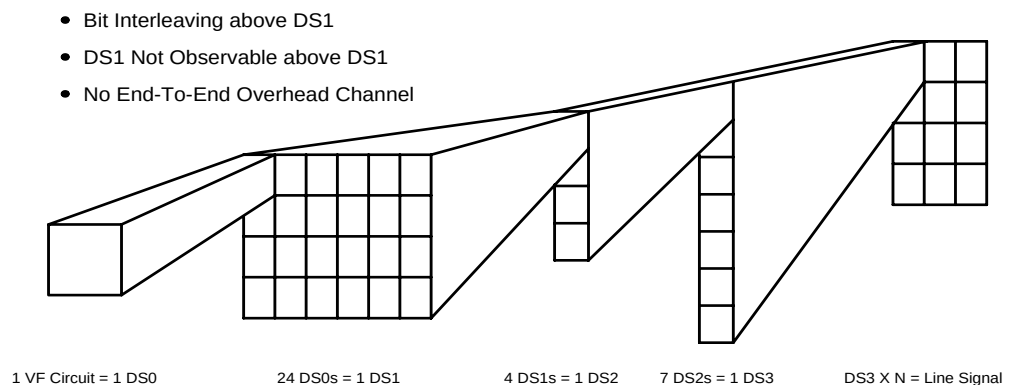
Currently, fiber optic facilities are primarily used to carry DS3 signals. The DS3 signal consists of a combination of the following payload signals:

- 28 DS1s
- 14 DS1Cs
- 7 DS2s.

Typically, 28 DS1 signals are multiplexed into a DS3 signal, using an M13 format. Refer to Figure A-8. M13 format is a process that includes bit-interleaving four DS1 into a DS2 signal and then bit-interleaving seven DS2 signals into a DS3. The DS3 rate is not a direct multiple of the DS1 or the DS2 rates due to the bit-stuffing synchronization technique used in asynchronous multiplexing.

Identification of DS0s contained in any DS-*N* signal, except DS1, is complex and DS0s cannot be directly extracted. Thus, an asynchronous DS3 signal must be demultiplexed down to the DS1 level to access and cross-connect DS0 and DS1 signals.

Another disadvantage of the M13 format is there is no end-to-end overhead channel for use by OAM&P groups.



---

**Figure A-8. Asynchronous Multiplexing**

## Synchronous Multiplexing

SONET's method of *byte-interleaving* DS1s to a higher signal rate permits economical extraction of a single DS1 without the need to demultiplex the entire STS-1 SPE. In addition, SONET provides overhead channels for use by OAM&P groups.

In SONET, a single asynchronous DS3 signal is mapped into an STS-1 SPE (Figure A-9).

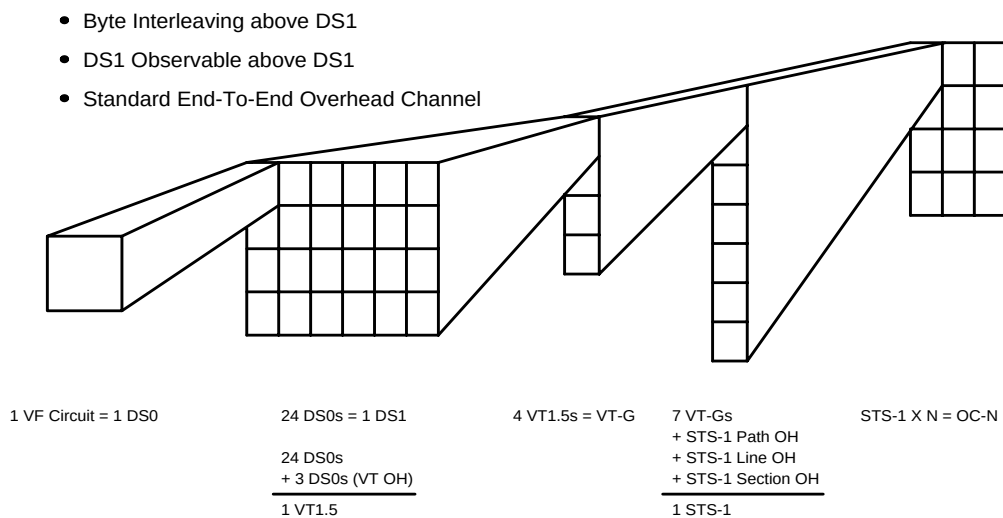


**Figure A-9. Synchronous Multiplexing**

## Virtual Tributary Signals

Sub-DS3 asynchronous signals (DS1, DS1C, DS2 and E1) are *byte-interleaved* into a digital signal called a virtual tributary (VT). The VT is a structure designed for the transport and switching of sub-DS3 payloads. Like the STS-1 signal, the VT signal has a floating pointer that allows each VT SPE to move within the VT structure. There are four sizes of virtual tributaries (VT1.5, VT2, VT3, VT6). Higher rate payloads are transported as one or more concatenated STS-1 signals.



## Concatenated Mode

For services requiring multiples of the STS-1 rate, STS-1 path payloads may be shared to create a single broadband payload called a concatenated STS-*N<sub>c</sub>* (OC-*N<sub>c</sub>*). STS-1 signals are mapped into an STS-*N<sub>c</sub>* SPE and transported as a concatenated STS-*N<sub>c</sub>* signal. This STS-*N<sub>c</sub>* signal can be carried by an STS-*N* or OC-*N* (or higher level) line signal.

The STS-*N* signal is multiplexed, switched, and transported through the network as a single entity. A concatenation indicator, used to show that the STS-1s of the STS-*N<sub>c</sub>* signal are linked together, is contained in the STS-1 payload pointer of all but the first STS-1. The line and section overhead is sent on the first STS-1 and the payload pointer for the first STS-1 is applied to all STS-1 signals in the concatenated signal.

Figure A-10 shows an example of an STS-3c SPE. It consists of 3 x 87 columns and 9 rows of bytes. The order of transmission is row by row, from left to right.

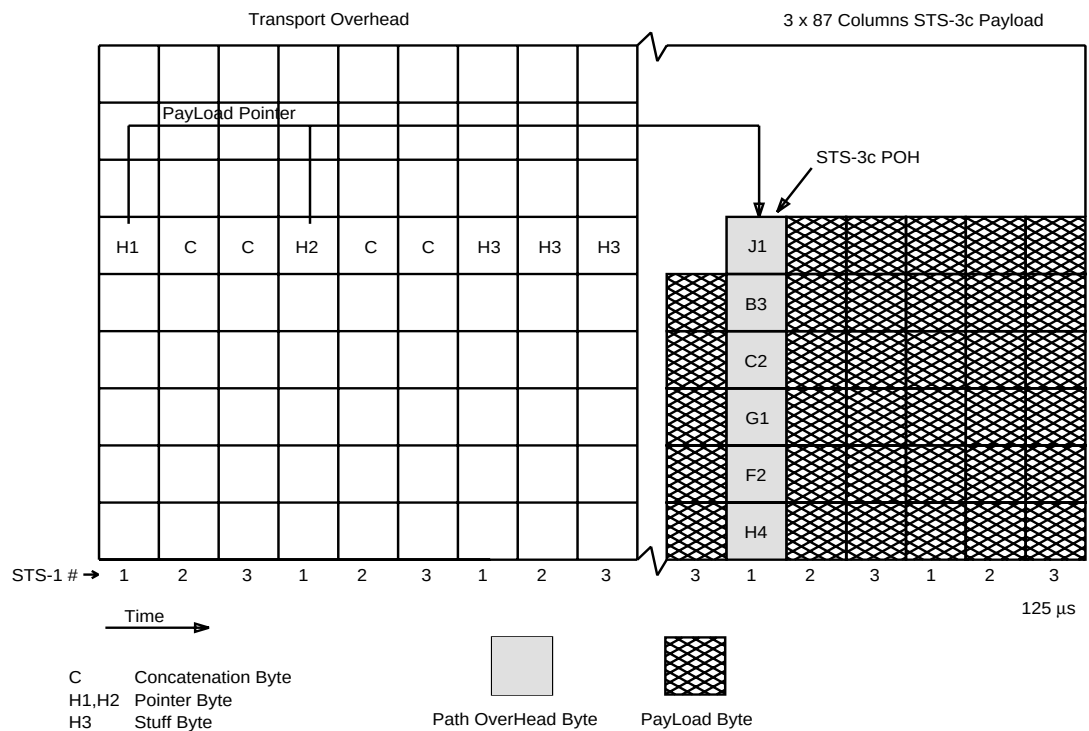


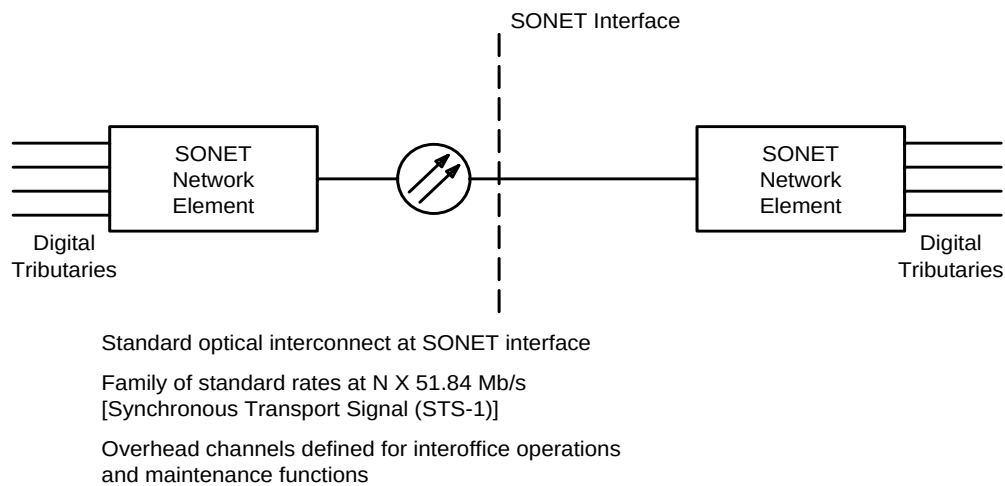
**Figure A-10. STS-3c Concatenated Payload**

## SONET Interface

---

The SONET interface (Figure A-11) provides the optical midspan meet between SONET network elements. A SONET network element is the hardware and software that processes one or more layers of the SONET signal.

---



---

**Figure A-11. SONET Interface**

## SONET Payloads

---

Table A-1 shows the digital signals that can be transported as SONET payloads.

**Table A-1. SONET Payloads**

| Input Tributary           | Equivalent Channels | Rate                  | SONET Signal | Rate        |
|---------------------------|---------------------|-----------------------|--------------|-------------|
| DS1                       | 24 DS0s             | 1.544 Mb/s            | VT1.5        | 1.728 Mb/s  |
| E1 (CEPT)                 | 32 DS0s             | 2.048 Mb/s            | VT2          | 2.304 Mb/s  |
| DS1C                      | 48 DS0s             | 3.152 Mb/s            | VT3          | 3.456 Mb/s  |
| DS2                       | 96 DS0s             | 6.312 Mb/s            | VT6          | 6.912 Mb/s  |
| DS3                       | 672 DS0s            | 44.736 Mb/s           | STS-1        | 51.840 Mb/s |
| DS4NA                     | 2016 DS0s           | 139.624 Mb/s          | STS-3c       | 150.336Mb/s |
| ATM                       |                     | 149.76 Mb/s           | STS-3c       | 150.336Mb/s |
| FDDI                      |                     | 125.00 Mb/s           | STS-3c       | 150.336Mb/s |
| Future payloads           |                     | up to 150 Mb/s        |              |             |
| Future broadband payloads |                     | Greater than 150 Mb/s |              |             |

DS1 and DS3 signals are the most important of these signals in the current network. Broadband payloads, such as asynchronous transfer mode (ATM) and fiber distributed data interface (FDDI), with rates of 150 Mb/s and higher, are also important. Other payloads may be defined for specific applications.

## Higher Rate Transport

---

Higher rate SONET signals are created by byte-interleaving  $N$  STS-1 to form an STS- $N$  signal. The STS- $N$  is then scrambled and converted to an optical carrier - level  $N$  (OC- $N$ ) signal. The OC- $N$  has a line rate of exactly  $N$  times the OC-1 signal (see Table A-2).

**Table A-2. SONET Transport Rates**

| OC Level | Line Rate (Mb/s) | Capacity             |
|----------|------------------|----------------------|
| OC-1     | 51.84            | 28 DS1s or 1 DS3     |
| OC-3     | 155.52           | 84 DS1s or 3 DS3s    |
| OC-9     | 466.56           | 252 DS1s or 9 DS3s   |
| OC-12    | 622.08           | 336 DS1s or 12 DS3s  |
| OC-18    | 933.122          | 504 DS1s or 18 DS3s  |
| OC-24    | 1244.16          | 672 DS1s or 24 DS3s  |
| OC-36    | 1866.24          | 1008 DS1s or 36 DS3s |
| OC-48    | 2488.32          | 1344 DS1s or 48 DS3s |

## Conclusion

---

The intent of this section is to present a short overview of SONET. More detailed expositions can be found in various literature. An excellent description of SONET can be found in Reference 3.

### **REFERENCES**

1. ANSI T1.106-1988 •American National Standard for Telecommunications - Digital Hierarchy Optical Interface Specifications, Single Mode,• and ANSI T1.105-1991 •American National Standard for Telecommunications - Digital Hierarchy Optical Rates and Formats Specification. •
2. CCITT Recommendations G.707, G.708, G.709.
3. R. Ballert and Y. C. Ching, •SONET: Now It's the Standard Optical Network,• *IEEE Communications Magazine*, Vol. 27, No. 3 (March 1989): 8-15.
4. ANSI T1.102-1993 Draft •American National Standard for Telecommunications - Digital Hierarchy Electrical Interface Specifications. •



---

# Glossary

---

## Numerics

### **0x1 Line Operation**

A 0x1 protection system has one service line and no protection line.

### **1+1 Line Protection**

A 1+1 protection system has two bidirectional lines. The transmitting terminal transmits the same signal on two lines. The receiving terminal monitors two lines independently and chooses one line as the active line and the other line as the standby line. When a protection switch occurs, the receiving terminal selects the signal from the standby line which causes the standby line to be the new active line. The original active line becomes the standby line. This status remains the same (nonrevertive) after the fault clears.

### **1x1 Line Protection**

A 1x1 protection system has two OC-48 high speed lines. One line is designated as the service line, and the other line is designated as the protection line. The service line normally carries traffic. When a protection switch occurs, the protection line is selected to carry traffic. When the fault clears, the original service line is selected (revertive). Also see Revertive Switching.

### **1xM Electrical Low Speed Interface Protection**

1xM electrical low speed interface protection refers to a system with one protection electrical low speed interface circuit pack (DS3 or STS1E) and up to 16 service electrical low speed interface circuit packs (DS3 or STS1E). When a low speed protection switch occurs, the electrical low speed signals are routed from the service low speed interface circuit pack to the protection low speed interface circuit pack. When the fault clears, the low speed signals are routed back to the service low speed interface circuit pack (revertive). Also see Revertive Switching.

---

## A

### **ABN**

Abnormal — An LED on the user panel that is lighted when a temporary condition potentially affecting transmission exists; for example, a CIT-initiated protection switch or lockout, or loopback.

### **ACO**

Alarm Cutoff — A pushbutton switch on the user panel that can be used to retire the active audible office alarm. If another alarmable condition occurs while the ACO is active, the highest level audible alarm is activated. The alarm cutoff function is also available from the CIT and from an operations system.

### **Active**

Active refers to a 1+1 protected OC-3 line that is currently selected by the receiving terminal at either end as the payload carrying signal. (Also refer to Standby.)

### **ADM**

Add/Drop Multiplexer

**Add/Drop-Rings Terminal Platform**

The SONET standards that are implemented in the FT-2000 OC-48 Lightwave System. More specifically, it is where one or more low speed shelves are interconnected to either one Enhanced high speed shelf (2-fiber ring) or two Enhanced high speed shelves (4-fiber ring). And, where any slot on the low speed shelf may be provisioned to have its traffic added/dropped from either the East or West OC-48 high speed links.

**AGNE**

Alarm Gateway Network Element — A network element that receives remote network element status information from other network elements in an alarm group and rebroadcasts the information to all network elements in the alarm group. Using AGNEs makes it unnecessary for each network element in the alarm group to communicate directly with all other network elements in the alarm group.

**AID**

Access Identifier — A unique identifier used to address circuit pack slots, ports, and tributaries.

**AIS**

Alarm Indication Signal — A maintenance signal transmitted downstream in a digital network that shows that an upstream failure has been detected and alarmed by some upstream equipment, if the upstream alarm has not been suppressed.

**Alarm**

A contact closure to the office alarm grid.

**Alarm Group**

A group of network elements that share remote alarm information through an alarm gateway network element (AGNE). Also refer to AGNE. The maximum size of an alarm group is 32 network elements.

**AMI**

Alternate Mark Inversion — A line code that employs a ternary signal to convey binary digits, in which successive binary ones are represented by signal elements that are normally of alternating, positive, and negative polarity but equal in amplitude, and in which binary zeros are represented by signal elements that have zero amplitude.

**ANSI**

American National Standards Institute — An organization consisting of producer, consumer, and general interest groups that establishes the procedures by which accredited organizations create and maintain voluntary industry standards in the United States.

**APD**

Avalanche Photodiode — A highly sensitive optical detector.

**APS**

Automatic Protection Switch — a protection switch that occurs in response to an automatically detected trouble condition.

**AS&C**

Alarm, Status, and Control.

**ASCII**

American Standard Code for Information Interchange — A standard 8-bit code used for exchanging information among data processing systems and associated equipment.

**ASN.1**

Abstract Syntax Notation 1.



**Asynchronous**

Refers to network elements that are not timed from references traceable to a single timing source.

**ATM**

Asynchronous Transfer Mode — A new technology based on a simple, fixed-length packet (or cell) format that allows integrated networking of voice, data, and video traffic. The packet length is set at 53 bytes. The first 5 bytes are reserved for address and control fields, and the remaining 48 bytes carry data.

**AUTO**

Automatic — One possible state of a low speed port or slot. When a port is in the automatic state and the presence of a good signal is detected, the port is automatically placed in the IS (in service) state. When a slot is in the automatic state and the presence of a circuit pack is detected, the slot is automatically placed in the EQ (equipped) state.

**Autonomous Indicators**

Autonomous indicators are those indicators that are subject to incoming signal alarm delay. These include office alarm outputs, parallel telemetry outputs, TL1 autonomous messages, the alarm LEDs (CR, MJ, and MN) on the user panel, and the network element status field in the upper left-hand corner of the CIT.

---

**B**

**B3ZS**

Bipolar 3-Zero Substitution — A line coding method that replaces a string of three zeros with a sequence of symbols having some special characteristic.

**B8ZS**

Bipolar 8-Zero Substitution — A line coding method that replaces a string of eight zeros with a sequence of symbols having some special characteristic.

**BCLAN**

Board Controller Local Area Network — The internal local area network that provides communications between the Line Controller circuit pack (terminals only) and board controllers on the circuit packs associated with an OC-48 high speed line. In the ^FT-2000 OC-48 Repeater Bay, the BCLAN provides communications between the System Controller circuit pack and board controllers on the circuit packs.

**BDFB**

Battery Distribution and Fuse Bay.

**BER**

Bit Error Rate — The ratio of bits received in error to bits sent.

**BIP**

Bit Interleaved Parity — A method of error monitoring over a specified number of bits (BIP-3 or BIP-8).

**BIP-N**

Bit Interleaved Parity - N — A method of error monitoring. With even parity, an N-bit code is generated by the transmitting equipment over a specified portion of the signal in such a way that the first bit of the code provides even parity over the first bit of all N-bit sequences in the covered portion of the signal. The second bit provides even parity over the second bits of all the N-bit

sequences within the specified portion, etc. Even parity is generated by setting the BIP-N bits so that there are an even number of ones in each of all the N-bit sequences including the BIP-N.

**BITS**

Building Integrated Timing Supply — A single clock that provides all the DS1 and DS0 synchronization references required by clocks in a building.

**BOC**

Bell Operating Company.

**Broadband Communications**

Voice, data, and/or video communications at rates greater than DS1 rates (1.544 Mb/s).

**Broadband Service Transport**

STS-3c and/or STS-12c transport over FT-2000 OC-48 links for ATM applications.

**BRT**

Business Remote Terminal.

---

**C**

**CC w/AIS**

Clear Channel with Alarm Indication Signal Insertion — A provisionable mode for the DS3 output in which the outgoing DS3 signals are passed through without any violation monitoring. If an incoming loss of signal is detected, a DS3 AIS is inserted.

**CC w/oAIS**

Clear Channel without Alarm Indication Signal Insertion — A provisionable mode for the DS3 output in which the outgoing DS3 signals are passed through without any violation monitoring. If an incoming loss of signal is detected, no DS3 AIS is inserted.

**CEV**

Controlled Environment Vault.

**CIT**

Craft Interface Terminal — A personal computer that meets FT-2000 minimum requirements loaded with the FT-2000 CIT-PC software or SNC-2000 CPro software (Release 5 and later). In Release 5 and later releases, an ASCII-based (dumb) terminal that meets FT-2000 minimum requirements can also be used as a craft interface terminal.

**CCITT**

International Telephone and Telegraph Consultative Committee — An international advisory committee under United Nations sponsorship that has composed and recommended for adoption worldwide standards for international communications. Recently changed to the International Telecommunications Union (ITU) Telecommunication Standardization Sector (TSS).

**CO**

Central Office — A telephone company building where switching and/or transmission system equipment is located.

**Concatenated Switching**

Concatenated switching provides protection for simultaneous failures in adjacent sections of the same service line or different service lines. In concatenated switching the protection line is divided into sections. Each section of the protection line can be used to protect any service line in the section. Consequently, simultaneous failures in more than one section can be protected.

**Condition**

Conditions persist in time and indicate that there is something not normal about the system. Conditions appear in the Retrieve-Alarm Report.

**CR**

Critical (alarm).

**Cross-Connection**

A reconfigurable transmission path interconnection.

**Cross Connection Map**

The cross-connection map for each FT-2000 ring node contains information about how signals are connected between the high speed time slots and low speed interface packs. (See also Squelch Map).

**CTS**

Customer Technical Support — A Lucent Technologies organization that helps customers maintain installed systems (formerly known as the Technical Support Organization).

**Current Value**

The value currently assigned to a provisionable parameter.

**CV**

Cting Violation — A performance-monitoring parameter. The coding violations are monitored for the OC-48 line, OC-3 line, STS-48 section, STS-3 section, STS-1 path, EC-1 line, and DS3 path.

---

**D**

**DACS**

Digital Access and Cross-Connect System.

**DACS III-2000**

One of Lucent Technologies SONET-compatible Digital Access and Cross-Connect Systems.

**DACS IV-2000**

One of Lucent Technologies SONET-compatible Digital Access and Cross-Connect Systems.

**Data**

A collection of system parameters and their associated values.

**DCC**

Data Communications Channel — The embedded overhead communications channel in the SONET line. This is used for end-to-end communications and maintenance. It carries alarm, control, and status information between network elements in a SONET network.

**DCC Link**

Data Communications Channel Link — A bidirectional point-to-point fiber connection at the OC-3 (Release 6 or later) or OC-48 rate that has the DCC bytes terminated at both ends.

**DCE**

Data Circuit Equipment — The equipment that provides the signal conversion and coding between the data terminating equipment and the line. The DCE may be separate equipment or a part of the data terminating equipment.

**DCMS**

Design Change Management System — The Design Change Management System is used to issue product change notifications (PCNs) to customers.

**DDM-2000**

Lucent Technologies SONET-ready network multiplexer that can function as a lightwave terminal. It is designed primarily for loop feeder and interoffice applications that will work in existing asynchronous as well as the emerging SONET networks.

**DDM-FiberReach**

The DDM-2000 FiberReach Multiplexer is an OC-1 low density access product offering DS0 and DS1 services for business carrier access, fiber-in-the-loop, or private network applications. DDM-2000 FiberReach is hosted by the DDM-2000 OC-3 or OC-12 Multiplexer or the SLC-2000 Access System with a single- or dual-homed ring or star topology.

**DWDM**

Dense Wave Division Multiplexing — Transmitting multiple OC-48 optical signals (simultaneously) over a single fiber. The DWDM signal is interface compatible with the OLS product.

**Demultiplexing**

A process applied to a multiplexed signal for recovering signals combined within it and for restoring the distinct individual channels of these signals.

**Destination Node**

The destination node is the node in a ring where the low speed signals on an STS-3 tributary terminate. If an STS-3 tributary is bridged at one or more nodes in a ring, the last node where the low speed signals are terminated is the destination node. The destination node and source node of an STS-3 tributary are always in the same ring (also see Source Node).

**DFB**

Distributed Feedback.

**DM**

Degraded Minutes — A performance-monitoring parameter.

**DMA**

Direct Memory Access — A data transfer technique that uses a control circuit to move data between a device and memory.

**DPLL**

Digital Phase-Locked Loop.

**DRAM**

Dynamic Random Access Memory — Semiconductor random-access memory that requires short interval refreshing to retain its contents.

**DS3**

Digital Signal Level 3 (44.736 M/bs).

**DS3 Interface Circuit Pack**

The FT-2000 OC-48 Lightwave System DS3 Interface circuit pack interfaces with up to three bidirectional DS3 signals.

**DS-NE**

Directory Service Network Element — The single reference point in a ring network. The Directory Service Network Element stores the IDs of the other network elements in the ring and updates the other network elements after configuration changes.

**DSX**

Digital Cross-Connect Panel — A panel designed to interconnect equipment that operates at a designated rate. For example, a DSX-3 interconnects equipment operating at the DS3 rate.

**DTE**

Data Terminating Equipment — The equipment that originates data for transmission and accepts transmitted data.

**Dual Ring Interworking**

Dual Ring Interworking (DRI) is a configuration of two ring networks that share two common nodes. DRI allows a circuit with one termination in one ring and one termination in another ring to survive a loss-of-signal failure of the shared node that is currently carrying service for the circuit.

---

**E**

**EC, EC-n**

Electrical Carrier — The basic logical building block signal with a rate of 51.840 Mb/s for an EC-1 signal and a rate of n times 51.840 Mb/s for an EC-n signal.

**EC-1**

An STS-1 signal that has been shaped and encoded for transmission over electrical media. This refers to the actual physical representation of an electrical signal with an EC-1 format at a manual or electronic cross-connect interface. This signal is typically carried by coaxial cables from one equipment location to another. The term EC-1 not only refers to the organization and data rate of a signal but also to the voltage template the signal must conform to and the impedances for which the voltage template is valid.

**ECI**

Equipment Catalog Item

**EEPROM**

Electrically Erasable Programmable Read-Only Memory — Readable memory that is nonvolatile in nature, erased electrically, and programmed externally from the processor that uses it.

**EIA**

Electronic Industries Association — A trade association of the electronics industry that establishes electrical and functional standards.

**Electrical Line Buildout**

An equalizer network between the FT-2000 OC-48 Add/Drop-Rings Terminal and the DSX or STSX1 panel (or equivalent). It guarantees the proper signal level and shape at the DSX panel (or equivalent).

**EMC**

Electromagnetic Compatibility.

**EMI**

Electromagnetic Interference — High-energy, electrically induced magnetic fields that cause data corruption in cables passing through the fields.

**EPROM**

Erasable Programmable Read-Only Memory — Readable memory that is nonvolatile in nature, erased by exposure to intense ultraviolet light, and programmed externally from the processor that uses it.

**EQ**

Equipped — A memory administrative state for slots. EQ refers to a slot that is fully monitored and alarmed.

**Equal Level Cross-Connect**

A cross-connect panel where the signal strength (power) is the same for all the inputs and outputs so that any output can be connected to any input. The DSX-3 is an example of an equal level cross-connect panel.

**ES**

Errored Seconds — A second in which one or more coding violations are detected. Errored seconds are monitored for the OC-48 line, OC-3 line, STS-48 section, STS-3 section, STS-1 path, EC-1 line, and the DS3 path/line.

**ESD**

Electrostatic Discharge — The discharge of static electricity into equipment that potentially causes component damage and logic errors.

**ESF**

Extended Super Frame — A DS1 format option.

**Event**

Events happen at a particular time (do not persist in time) and appear in the Retrieve-History Report.

**Executable Code**

The "program" that controls the operation of the system.

**Externally Timed**

An operating condition of a network element in which it is locked to an external reference. (Also referred to as Phase Locked in releases prior to Release 5.)

**Extra Traffic**

Extra traffic is a capability of the FT-2000 OC-48 Add/Drop-Rings terminal that allows access to the protection STS-3 tributaries and STS-1 tributaries (Release 7 and later) of an OC-48 high speed line for low priority traffic or for restoration purposes.

---

## F

### **FDA**

Food and Drug Administration.

### **FDDI**

Fiber Distributed Data Interface.

### **FE ACTY**

Far-End Activity — An LED on the user panel that is lighted when an alarm or status condition exists at a remote FT-2000 OC-48 Repeater Bay or FT-2000 OC-48 Add/Drop-Rings Terminal.

### **FEBE**

Far-End-Block Error — An indication returned to the transmitting terminal that an errored block has been detected at the receiving terminal. A block is a specified grouping of bits.

### **FERF**

Far-End-Receive Failure — An indication returned to a transmitting terminal that the receiving terminal has detected an incoming failure. Also refer to the line remote defect indicator (RDI-L) signal.

### **FIT**

Failures in Time — Circuit pack failure rates per 10<sup>4</sup> hours as calculated using the method described in TR-TSY-000332, Issue 3, "*Reliability Prediction Procedure for Electronic Equipment (RPP)*".

### **Flash EPROM**

A new technology that combines the nonvolatility of EPROM with the in-circuit reprogrammability of EEPROM (electrically-erasable PROM).

### **FLS**

Frame Loss Seconds.

### **Folded Rings**

Folded (collapsed) rings are ring networks without fiber diversity. The terminology derives from the image of folding a ring into a linear segment.

### **Free Running**

An operating condition of a network element in which its local oscillator is not locked to any synchronization reference and is using no storage techniques to sustain its accuracy.

### **FT-LBA**

FT-Lightwave Booster Amplifier

### **FT-RDS**

FT-Ring Diversity Switch

---

## G

### **Gb/s**

Gigabits (10<sup>9</sup> bits) per Second.

**GCNS-2000**

Lucent Technologies' Gigabit Cell Network Switch.

**GNE**

Gateway Network Element — A network element that serves as a single interface to the local X.25 message-based operations system for all the FT-2000 OC-48 Add/Drop-Rings Terminals in a ring network or all the network elements in a SONET subnetwork (for example, DDM-2000 Multiplexer).

**Grooming**

The rearrangement of STS-1 tributaries within an STS-3 tributary. DS3 and EC-1 signals may be rearranged within an STS-3 tributary using time slot assignment with STS-1 granularity.

---

## H

**Hardware Ready**

The shelf, connector, and backplane have been designed to accept hardware (circuit packs) that is not available yet. Additional cables may be required.

**Headend**

Refers to the transmitting end where a signal originates.

**Holdover**

An operating condition of a network element in which its local oscillator is not locked to any synchronization reference but is using storage techniques to maintain its accuracy with respect to the last known frequency comparison with a synchronization reference.

---

## I

**IMTV**

Interactive Multimedia Television.

**I/O**

Input/Output.

**IR**

Intermediate Reach.

**IS**

In Service — A memory administrative state for ports. IS refers to a port that is fully monitored and alarmed.

**IS-3 Interface Circuit Pack**

The FT-2000 OC-48 low-cost low speed (STS-3 rate) intraoffice optical interface.

**ITU**

International Telecommunications Union — An international advisory committee under United Nations sponsorship that has composed and recommended for adoption worldwide standards for international communications (also refer to CCITT).



---

## J

### **Jitter**

Timing jitter is defined as short-term variations of the significant instants of a digital signal from their ideal positions in time.

---

## L

### **LAN**

Local Area Network.

### **LBA**

Lightwave Booster Amplifier.

### **LBO**

Line Buildout — An equalizer network that guarantees the proper signal level. Also see electrical line buildout and optical line buildout.

### **LCLAN**

Line Controller Local Area Network — The internal local area network that provides communications between the System Controller circuit pack and the Line Controller circuit packs.

### **LED**

Light-Emitting Diode.

### **Line**

A bidirectional optical transmission line. In T1/Bellcore terminology, "line" refers to a transmission medium, together with the associated high speed equipment, required to provide the means of transporting information between two consecutive Network Elements, one of which originates the line signal and the other terminates the line signal.

### **LNCTL**

The FT-2000 OC-48 Lightwave System Line Controller circuit pack performs line specific computations.

### **LOF**

Loss of Frame — A failure to synchronize to an incoming signal.

### **LOHCTL**

The FT-2000 OC-48 Lightwave System Overhead Controller - LAN circuit pack provides user access to the LAN SONET overhead channels.

### **LOP**

Loss of Pointer — A failure to extract a good pointer from an STS-1 signal.

### **LOS**

Loss of Signal — The absence of an adequate incoming signal.

### **LSSW**

The FT-2000 OC-48 Lightwave System Low Speed Switch circuit pack performs low speed interface protection switching.

---

## M

### **μm**

Micrometer —  $10^{-6}$  meters

### **Menu**

A set of possible values for a parameter.

### **Midspan Meet**

The capability to interface between two lightwave terminals of different vendors. This applies to high speed optical interfaces.

### **MJ**

Major (alarm).

### **MML**

Human-Machine Language.

### **MN**

Minor (alarm).

### **Multiplexing**

The process of combining several distinct digital signals into a single composite digital signal.

---

## N

### **NCDRH**

National Center for Devices and Radiological Health.

### **NE**

Network Element — Refers to an FT-2000 OC-48 Repeater Bay or FT-2000 OC-48 Add/Drop-Rings Terminal in a network.

### **NE ACTY**

Near-End Activity — An LED on the user panel that is lighted when an alarm or status condition exists at the local FT-2000 OC-48 Repeater Bay or FT-2000 OC-48 Add/Drop-Rings Terminal.

### **NEBS**

Network Equipment-Building System.

### **NID**

Node Identifier — A unique number from 0 to 15 used to identify an FT-2000 OC-48 Add/Drop-Rings Terminal in a bidirectional ring application. In Release 6 and later releases, the NID is automatically provisioned.

### **nm**

Nanometer —  $10^{-9}$  meters.

### **NMA**

Network Monitoring and Analysis System — Bellcore's alarm surveillance operations system.

**NMON**

Not Monitored — A memory administrative state for ports. This refers to a port that is not monitored and will not transition to the IS state if a good signal is detected.

**Node ID**

Node Identifier — See NID.

**Nonautonomous Indicators**

The nonautonomous indicators are those indicators that are not subject to incoming signal alarm delay. These indicators include the circuit pack FAULT LEDs, all CIT reports, TBOS alarm and status points, TL1 command response messages, and the LEDs on the user panel (except CR, MJ, and MN).

**Non-Preemptible Protection Access**

Available on a per STS-3 tributary basis. This feature increases a nodes "service traffic" capacity at the expense of reducing bandwidth on the protection fiber. Tributaries that are used this way are themselves "non-protected" and are not preempted in the advent of a switch from the service fiber to the protection fiber.

**Nonrevertive Switching**

In nonrevertive switching, there is an active and standby line, circuit pack, etc. When a protection switch occurs, the standby line, circuit pack, etc., is selected causing the old standby line, circuit pack, etc., to become the new active line, circuit pack, etc. The original active line, circuit pack, etc., becomes the standby line, circuit pack, etc. This status remains in effect when the fault clears. Therefore, this protection scheme is "nonrevertive" in that there is no switch back to the original status in effect before the fault occurred.

**NRZ**

Nonreturn to Zero.

**NSA**

Non-Service Affecting.

**NSAP**

AddressNetwork Services Access Point Address — An automatically assigned number that uniquely identifies a Network Element for the purpose of routing DCC messages.

**Null Modem**

A null modem cable allows a CIT local access to the FT-2000 OC-48 Lightwave System via the DTE port on the interconnection panel of the Low Speed Shelf - System Controller or Repeater Shelf - System Controller.

---

**O**

**O&M**

Operation and Maintenance.

**OALAN**

Overhead Access Local Area Network — The internal local area network that provides communications between the System Controller circuit pack and the Overhead Controller circuit pack.

**OAM&P**

Operations, Administration, Maintenance, and Provisioning.

**OC, OC-n**

Optical Carrier — The optical signal that results from an optical conversion of an STS signal; that is, OC-1 from STS-1 and OC-n from STS-n.

**OC3 Optical Interface Circuit Pack**

The FT-2000 OC-48 Lightwave System OC3 Optical Interface circuit pack interfaces with one bidirectional OC-3 signal.

**OC12 Optical Interface Circuit Pack**

The FT-2000 OC-48 Lightwave System OC12 Optical Interface circuit pack interfaces with one bidirectional OC-12 signal.

**OC48 RCVR**

The FT-2000 OC-48 Lightwave System OC48 Receiver circuit pack interfaces with the incoming OC-48 high speed line (receive direction).

**OC48 REGENR**

The FT-2000 OC-48 Lightwave System OC48 Regenerator circuit pack regenerates one OC-48 signal.

**OC48 TRMTR**

The FT-2000 OC-48 Lightwave System OC48 Transmitter circuit pack interfaces with an outgoing OC-48 high speed line (transmit direction).

**OHCTL (TERM)**

The FT-2000 OC-48 Lightwave System Overhead Controller - Terminal circuit pack provides user access to the OC-48 SONET overhead channels.

**OLS**

OLS is a standalone optical multiplexing (up to 20 Gb/s per fiber) system. The OLS has its own telemetry, fault location, and performance monitoring capability.

**Operations Interface**

Any interface that provides information on the system behavior or control. These include the equipment LEDs, user panel, CIT, office alarms, and all telemetry interfaces.

**Operations Interworking**

The capability to access, operate, provision, and administer remote systems through CIT access from any site in a SONET network or from a centralized operations system.

**Optical Line Buildout**

An equalizer network between the FT-2000 OC-48 Add/Drop-Rings Terminal and the lightguide cross-connect panel (or equivalent). It guarantees the proper received signal level at OC48 RCVR circuit pack.

**Original Value**

The value given to a CIT provisionable parameter at the factory.

**Original Value Provisioning**

The original values are the values given to CIT provisionable parameters at the factory. At system startup, the original values are copied and become the current values. These current values can be changed using local or remote provisioning. The original values can never be changed.

**OS**

Operations System — A central computer-based system used to provide operations, administration, and maintenance functions.

**OSI**

Open Systems Interconnection — Referring to the OSI reference model, a logical structure for network operations standardized by the International Standards Organization (ISO).

**OTDR**

Optical Time Domain Reflectometer.

---

**P**

**Parameter**

A characteristic of the system that affects its operation.

**Path**

A path at a given rate is a logical connection between the point at which a standard frame format for a signal at the given rate is assembled and the point at which the standard frame format for the signal is disassembled.

**Path AIS**

Path Alarm Indication Signal — A path level code that is sent downstream in a digital network as an indication that an upstream failure has been detected and alarmed by some upstream equipment, if the upstream alarm has not been suppressed.

**Path Terminating Equipment**

Network elements in which the path overhead is terminated.

**Path Trace Signal**

The path trace signal is provided in the STS-1 path trace byte (J1) of the path overhead. The path trace signal is transmitted repetitively to verify the continuity of the STS-1 path. The DS3 circuit pack is the only low speed interface circuit pack that generates or terminates the STS-1 path overhead.

**Path Unequipped Signal**

The path unequipped signal is provided in the STS-1 path signal label byte (C2) of the path overhead. The path unequipped signal is transmitted to notify downstream equipment that the STS-1 path is incomplete (for example, a low speed interface slot in the AUTO state or the absence of a valid cross-connection). The DS3 circuit pack is the only low speed interface circuit pack that terminates the STS-1 path overhead.

**Path Yellow Signal**

A maintenance signal used to indicate the presence of an alarm, detected at the remote terminal, in the path originating locally. Also, refer to the path remote defect indicator <sup>^</sup>(RDI-P) signal.

**Payload Pointer**

The offset in bytes from the Pointer Action Byte (H3) to the beginning of the synchronous payload envelope.

**PC**

Personal Computer.

**PCN**

Product Change Notification — Product change notifications are issued to notify customers that in-service product changes are required to correct an existing or potential problem. Product change notifications are issued through the Design Change Management System.

**Phase Locked**

An operating condition of a network element in which it is locked to an external reference. (Also referred to as Externally Timed in Release 5 and later releases.)

**Platform**

In the FT-2000 OC-48 Lightwave System, a platform is a family of equipment and software configurations designed to support a particular set of applications.

**Plesiochronous Network**

A network that contains multiple subnetworks, each internally synchronous and all operating at the same nominal frequency, but whose timing may be slightly different at any particular instant.

**PLL**

Phase Locked Loop.

**PM**

Performance Monitoring — Measures the quality of service and identifies degrading or marginally operating systems (before an alarm would be generated).

**POH**

Path Overhead — Overhead assigned to and transported with the payload until the payload is demultiplexed. It is used for functions that are necessary to transport the payload.

**Preemptible Traffic**

Also called "Extra Traffic"/ Traffic, which has a lower priority than that carried on the service fiber. Low priority (unprotected) traffic that is carried on a protection fiber and is subject to being dropped — if the service fiber should fail and a protection switch occurs.

**Preprovisioning**

The capability to provision a slot before installing a circuit pack.

**Primary Node**

The primary node is the interconnecting node in the Dual Ring Interworking application where a 1-way-dri cross-connection has been entered. When a fault occurs, the through cross-connection at the primary node switches to the active state, and the add cross-connection switches to the standby state.

**Proactive Maintenance**

Refers to the process of detecting degraded conditions not severe enough to initiate protection switching or alarming, but indicative of an impending signal fail or signal degrade defect.

**Provisioning**

Assigning a value to a parameter in memory.

**PSC**

Protection Switch Counts — The number of times that a protection switch has been performed. The protection switch counts are monitored for the OC-48 line and OC-3 line.

**PVC**

Permanent Virtual Circuit.

**PWR**

Power.

---

## **R**

### **RBP**

Receive Byte Processor — A device on the OC48 RCVR and OC48 REGENR circuit packs.

### **RDI-L Signal**

Line Remote Defect Indication Signal— An indication returned to a transmitting terminal that the receiving terminal has detected an incoming failure. This is new terminology adopted by the T1M1 committee for the SONET environment, which replaces the far-end-receive-failure (FERF) signal. If the RDI-L signal is integrated for a specified period of time, it becomes a remote failure indication (RFI) signal (a defect becomes a failure). Also refer to RFI.

### **RDI-P Signal**

Path Remote Defect Indicator — A maintenance signal inserted toward the OC-48 line by the DS3 circuit pack to show the presence of an alarm, detected at the remote terminal, in the path originating locally. This is new terminology adopted by the T1M1 committee for the SONET environment, which replaces the path yellow signal. If the RDI-P signal is integrated for a specified period of time, it becomes a remote failure indication (RFI) signal (a defect becomes a failure). Also refer to RFI.

### **RDS**

Ring Diversity Switch.

### **Reactive Maintenance**

Refers to detecting defects/failures and clearing them.

### **Revertive Switching**

In revertive switching, there is a service and protection high speed line, circuit pack, etc. When a protection switch occurs, the protection line, circuit pack, etc., is selected. When the fault clears, service "reverts" back to the original service line. Also see 1x1 Line Protection.

### **RF**

Raio Frequency.

### **RFI**

Remote Failure Indication — A signal that alerts upstream STS-1 path terminating equipment that a downstream failure has been alarmed along the STS-1 path. This action prevents multiple alarms from being activated for the same failure.

### **RMS**

Root Mean Square.

### **RPP**

Reliability Prediction Procedure.

### **RTAC**

Regional Technical Assistance Center — A Lucent Technologies organization that helps customers maintain installed systems.

### **RZ**

Return to Zero.

---

## S

### SA

Service Affecting.

### SAS

Severely Errored Frame/AIS Seconds — A second in which there is an out of frame or AIS defect detected in the DS3 signal received from OC-48 line (DS3 path).

### SD

Signal Degrade — A provisionable parameter for the incoming OC-48 high speed signal and DS3 signal (from cross-connect panel) that determines the threshold at which a signal degrade condition is declared.

### SDH

Synchronous Digital Hierarchy.

### Secondary Node

The secondary node is the interconnecting node in the Dual Ring Interworking application where only a 2-way add/drop (or a 1-way add and a 1-way drop) cross-connection has been entered. The secondary node is adjacent to the primary node (also see Primary Node).

### SEFS

Severely Errored Frame Seconds — The number of seconds during which an out-of-frame event occurred. The severely errored frame seconds are monitored for the ^STS-48 section, STS-3 section, and EC-1 section.

### SES

Severely Errored Seconds — A second in which the coding violations detected reach a fixed threshold. The severely errored seconds are monitored for the OC-48 line, OC-3 line, STS-48 section, STS-3 section, STS-1 path, EC-1 line, and DS3 path.

### SF

Signal Fail — Refers to a condition when an incoming signal's bit error rate exceeds a fixed value.

### Single-Ended Operations

The capability to perform operations, administration, maintenance, and provisioning of remote network elements on a centralized basis.

### Site Address

The confirmed F-byte address of the network element on OC-48 high speed lines. The site address consists of the line address and site number (for example, site-1e-0/site-1w-4).

### SLM

Single Longitudinal Mode.

### Software Ready

The software that runs on the FT-2000 OC-48 Lightwave System will accept, run, boot, and operate normally when future hardware (circuit packs) is installed.

### SONET

Synchronous Optical NETWORK — The American National Standards Institute's standard for broadband optical fiber communications.



**Source Node**

The source node is the node in a ring where the low speed signals on an STS-3 tributary originate. The source node and destination node of an STS-3 tributary are always in the same ring. Also see Destination Node.

**SPE**

Synchronous Payload Envelope — A 125-microsecond frame structure composed of STS path overhead and bandwidth for the payload.

**Squelch Map**

A map (table) containing information for each cross connection in the ring and indicates the source and destination nodes for the low speed circuit that define the cross connection. This information is used to prevent traffic misconnection in rings with isolated nodes or arguments. (This term is sometimes referred to as "Ring Squelch Map").

**Squelching**

To avoid misconnected traffic when performing a 2-fiber ring protection switch, the FT-2000 OC-48 Add/Drop-Rings Terminal inserts STS-1 path AIS on protection tributaries for traffic that cannot be terminated because of node failures or multiple failures in the ring. Squelching is only performed by nodes that have an active protection switch.

**Standby**

Standby refers to a 1+1 protected OC-3 line that is not currently selected by the receiving terminal at either end as the payload carrying signal. (Also refer to Active.)

**Status**

Status is a condition that does not raise an alarm.

**STS, STS-n**

Synchronous Transport Signal — The basic logical building block signal with a rate of 51.840 Mb/s for an STS-1 signal and a rate of n times 51.840 Mb/s for an STS-n signal.

**STS-1**

This refers to the FT-2000 OC-48 Add/Drop-Rings Terminal internal electrical signal with an EC-1 format.

**STS-1E**

The FT-2000 OC-48 Lightwave System STS1 Electrical Interface circuit pack interfaces with up to three bidirectional EC-1 signals.

**STS-1 Granularity**

In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal has an STS-1 time slot assignment capability that allows any service STS-1 tributary to be cross-connected to any service low speed port or to the same-numbered service STS-1 tributary in the opposite direction.

**STS-1 Group**

Three STS-1 signals in the same STS-3 signal.

**Subnetwork**

SONET networks with data communications channel connectivity. When Release 6 of the FT-2000 OC-48 Add/Drop-Rings Terminal interworks with Release 7.2 or Release 9 of the DDM-2000 OC-3 Multiplexer, the recommended maximum subnetwork size is 24 network elements. In Release 7 and later releases, the recommended maximum subnetwork size is 32 network elements.

**Sync Messaging**

A method whereby nodes within a FT2000 ring communicate their synchronization status with other nodes in the ring.

**Synchronous**

Refers to network elements that are timed from references traceable to a single timing source.

**Synchronous Network**

The synchronization of synchronous transmission systems with synchronous payloads to a master (network clock that can be traced to a single reference clock).

**SYSCTL**

The System Controller and System Memory circuit packs provide the highest level of system control for the FT-2000 OC-48 Lightwave System. The System Controller circuit pack provides overall administrative control of the system.

**SYSTEMEM**

The System Memory and System Controller circuit packs provide the highest level of system control for the FT-2000 OC-48 Lightwave System. The System Memory circuit pack provides memory support for the SYSCTL circuit pack.

---

**T**

**T1X1 and T1M1**

The *ANSI* committees responsible for telecommunications standards.

**TA**

Technical Advisory.

**TBOS**

Telemetry Byte-Oriented Serial (Protocol) — Defines one physical interface for direct connection between the telemetry remote and the monitored equipment. An EIA-422 port is used to provide the operations system with enough alarm and status information to localize a problem and determine the severity.

**TBP**

Transmit Byte Processor — A device on the OC48 TRMTR and OC48 REGENR circuit packs.

**TCA**

Threshold-Crossing Alert — A condition set when a counter exceeds a user-selected high or low threshold. A TCA does not generate an alarm but is available on demand through the CIT.

**TEC**

Thermo-Electric Cooler.

**TG3 (DS1)**

The FT-2000 OC-48 Lightwave System Timing Generator, Stratum 3 - DS1 circuit pack generates clock signals for distribution to the transmit circuits. It operates in the free running, loop timed, through timed, externally timed, and holdover modes.

**Through Timed**

Refers to a network element that derives its transmit timing in the east direction (Line 1E) from a received line signal in the west direction (Line 1W) and the transmit timing in the west direction (Line 1W) from a received line signal in the east direction (Line 1E).

**TID**

Target Identifier — A provisionable parameter used to identify the FT-2000 OC-48 Lightwave System.

**TIE**

Time Interval Error — The variation of time delay of a given timing signal with respect to an ideal timing signal over a particular period of time.

**TL1**

Transaction Language 1 — A machine-to-machine communications language that is a subset of CCITT's human-machine language.

**TOHCTL**

The FT-2000 OC-48 Lightwave System Overhead Controller - Tributary circuit pack provides user access to the OC-3 data communications channel (DCC). The TOHCTL terminates up to eight OC-3 DCCs.

**TOP**

Task Oriented Practice.

**TR**

Technical Reference.

**Tributary**

A 51.84 Mb/s signal (STS-1 level) or 155 Mb/s signal (STS-3 level) within the FT-2000 OC-48 Lightwave System. The maximum capacity of an OC-48 signal is forty-eight 51.84 Mb/s tributaries (or sixteen 155 Mb/s tributaries).

**TSA**

Time Slot Assignment — This is a capability of the FT-2000 OC-48 Add/Drop-Rings Terminal that allows any service STS-3 tributary to be cross-connected to any service low speed interface slot or to the same-numbered service STS-3 tributary in the opposite direction. In Release 7 and later releases, the FT-2000 OC-48 Add/Drop-Rings Terminal has an STS-1 TSA capability that allows any service STS-1 tributary to be cross-connected to any service low speed port or to the same-numbered service STS-1 tributary in the opposite direction.

**TSO**

Technical Support Organization — Refer to as Customer Technical Support (CTS).

---

**U**

**UAS**

Unavailable Seconds — A second during which the monitored facility is unavailable. The unavailable seconds are monitored for the OC-48 line, OC-3 line, STS-1 path, EC-1 line, and DS3 path.

**Upgrade**

An upgrade is the addition of new capabilities (features) to an existing platform. This requires new software and may require new hardware.

---

## V

### **Value**

A number, text string, or other menu selection associated with a parameter.

### **VF Circuit**

Voice Frequency Circuit — A 64-kb/s digitized signal.

### **VM**

Violation Monitor — A provisionable mode for the DS3 output that causes parity violations to be monitored but not corrected before the DS3 signal is B3ZS encoded.

### **VMR**

Violation, Monitor, and Removal — A provisionable mode for the DS3 output that causes parity violations to be monitored and corrected before the DS3 signal is B3ZS encoded.

### **VT**

Virtual Tributary — A structure designed for transport and switching of sub-STS-1 payloads. There are currently four sizes: VT1.5 (1.728 Mb/s), VT2 (2.304 Mb/s), VT3 (3.456 Mb/s), and VT6 (6.912 Mb/s).

### **VT-G**

Virtual Tributary Group — A 9-row by 12-column structure (108 bytes) that carries one or more VTs of the same size. Seven VT groups (756 bytes) are byte interleaved with the VT-organized synchronous payload envelope.

---

## W

### **Wander**

The wander of a digital signal is defined as the *long term* variations of the significant instants of a digital signal from their ideal positions in time.

### **WDM**

Wavelength Division Multiplexing.

### **Wideband Communications**

Voice, data, and/or video communications from the DS0 rate (64 kb/s) to the DS1 rate (1.544 Mb/s).

---

## Z

### **Zero Code Suppression**

A technique used to reduce the number of consecutive zeros in a line-codes signal (B3ZS for DS3 signals).



