

AUB67 XTC TEST UNIT "C"
DATA SHEET
SLC® SERIES 5 CARRIER SYSTEM

The AUB67 XTC test unit C (XTUC) is used in the extended test controller (XTC). The AUB67 XTUC is controlled by a microcomputer on the AUB65 digital test unit. A complete XTC tester unit consists of one AUB65 (XTUD), one AUB67, and one AUB68 (XTUB). The AUB67 provides the following functions:

- Test bus distribution
- Mechanized loop testing (MLT) signaling interface
- DX (duplex) signaling interface and DS0 (digital signal, zero rate) interface for circuit testing via the switched access remote test system (SARTS)
- Microcomputer interface and input/output expander
- Power supply cut-off for 48V relays on AUB67 and AUB68
- High voltage signal supply.

A detailed description of the interface is contained in the following paragraphs.

The AUB67 distributes the 5-pair (10-lead) metallic test bus from the AUB66 to the AUB68, switched access remote testing system (SARTS), DS0 interface, mechanized loop testing (MLT), and the bypass test pair. This allows interconnection of the proper test circuitry to the desired circuit under test and the appropriate test system.

The MLT interface provides communication with MLT for test setup, execution, and result reporting.

The DX interface provides the channel unit testing circuitry on the AUB68 with DX signaling capabilities.

The DS0 interface allows the AUB65 to send and receive DS0 signals. The AUB67 derives the required 333-Hz and 64-kHz clocks from a balanced composite clock and converts DS0 bipolar signals to TTL level digital signals and vice versa. The DS0 line drivers and receivers are also included in the AUB67.

The microcomputer interface allows the microcomputer on the AUB65 to control the AUB67. The interface also expands the 8-lead data bus from the AUB65 to allow the microcomputer to read or write from 48 input/output ports.

The power cut-off function for the 48V relays is provided by the AUB67 to release all of the relays on the AUB67 and AUB68 during the power-up and initialization phases of the two boards.

The high voltage signal supply interface of the AUB67 controls the signaling voltages required by the 2-wire channel unit testing circuitry on the AUB68.

LEDS: The AUB67 has two LEDs on the faceplate. The red (FAIL) LED indicates a failure on either the AUB65, AUB67, or AUB68. The red LED on each of these boards will light in parallel because faults cannot be sectionalized between them. These three boards function together to comprise an XTC tester unit. The green (BUSY) LED is controlled by the AUB65 (XTUD) and lights when a test is being performed.

Figure 1 is a functional block diagram of the AUB67 (XTUC). Figure 2 shows the faceplate.

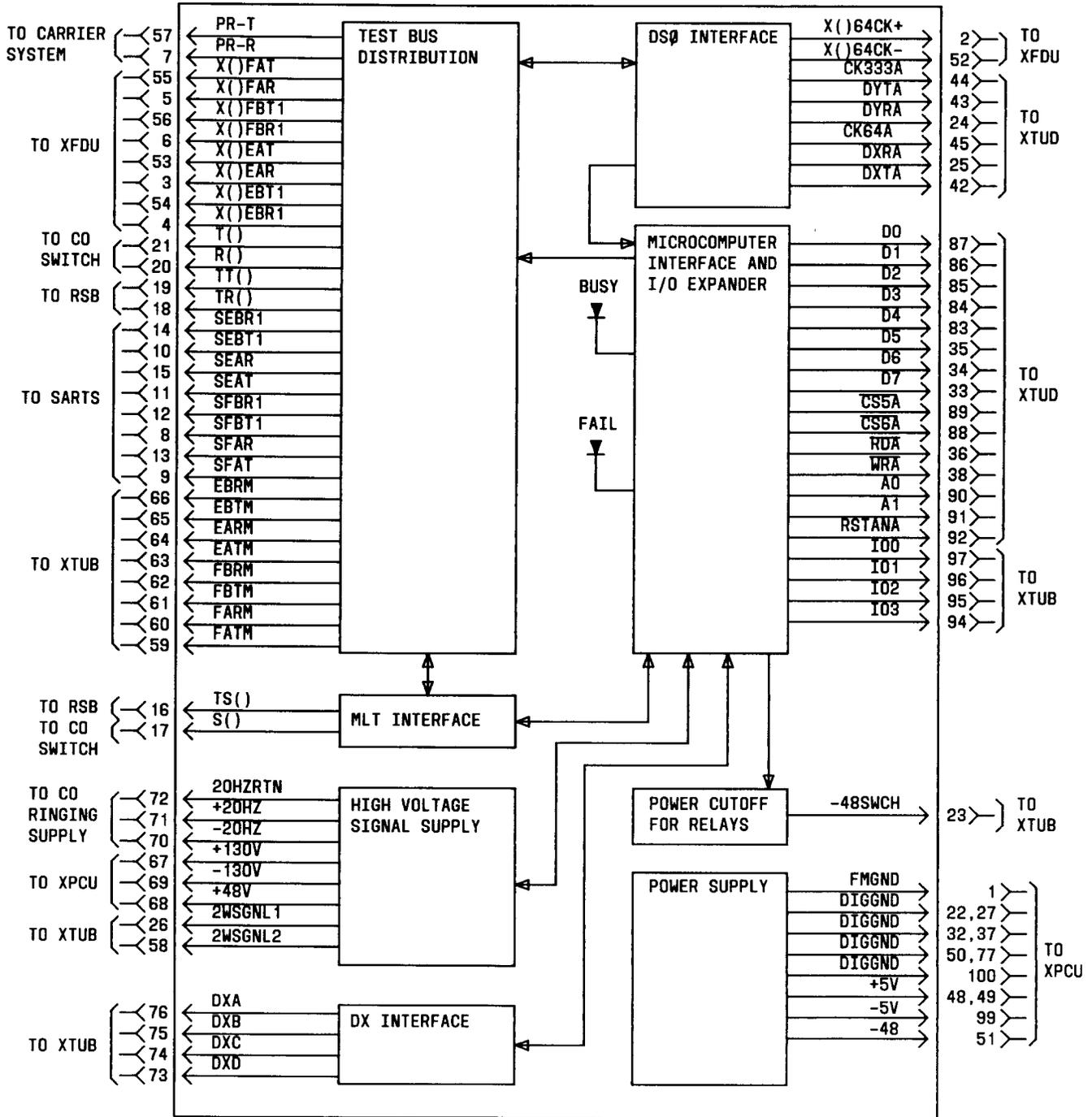


Fig. 1 — AUB67 XTUC Block Diagram



Fig. 2—AUB67 Faceplate Diagram