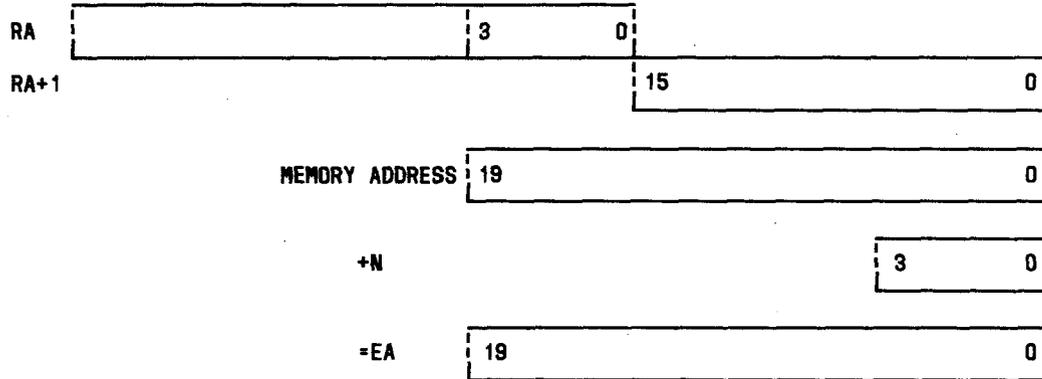


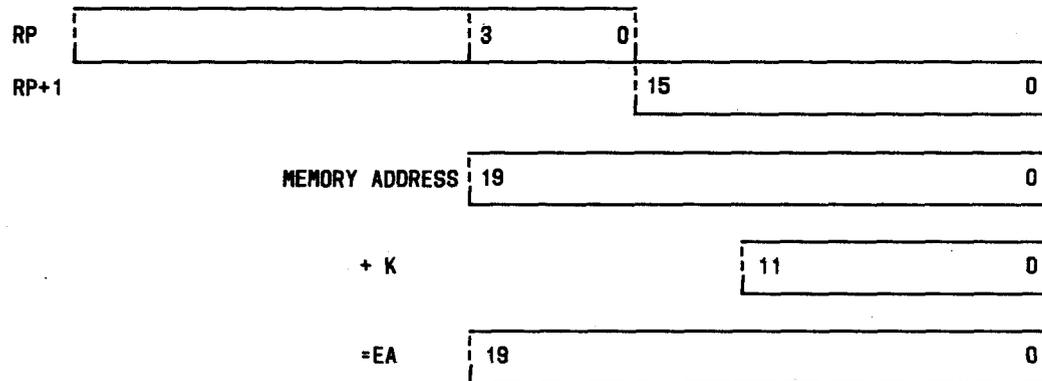
COMPUTING EFFECTIVE MEMORY ADDRESSES (EA)

1. COMPUTING EFFECTIVE MEMORY ADDRESSES (EA)

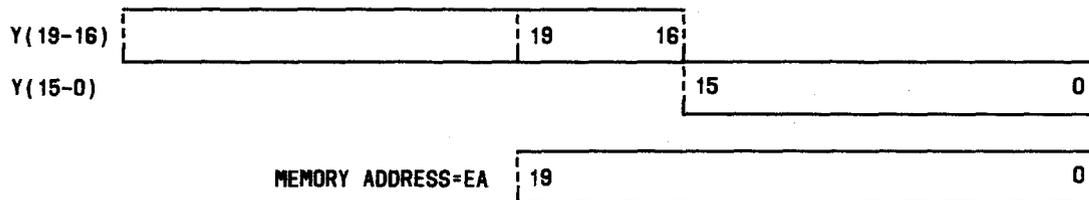
$N(RA) = N + (RA)$



$K(RP) = K + (RP)$



$Y = (Y)$

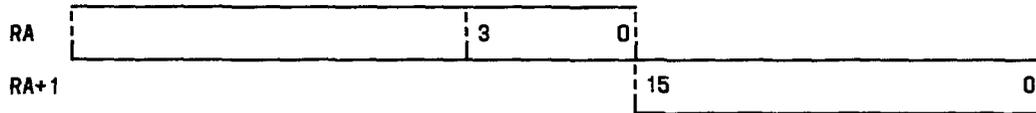


NOTICE

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1. COMPUTING EFFECTIVE MEMORY ADDRESSES (EA) (Contd)

$$R_X(RA) = (R_X) + (RA)$$

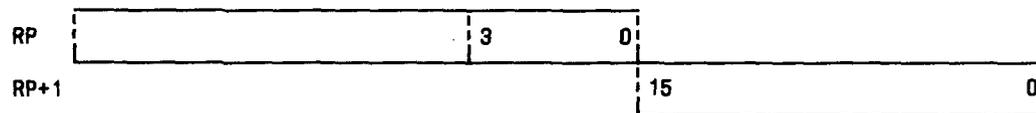


MEMORY ADDRESS	19	0
----------------	----	---

+R _X	15	0
-----------------	----	---

=EA	19	0
-----	----	---

$$R_X(RP) = (R_X) + (RP)$$



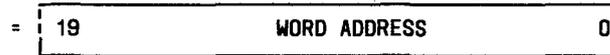
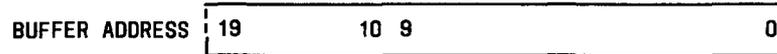
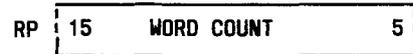
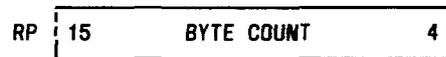
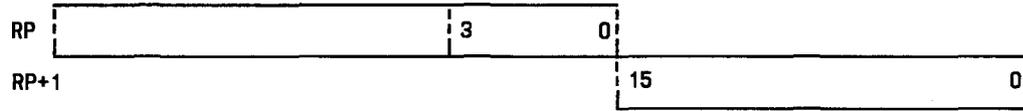
MEMORY ADDRESS	19	0
----------------	----	---

+R _X	15	0
-----------------	----	---

=EA	19	0
-----	----	---

CAUTION: There is no check made for out-of-range memory address calculations.

2. BYTE INSTRUCTION DEFINITIONS



BUFFER BYTE (IF RP BIT 4=0) = [WORD ADDRESS (15-8)]

(IF RP BIT 4=1) = [WORD ADDRESS (7-0)]

REGISTER BYTE = (R_X) 7-0