

SUBSCRIBER DISTRICT JUNCTORS
TESTS USING DISTRICT JUNCTOR TEST CIRCUIT
NO. 1 CROSSBAR OFFICES

1. GENERAL

1.001 This addendum supplements Section 216-230-501, Issue 5.

1.002 The addendum is issued to incorporate Test A of Section 216-769-501, Issue 3.

The following change applies to Part 1 of the section:

(a) 1.03—added Test L

1.03 (Add to list of tests)

(L) 1000-Cycle Transmission Loss Test

2. APPARATUS

The following changes apply to Part 2 of the section:

(a) 2.05—added

(b) 2.06—added

Test L

2.05 23A transmission measuring set (TMS) J94023A.

2.06 Two patching cords, P3E cords, 6 feet long, equipped with two 310 plugs (3P7A cords).

4. METHOD

The following changes apply to Part 4 of the section:

(a) Sub-Heading following 4.31—(L) 1000-Cycle Transmission Loss Test—added

(b) 4.32 through 4.35—added

(L) 1000-Cycle Transmission Loss Test

4.32 This test checks the loss of the district junctor circuit.

4.33 Using two 3P7A cords, on the test circuit connect S jack to OS jack and R jack to MEAS jack of 23A TMS. On the 23A TMS, the DIAL-MEAS-SLV key should be in the MEAS position. The INPUT key should be in the 600 position. The ADD DB switch should be set to zero. On the test circuit, operate LC and ST keys.

4.34 After the S lamp lights, the TMS meter reading shall meet the transmission requirements shown on SD. If the test frame is to be advanced to the next circuit, operate the CA key momentarily.

4.35 Repeat 4.34 until all circuits have been tested. After all circuits have been tested, restore all keys and operate the RN key. After the N lamp lights, restore the RN key and remove all cords.

5. MISCELLANEOUS FEATURES

The following changes apply to Part 5 of the section:

(a) 5.10—revised

5.10 Where C jacks appear on the zone registration district connector frames, coin supervisory link frame, and zone registration timing interrupter frame, the test circuit can be controlled as in 5.09

from these frames. The test frame may also be controlled on the remote control (RC) jack at the zone registration timing interrupter frame and the zone registration control frame. When testing AMA district junctors, the test circuit can be controlled as in 5.09 from the DJT jacks located on the recorder and call identity indexer frames. The DST jack at the transverter trouble indicator frame can be used to advance the test circuit or to repeat on the same district junctor depending on the position of the REP key.