LOCATION SECTION

Contents

1. Overview of Disk Subsystem

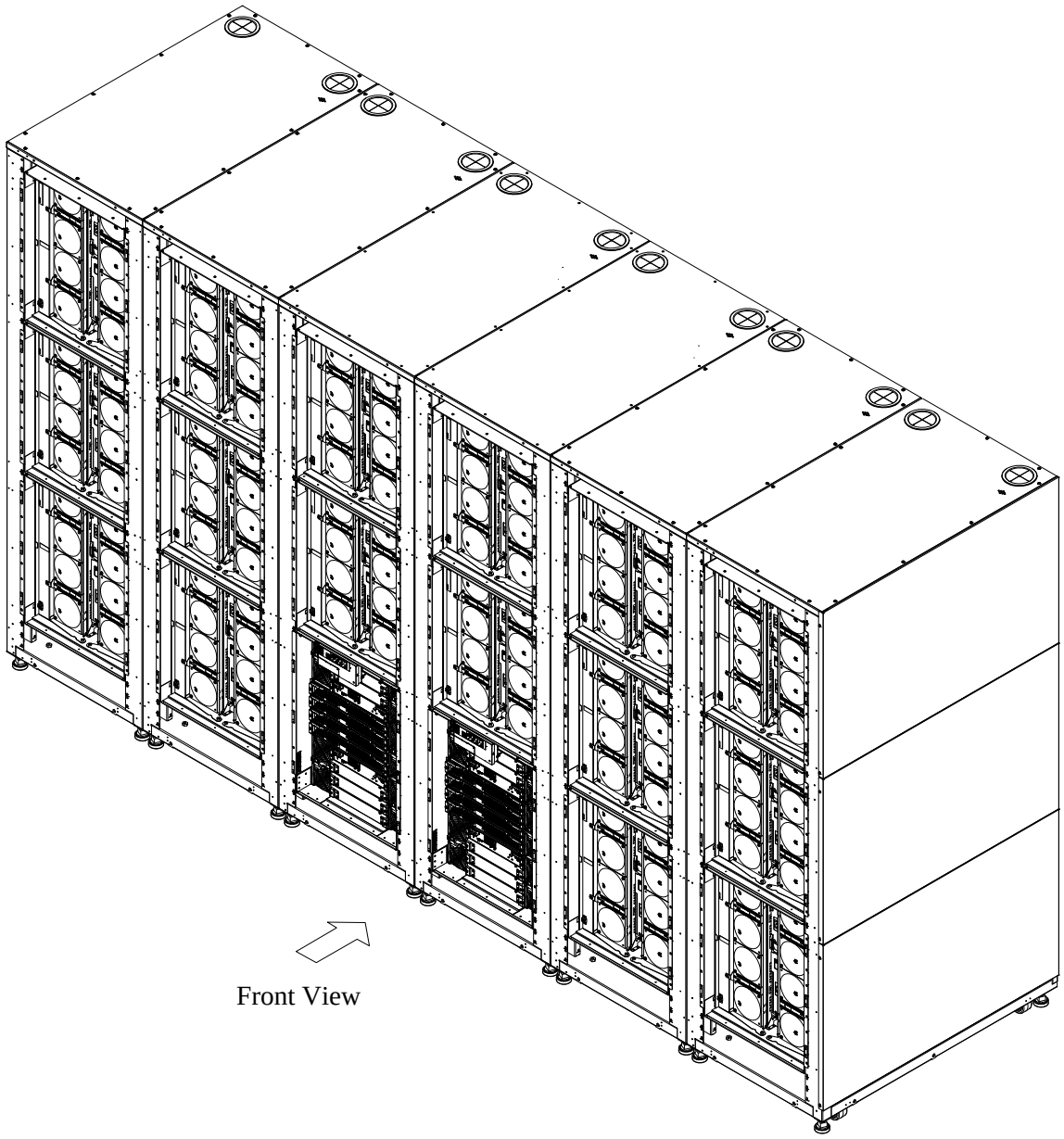


Fig. 1-1 Overview of Disk Subsystem

2. Parts Location

2.1 Configuration example of main parts

The following figure shows a configuration example of installing main parts in 42 units rack frame.

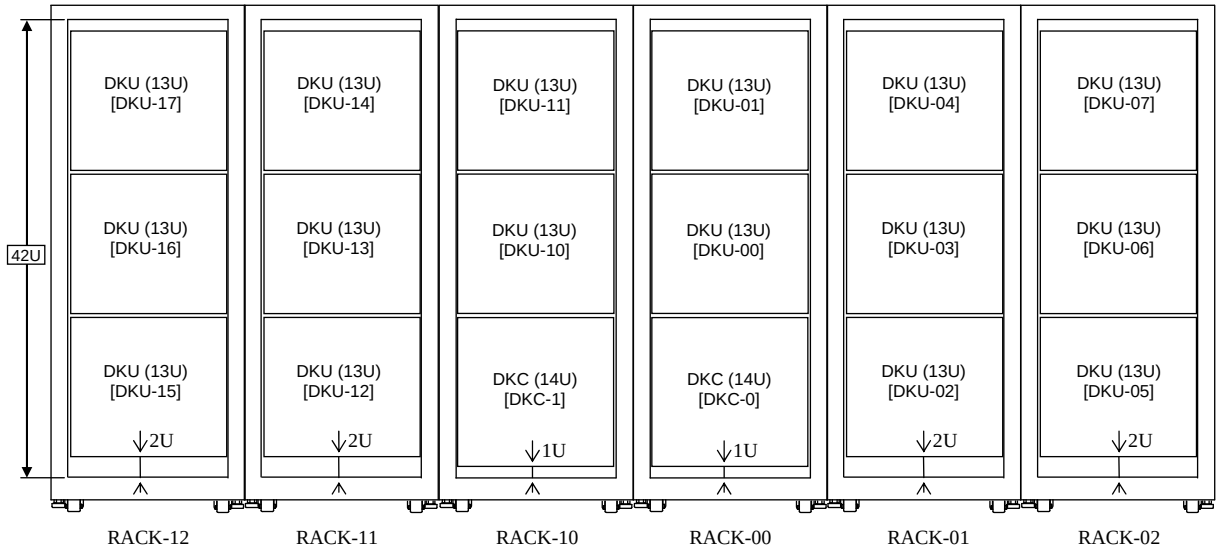
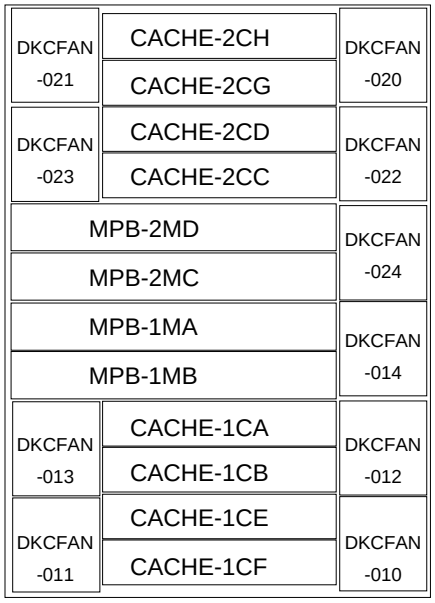


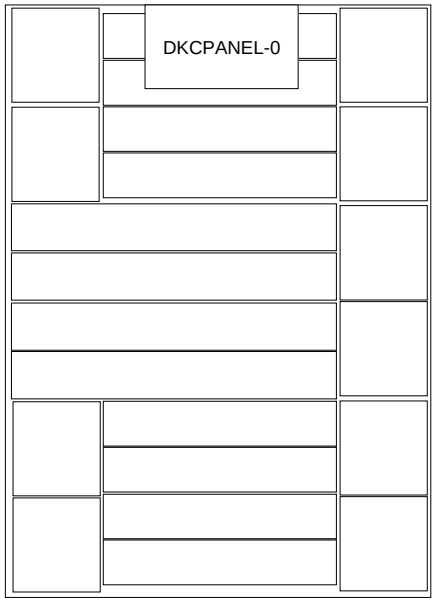
Fig. 2.1-1 Configuration example of main parts

2.2 DKC (14U)

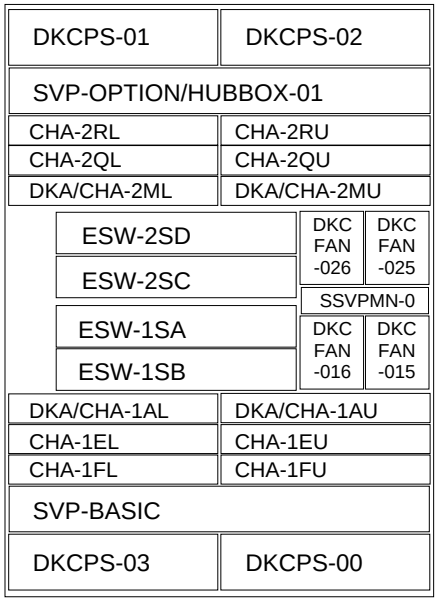
1. DKC-0



Front View of DKC-0
(DKCPANEL is opened.)



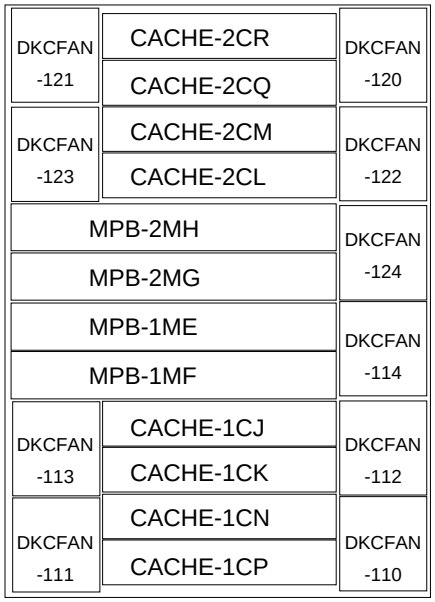
Front View of DKC-0
(DKCPANEL is closed.)



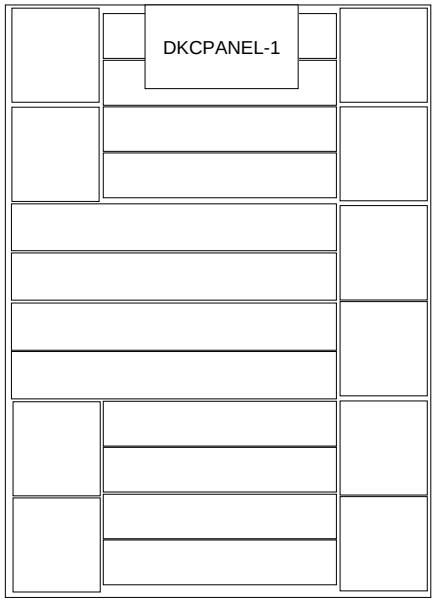
Rear View of DKC-0

Fig. 2.2-1 Parts Location of DKC-0

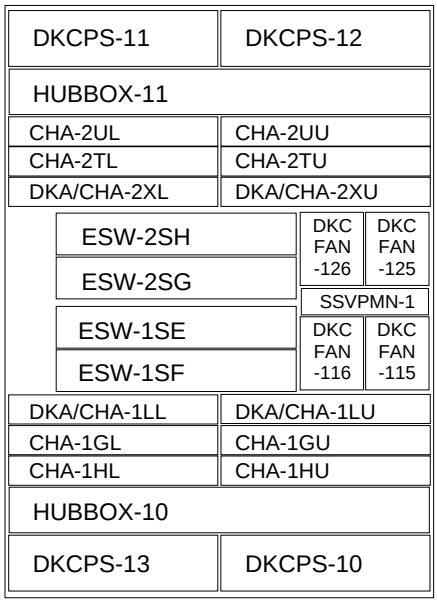
2. DKC-1



Front View of DKC-1
(DKCPANEL is opened.)



Front View of DKC-1
(DKCPANEL is closed.)



Rear View of DKC-1

Fig. 2.2-2 Parts Location of DKC-1

2.3 DKU (13U)

The number (DKU-xy) of DKU doesn't indicate the installation location of DKU, but indicates the order of installation. Therefore, while performing process to DKU, it is necessary to confirm location with the location label put on DKU, and be careful not to make a mistake of target location.

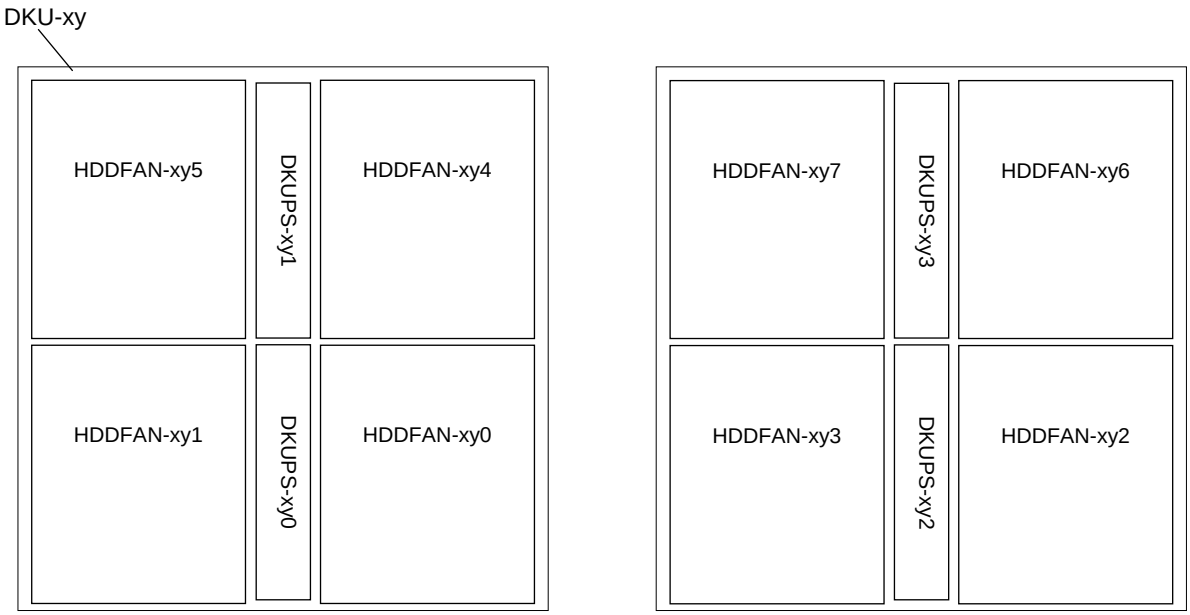
1. LFF DKU (DKU for 3.5 inch Drive)

*1: The name in parentheses in the SVP messages shows HDDXXX-YY here.

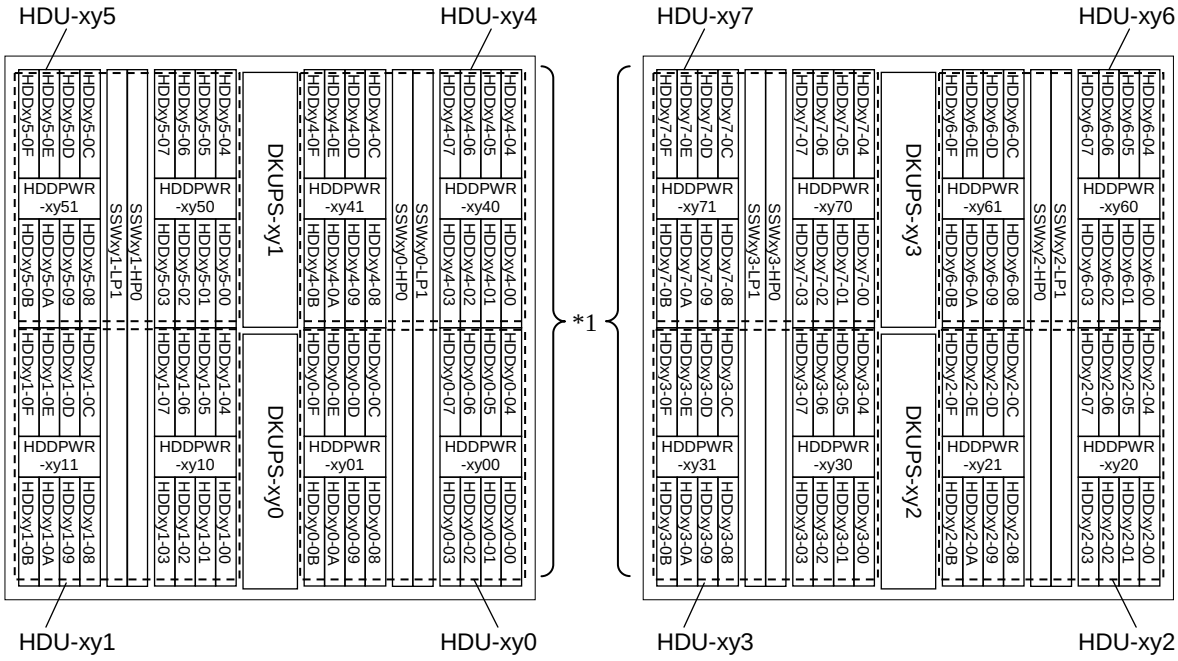
*2: DKU-xy
- DKU No. (0, 1, 2,....., 7)
- DKC No. (0, 1)

Fig. 2.3-1 Parts Location of LFF DKU

2. SFF DKU (DKU for 2.5 inch Drive)



Front View of DKU　　Rear View of DKU



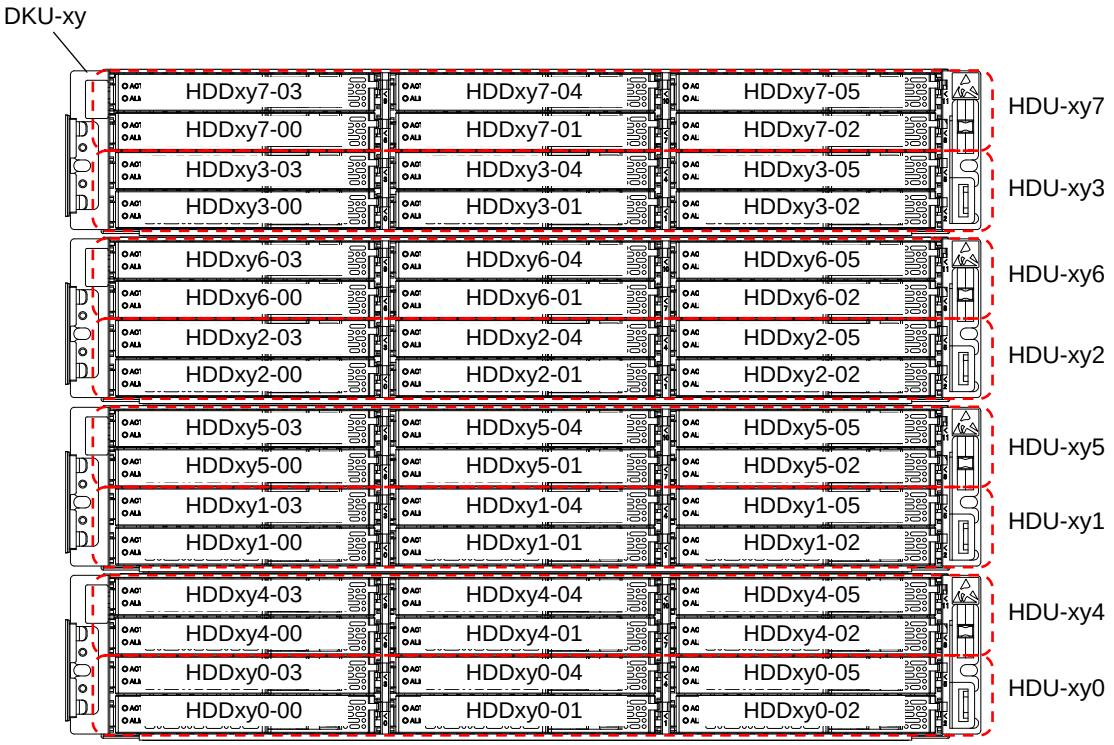
Front View of DKU　　Rear View of DKU

*1: The name in parentheses in the SVP messages shows HDDXXX-YY here.

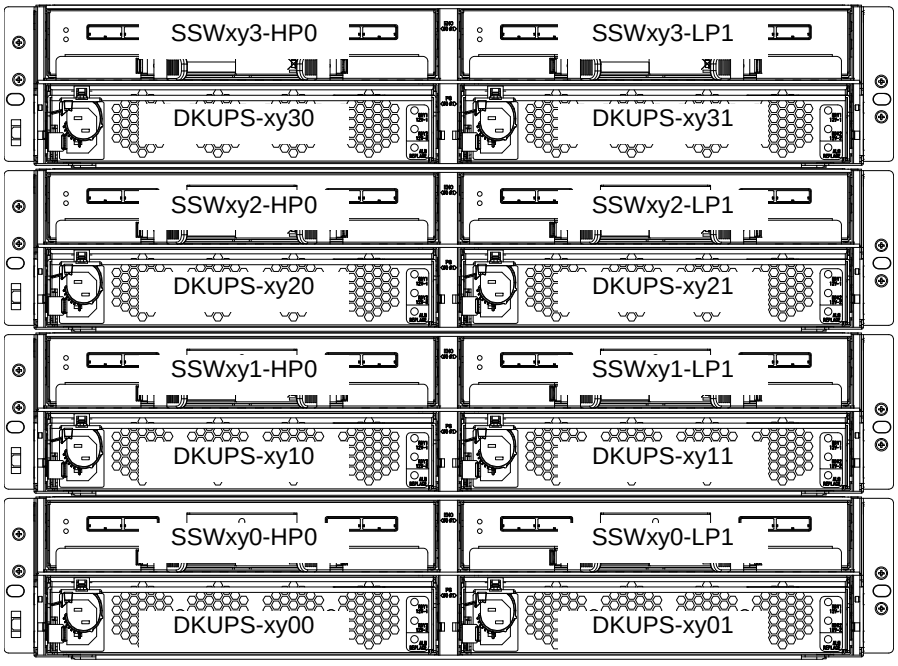
*2: DKU-xy
- DKU No. (0, 1, 2,….., 7)
- DKC No. (0, 1)

Fig. 2.3-2 Parts Location of SFF DKU

3. FBX DKU



Front View of FBX DKU



Rear View of FBX DKU

*1: The name in parentheses in the SVP messages shows HDDXXX-YY here.

*2: DKU-xy
- y: DKU No. (0, 1, 2,....., 7)
- x: DKC No. (0, 1)

Fig. 2.3-3 Parts Location of FBX DKU

2.4 PCB Location

1. DKC-0 Location (In case of DKC710I-CBX)

DKC-0 LOCATION (FRONT)

CL2	2CH	WP741-A	*D	CACHE-2CH	Option 3
	2CG	WP741-A	*C	CACHE-2CG	Option 2
	2CD	WP741-A	*B	CACHE-2CD	Option 1
	2CC	WP741-A	*A	CACHE-2CC	Basic
	2MD	WP752-A	*F	MPB-2MD	Option 1
		WP752-B	*H		
	2MC	WP752-A	*E	MPB-2MC	Basic
		WP752-B	*G		
CL1	1MA	WP752-A	*E	MPB-1MA	Basic
		WP752-B	*G		
	1MB	WP752-A	*F	MPB-1MB	Option 1
		WP752-B	*H		
	1CA	WP741-A	*A	CACHE-1CA	Basic
	1CB	WP741-A	*B	CACHE-1CB	Option 1
	1CE	WP741-A	*C	CACHE-1CE	Option 2
	1CF	WP741-A	*D	CACHE-1CF	Option 3

*A: DKC-F710I-CPC × 1 set
*B: DKC-F710I-CPC × 2 sets
*C: DKC-F710I-CPC × 3 sets
*D: DKC-F710I-CPC × 4 sets
*E: DKC-F710I-MP × 1 set
*F: DKC-F710I-MP × 2 sets
*G: DKC-F710I-MPR × 1 set
*H: DKC-F710I-MPR × 2 sets

Fig. 2.4-1 DKC-0 PCB Location (In case of DKC710I-CBX)

DKC-0 LOCATION (REAR)

CL1/	—	DKCPS-01			Basic	—	DKCPS-02	*J		Option 1
CL2	—	SVP-OPTION	*L				/ HUBBOX-01	*M		Option 1
CL2	2RL	4th CHA	*G	CHA-2RL	Option 3	2RU	3rd CHA	*G	CHA-2RU	Option 2
	2QL	2nd CHA	*G	CHA-2QL	Option 1	2QU	1st CHA	*G	CHA-2QU	Basic
	2ML	2nd DKA	*H	DKA-2ML	Option 1	2MU	1st DKA	*H	DKA-2MU	Basic
		5th CHA	*G	CHA-2ML	Option 4		6th CHA	*G	CHA-2MU	Option 5
	2SD	WP730-A	*F	ESW-2SD	Option 1					
	2SC	WP730-A	*E	ESW-2SC	Basic					
CL1	1SA	WP730-A	*E	ESW-1SA	Basic		SSVPMN			
	1SB	WP730-A	*F	ESW-1SB	Option 1					
	1AL	2nd DKA	*H	DKA-1AL	Option 1	1AU	1st DKA	*H	DKA-1AU	Basic
		5th CHA	*G	CHA-1AL	Option 4		6th CHA	*G	CHA-1AU	Option 5
	1EL	2nd CHA	*G	CHA-1EL	Option 1	1EU	1st CHA	*G	CHA-1EU	Basic
	1FL	4th CHA	*G	CHA-1FL	Option 3	1FU	3rd CHA	*G	CHA-1FU	Option 2
CL1/	—	SVP-BASIC	*K							Basic
CL2	—	DKCPS-03	*J		Option 1	—	DKCPS-00			Basic

*E: DKC-F710I-ESW × 1 set
*F: DKC-F710I-ESW × 2 sets
*G: Description of CHA PCBs

CL1/CL2 (1EU, 1EL, 1FU, 1FL, 1AU, 1AL, 2QU, 2QL, 2RU, 2RL, 2MU, 2ML)	WP711-A	*1
	WP711-B	*2
	WP712-B	*3
	WP712-A	*4
	WP711-E	*5
	WP711-F	*6
	WP713-A	*7
	WP712-D	*8
	WP712-C	*9
	WP711-G	*10

*1: DKC-F710I-16MFS
*2: DKC-F710I-16MFL
*3: DKC-F710I-8UFC
*4: DKC-F710I-16UFC
*5: DKC-F710I-16MUS
*6: DKC-F710I-16MUL
*7: DKC-F710I-8FOE
*8: DKC-F710I-8UFCR
*9: DKC-F710I-16UFCR
*10: DKC-F710I-16MUSR

*H: Description of DKA PCBs

CL1/CL2 (1AU, 1AL, 2MU, 2ML)	WP720-A	*1

*1: DKC-F710I-SCA

*J: DKC-F710I-APC
*K: DKC-F710I-SVP × 1 set
*L: DKC-F710I-SVP × 2 sets
*M: DKC-F710I-HUB

Fig. 2.4-2 DKC-0 PCB Location (In case of DKC710I-CBX)

2. DKC-0 Location (In case of DKC710I-CBXA)

DKC-0 LOCATION (FRONT)

CL2	2CH	WP741-A	*C	CACHE-2CH	Option 3
	2CG	WP741-A	*B	CACHE-2CG	Option 2
	2CD	WP741-A	*A	CACHE-2CD	Option 1
	2CC	WP741-A		CACHE-2CC	Basic
	2MD	WP752-A	*D	MPB-2MD	Option 1
		WP752-B	*E		
	2MC	WP752-A		MPB-2MC	Basic
		WP752-B			
CL1	1MA	WP752-A		MPB-1MA	Basic
		WP752-B			
	1MB	WP752-A	*D	MPB-1MB	Option 1
		WP752-B	*E		
	1CA	WP741-A		CACHE-1CA	Basic
	1CB	WP741-A	*A	CACHE-1CB	Option 1
	1CE	WP741-A	*B	CACHE-1CE	Option 2
	1CF	WP741-A	*C	CACHE-1CF	Option 3

*A: DKC-F710I-CPC × 1 set
*B: DKC-F710I-CPC × 2 sets
*C: DKC-F710I-CPC × 3 sets
*D: DKC-F710I-MP
*E: DKC-F710I-MPR

Fig. 2.4-3 DKC-0 PCB Location (In case of DKC710I-CBXA)

DKC-0 LOCATION (REAR)

CL1/	—	DKCPS-01			Basic	—	DKCPS-02	*H		Option 1
CL2	—	SVP-OPTION	*J				/ HUBBOX-01	*K		Option 1
CL2	2RL	4th CHA	*F	CHA-2RL	Option 3	2RU	3rd CHA	*F	CHA-2RU	Option 2
	2QL	2nd CHA	*F	CHA-2QL	Option 1	2QU	1st CHA	*F	CHA-2QU	Basic
	2ML	2nd DKA	*G	DKA-2ML	Option 1	2MU	1st DKA	*G	DKA-2MU	Basic
		5th CHA	*F	CHA-2ML	Option 4		6th CHA	*F	CHA-2MU	Option 5
	2SD	WP730-A	*E	ESW-2SD	Option 1					
	2SC	WP730-A		ESW-2SC	Basic					
CL1	1SA	WP730-A		ESW-1SA	Basic		SSVPMN-0			
	1SB	WP730-A	*E	ESW-1SB	Option 1					
	1AL	2nd DKA	*G	DKA-1AL	Option 1	1AU	1st DKA	*G	DKA-1AU	Basic
		5th CHA	*F	CHA-1AL	Option 4		6th CHA	*F	CHA-1AU	Option 5
	1EL	2nd CHA	*F	CHA-1EL	Option 1	1EU	1st CHA	*F	CHA-1EU	Basic
	1FL	4th CHA	*F	CHA-1FL	Option 3	1FU	3rd CHA	*F	CHA-1FU	Option 2
CL1/	—	SVP-BASIC								Basic
CL2	—	DKCPS-03	*H		Option 1	—	DKCPS-00			Basic

*E: DKC-F710I-ESW

*F: Description of CHA PCBs

CL1/CL2 (1EU, 1EL, 1FU, 1FL, 1AU, 1AL, 2QU, 2QL, 2RU, 2RL, 2MU, 2ML)	WP711-A	*1
	WP711-B	*2
	WP712-B	*3
	WP712-A	*4
	WP711-E	*5
	WP711-F	*6
	WP713-A	*7
	WP712-D	*8
	WP712-C	*9
	WP711-G	*10

*1: DKC-F710I-16MFS
*2: DKC-F710I-16MFL
*3: DKC-F710I-8UFC
*4: DKC-F710I-16UFC
*5: DKC-F710I-16MUS
*6: DKC-F710I-16MUL
*7: DKC-F710I-8FOE
*8: DKC-F710I-8UFCR
*9: DKC-F710I-16UFCR
*10: DKC-F710I-16MUSR

*G: Description of DKA PCBs

CL1/CL2 (1AU, 1AL, 2MU, 2ML)	WP720-A	*1

*1: DKC-F710I-SCA

*H: DKC-F710I-APC
*J: DKC-F710I-SVP
*K: DKC-F710I-HUB

Fig. 2.4-4 DKC-0 PCB Location (In case of DKC710I-CBXA)

3. DKC-0 Location (In case of DKC710I-CBXG)

DKC-0 LOCATION (FRONT)

CL2	2CH	WP741-A	*C	CACHE-2CH	Option 3
	2CG	WP741-A	*B	CACHE-2CG	Option 2
	2CD	WP741-A	*A	CACHE-2CD	Option 1
	2CC	WP741-A		CACHE-2CC	Basic
	2MD	WP752-B	*D	MPB-2MD	Option 1
	2MC	WP752-B		MPB-2MC	Basic
CL1	1MA	WP752-B		MPB-1MA	Basic
	1MB	WP752-B	*D	MPB-1MB	Option 1
	1CA	WP741-A		CACHE-1CA	Basic
	1CB	WP741-A	*A	CACHE-1CB	Option 1
	1CE	WP741-A	*B	CACHE-1CE	Option 2
	1CF	WP741-A	*C	CACHE-1CF	Option 3

*A: DKC-F710I-CPC × 1 set
*B: DKC-F710I-CPC × 2 sets
*C: DKC-F710I-CPC × 3 sets
*D: DKC-F710I-MPR

Fig. 2.4-5 DKC-0 PCB Location (In case of DKC710I-CBXG)

DKC-0 LOCATION (REAR)

CL1/	—	DKCPS-01			Basic	—	DKCPS-02	*H		Option 1
CL2	—	SVP-OPTION	*J				/ HUBBOX-01	*K		Option 1
CL2	2RL	4th CHA	*F	CHA-2RL	Option 3	2RU	3rd CHA	*F	CHA-2RU	Option 2
	2QL	2nd CHA	*F	CHA-2QL	Option 1	2QU	1st CHA	*F	CHA-2QU	Basic
	2ML	2nd DKA	*G	DKA-2ML	Option 1	2MU	1st DKA	*G	DKA-2MU	Basic
		5th CHA	*F	CHA-2ML	Option 4		6th CHA	*F	CHA-2MU	Option 5
	2SD	WP730-A	*E	ESW-2SD	Option 1					
	2SC	WP730-A		ESW-2SC	Basic					
CL1	1SA	WP730-A		ESW-1SA	Basic		SSVPMN-0			
	1SB	WP730-A	*E	ESW-1SB	Option 1					
	1AL	2nd DKA	*G	DKA-1AL	Option 1	1AU	1st DKA	*G	DKA-1AU	Basic
		5th CHA	*F	CHA-1AL	Option 4		6th CHA	*F	CHA-1AU	Option 5
	1EL	2nd CHA	*F	CHA-1EL	Option 1	1EU	1st CHA	*F	CHA-1EU	Basic
	1FL	4th CHA	*F	CHA-1FL	Option 3	1FU	3rd CHA	*F	CHA-1FU	Option 2
CL1/	—	SVP-BASIC								Basic
CL2	—	DKCPS-03	*H		Option 1	—	DKCPS-00			Basic

*E: DKC-F710I-ESW

*F: Description of CHA PCBs

CL1/CL2 (1EU, 1EL, 1FU, 1FL, 1AU, 1AL, 2QU, 2QL, 2RU, 2RL, 2MU, 2ML)	WP711-A	*1
	WP711-B	*2
	WP712-D	*3
	WP712-C	*4
	WP711-G	*5
	WP711-F	*6
	WP713-A	*7

*1: DKC-F710I-16MFS
*2: DKC-F710I-16MFL
*3: DKC-F710I-8UFCR
*4: DKC-F710I-16UFCR
*5: DKC-F710I-16MUSR
*6: DKC-F710I-16MUL
*7: DKC-F710I-8FOE

*G: Description of DKA PCBs

CL1/CL2 (1AU, 1AL, 2MU, 2ML)	WP720-A	*1

*1: DKC-F710I-SCA

*H: DKC-F710I-APC
*J: DKC-F710I-SVP
*K: DKC-F710I-HUB

Fig. 2.4-6 DKC-0 PCB Location (In case of DKC710I-CBXG)

4. DKC-1 Location (In case of DKC710I-CBX)

DKC-1 LOCATION (FRONT)

CL2	2CR	WP741-A	*D	CACHE-2CR	Option 3
	2CQ	WP741-A	*C	CACHE-2CQ	Option 2
	2CM	WP741-A	*B	CACHE-2CM	Option 1
	2CL	WP741-A	*A	CACHE-2CL	Basic
	2MH	WP752-A	*F	MPB-2MH	Option 1
		WP752-B	*H		
	2MG	WP752-A	*E	MPB-2MG	Basic
		WP752-B	*G		
CL1	1ME	WP752-A	*E	MPB-1ME	Basic
		WP752-B	*G		
	1MF	WP752-A	*F	MPB-1MF	Option 1
		WP752-B	*H		
	1CJ	WP741-A	*A	CACHE-1CJ	Basic
	1CK	WP741-A	*B	CACHE-1CK	Option 1
	1CN	WP741-A	*C	CACHE-1CN	Option 2
	1CP	WP741-A	*D	CACHE-1CP	Option 3

*A: DKC-F710I-CPC × 1 set
*B: DKC-F710I-CPC × 2 sets
*C: DKC-F710I-CPC × 3 sets
*D: DKC-F710I-CPC × 4 sets
*E: DKC-F710I-MP × 1 set
*F: DKC-F710I-MP × 2 sets
*G: DKC-F710I-MPR × 1 set
*H: DKC-F710I-MPR × 2 sets

Fig. 2.4-7 DKC-1 PCB Location (In case of DKC710I-CBX)

DKC-1 LOCATION (REAR)

CL1/	—	DKCPS-11			Basic	—	DKCPS-12	*J	Option 1
CL2	—	HUBBOX-11	*L						Option 1
CL2	2UL	4th CHA	*G	CHA-2UL	Option 3	2UU	3rd CHA	*G	CHA-2UU	Option 2
	2TL	2nd CHA	*G	CHA-2TL	Option 1	2TU	1st CHA	*G	CHA-2TU	Basic
	2XL	2nd DKA	*H	DKA-2XL	Option 1	2XU	1st DKA	*H	DKA-2XU	Basic
		5th CHA	*G	CHA-2XL	Option 4		6th CHA	*G	CHA-2XU	Option 5
	2SH	WP730-A	*F	ESW-2SH	Option 1					
	2SG	WP730-A	*E	ESW-2SG	Basic					
CL1	1SE	WP730-A	*E	ESW-1SE	Basic	SSVPMN				
	1SF	WP730-A	*F	ESW-1SF	Option 1					
	1LL	2nd DKA	*H	DKA-1LL	Option 1	1LU	1st DKA	*H	DKA-1LU	Basic
		5th CHA	*G	CHA-1LL	Option 4		6th CHA	*G	CHA-1LU	Option 5
	1GL	2nd CHA	*G	CHA-1GL	Option 1	1GU	1st CHA	*G	CHA-1GU	Basic
	1HL	4th CHA	*G	CHA-1HL	Option 3	1HU	3rd CHA	*G	CHA-1HU	Option 2
CL1/	—	HUBBOX-10	*K							Basic
CL2	—	DKCPS-13	*J		Option 1	—	DKCPS-10			Basic

*E: DKC-F710I-ESW × 1 set
*F: DKC-F710I-ESW × 2 sets
*G: Description of CHA PCBs

CL1/CL2 (1GU, 1GL, 1HU, 1HL, 1LU, 1LL, 2TU, 2TL, 2UU, 2UL, 2XU, 2XL)	WP711-A	*1
	WP711-B	*2
	WP712-B	*3
	WP712-A	*4
	WP711-E	*5
	WP711-F	*6
	WP713-A	*7
	WP712-D	*8
	WP712-C	*9
	WP711-G	*10

*1: DKC-F710I-16MFS
*2: DKC-F710I-16MFL
*3: DKC-F710I-8UFC
*4: DKC-F710I-16UFC
*5: DKC-F710I-16MUS
*6: DKC-F710I-16MUL
*7: DKC-F710I-8FOE
*8: DKC-F710I-8UFCR
*9: DKC-F710I-16UFCR
*10: DKC-F710I-16MUSR

*H: Description of DKA PCBs

CL1/CL2 (1LU, 1LL, 2XU, 2XL)	WP720-A	*1

*1: DKC-F710I-SCA

*J: DKC-F710I-APC
*K: DKC-F710I-HUB × 1 set
*L: DKC-F710I-HUB × 2 sets

Fig. 2.4-8 DKC-1 PCB Location (In case of DKC710I-CBX)

5. DKC-1 Location (In case of DKC710I-CBXB)

DKC-1 LOCATION (FRONT)

CL2	2CR	WP741-A	*C	CACHE-2CR	Option 3
	2CQ	WP741-A	*B	CACHE-2CQ	Option 2
	2CM	WP741-A	*A	CACHE-2CM	Option 1
	2CL	WP741-A		CACHE-2CL	Basic
	2MH	WP752-A	*D	MPB-2MH	Option 1
		WP752-B	*E		
	2MG	WP752-A		MPB-2MG	Basic
		WP752-B			
CL1	1ME	WP752-A		MPB-1ME	Basic
		WP752-B			
	1MF	WP752-A	*D	MPB-1MF	Option 1
		WP752-B	*E		
	1CJ	WP741-A		CACHE-1CJ	Basic
	1CK	WP741-A	*A	CACHE-1CK	Option 1
	1CN	WP741-A	*B	CACHE-1CN	Option 2
	1CP	WP741-A	*C	CACHE-1CP	Option 3

*A: DKC-F710I-CPC × 1 set
*B: DKC-F710I-CPC × 2 sets
*C: DKC-F710I-CPC × 3 sets
*D: DKC-F710I-MP
*E: DKC-F710I-MPR

Fig. 2.4-9 DKC-1 PCB Location (In case of DKC710I-CBXB)

DKC-1 LOCATION (REAR)

CL1/	—	DKCPS-11			Basic	—	DKCPS-12 *H		Option 1
CL2	—	HUBBOX-11 *J							Option 1
CL2	2UL	4th CHA	*F	CHA-2UL	Option 3	2UU	3rd CHA *F	CHA-2UU	Option 2
	2TL	2nd CHA	*F	CHA-2TL	Option 1	2TU	1st CHA *F	CHA-2TU	Basic
	2XL	2nd DKA	*G	DKA-2XL	Option 1	2XU	1st DKA *G	DKA-2XU	Basic
		5th CHA	*F	CHA-2XL	Option 4		6th CHA *F	CHA-2XU	Option 5
	2SH	WP730-A	*E	ESW-2SH	Option 1				
	2SG	WP730-A		ESW-2SG	Basic				
CL1	1SE	WP730-A		ESW-1SE	Basic	SSVPMN-1			
	1SF	WP730-A	*E	ESW-1SF	Option 1				
	1LL	2nd DKA	*G	DKA-1LL	Option 1	1LU	1st DKA *G	DKA-1LU	Basic
		5th CHA	*F	CHA-1LL	Option 4		6th CHA *F	CHA-1LU	Option 5
	1GL	2nd CHA	*F	CHA-1GL	Option 1	1GU	1st CHA *F	CHA-1GU	Basic
	1HL	4th CHA	*F	CHA-1HL	Option 3	1HU	3rd CHA *F	CHA-1HU	Option 2
CL1/	—	HUBBOX-10							Basic
CL2	—	DKCPS-13 *H			Option 1	—	DKCPS-10		Basic

*E: DKC-F710I-ESW

*F: Description of CHA PCBs

CL1/CL2 (1GU, 1GL, 1HU, 1HL, 1LU, 1LL, 2TU, 2TL, 2UU, 2UL, 2XU, 2XL)	WP711-A	*1
	WP711-B	*2
	WP712-B	*3
	WP712-A	*4
	WP711-E	*5
	WP711-F	*6
	WP713-A	*7
	WP712-D	*8
	WP712-C	*9
	WP711-G	*10

*1: DKC-F710I-16MFS
*2: DKC-F710I-16MFL
*3: DKC-F710I-8UFC
*4: DKC-F710I-16UFC
*5: DKC-F710I-16MUS
*6: DKC-F710I-16MUL
*7: DKC-F710I-8FOE
*8: DKC-F710I-8UFCR
*9: DKC-F710I-16UFCR
*10: DKC-F710I-16MUSR

*G: Description of DKA PCBs

CL1/CL2 (1LU, 1LL, 2XU, 2XL)	WP720-A	*1

*1: DKC-F710I-SCA

*H: DKC-F710I-APC
*J: DKC-F710I-HUB

Fig. 2.4-10 DKC-1 PCB Location (In case of DKC710I-CBXB)

6. DKC-1 Location (In case of DKC710I-CBXH)

DKC-1 LOCATION (FRONT)

CL2	2CR	WP741-A	*C	CACHE-2CR	Option 3
	2CQ	WP741-A	*B	CACHE-2CQ	Option 2
	2CM	WP741-A	*A	CACHE-2CM	Option 1
	2CL	WP741-A		CACHE-2CL	Basic
	2MH	WP752-B	*D	MPB-2MH	Option 1
	2MG	WP752-B		MPB-2MG	Basic
CL1	1ME	WP752-B		MPB-1ME	Basic
	1MF	WP752-B	*D	MPB-1MF	Option 1
	1CJ	WP741-A		CACHE-1CJ	Basic
	1CK	WP741-A	*A	CACHE-1CK	Option 1
	1CN	WP741-A	*B	CACHE-1CN	Option 2
	1CP	WP741-A	*C	CACHE-1CP	Option 3

*A: DKC-F710I-CPC × 1 set
*B: DKC-F710I-CPC × 2 sets
*C: DKC-F710I-CPC × 3 sets
*D: DKC-F710I-MPR

Fig. 2.4-11 DKC-1 PCB Location (In case of DKC710I-CBXH)

DKC-1 LOCATION (REAR)

CL1/	—	DKCPS-11			Basic	—	DKCPS-12	*H		Option 1
CL2	—	HUBBOX-11	*J							Option 1
CL2	2UL	4th CHA	*F	CHA-2UL	Option 3	2UU	3rd CHA	*F	CHA-2UU	Option 2
	2TL	2nd CHA	*F	CHA-2TL	Option 1	2TU	1st CHA	*F	CHA-2TU	Basic
	2XL	2nd DKA	*G	DKA-2XL	Option 1	2XU	1st DKA	*G	DKA-2XU	Basic
		5th CHA	*F	CHA-2XL	Option 4		6th CHA	*F	CHA-2XU	Option 5
	2SH	WP730-A	*E	ESW-2SH	Option 1					
	2SG	WP730-A		ESW-2SG	Basic					
CL1	1SE	WP730-A		ESW-1SE	Basic	SSVPMN-1				
	1SF	WP730-A	*E	ESW-1SF	Option 1					
	1LL	2nd DKA	*G	DKA-1LL	Option 1	1LU	1st DKA	*G	DKA-1LU	Basic
		5th CHA	*F	CHA-1LL	Option 4		6th CHA	*F	CHA-1LU	Option 5
	1GL	2nd CHA	*F	CHA-1GL	Option 1	1GU	1st CHA	*F	CHA-1GU	Basic
	1HL	4th CHA	*F	CHA-1HL	Option 3	1HU	3rd CHA	*F	CHA-1HU	Option 2
CL1/	—	HUBBOX-10								Basic
CL2	—	DKCPS-13	*H		Option 1	—	DKCPS-10			Basic

*E: DKC-F710I-ESW

*F: Description of CHA PCBs

CL1/CL2 (1GU, 1GL, 1HU, 1HL, 1LU, 1LL, 2TU, 2TL, 2UU, 2UL, 2XU, 2XL)	WP711-A	*1
	WP711-B	*2
	WP712-D	*3
	WP712-C	*4
	WP711-G	*5
	WP711-F	*6
	WP713-A	*7

*1: DKC-F710I-16MFS
*2: DKC-F710I-16MFL
*3: DKC-F710I-8UFCR
*4: DKC-F710I-16UFCR
*5: DKC-F710I-16MUSR
*6: DKC-F710I-16MUL
*7: DKC-F710I-8FOE

*G: Description of DKA PCBs

CL1/CL2 (1LU, 1LL, 2XU, 2XL)	WP720-A	*1

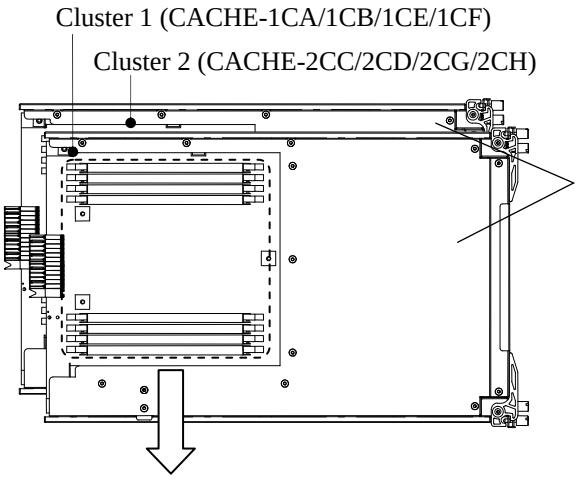
*1: DKC-F710I-SCA

*H: DKC-F710I-APC
*J: DKC-F710I-HUB

Fig. 2.4-12 DKC-1 PCB Location (In case of DKC710I-CBXH)

2.5 Cache Memory Module Location

1. DKC-0



(Note 1) The CM module location *1 through *16 correspond to the CM Location listed in Table 2.5-1 through Table 2.5-6.

CACHE-1CA/2CC (Basic)

CM10:	*2
CM30:	*4
CM11:	*2
CM31:	*4

CM21:	*3
CM01:	*1
CM20:	*3
CM00:	*1

CACHE-1CB/2CD (Option 1)

CM10:	*6
CM30:	*8
CM11:	*6
CM31:	*8

CM21:	*7
CM01:	*5
CM20:	*7
CM00:	*5

CACHE-1CE/2CG (Option 2)

CM10:	*10
CM30:	*12
CM11:	*10
CM31:	*12

CM21:	*11
CM01:	*9
CM20:	*11
CM00:	*9

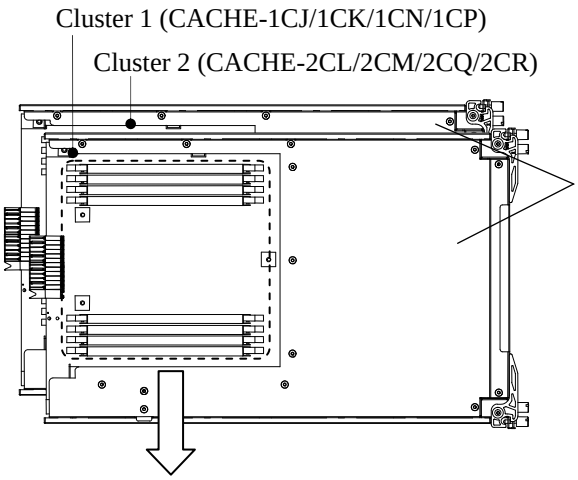
CACHE-1CF/2CH (Option 3)

CM10:	*14
CM30:	*16
CM11:	*14
CM31:	*16

CM21:	*15
CM01:	*13
CM20:	*15
CM00:	*13

Fig. 2.5-1 Cache Memory (CM) Module Location(DKC-0)

2. DKC-1



(Note 1) The CM module location *17 through *32 correspond to the CM Location listed in Table 2.5-1 through Table 2.5-6.

CACHE-1CJ/2CL (Basic)

CM10:	*18
CM30:	*20
CM11:	*18
CM31:	*20

CM21:	*19
CM01:	*17
CM20:	*19
CM00:	*17

CACHE-1CK/2CM (Option 1)

CM10:	*22
CM30:	*24
CM11:	*22
CM31:	*24

CM21:	*23
CM01:	*21
CM20:	*23
CM00:	*21

CACHE-1CN/2CQ (Option 2)

CM10:	*26
CM30:	*28
CM11:	*26
CM31:	*28

CM21:	*27
CM01:	*25
CM20:	*27
CM00:	*25

CACHE-1CP/2CR (Option 3)

CM10:	*30
CM30:	*32
CM11:	*30
CM31:	*32

CM21:	*31
CM01:	*29
CM20:	*31
CM00:	*29

Fig. 2.5-2 Cache Memory (CM) Module Location (DKC-1)

Table 2.5-1 Cache memory capacity and number of necessary options ①
(Serial Installation)

Serial Installation											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
16GB	*1	—	—	—	—	—	—	—	1	0	0
32GB	*1,*2	—	—	—	—	—	—	—	2	0	0
48GB	*1,*2, *3	—	—	—	—	—	—	—	3	0	0
64GB	*1,*2, *3,*4	—	—	—	—	—	—	—	4	0	1
80GB	*1,*2, *3,*4	*5	—	—	—	—	—	—	5	1	1
96GB	*1,*2, *3,*4	*5,*6	—	—	—	—	—	—	6	1	1
112GB	*1,*2, *3,*4	*5,*6, *7	—	—	—	—	—	—	7	1	1
128GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	—	—	—	—	8	1	2
144GB	*1,*2, *3,*4	*5,*6, *7,*8	*9	—	—	—	—	—	9	2	2
160GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10	—	—	—	—	—	10	2	2
176GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	—	—	—	—	—	11	2	2
192GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	—	—	—	—	12	2	3
208GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13	—	—	—	—	13	3	3
224GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14	—	—	—	—	14	3	3
240GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15	—	—	—	—	15	3	3
256GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	—	—	—	—	16	3	4
272GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17	—	—	—	17	3	4
288GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18	—	—	—	18	3	4
304GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19	—	—	—	19	3	4
320GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	—	—	—	20	3	5

(To be continued.)

(Continued from preceding sheet.)

Serial Installation											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
336GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21	—	—	21	4	5
352GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22	—	—	22	4	5
368GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23	—	—	23	4	5
384GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	—	—	24	4	6
400GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25	—	25	5	6
416GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26	—	26	5	6
432GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27	—	27	5	6
448GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	—	28	5	7
464GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29	29	6	7
480GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30	30	6	7
496GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30, *31	31	6	7
512GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30, *31,*32	32	6	8

Note 1: The above numbers (*1 through *32) represent the CM Module locations shown in Fig. 2.5-1 or Fig. 2.5-2.

Table 2.5-2 Cache memory capacity and number of necessary options ② (Serial Installation)

Serial Installation												
Cache Memory Capacity	CM Location (Note 1)								Number of options			
	DKC-0				DKC-1				DKC-F710I			
	CPC-0 (1CA/ 2CC)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C32G/ -C32GR	-CPC	-BM 64 (Note 2)	-BM 128
32GB	*1	—	—	—	—	—	—	—	1	0	0	0
64GB	*1,*2	—	—	—	—	—	—	—	2	0	1	0
96GB	*1,*2, *3	—	—	—	—	—	—	—	3	0	1	0
128GB	*1,*2, *3,*4	—	—	—	—	—	—	—	4	0	0	1
160GB	*1,*2, *3,*4	*5	—	—	—	—	—	—	5	1	0	1
192GB	*1,*2, *3,*4	*5,*6	—	—	—	—	—	—	6	1	1	1
224GB	*1,*2, *3,*4	*5,*6, *7	—	—	—	—	—	—	7	1	1	1
256GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	—	—	—	—	8	1	0	2
288GB	*1,*2, *3,*4	*5,*6, *7,*8	*9	—	—	—	—	—	9	2	0	2
320GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10	—	—	—	—	—	10	2	1	2
352GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	—	—	—	—	—	11	2	1	2
384GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	—	—	—	—	12	2	0	3
416GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13	—	—	—	—	13	3	0	3
448GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14	—	—	—	—	14	3	1	3
480GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15	—	—	—	—	15	3	1	3
512GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	—	—	—	—	16	3	0	4
544GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17	—	—	—	17	3	0	4
576GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18	—	—	—	18	3	1	4
608GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19	—	—	—	19	3	1	4
640GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	—	—	—	20	3	0	5

(To be continued.)

(Continued from preceding sheet.)

Serial Installation												
Cache Memory Capacity	CM Location (Note 1)								Number of options			
	DKC-0				DKC-1				DKC-F710I			
	CPC-0 (1CA/ 2CC)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C32G/ -C32GR	-CPC	-BM 64 (Note 2)	-BM 128
672GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21	—	—	21	4	0	5
704GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22	—	—	22	4	1	5
736GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23	—	—	23	4	1	5
768GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	—	—	24	4	0	6
800GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25	—	25	5	0	6
832GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26	—	26	5	1	6
864GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27	—	27	5	1	6
896GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	—	28	5	0	7
928GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29	29	6	0	7
960GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30	30	6	1	7
992GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30, *31	31	6	1	7
1024GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30, *31,*32	32	6	0	8

Note 1: The above numbers (*1 through *32) represent the CM Module locations shown in Fig. 2.5-1 or Fig. 2.5-2.

Note 2: DKC-F710I-BM128 can be used instead of DKC-F710I-BM64.

Table 2.5-3 Cache memory capacity and number of necessary options ③
(Single DKC Model, Parallel Installation)

Parallel Installation (Single DKC model)							
Cache Memory Capacity	CM Location (Note 1)				Number of options		
	DKC-0				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	-C16G/ -C16GR	-CPC	-BM64
32GB	*1	*5	—	—	2	1	0
48GB	*1,*2	*5	—	—	3	1	0
	*1	*5	*9	—	3	2	0
64GB	*1,*2, *3	*5	—	—	4	1	0
	*1,*2	*5,*6	—	—	4	1	0
	*1,*2	*5	*9	—	4	2	0
	*1	*5	*9	*13	4	3	0
80GB	*1,*2, *3,*4	*5	—	—	5	1	1
	*1,*2, *3	*5,*6	—	—	5	1	0
	*1,*2, *3	*5	*9	—	5	2	0
	*1,*2	*5,*6	*9	—	5	2	0
	*1,*2	*5	*9	*13	5	3	0
96GB	*1,*2, *3,*4	*5,*6	—	—	6	1	1
	*1,*2, *3	*5,*6, *7	—	—	6	1	0
	*1,*2, *3,*4	*5	*9	—	6	2	1
	*1,*2, *3	*5,*6	*9	—	6	2	0
	*1,*2	*5,*6	*9,*10	—	6	2	0
	*1,*2, *3	*5	*9	*13	6	3	0
	*1,*2	*5,*6	*9	*13	6	3	0
112GB	*1,*2, *3,*4	*5,*6, *7	—	—	7	1	1
	*1,*2, *3,*4	*5,*6	*9	—	7	2	1
	*1,*2, *3	*5,*6	*9,*10	—	7	2	0
	*1,*2, *3,*4	*5	*9	*13	7	3	1
	*1,*2, *3	*5,*6	*9	*13	7	3	0
	*1,*2	*5,*6	*9,*10	*13	7	3	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Single DKC model)							
Cache Memory Capacity	CM Location (Note 1)				Number of options		
	DKC-0				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	-C16G/ -C16GR	-CPC	-BM64
128GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	8	1	2
	*1,*2, *3,*4	*5,*6	*9,*10	—	8	2	1
	*1,*2, *3	*5,*6, *7	*9,*10	—	8	2	0
	*1,*2, *3,*4	*5,*6	*9	*13	8	3	1
	*1,*2, *3	*5,*6	*9,*10	*13	8	3	0
	*1,*2	*5,*6	*9,*10	*13,*14	8	3	0
144GB	*1,*2, *3,*4	*5,*6, *7	*9,*10	—	9	2	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	9	2	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13	9	3	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	9	3	0
160GB	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	10	2	1
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	10	3	1
	*1,*2, *3	*5,*6, *7	*9,*10	*13,*14	10	3	0
176GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	—	11	2	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10	*13,*14	11	3	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14	11	3	0
192GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	12	2	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14	12	3	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	12	3	0
208GB	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	13	3	1
224GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	*13,*14, *15	14	3	2
240GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15	15	3	3
256GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	16	3	4

Note 1: The above numbers (*1 through *32) represent the CM Module locations shown in Fig. 2.5-1 or Fig. 2.5-2.

Note 2: The required SM capacity shown in Table 6.2-1 (INST06-70) must be installed in CPC-0.

Table 2.5-4 Cache memory capacity and number of necessary options ④
(Single DKC Model, Parallel Installation)

Parallel Installation (Single DKC model)								
Cache Memory Capacity	CM Location (Note 1)				Number of options			
	DKC-0				DKC-F710I			
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	-C32G/ -C32GR	-CPC	-BM64 (Note 3)	-BM128
64GB	*1	*5	—	—	2	1	0	0
96GB	*1,*2	*5	—	—	3	1	1	0
	*1	*5	*9	—	3	2	0	0
128GB	*1,*2, *3	*5	—	—	4	1	1	0
	*1,*2	*5,*6	—	—	4	1	2	0
	*1,*2	*5	*9	—	4	2	1	0
	*1	*5	*9	*13	4	3	0	0
160GB	*1,*2, *3	*5,*6	—	—	5	1	2	0
	*1,*2, *3	*5	*9	—	5	2	1	0
	*1,*2	*5,*6	*9	—	5	2	2	0
	*1,*2	*5	*9	*13	5	3	1	0
192GB	*1,*2, *3,*4	*5,*6	—	—	6	1	1	1
	*1,*2, *3	*5,*6, *7	—	—	6	1	2	0
	*1,*2, *3	*5,*6	*9	—	6	2	2	0
	*1,*2	*5,*6	*9,*10	—	6	2	3	0
	*1,*2, *3	*5	*9	*13	6	3	1	0
	*1,*2	*5,*6	*9	*13	6	3	2	0
224GB	*1,*2, *3,*4	*5,*6, *7	—	—	7	1	1	1
	*1,*2, *3	*5,*6	*9,*10	—	7	2	3	0
	*1,*2, *3	*5,*6	*9	*13	7	3	2	0
	*1,*2	*5,*6	*9,*10	*13	7	3	3	0
256GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	8	1	0	2
	*1,*2, *3,*4	*5,*6	*9,*10	—	8	2	2	1
	*1,*2, *3	*5,*6, *7	*9,*10	—	8	2	3	0
	*1,*2, *3	*5,*6	*9,*10	*13	8	3	3	0
	*1,*2	*5,*6	*9,*10	*13,*14	8	3	4	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Single DKC model)								
Cache Memory Capacity	CM Location (Note 1)				Number of options			
	DKC-0				DKC-F710I			
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	-C32G/ -C32GR	-CPC	-BM64 (Note 3)	-BM128
288GB	*1,*2, *3,*4	*5,*6, *7	*9,*10	—	9	2	2	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	9	2	3	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	9	3	4	0
320GB	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	10	2	2	1
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	10	3	3	1
	*1,*2, *3	*5,*6, *7	*9,*10	*13,*14	10	3	4	0
352GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	—	11	2	1	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10	*13,*14	10	3	3	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14	11	3	4	0
384GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	12	2	0	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14	12	3	3	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	12	3	4	0
416GB	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	13	3	3	1
448GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	*13,*14, *15	14	3	2	2
480GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15	15	3	1	3
512GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	16	3	0	4

Note 1: The above numbers (*1 through *32) represent the CM Module locations shown in Fig. 2.5-1 or Fig. 2.5-2.

Note 2: The required SM capacity shown in Table 6.2-1 (INST06-70) must be installed in CPC-0.

Note 3: DKC-F710I-BM128 can be used instead of DKC-F710I-BM64.

Table 2.5-5 Cache memory capacity and number of necessary options ⑤ (Twin DKC Model, Parallel Installation)

Parallel Installation (Twin DKC model)											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
32GB	*1	—	—	—	*17	—	—	—	2	0	0
48GB	*1,*2	—	—	—	*17	—	—	—	3	0	0
	*1	*5	—	—	*17	—	—	—	3	1	0
64GB	*1,*2, *3	—	—	—	*17	—	—	—	4	0	0
	*1,*2	—	—	—	*17,*18	—	—	—	4	0	0
	*1,*2	*5	—	—	*17	—	—	—	4	1	0
	*1	*5	—	—	*17	*21	—	—	4	2	0
80GB	*1,*2, *3,*4	—	—	—	*17	—	—	—	5	0	1
	*1,*2, *3	—	—	—	*17,*18	—	—	—	5	0	0
	*1,*2, *3	*5	—	—	*17	—	—	—	5	1	0
	*1,*2	*5,*6	—	—	*17	—	—	—	5	1	0
	*1,*2	*5	—	—	*17	*21	—	—	5	2	0
	*1	*5	*9	—	*17	*21	—	—	5	3	0
96GB	*1,*2, *3,*4	—	—	—	*17,*18	—	—	—	6	0	1
	*1,*2, *3	—	—	—	*17,*18, *19	—	—	—	6	0	0
	*1,*2, *3,*4	*5	—	—	*17	—	—	—	6	1	1
	*1,*2, *3	*5,*6	—	—	*17	—	—	—	6	1	0
	*1,*2	*5,*6	—	—	*17,*18	—	—	—	6	1	0
	*1,*2, *3	*5	—	—	*17	*21	—	—	6	2	0
	*1,*2	*5,*6	—	—	*17	*21	—	—	6	2	0
	*1,*2	*5	*9	—	*17	*21	—	—	6	3	0
	*1	*5	*9	—	*17	*21	*25	—	6	4	0
112GB	*1,*2, *3,*4	—	—	—	*17,*18, *19	—	—	—	7	0	1
	*1,*2, *3,*4	*5,*6	—	—	*17	—	—	—	7	1	1
	*1,*2, *3	*5,*6	—	—	*17,*18	—	—	—	7	1	0
	*1,*2, *3,*4	*5	—	—	*17	*21	—	—	7	2	1
	*1,*2, *3	*5,*6	—	—	*17	*21	—	—	7	2	0
	*1,*2	*5,*6	—	—	*17,*18	*21	—	—	7	2	0
	*1,*2, *3	*5	*9	—	*17	*21	—	—	7	3	0
	*1,*2	*5,*6	*9	—	*17	*21	—	—	7	3	0
	*1,*2	*5	*9	—	*17	*21	*25	—	7	4	0
	*1	*5	*9	*13	*17	*21	*25	—	7	5	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
128GB	*1,*2, *3,*4	—	—	—	*17,*18, *19,*20	—	—	—	8	0	2
	*1,*2, *3,*4	*5,*6	—	—	*17,*18	—	—	—	8	1	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18	—	—	—	8	1	0
	*1,*2, *3,*4	*5,*6	—	—	*17	*21	—	—	8	2	1
	*1,*2, *3	*5,*6	—	—	*17,*18	*21	—	—	8	2	0
	*1,*2	*5,*6	—	—	*17,*18	*21,*22	—	—	8	2	0
	*1,*2, *3,*4	*5	*9	—	*17	*21	—	—	8	3	1
	*1,*2, *3	*5,*6	*9	—	*17	*21	—	—	8	3	0
	*1,*2	*5,*6	*9,*10	—	*17	*21	—	—	8	3	0
	*1,*2, *3	*5	*9	—	*17	*21	*25	—	8	4	0
	*1,*2	*5,*6	*9	—	*17	*21	*25	—	8	4	0
	*1,*2	*5	*9	*13	*17	*21	*25	—	8	5	0
	*1	*5	*9	*13	*17	*21	*25	*29	8	6	0
144GB	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18	—	—	—	9	1	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18, *19	—	—	—	9	1	0
	*1,*2, *3,*4	*5,*6	—	—	*17,*18	*21	—	—	9	2	1
	*1,*2, *3	*5,*6	—	—	*17,*18	*21,*22	—	—	9	2	0
	*1,*2, *3,*4	*5,*6	*9	—	*17	*21	—	—	9	3	1
	*1,*2, *3	*5,*6	*9,*10	—	*17	*21	—	—	9	3	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21	—	—	9	3	0
	*1,*2, *3,*4	*5	*9	—	*17	*21	*25	—	9	4	1
	*1,*2, *3	*5,*6	*9	—	*17	*21	*25	—	9	4	0
	*1,*2	*5,*6	*9,*10	—	*17	*21	*25	—	9	4	0
	*1,*2, *3	*5	*9	*13	*17	*21	*25	—	9	5	0
	*1,*2	*5,*6	*9	*13	*17	*21	*25	—	9	5	0
	*1,*2	*5	*9	*13	*17	*21	*25	*29	9	6	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
160GB	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18, *19	—	—	—	10	1	1
	*1,*2, *3,*4	*5,*6	—	—	*17,*18	*21,*22	—	—	10	2	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18	*21,*22	—	—	10	2	0
	*1,*2, *3,*4	*5,*6	*9,*10	—	*17	*21	—	—	10	3	1
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21	—	—	10	3	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21,*22	—	—	10	3	0
	*1,*2, *3,*4	*5,*6	*9	—	*17	*21	*25	—	10	4	1
	*1,*2, *3	*5,*6	*9,*10	—	*17	*21	*25	—	10	4	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21	*25	—	10	4	0
	*1,*2, *3,*4	*5	*9	*13	*17	*21	*25	—	10	5	1
	*1,*2, *3	*5,*6	*9	*13	*17	*21	*25	—	10	5	0
	*1,*2	*5,*6	*9,*10	*13	*17	*21	*25	—	10	5	0
	*1,*2, *3	*5	*9	*13	*17	*21	*25	*29	10	6	0
	*1,*2	*5,*6	*9	*13	*17	*21	*25	*29	10	6	0
176GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19	—	—	—	11	1	2
	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18	*21,*22	—	—	11	2	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18, *19	*21,*22	—	—	11	2	0
	*1,*2, *3,*4	*5,*6	*9,*10	—	*17,*18	*21	—	—	11	3	1
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21,*22	—	—	11	3	0
	*1,*2, *3,*4	*5,*6	*9,*10	—	*17	*21	*25	—	11	4	1
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21	*25	—	11	4	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21,*22	*25	—	11	4	0
	*1,*2, *3,*4	*5,*6	*9	*13	*17	*21	*25	—	11	5	1
	*1,*2, *3	*5,*6	*9,*10	*13	*17	*21	*25	—	11	5	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17	*21	*25	—	11	5	0
	*1,*2, *3,*4	*5	*9	*13	*17	*21	*25	*29	11	6	1
	*1,*2, *3	*5,*6	*9	*13	*17	*21	*25	*29	11	6	0
	*1,*2	*5,*6	*9,*10	*13	*17	*21	*25	*29	11	6	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
192GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19,*20	—	—	—	12	1	3
	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18, *19	*21,*22	—	—	12	2	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18, *19	*21,*22, *23	—	—	12	2	0
	*1,*2, *3,*4	*5,*6	*9,*10	—	*17,*18	*21,*22	—	—	12	3	1
	*1,*2, *3	*5,*6, *7	*9,*10	—	*17,*18	*21,*22	—	—	12	3	0
	*1,*2, *3,*4	*5,*6	*9,*10	—	*17,*18	*21	*25	—	12	4	1
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21,*22	*25	—	12	4	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21,*22	*25,*26	—	12	4	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13	*17	*21	*25	—	12	5	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17	*21	*25	—	12	5	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	—	12	5	0
	*1,*2, *3,*4	*5,*6	*9	*13	*17	*21	*25	*29	12	6	1
	*1,*2, *3	*5,*6	*9,*10	*13	*17	*21	*25	*29	12	6	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17	*21	*25	*29	12	6	0
208GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19,*20	*21	—	—	13	2	3
	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18, *19	*21,*22, *23	—	—	13	2	1
	*1,*2, *3,*4	*5,*6, *7	*9,*10	—	*17,*18	*21,*22	—	—	13	3	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18	*21,*22	—	—	13	3	0
	*1,*2, *3,*4	*5,*6	*9,*10	—	*17,*18	*21,*22	*25	—	13	4	1
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21,*22	*25,*26	—	13	4	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17	*21	*25	—	13	5	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	—	13	5	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25	—	13	5	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13	*17	*21	*25	*29	13	6	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17	*21	*25	*29	13	6	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	*29	13	6	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
224GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19,*20	*21,*22	—	—	14	2	3
	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19	*21,*22, *23	—	—	14	2	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18	*21,*22	—	—	14	3	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22	—	—	14	3	0
	*1,*2, *3,*4	*5,*6	*9,*10	—	*17,*18	*21,*22	*25,*26	—	14	4	1
	*1,*2, *3	*5,*6, *7	*9,*10	—	*17,*18	*21,*22	*25,*26	—	14	4	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	—	14	5	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25	—	14	5	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	14	5	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17	*21	*25	*29	14	6	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	*29	14	6	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25	*29	14	6	0
240GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19,*20	*21,*22, *23	—	—	15	2	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22	—	—	15	3	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	—	—	15	3	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10	—	*17,*18	*21,*22	*25,*26	—	15	4	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18	*21,*22	*25,*26	—	15	4	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25	—	15	5	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	15	5	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	*29	15	6	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25	*29	15	6	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29	15	6	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
256GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19,*20	*21,*22, *23,*24	—	—	16	2	4
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	—	—	16	3	1
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18	*21,*22	*25,*26	—	16	4	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22	*25,*26	—	16	4	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	16	5	1
	*1,*2, *3	*5,*6, *7	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	16	5	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	*29	16	6	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29	16	6	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	16	6	0
272GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21	—	—	17	3	4
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	—	—	17	3	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22	*25,*26	—	17	4	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26	—	17	4	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	17	5	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14	*17,*18	*21,*22	*25,*26	—	17	5	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29	17	6	1
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	17	6	0
288GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22	—	—	18	3	4
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19	*21,*22, *23	—	—	18	3	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26	—	18	4	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	18	4	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14	*17,*18	*21,*22	*25,*26	—	18	5	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18	*21,*22	*25,*26	—	18	5	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*27,*28	18	6	1
	*1,*2, *3	*5,*6, *7	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	18	6	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
304GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23	—	—	19	3	4
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	19	4	1
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18	*21,*22	*25,*26	—	19	5	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22	*25,*26	—	19	5	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	19	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	19	6	0
320GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23,*24	—	—	20	3	5
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	20	4	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22	*25,*26	—	20	5	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26	—	20	5	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	20	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18	*21,*22	*25,*26	*29,*30	20	6	0
336GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	21	4	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26	—	21	5	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	21	5	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18	*21,*22	*25,*26	*29,*30	21	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22	*25,*26	*29,*30	21	6	0
352GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23	*25,*26, *27	—	22	4	4
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	22	5	1
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22	*25,*26	*29,*30	22	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26	*29,*30	22	6	0
368GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27	—	23	4	5
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	23	5	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26	*29,*30	23	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30	23	6	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)											
Cache Memory Capacity	CM Location (Note 1)								Number of options		
	DKC-0				DKC-1				DKC-F710I		
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C16G/ -C16GR	-CPC	-BM64
384GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	—	24	4	6
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	24	5	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30	24	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	24	6	0
400GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	25	5	4
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	25	6	1
416GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23	*25,*26, *27	—	26	5	5
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	26	6	2
432GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *23,*24	*25,*26, *19,*20	*21,*22, *27	—	27	5	6
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	27	6	3
448GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	—	28	5	7
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	28	6	4
464GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23	*25,*26, *27	*29,*30, *31	29	6	5
480GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27	*29,*30, *31	30	6	6
496GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30, *31	31	6	7
512GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30, *31,*32	32	6	8

Note 1: The above numbers (*1 through *32) represent the CM Module locations shown in Fig. 2.5-1 or Fig. 2.5-2.

Note 2: The required SM capacity shown in Table 6.2-1 (INST06-70) must be installed in CPC-0.

Table 2.5-6 Cache memory capacity and number of necessary options ⑥ (Twin DKC Model, Parallel Installation)

Parallel Installation (Twin DKC model)												
Cache Memory Capacity	CM Location (Note 1)								Number of options			
	DKC-0				DKC-1				DKC-F710I			
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C32G/ -C32GR	-CPC	-BM 64 (Note 3)	-BM 128
64GB	*1	—	—	—	*17	—	—	—	2	0	0	0
96GB	*1,*2	—	—	—	*17	—	—	—	3	0	1	0
	*1	*5	—	—	*17	—	—	—	3	1	0	0
128GB	*1,*2, *3	—	—	—	*17	—	—	—	4	0	1	0
	*1,*2	—	—	—	*17,*18	—	—	—	4	0	2	0
	*1,*2	*5	—	—	*17	—	—	—	4	1	1	0
	*1	*5	—	—	*17	*21	—	—	4	2	0	0
160GB	*1,*2, *3	—	—	—	*17,*18	—	—	—	5	0	2	0
	*1,*2, *3	*5	—	—	*17	—	—	—	5	1	1	0
	*1,*2	*5,*6	—	—	*17	—	—	—	5	1	2	0
	*1,*2	*5	—	—	*17	*21	—	—	5	2	1	0
	*1	*5	*9	—	*17	*21	—	—	5	3	0	0
192GB	*1,*2, *3,*4	—	—	—	*17,*18	—	—	—	6	0	1	1
	*1,*2, *3	—	—	—	*17,*18, *19	—	—	—	6	0	2	0
	*1,*2, *3	*5,*6	—	—	*17	—	—	—	6	1	2	0
	*1,*2	*5,*6	—	—	*17,*18	—	—	—	6	1	3	0
	*1,*2, *3	*5	—	—	*17	*21	—	—	6	2	1	0
	*1,*2	*5,*6	—	—	*17	*21	—	—	6	2	2	0
	*1,*2	*5	*9	—	*17	*21	—	—	6	3	1	0
	*1	*5	*9	—	*17	*21	*25	—	6	4	0	0
224GB	*1,*2, *3,*4	—	—	—	*17,*18, *19	—	—	—	7	0	1	1
	*1,*2, *3	*5,*6	—	—	*17,*18	—	—	—	7	1	3	0
	*1,*2, *3	*5,*6	—	—	*17	*21	—	—	7	2	2	0
	*1,*2	*5,*6	—	—	*17,*18	*21	—	—	7	2	3	0
	*1,*2, *3	*5	*9	—	*17	*21	—	—	7	3	1	0
	*1,*2	*5,*6	*9	—	*17	*21	—	—	7	3	2	0
	*1,*2	*5	*9	—	*17	*21	*25	—	7	4	1	0
	*1	*5	*9	*13	*17	*21	*25	—	7	5	0	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)												
Cache Memory Capacity	CM Location (Note 1)								Number of options			
	DKC-0				DKC-1				DKC-F710I			
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C32G/ -C32GR	-CPC	-BM 64 (Note 3)	-BM 128
256GB	*1,*2, *3,*4	—	—	—	*17,*18, *19,*20	—	—	—	8	0	0	2
	*1,*2, *3,*4	*5,*6	—	—	*17,*18	—	—	—	8	1	2	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18	—	—	—	8	1	3	0
	*1,*2, *3	*5,*6	—	—	*17,*18	*21	—	—	8	2	3	0
	*1,*2	*5,*6	—	—	*17,*18	*21,*22	—	—	8	2	4	0
	*1,*2, *3	*5,*6	*9	—	*17	*21	—	—	8	3	2	0
	*1,*2	*5,*6	*9,*10	—	*17	*21	—	—	8	3	3	0
	*1,*2, *3	*5	*9	—	*17	*21	*25	—	8	4	1	0
	*1,*2	*5,*6	*9	—	*17	*21	*25	—	8	4	2	0
	*1,*2	*5	*9	*13	*17	*21	*25	—	8	5	1	0
	*1	*5	*9	*13	*17	*21	*25	*29	8	6	0	0
288GB	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18	—	—	—	9	1	2	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18, *19	—	—	—	9	1	3	0
	*1,*2, *3	*5,*6	—	—	*17,*18	*21,*22	—	—	9	2	4	0
	*1,*2, *3	*5,*6	*9,*10	—	*17	*21	—	—	9	3	3	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21	—	—	9	3	4	0
	*1,*2, *3	*5,*6	*9	—	*17	*21	*25	—	9	4	2	0
	*1,*2	*5,*6	*9,*10	—	*17	*21	*25	—	9	4	3	0
	*1,*2, *3	*5	*9	*13	*17	*21	*25	—	9	5	1	0
	*1,*2	*5,*6	*9	*13	*17	*21	*25	—	9	5	2	0
	*1,*2	*5	*9	*13	*17	*21	*25	*29	9	6	1	0
320GB	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18, *19	—	—	—	10	1	2	1
	*1,*2, *3,*4	*5,*6	—	—	*17,*18	*21,*22	—	—	10	2	3	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18	*21,*22	—	—	10	2	4	0
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21	—	—	10	3	4	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21,*22	—	—	10	3	4	0
	*1,*2, *3	*5,*6	*9,*10	—	*17	*21	*25	—	10	4	3	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21	*25	—	10	4	4	0
	*1,*2, *3	*5,*6	*9	*13	*17	*21	*25	—	10	5	2	0
	*1,*2	*5,*6	*9,*10	*13	*17	*21	*25	—	10	5	3	0
	*1,*2, *3	*5	*9	*13	*17	*21	*25	*29	10	6	1	0
	*1,*2	*5,*6	*9	*13	*17	*21	*25	*29	10	6	2	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)												
Cache Memory Capacity	CM Location (Note 1)								Number of options			
	DKC-0				DKC-1				DKC-F710I			
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C32G/ -C32GR	-CPC	-BM 64 (Note 3)	-BM 128
352GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19	—	—	—	11	1	1	2
	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18	*21,*22	—	—	11	2	3	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18, *19	*21,*22	—	—	11	2	4	0
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21,*22	—	—	11	3	5	0
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21	*25	—	11	4	4	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21,*22	*25	—	11	4	5	0
	*1,*2, *3	*5,*6	*9,*10	*13	*17	*21	*25	—	11	5	3	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17	*21	*25	—	11	5	4	0
	*1,*2, *3	*5,*6	*9	*13	*17	*21	*25	*29	11	6	2	0
	*1,*2	*5,*6	*9,*10	*13	*17	*21	*25	*29	11	6	3	0
384GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19,*20	—	—	—	12	1	0	3
	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18, *19	*21,*22	—	—	12	2	3	1
	*1,*2, *3	*5,*6, *7	—	—	*17,*18, *19	*21,*22, *23	—	—	12	2	4	0
	*1,*2, *3,*4	*5,*6	*9,*10	—	*17,*18	*21,*22	—	—	12	3	5	1
	*1,*2, *3	*5,*6, *7	*9,*10	—	*17,*18	*21,*22	—	—	12	3	5	0
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21,*22	*25	—	12	4	5	0
	*1,*2	*5,*6	*9,*10	—	*17,*18	*21,*22	*25,*26	—	12	4	6	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17	*21	*25	—	12	5	4	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	—	12	5	5	0
	*1,*2, *3	*5,*6	*9,*10	*13	*17	*21	*25	*29	12	6	3	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17	*21	*25	*29	12	6	4	0
416GB	*1,*2, *3,*4	*5,*6, *7	—	—	*17,*18, *19	*21,*22, *23	—	—	13	2	3	1
	*1,*2, *3,*4	*5,*6, *7	*9,*10	—	*17,*18	*21,*22	—	—	13	3	4	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18	*21,*22	—	—	13	3	5	0
	*1,*2, *3	*5,*6	*9,*10	—	*17,*18	*21,*22	*25,*26	—	13	4	6	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	—	13	5	5	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25	—	13	5	6	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17	*21	*25	*29	13	6	4	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	*29	13	6	5	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)												
Cache Memory Capacity	CM Location (Note 1)								Number of options			
	DKC-0				DKC-1				DKC-F710I			
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C32G/ -C32GR	-CPC	-BM 64 (Note 3)	-BM 128
448GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19	*21,*22, *23	—	—	14	2	2	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18	*21,*22	—	—	14	3	4	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22	—	—	14	3	5	0
	*1,*2, *3,*4	*5,*6	*9,*10	—	*17,*18	*21,*22	*25,*26	—	14	4	5	1
	*1,*2, *3	*5,*6, *7	*9,*10	—	*17,*18	*21,*22	*25,*26	—	14	4	6	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25	—	14	5	6	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	14	5	7	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21	*25	*29	14	6	5	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25	*29	14	6	6	0
480GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19,*20	*21,*22, *23	—	—	15	2	1	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22	—	—	15	3	4	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	—	—	15	3	5	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10	—	*17,*18	*21,*22	*25,*26	—	15	4	5	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18	*21,*22	*25,*26	—	15	4	6	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	15	5	7	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25	*29	15	6	6	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29	15	6	7	0
512GB	*1,*2, *3,*4	*5,*6, *7,*8	—	—	*17,*18, *19,*20	*21,*22, *23,*24	—	—	16	2	0	4
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	—	—	16	3	4	1
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18	*21,*22	*25,*26	—	16	4	5	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22	*25,*26	—	16	4	6	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	16	5	6	1
	*1,*2, *3	*5,*6, *7	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	16	5	7	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29	16	6	7	0
	*1,*2	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	16	6	8	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)												
Cache Memory Capacity	CM Location (Note 1)								Number of options			
	DKC-0				DKC-1				DKC-F710I			
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C32G/ -C32GR	-CPC	-BM 64 (Note 3)	-BM 128
544GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	—	—	17	3	3	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22	*25,*26	—	17	4	5	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26	—	17	4	6	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	—	17	5	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14	*17,*18	*21,*22	*25,*26	—	17	5	7	0
	*1,*2, *3	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	17	6	8	0
576GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19	*21,*22, *23	—	—	18	3	2	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26	—	18	4	5	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	18	4	6	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14	*17,*18	*21,*22	*25,*26	—	18	5	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18	*21,*22	*25,*26	—	18	5	7	0
	*1,*2, *3,*4	*5,*6	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	18	6	7	1
	*1,*2, *3	*5,*6, *7	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	18	6	8	0
608GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23	—	—	19	3	1	4
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	19	4	5	1
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18	*21,*22	*25,*26	—	19	5	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22	*25,*26	—	19	5	7	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	19	6	7	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	19	6	8	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)												
Cache Memory Capacity	CM Location (Note 1)								Number of options			
	DKC-0				DKC-1				DKC-F710I			
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C32G/ -C32GR	-CPC	-BM 64 (Note 3)	-BM 128
640GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23,*24	—	—	20	3	0	5
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	—	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	20	4	4	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22	*25,*26	—	20	5	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26	—	20	5	7	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14	*17,*18	*21,*22	*25,*26	*29,*30	20	6	7	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18	*21,*22	*25,*26	*29,*30	20	6	8	0
672GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	21	4	3	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26	—	21	5	6	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	21	5	7	0
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18	*21,*22	*25,*26	*29,*30	21	6	7	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22	*25,*26	*29,*30	21	6	8	0
704GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23	*25,*26, *27	—	22	4	2	4
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	22	5	6	1
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22	*25,*26	*29,*30	22	6	7	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26	*29,*30	22	6	8	0
736GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27	—	23	4	1	5
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	23	5	5	2
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26	*29,*30	23	6	7	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30	23	6	8	0
768GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	—	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	—	24	4	0	6
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	24	5	4	3
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	24	6	7	1
	*1,*2, *3	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	24	6	8	0

(To be continued.)

(Continued from the preceding page)

Parallel Installation (Twin DKC model)												
Cache Memory Capacity	CM Location (Note 1)								Number of options			
	DKC-0				DKC-1				DKC-F710I			
	CPC-0 (1CA/ 2CC) (Note 2)	CPC-1 (1CB/ 2CD)	CPC-2 (1CE/ 2CG)	CPC-3 (1CF/ 2CH)	CPC-4 (1CJ/ 2CL)	CPC-5 (1CK/ 2CM)	CPC-6 (1CN/ 2CQ)	CPC-7 (1CP/ 2CR)	-C32G/ -C32GR	-CPC	-BM 64 (Note 3)	-BM 128
800GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19	*21,*22, *23	*25,*26, *27	—	25	5	3	4
	*1,*2, *3,*4	*5,*6, *7	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	25	6	7	1
832GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23	*25,*26, *27	—	26	5	2	5
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	26	6	6	2
864GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *23,*24	*25,*26, *19,*20	*21,*22, *27	—	27	5	1	6
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	27	6	5	3
896GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	—	28	5	0	7
	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19	*21,*22, *23	*25,*26, *27	*29,*30, *31	28	6	4	4
928GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23	*25,*26, *27	*29,*30, *31	29	6	3	5
960GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27	*29,*30, *31	30	6	2	6
992GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30, *31	31	6	1	7
1024GB	*1,*2, *3,*4	*5,*6, *7,*8	*9,*10, *11,*12	*13,*14, *15,*16	*17,*18, *19,*20	*21,*22, *23,*24	*25,*26, *27,*28	*29,*30, *31,*32	32	6	0	8

Note 1: The above numbers (*1 through *32) represent the CM Module locations shown in Fig. 2.5-1 or Fig. 2.5-2.

Note 2: The required SM capacity shown in Table 6.2-1 (INST06-70) must be installed in CPC-0.

Note 3: DKC-F710I-BM128 can be used instead of DKC-F710I-BM64.

2.6 SSD Memory Location

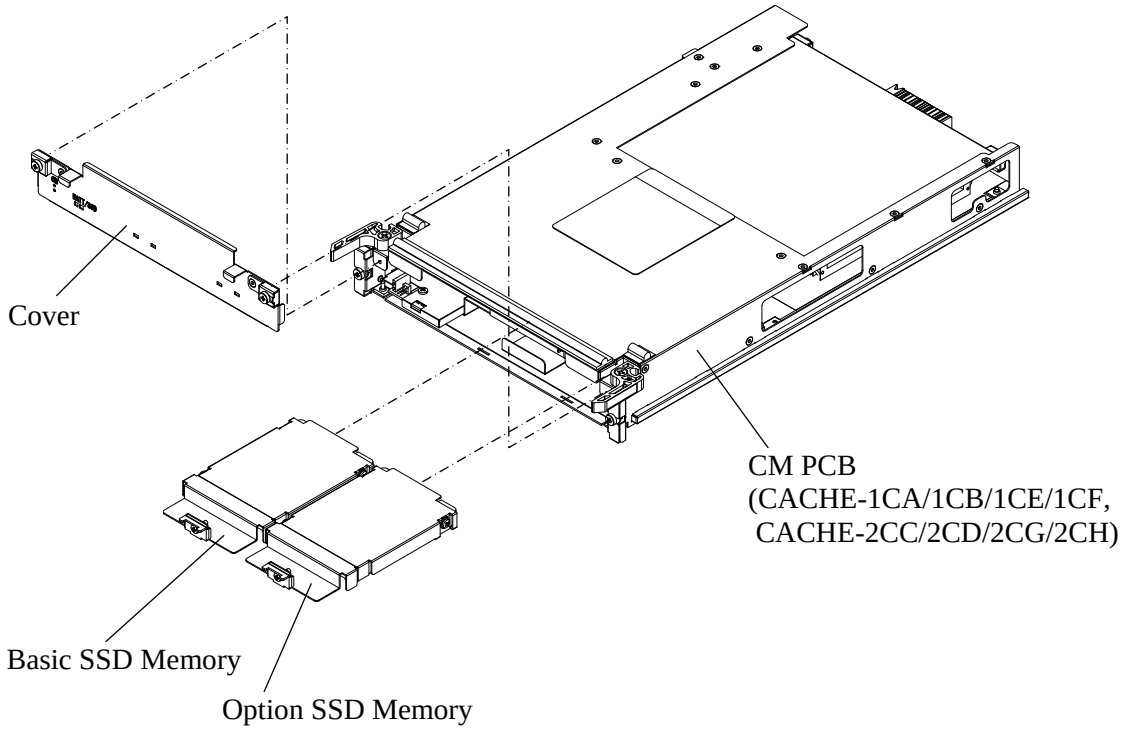


Fig. 2.6-1 SSD Memory Location

3. Switches and LEDs

3.1 DKCPANEL

Fig. 3.1-1 and Table 3.1-1 show the Operator Panel and its functions respectively.
Circled numbers in Fig. 3.1-1 correspond to the numbers in Table 3.1-1.

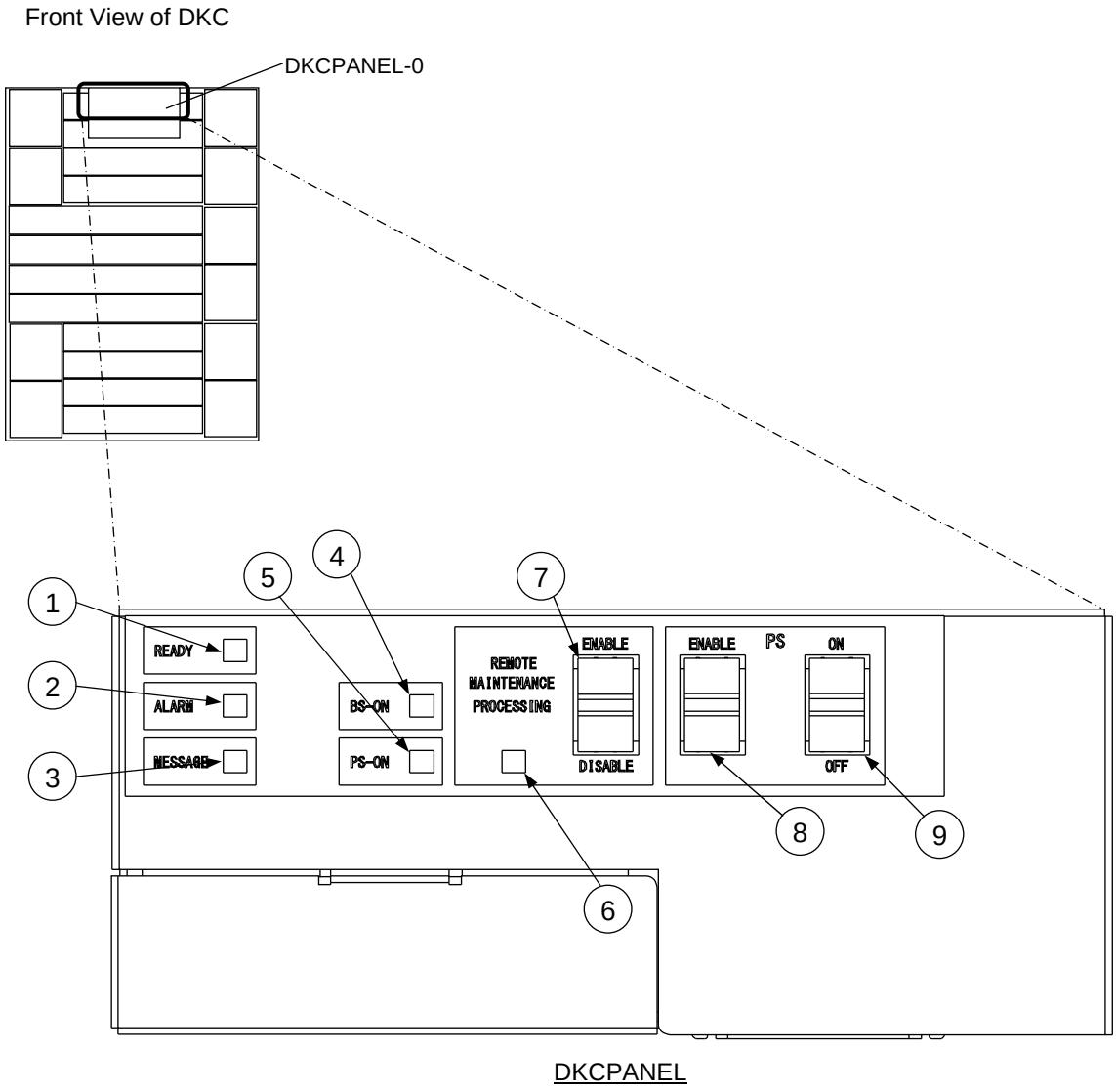


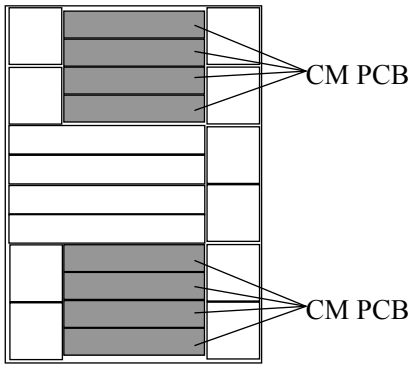
Fig. 3.1-1 DKCPANEL

Table 3.1-1 Part Function on DKCPANEL

No.	Parts Name	Class	Function
①	SUBSYSTEM READY	LED (Green)	Indicates that input/output operation on the channel interface is enabled.
②	SUBSYSTEM ALARM	LED (Red)	ON: Indicates DC under voltage of DKC part, DC over current, abnormally high temperature, or an unrecoverable failure occurred.
③	SUBSYSTEM MESSAGE	LED (Amber)	ON: Indicates that a SIM (Message) was generated from either of the clusters. Applied to both storage clusters. Blinking: Indicates that the SVP failure has occurred.
④	BS ON	LED (Amber)	Indicates that the Sub-PS is on. (CL 1 or CL 2)
⑤	PS ON	LED (Green)	Indicates that the subsystem is powered on.
⑥	REMOTE MAINTENANCE PROCESSING	LED (Amber)	Indicates that remote maintenance is being processed.
⑦	REMOTE MAINTENANCE ENABLE/DISABLE	Switch	Used to permit remote maintenance.
⑧	PS SW ENABLE	Switch	Used to enable the PS ON/PS OFF switch. To enable the PS ON/PS OFF switch, turn the PS SW ENABLE switch to the ENABLE position.
⑨	PS ON/PS OFF	Switch	To switch on/off the subsystem, use this switch while turning the PS SW ENABLE switch to the ENABLE position.

3.2 Other Switches and LEDs

[1] CM PCB



Front View of DKC

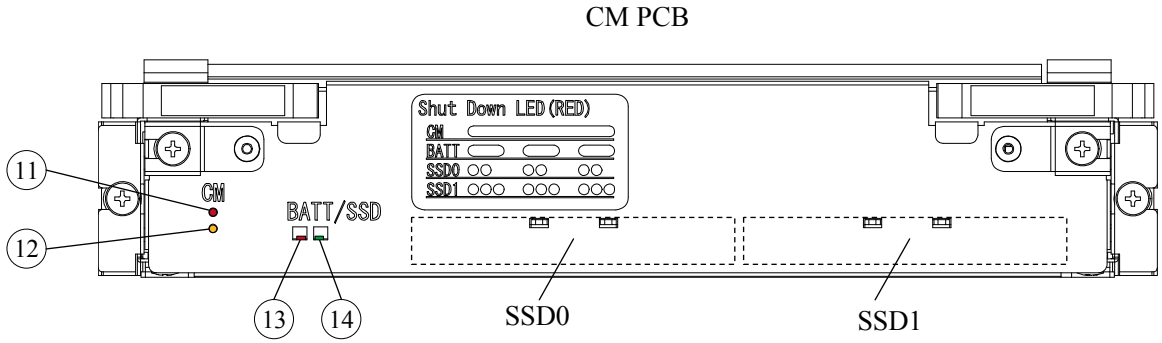


Fig. 3.2-1 LEDs of CM PCB

Table 3.2-1 Function of CM PCB LEDs

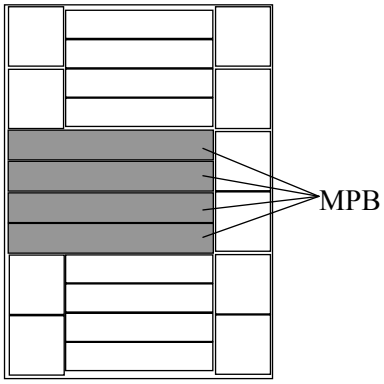
No.	Parts Name	Class	Function
⑪	SHUT DOWN	LED (Red)	Indicates that the removal of the PCB is possible when the subsystem is powered on.
⑫	PS FAILURE	LED (Amber)	Indicates that the voltage in the PCB is abnormal.

(To be continued)

(Continued from preceding page)

No.	Parts Name	Class	Function
⑬	BATTERY SHUT DOWN/ SSD SHUT DOWN	LED (Red)	This LED (Red) indicates the status of replacing battery or SSD. • The light blinks once per three seconds. (ON: about two seconds, OFF: about one second): The battery can be replaced. • The light blinks twice per two seconds. (ON: twice in about 0.6 seconds, OFF: about 1.4 seconds): The SSD0 can be replaced. • The light blinks three times per two seconds. (ON: three times in about one second, OFF: about one second): The SSD1 can be replaced. • Going out: The battery or SSD cannot be replaced.
⑭	BATTERY CHARGE	LED (Green)	This LED (Green) indicates the status of battery. • Lighting: The battery charge is completed. • High-Speed Blinking (ON: about one second, OFF: about one second): The battery is charging. • Medium-Speed Blinking (ON: about two seconds, OFF: about two seconds): The subsystem is powered on and the data in cache memory is saved in SSD. • Low-Speed Blinking (ON: about one second, OFF: about five seconds): The battery is discharging. • Going out: The power is off. The battery discharge is completed. The battery has trouble. After the power is turned on, the Battery Box is started to be charged and the LED blinks. Though the LED becomes kept on after the charge is completed, it repeats the operation that indicates the refilling charge (blink) and completion of the refilling charge (being kept on) after that.

[2] MPB



Front View of DKC

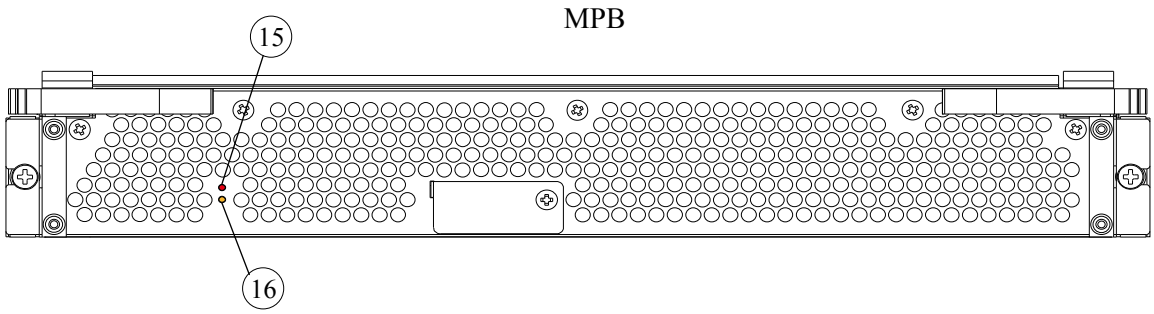
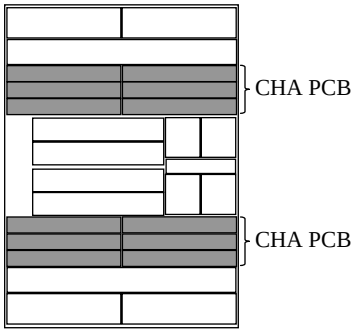


Fig. 3.2-2 LEDs of MPB

Table 3.2-2 Function of MPB LEDs

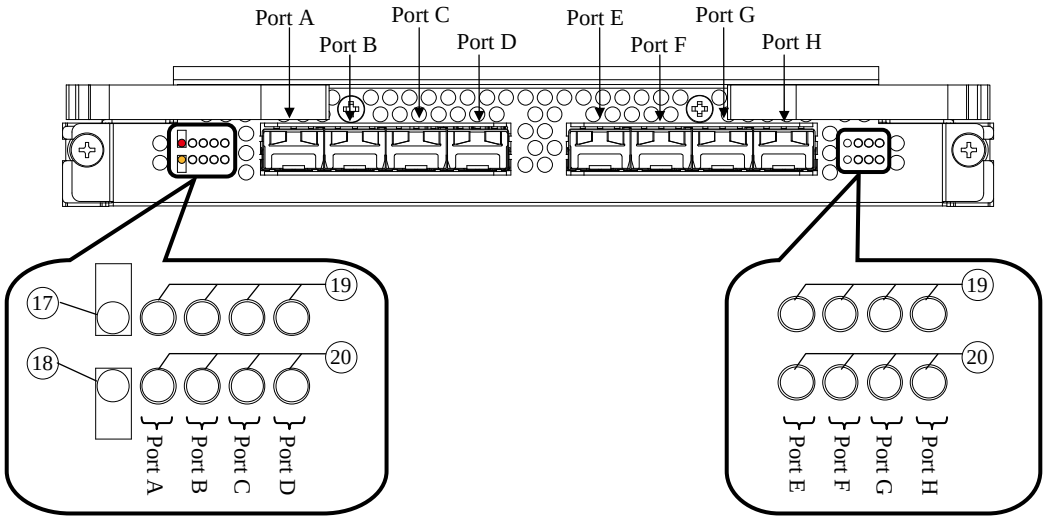
No.	Parts Name	Class	Function
⑮	SHUT DOWN	LED (Red)	Indicates that the removal of the PCB is possible when the subsystem is powered on.
⑯	PS FAILURE	LED (Amber)	Indicates that the voltage in the PCB is abnormal.

[3] CHA PCB (8UFC/8UFCR/16UFC/16UFCR)



Rear View of DKC

CHA PCB (16UFC/16UFCR)



CHA PCB (8UFC/8UFCR)

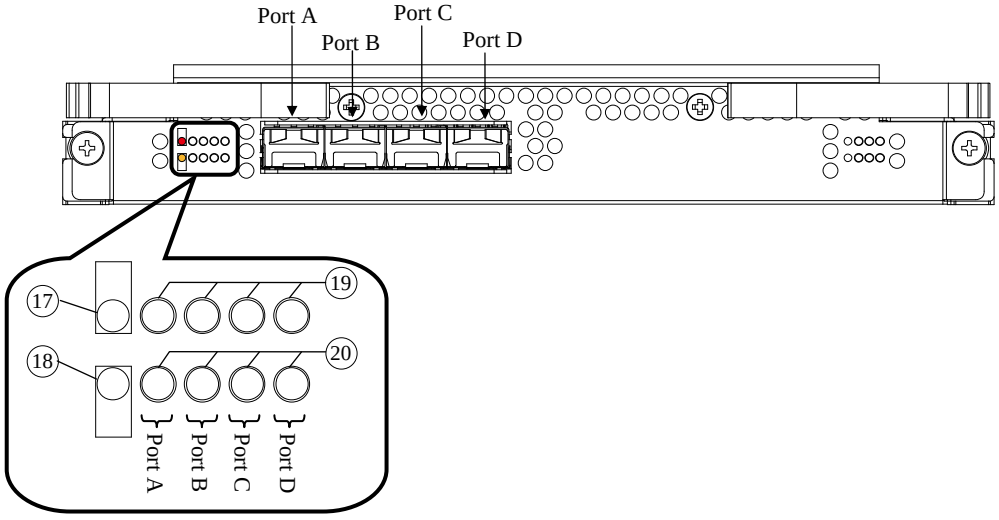
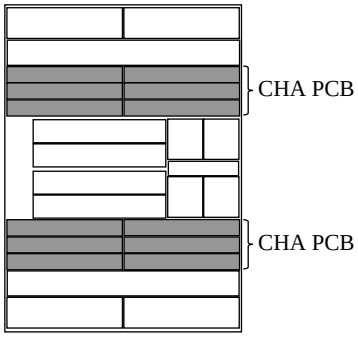


Fig. 3.2-3 LEDs of CHA PCB

Table 3.2-3 Function of CHA PCB LEDs

No.	Parts Name	Class	Function			
⑰	SHUT DOWN	LED (Red)	Indicates that the removal of the PCB is possible when the subsystem is powered on.			
⑱	PS FAILURE	LED (Amber)	Indicates that the voltage in the PCB is abnormal.			
⑲ ⑳	CHANNEL PORT 1 CHANNEL PORT 2	LED (Green)	Indicates the status of channel port by the combination of ⑲ and ⑳.			
			○ ○	OFF + OFF	Not Ready	Link activity is unavailable, due to power-down or initialization not completed.
			○ ●	OFF + ON	Ready	Link is available by initialization completion, but connection to the host has not been established.
			● ●	ON + ON	Link	Interface operation with the host is available by the connection establishment.
			● ●	ON + ON (Same as Link Up)	Active	Interface operation is active between the hosts.

[4] CHA PCB (16MFL/16MFS/16MUL/16MUS/16MUSR)



Rear View of DKC

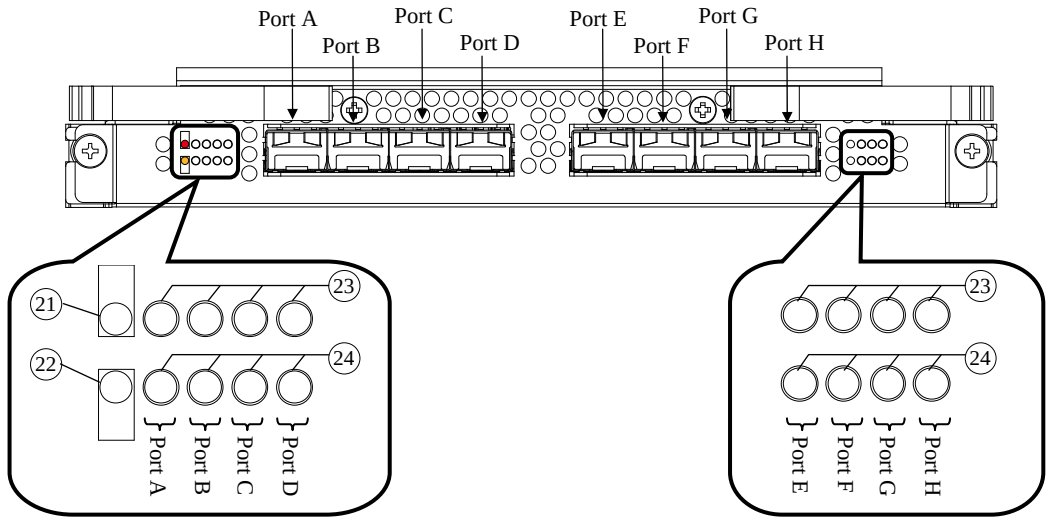


Fig. 3.2-4 LEDs of CHA PCB

Table 3.2-4 Function of CHA PCB LEDs

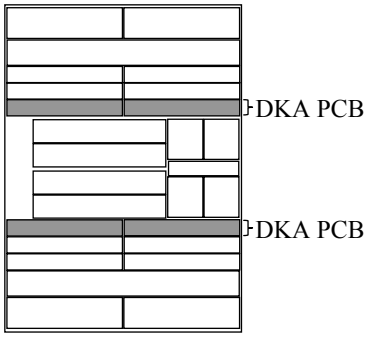
No.	Parts Name	Class	Function
㉑	SHUT DOWN	LED (Red)	Indicates that the removal of the PCB is possible when the subsystem is powered on.
㉒	PS FAILURE	LED (Amber)	Indicates that the voltage in the PCB is abnormal.

(To be continued)

(Continued from preceding page)

No.	Parts Name	Class	Function			
㉓ ㉔	CHANNEL PORT 1 CHANNEL PORT 2	LED (Green)	Indicates the status of channel port by the combination of ㉓ and ㉔.			
			○ ○	OFF + OFF	Not Ready	Link activity is unavailable, due to power-down or initialization not completed.
			◘ ○	Blink (Slow) + OFF	Ready	Link is available by initialization completion, but connection to the host has not been established.
			● ○	ON + OFF	Link	Interface operation with the host is available by the connection establishment.
			◘ ○	Blink (Fast) + OFF	Active	Interface operation is active between the hosts.

[5] DKA PCB



Rear View of DKC

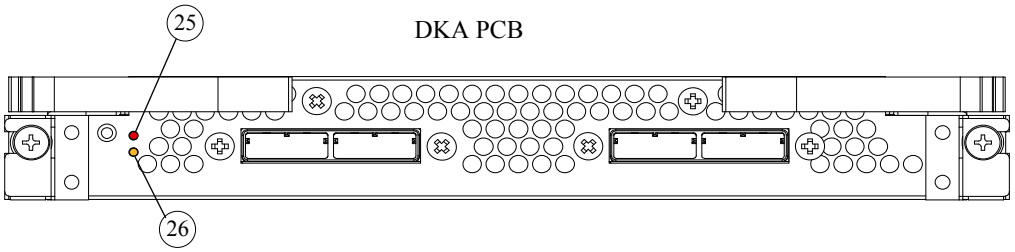
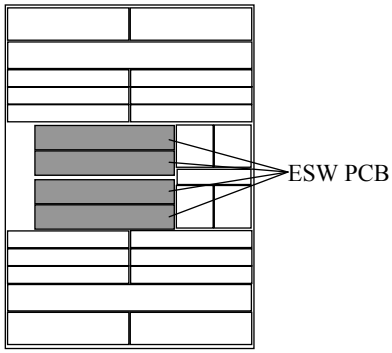


Fig. 3.2-5 LEDs of DKA PCB

Table 3.2-5 Function of DKA PCB LEDs

No.	Parts Name	Class	Function
㉕	SHUT DOWN	LED (Red)	Indicates that the removal of the PCB is possible when the subsystem is powered on.
㉖	PS FAILURE	LED (Amber)	Indicates that the voltage in the PCB is abnormal.

[6] ESW PCB



Rear View of DKC

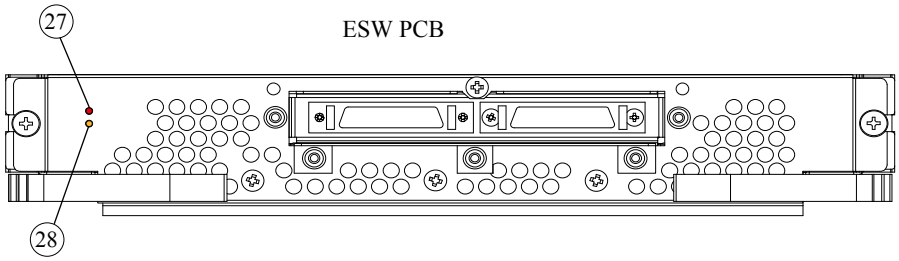


Fig. 3.2-6 LEDs of ESW PCB

Table 3.2-6 Function of ESW PCB LEDs

No.	Parts Name	Class	Function
㉗	SHUT DOWN	LED (Red)	Indicates that the removal of the PCB is possible when the subsystem is powered on.
㉘	PS FAILURE	LED (Amber)	Indicates that the voltage in the PCB is abnormal.

[7] DKCPANEL

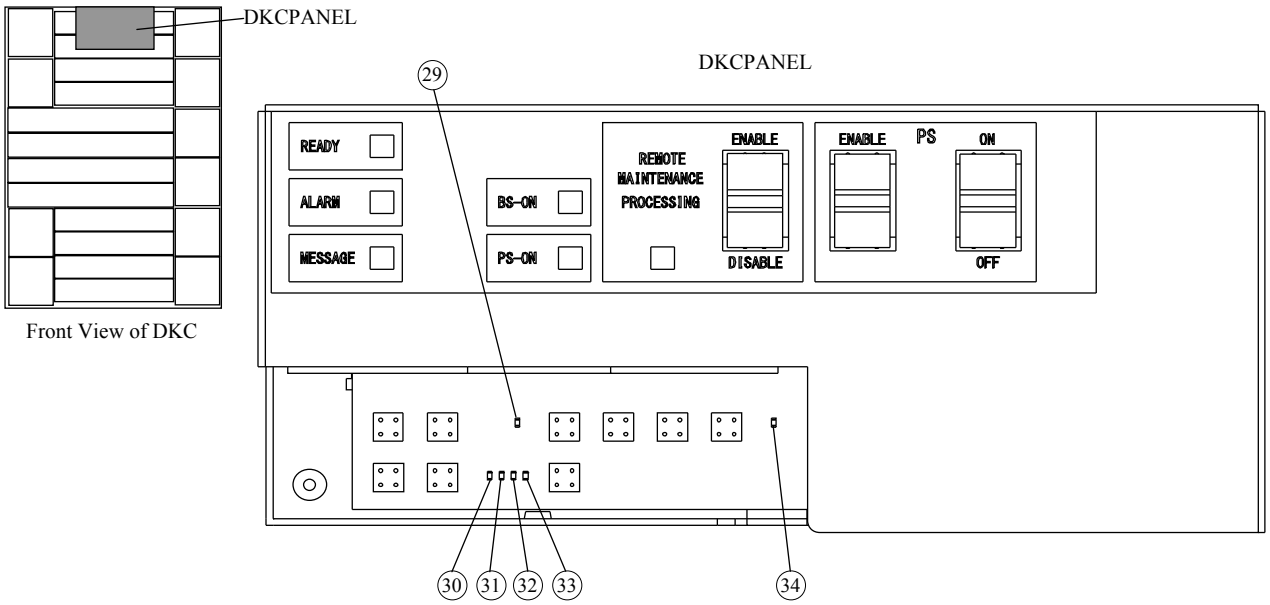
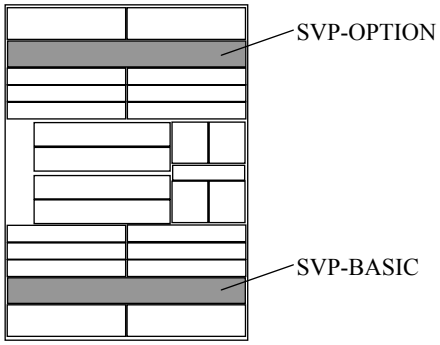


Fig. 3.2-7 LEDs of DKCPANEL

Table 3.2-7 Function of DKCPANEL LEDs

No.	Parts Name	Class	Function
㉙	FORCE MODE	LED (Amber)	This LED lights up when the subsystem is forced to power off with the FORCE MODE jumper.
㉚ ㉛ ㉜ ㉝	SSVP ALARM SSVP STATUS 1 SSVP STATUS 2 SSVP STATUS 3	LED (Red)	Indicates the status of SSVP by the combination of ㉚, ㉛, ㉜ and ㉝. The LED indicates the following statuses. ○: Indicates that the light is off. ●: Indicates that the light is on. ◘: Indicates that the light is blinking. ㉚ ㉛ ㉜ ㉝ ○ ○ ○ ○ Normal status ● ○ ○ ○ Self-contradiction of SSVP microprogram or hardware abnormality is detected. ○ ● ● ● During the memory test ● ● ● ● Memory system hardware error ○ ● ● ○ During the DUMP collection ● ● ● ○ DUMP terminated abnormally. ◘ ○ ○ ○ DUMP collection is completed ○ ● ○ ○ During microprogram replacement ● ● ○ ○ Microprogram replacement error
㉞	DKCPANEL SHUT DOWN	LED (Red)	Indicates that the removal of the DKCPANEL is possible when the subsystem is powered on.

[8] SVP



Rear View of DKC-0

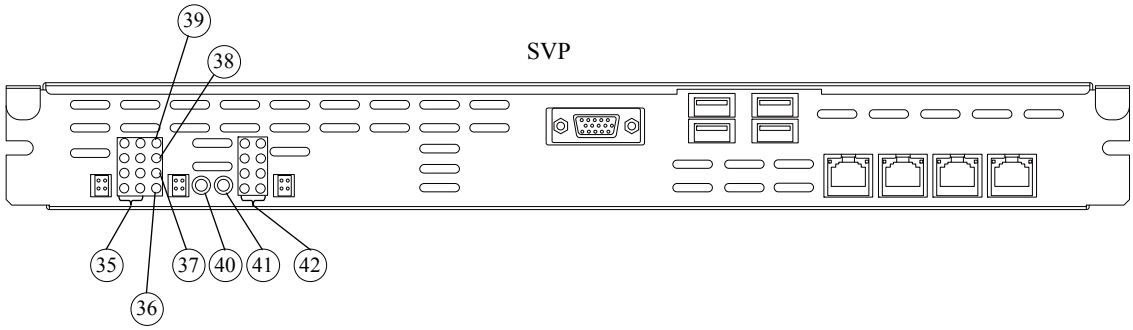


Fig. 3.2-8 LEDs and Switches of SVP

Table 3.2-8 Function of SVP LEDs and Switches

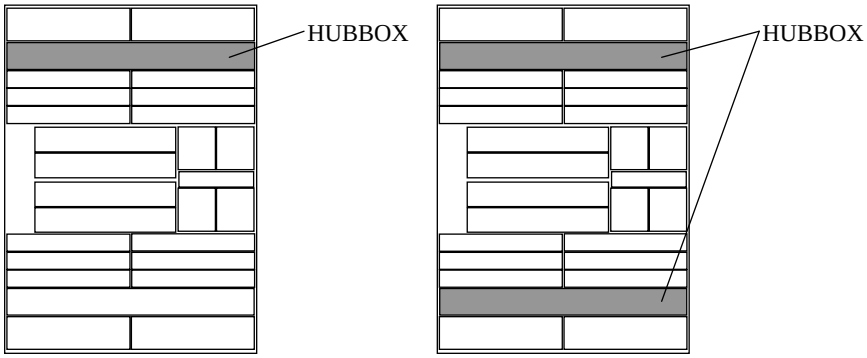
No.	Parts Name	Class	Function
(35)	SVP STATUS	LED (Green)	Indicates a status of the SVP using the SVP microprogram. The LED indicates the following status usually. ○: Indicates that the light is off. ●: Indicates that the light is on. ◘: Indicates that the light is blinking. ○ ● } The LED status at the time of Master SVP. ○ ○ ○ ○ ◘ ○ ○ ○ } The LED status at the time of Standby SVP ○ ● ○ ○ ◘ ○ For the other LED display, see " 1.10 SVP LED display specification" (SVP01-170 ~ 190).

(To be continued)

(Continued from preceding page)

No.	Parts Name	Class	Function
㊱	SVP DCIN	LED (Green)	Indicates that the DC power is supplied to the SVP. • Power of the built-in Hub in the SVP is turned on. • Power of the PC in the SVP is kept off.
㊲	SVP POWER	LED (Green)	Indicates that the power of the SVP has been turned on. • Power of the built-in Hub in the SVP has been turned on. • Power of the PC in the SVP is turned on.
㊳	SVP HDD	LED (Green)	This LED lights up when accessing to HDD in SVP.
㊴	SVP SHUT DOWN	LED (Red)	Indicates that the removal of the SVP is possible when the subsystem is powered on.
㊵	SVP PS ON	Switch	A pressing of this switch turns on the power of the PC in the SVP.
㊶	SVP PS OFF	Switch	A pressing of this switch quits Windows and then turns off the power of the PC in the SVP. When this switch is pressed with the SVP PS ON switch (No. 40) at the same time, Windows is quit forcibly and then the power of the PC in the SVP is turned off.
㊷	LAN STATUS	LED (Amber)	This LED indicates the status of LAN. • Lighting: The link is established. • Blinking: In the status of sending data or receiving data.

[9] HUBBOX



Rear View of DKC-0 Rear View of DKC-1

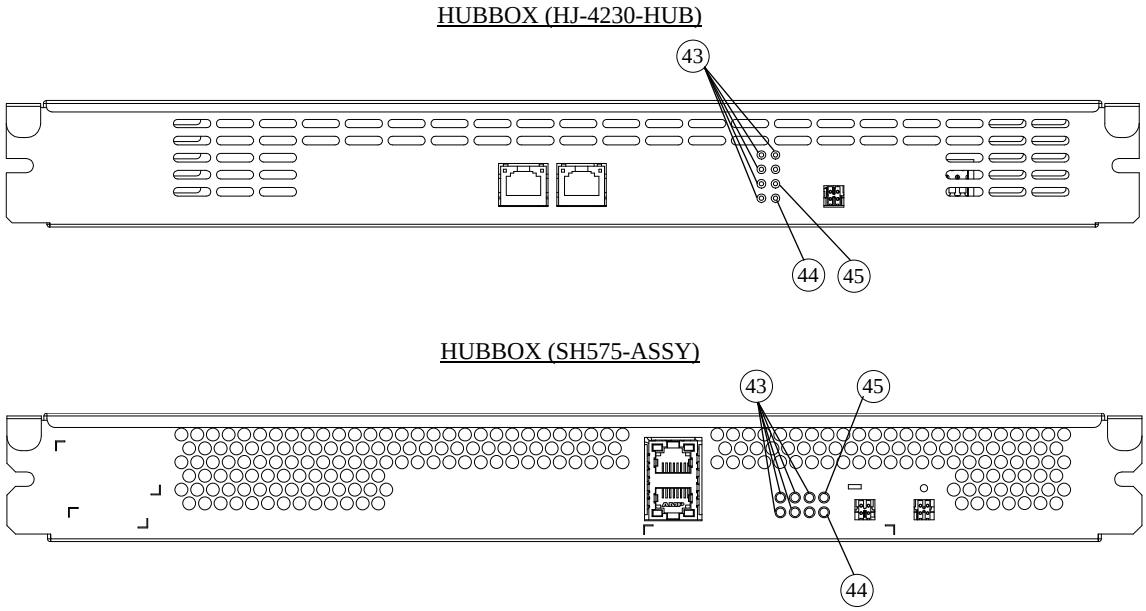
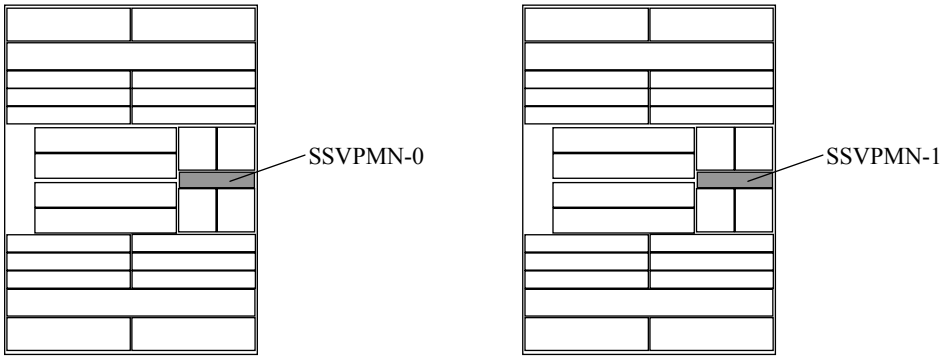


Fig. 3.2-9 LEDs of HUBBOX

Table 3.2-9 Function of HUBBOX LEDs

No.	Parts Name	Class	Function
㊸	LAN STATUS	LED (Green)	This LED indicates the status of LAN. • Lighting: The link is established. • Blinking: In the status of sending data or receiving data.
㊹	HUB POWER	LED (Green)	Indicates that the power of the HUB has been turned on.
㊺	HUB SHUT DOWN	LED (Red)	Indicates that the removal of the HUB is possible when the subsystem is powered on.

[10] SSVPMN



Rear View of DKC-0 Rear View of DKC-1

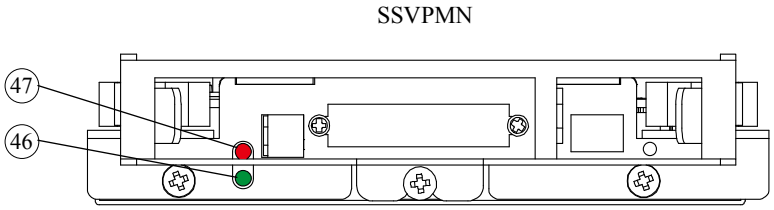


Fig. 3.2-10 LEDs of SSVPMN

Table 3.2-10 Function of SSVPMN LEDs

No.	Parts Name	Class	Function
㊻	SSVPMN POWER	LED (Green)	Indicates that the power of the SSVPMN has been turned on.
㊼	SSVPMN REPLACE	LED (Red)	Indicates that the removal of the SSVPMN is possible.

[11] FAN

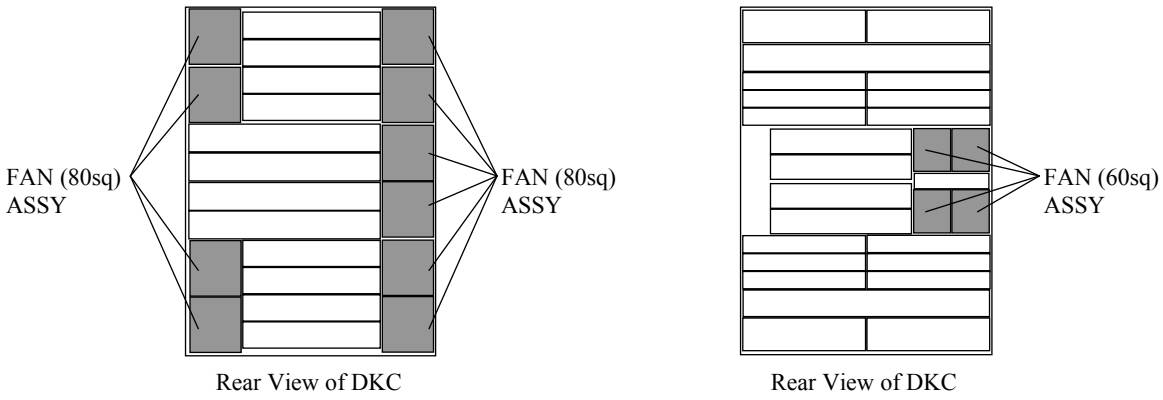


Fig. 3.2-11 LEDs of FAN

Table 3.2-11 Function of FAN LEDs

No.	Parts Name	Class	Function
(48)	FAN REPLACE	LED (Red)	Indicates that the removal of the FAN is possible when the subsystem is powered on.
(49)	FAN REPLACE	LED (Red)	Indicates that the removal of the FAN is possible when the subsystem is powered on.

[12] DKCPS/DKUPS

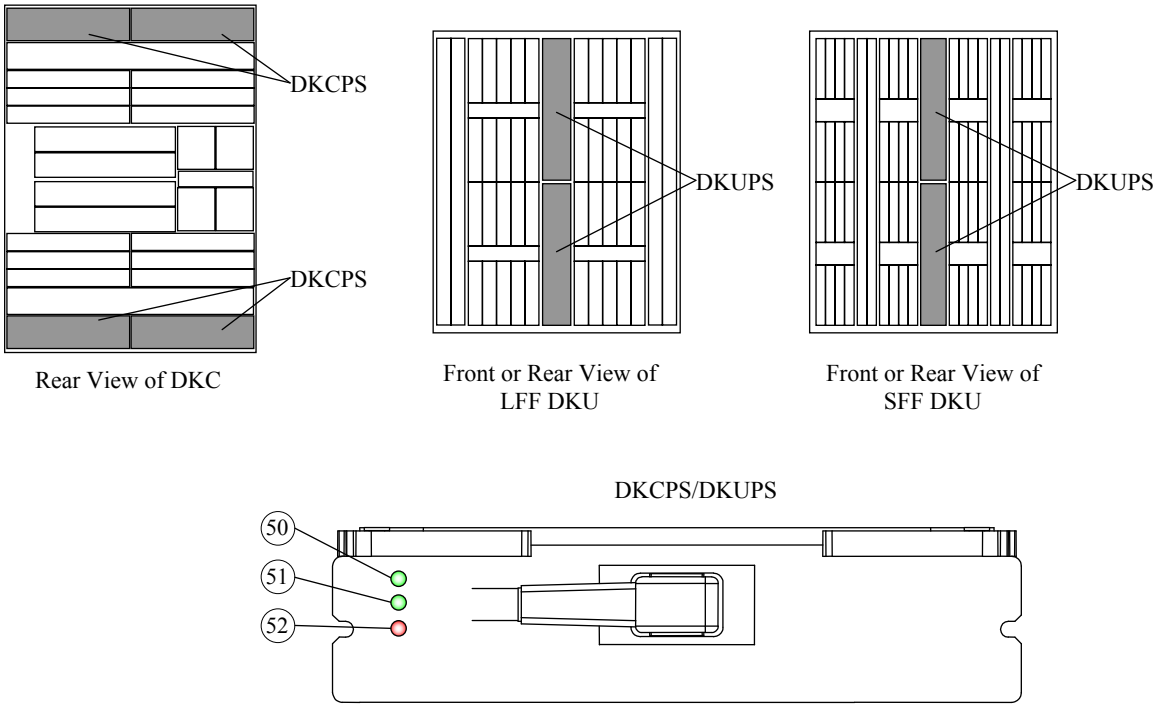


Fig. 3.2-12 LEDs of DKCPS/DKUPS

Table 3.2-12 Function of DKCPS/DKUPS LEDs

No.	Parts Name	Class	Function
(50)	PS ENABLE	LED (Green)	Indicates that the PS is powered on.
(51)	PS ENABLE	LED (Green)	Indicates that the PS is powered on.
(52)	PS REPLACE	LED (Red)	Indicates that the removal of the PS is possible when the subsystem is powered on.

[13] SSW

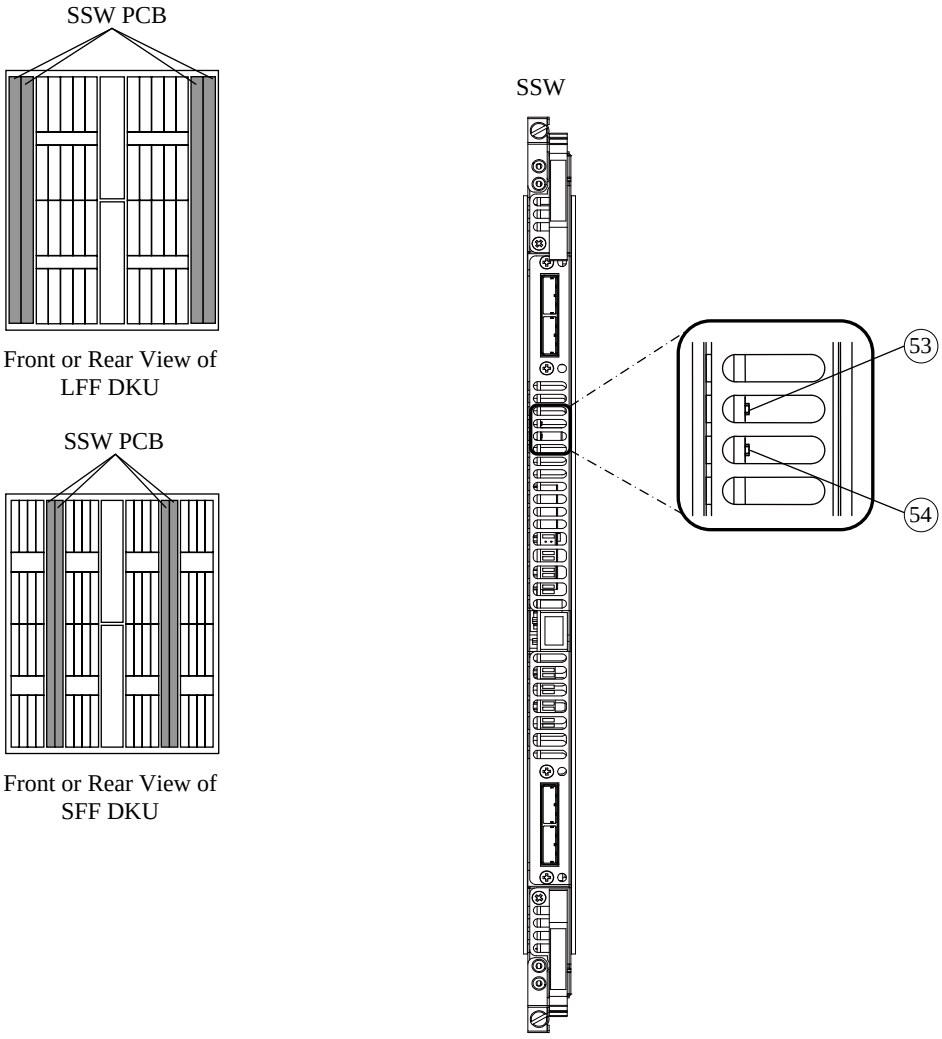


Fig. 3.2-13 LEDs of SSW

Table 3.2-13 Function of SSW LEDs

No.	Parts Name	Class	Function
⑤③	SSW Shut Down	LED (Red)	Indicates that the removal of the SSW PCB is possible when the subsystem is powered on.
⑤④	SSW ENABLE	LED (Green)	Indicates that the power of the SSW PCB has been turned on.

[14] HDDPWR

In the case of LFF DKU (DKU for 3.5 inch drive)

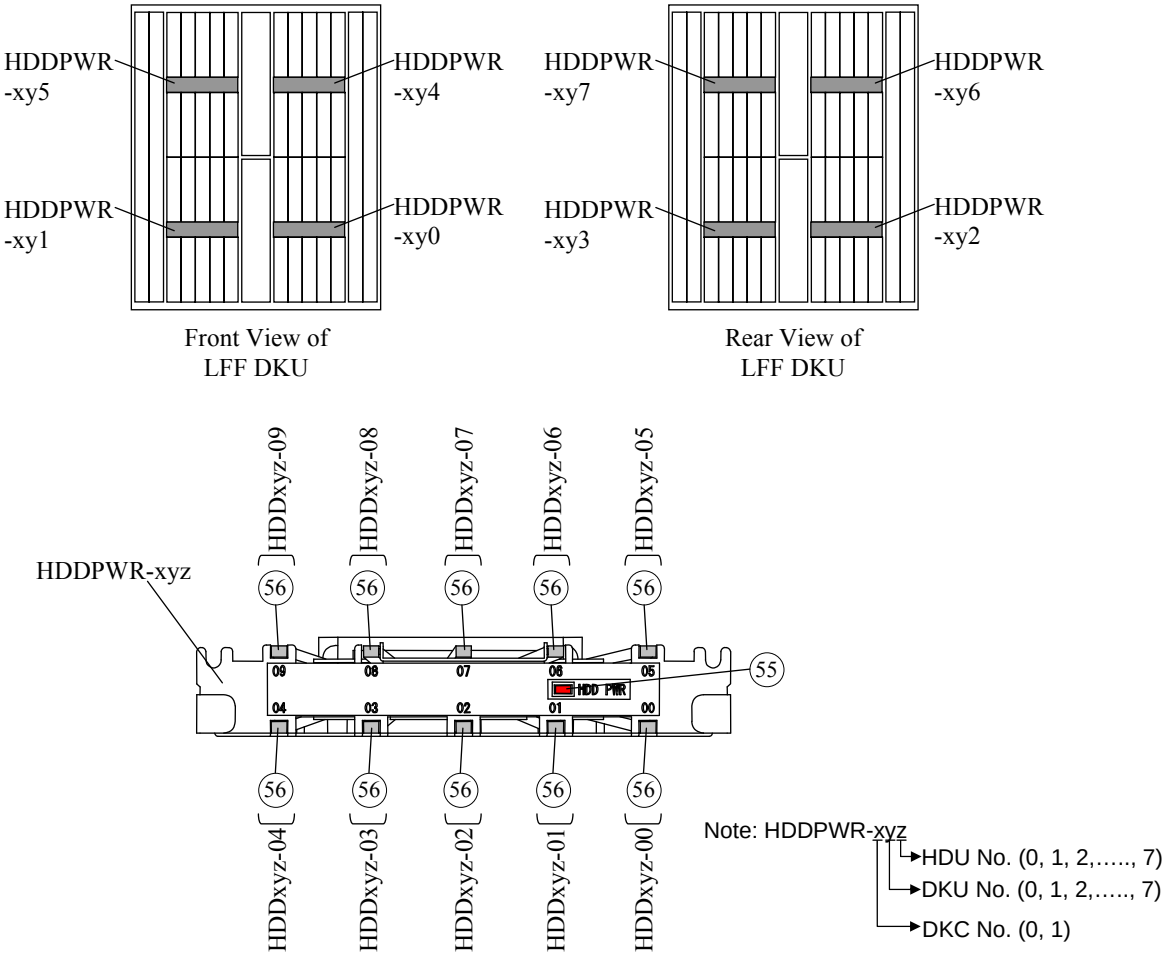


Fig 3.2-14 LEDs of HDDPWR (In the case of LFF DKU)

In the case of SFF DKU (DKU for 2.5 inch drive)

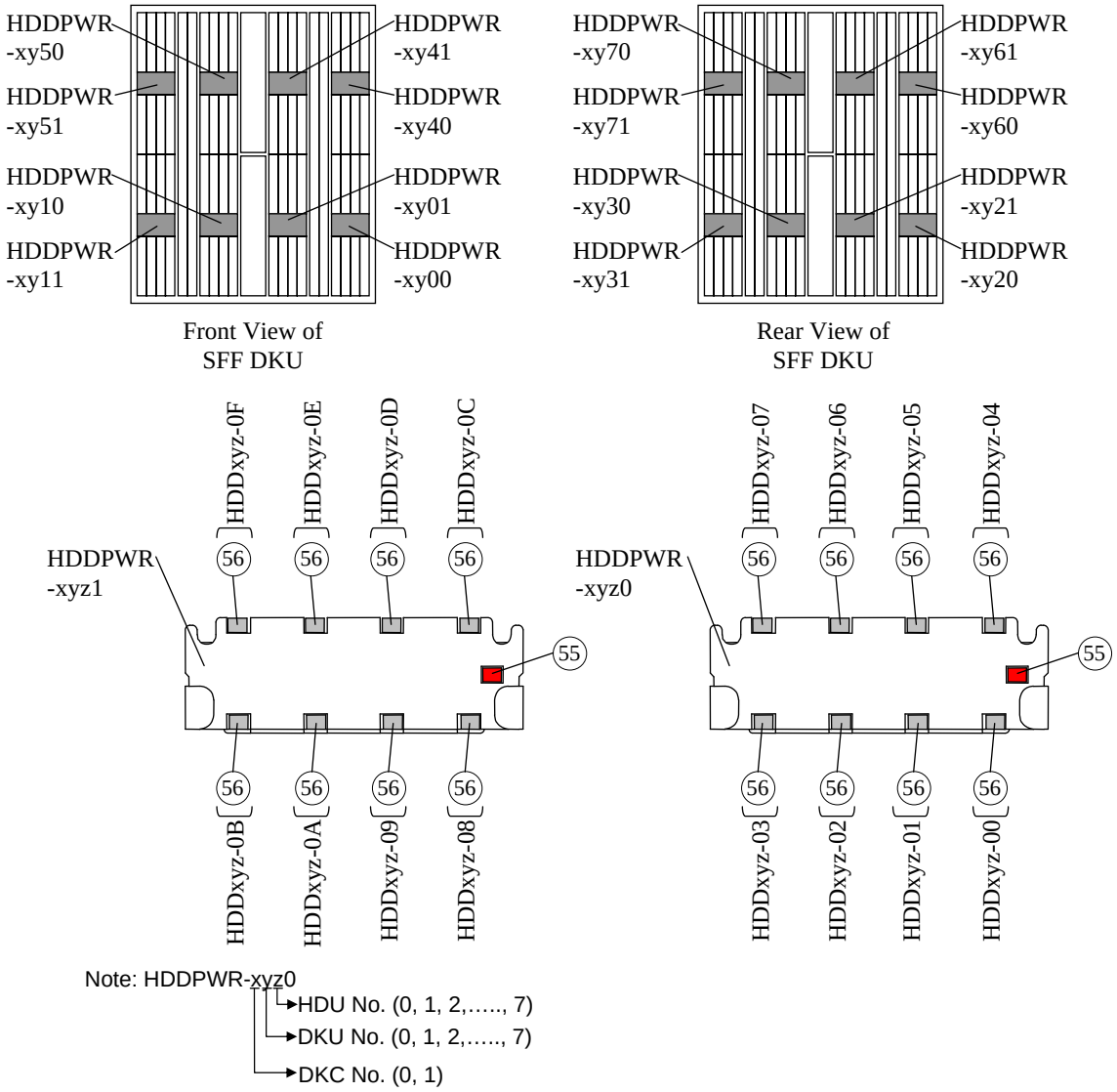


Fig 3.2-15 LEDs of HDDPWR (In the case of SFF DKU)

Table 3.2-14 Function of HDDPWR LEDs

No.	Parts Name	Class	Function
⑤⑤	HDDPWR Shut Down	LED (Red)	Indicates that the removal of the HDDPWR is possible when the subsystem is powered on.
⑤⑥	HDD ENABLE	LED (Green)	This LED shows the state of HDD/SSD. • Lighting: Indicates that the HDD/SSD is powered on. • Blinking: Indicates that the HDD/SSD is active. Note: The interval of blinking may be different in HDD and SSD, however it is not abnormal.
	HDD Shut Down LED	LED (Red/ Amber*1)	Indicates that the removal of the HDD/SSD is possible when the subsystem is powered on.

*1: The LED color may become amber because HDD Shut Down LED (Red) and HDD ENABLE LED (Green) light concurrently depending on the condition of HDDs/SSDs.

[15] CHA PCB (8FOE)

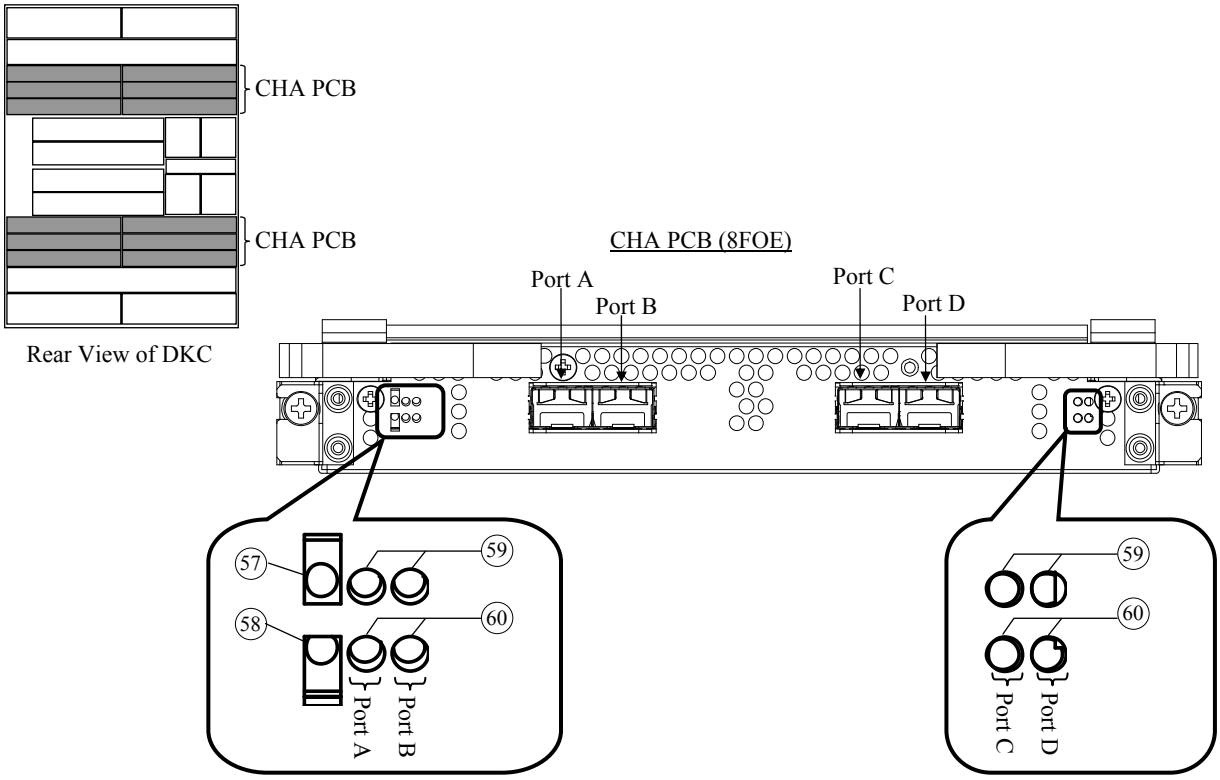
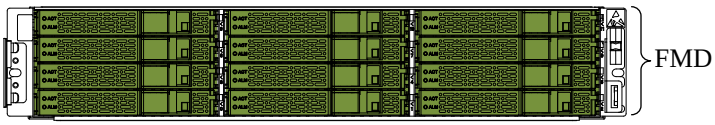


Fig. 3.2-16 LEDs of CHA PCB

Table 3.2-15 Function of CHA PCB LEDs

No.	Parts Name	Class	Function			
⑤⑦	SHUT DOWN	LED (Red)	Indicates that the removal of the PCB is possible when the subsystem is powered on.			
⑤⑧	PS FAILURE	LED (Amber)	Indicates that the voltage in the PCB is abnormal.			
⑤⑨	CHANNEL PORT 1	LED (Green)	Indicates the status of channel port by the combination of ⑤⑨ and ⑥⓪.			
⑥⓪	CHANNEL PORT 2		○ ○	OFF + OFF	Not Ready	Link activity is unavailable, due to power-down or initialization not completed.
			○ ●	OFF + ON	Ready	Link is available by initialization completion, but connection to the host has not been established.
			● ●	ON + ON	Link	Interface operation with the host is available by the connection establishment.
			● ●	ON + ON (Same as Link Up)	Active	Interface operation is active between the hosts.

[16] FMD



Front View of FBX DKU

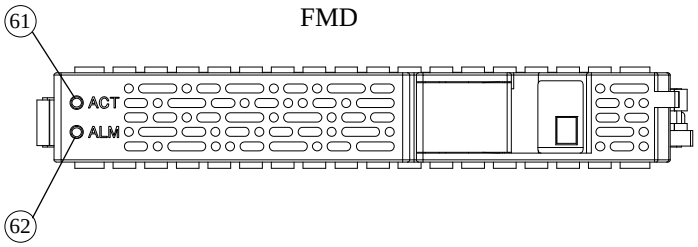
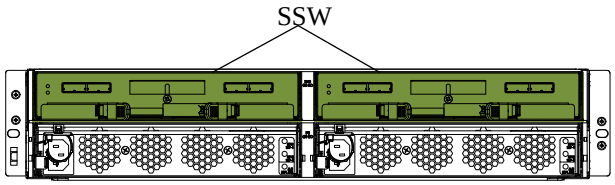


Fig. 3.2-17 LEDs of FMD

Table 3.2-16 Function of FMD LEDs

No.	Parts Name	Class	Function
(61)	FMD ACTIVE	LED (Green)	This LED shows the state of FMD. • Lighting: Indicates that the FMD is powered on. • Blinking: Indicates that the FMD is active. (ON: When in/out data transfer is not performed. OFF: When in/out data transfer is performed.) Note: The interval of blinking may be different in FMD, however it is not abnormal. • Low-Speed Blinking (ON: 1.5 seconds, OFF: 1.5 seconds): Indicates that the battery charge of the FMD is insufficient.
(62)	FMD SHUT DOWN (ALM)	LED (Red)	Indicates that the removal of the FMD is possible when the subsystem is powered on.

[17] SSW



Rear View of FBX DKU

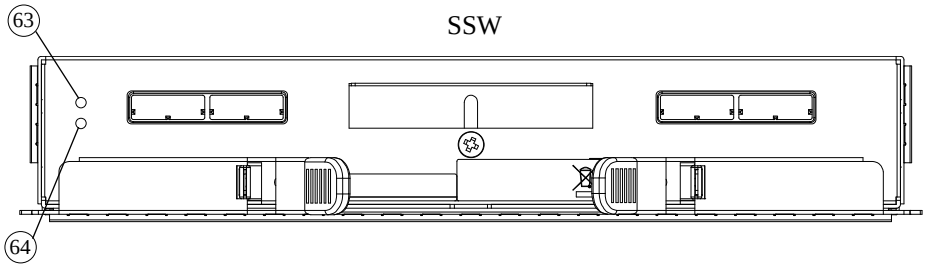
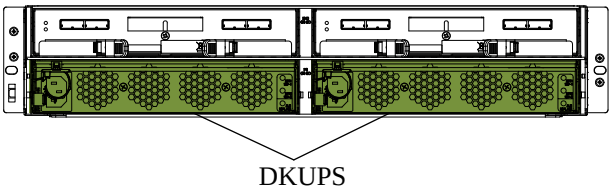


Fig. 3.2-18 LEDs of SSW

Table 3.2-17 Function of SSW LEDs

No.	Parts Name	Class	Function
⑥③	SSW POWER	LED (Green)	Indicates that the power of the SSW has been turned on.
⑥④	SSW SHUT DOWN (ALM)	LED (Red)	Indicates that the removal of the SSW is possible when the subsystem is powered on.

[18] DKUPS



Rear View of FBX DKU

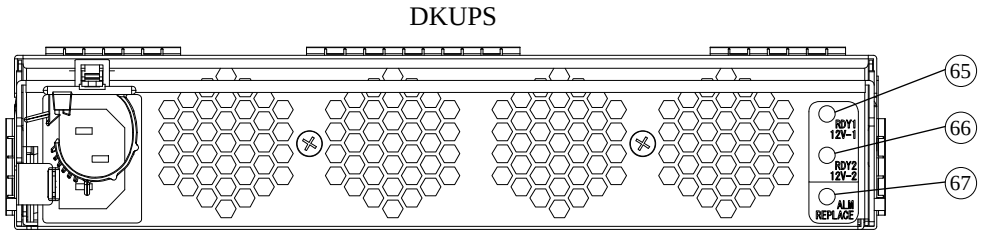


Fig. 3.2-19 LEDs of DKUPS

Table 3.2-18 Function of DKUPS LEDs

No.	Parts Name	Class	Function
⑥⑤	PS READY 1	LED (Green)	Indicates that the PS is powered on.
⑥⑥	PS READY 2	LED (Green)	Indicates that the PS is powered on.
⑥⑦	PS REPLACE	LED (Red)	Indicates that the removal of the PS is possible when the subsystem is powered on.

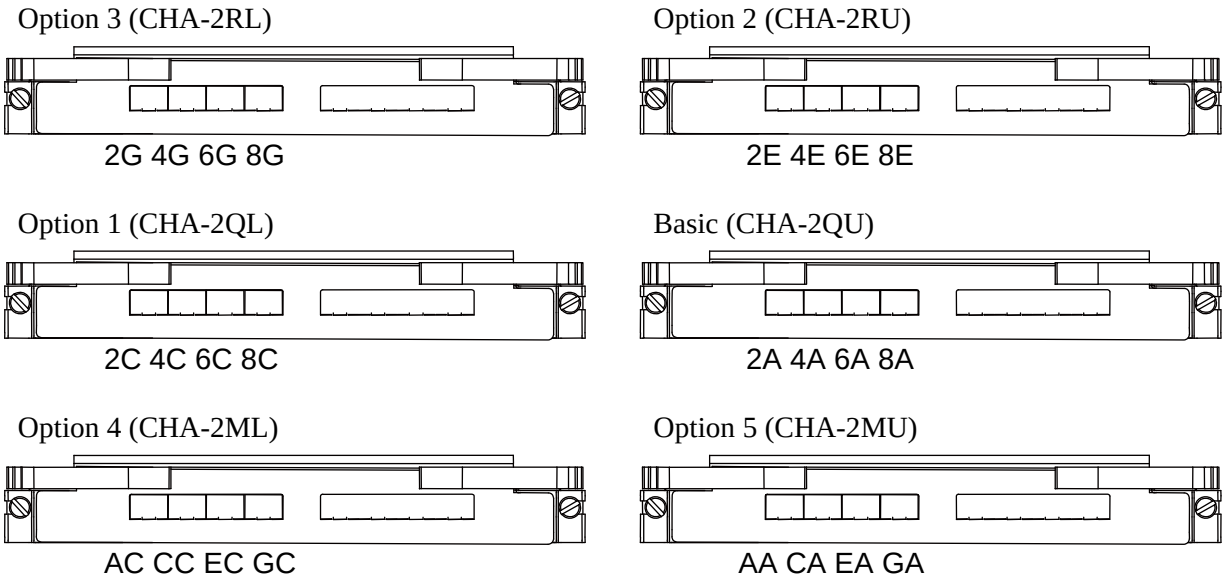
4. Connection of External Cable

4.1 Channel Interface

1. Fibre 4-port CHA PCB (DKC-F710I-8UFC/8UFCR)

[1] DKC-0

CHA PCB (Cluster 2)



CHA PCB (Cluster 1)

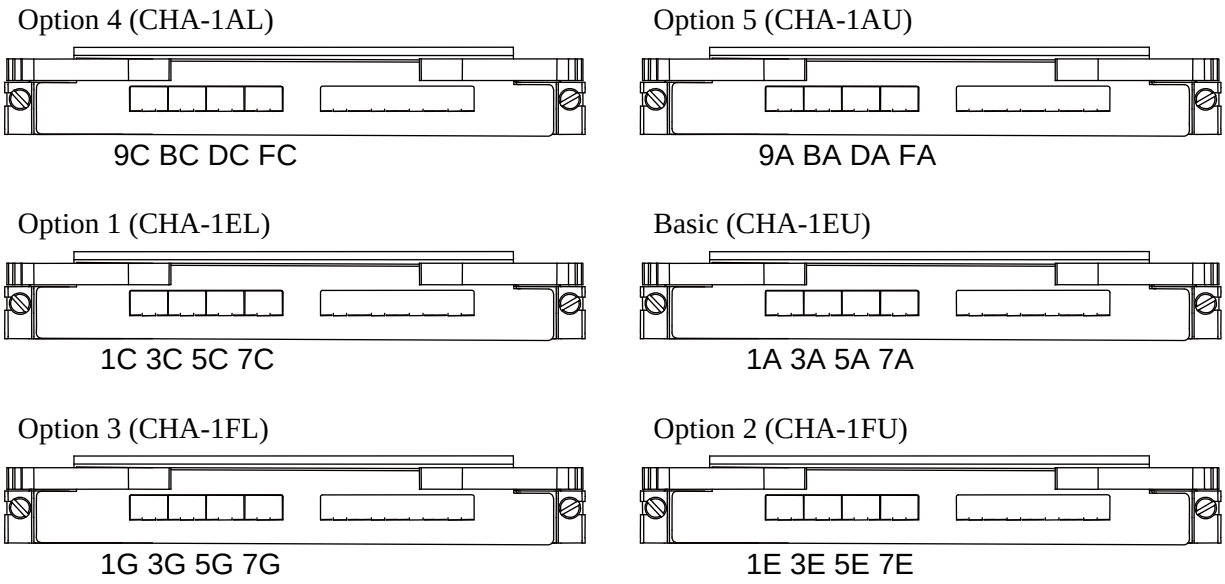
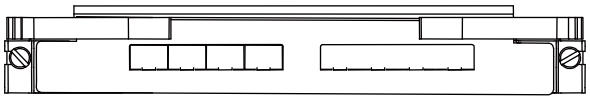


Fig. 4.1-1 Port Number of Fibre 4-port CHA PCB

[2] DKC-1

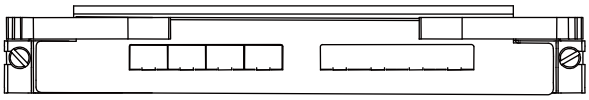
CHA PCB (Cluster 2)

Option 3 (CHA-2UL)



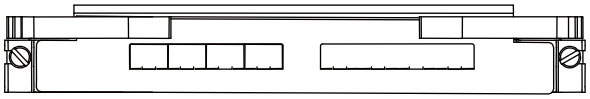
2Q 4Q 6Q 8Q

Option 2 (CHA-2UU)



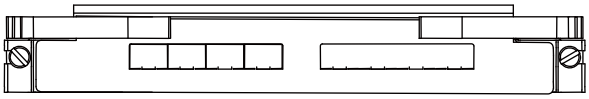
2N 4N 6N 8N

Option 1 (CHA-2TL)



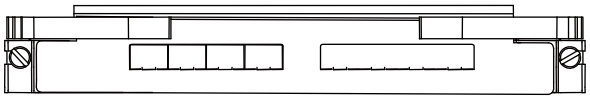
2L 4L 6L 8L

Basic (CHA-2TU)



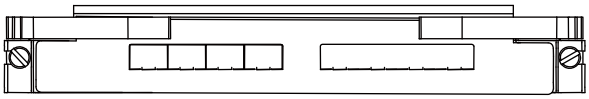
2J 4J 6J 8J

Option 4 (CHA-2XL)



AL CL EL GL

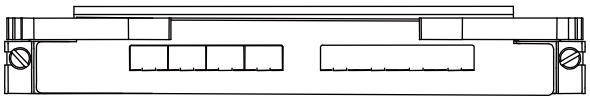
Option 5 (CHA-2XU)



AJ CJ EJ GJ

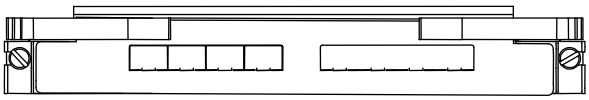
CHA PCB (Cluster 1)

Option 4 (CHA-1LL)



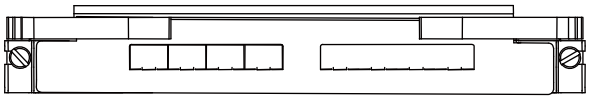
9L BL DL FL

Option 5 (CHA-1LU)



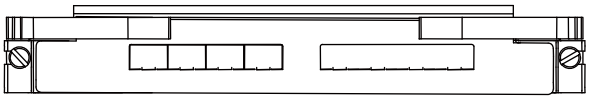
9J BJ DJ FJ

Option 1 (CHA-1GL)



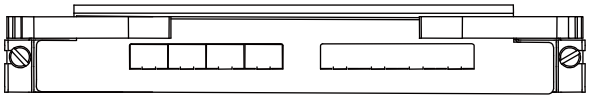
1L 3L 5L 7L

Basic (CHA-1GU)



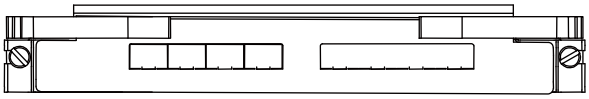
1J 3J 5J 7J

Option 3 (CHA-1HL)



1Q 3Q 5Q 7Q

Option 2 (CHA-1HU)



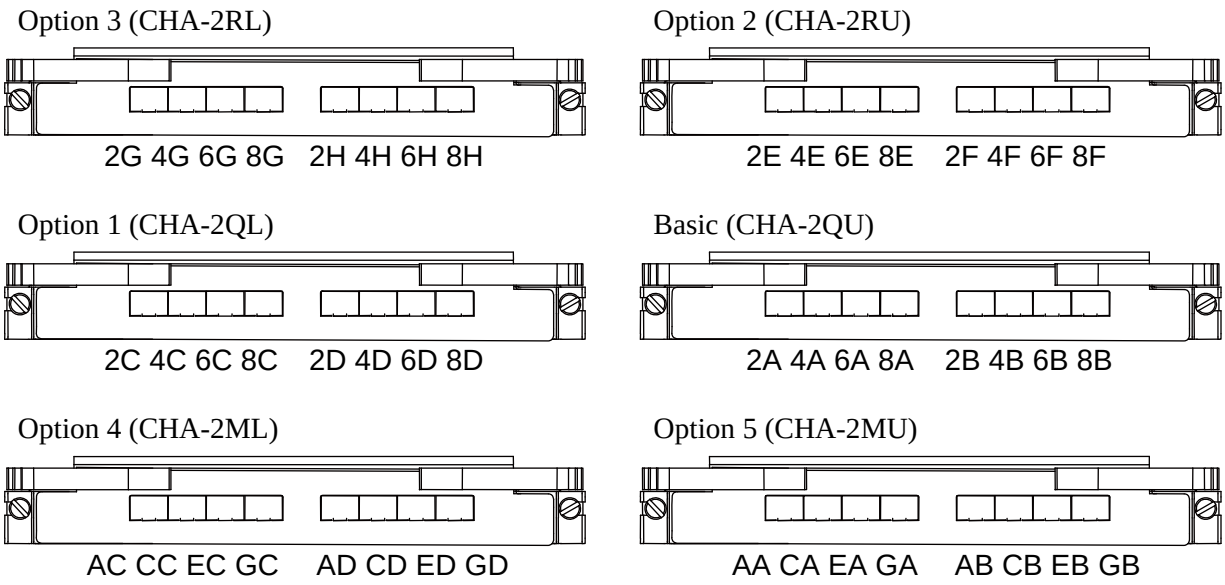
1N 3N 5N 7N

Fig. 4.1-2 Port Number of Fibre 4-port CHA PCB

2. Fibre 8-port CHA PCB (DKC-F710I-16UFC/16UFCR)

[1] DKC-0

CHA PCB (Cluster 2)



CHA PCB (Cluster 1)

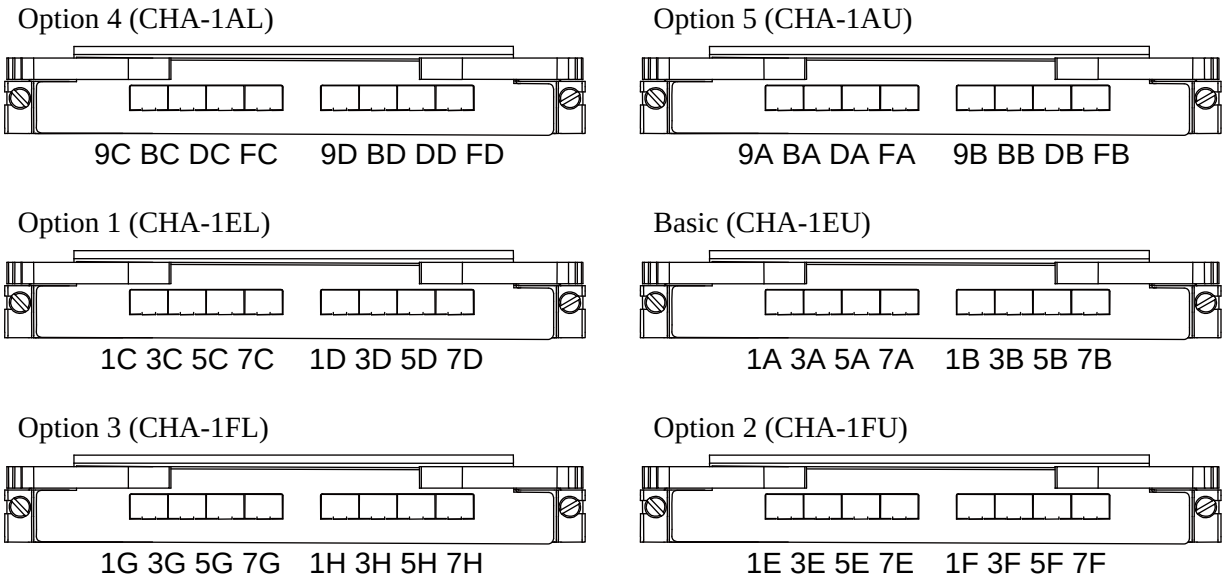
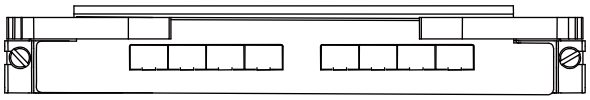


Fig. 4.1-3 Port Number of Fibre 8-port CHA PCB

[2] DKC-1

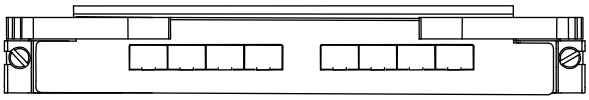
CHA PCB (Cluster 2)

Option 3 (CHA-2UL)



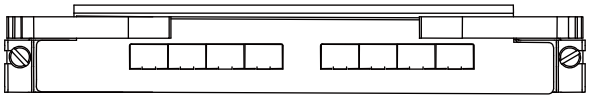
2Q 4Q 6Q 8Q 2R 4R 6R 8R

Option 2 (CHA-2UU)



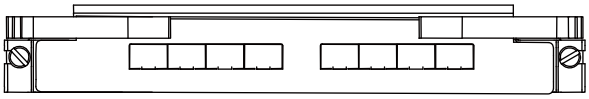
2N 4N 6N 8N 2P 4P 6P 8P

Option 1 (CHA-2TL)



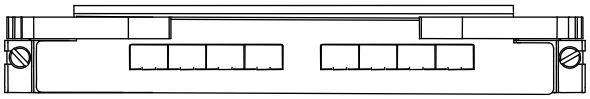
2L 4L 6L 8L 2M 4M 6M 8M

Basic (CHA-2TU)



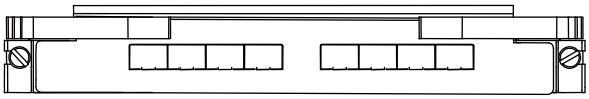
2J 4J 6J 8J 2K 4K 6K 8K

Option 4 (CHA-2XL)



AL CL EL GL AM CM EM GM

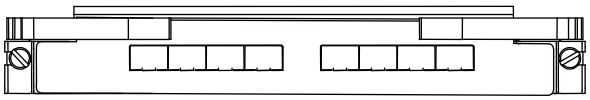
Option 5 (CHA-2XU)



AJ CJ EJ GJ AK CK EK GK

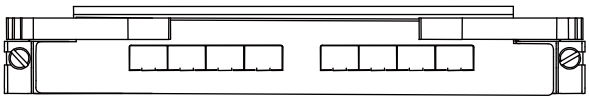
CHA PCB (Cluster 1)

Option 4 (CHA-1LL)



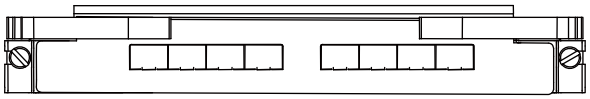
9L BL DL FL 9M BM DM FM

Option 5 (CHA-1LU)



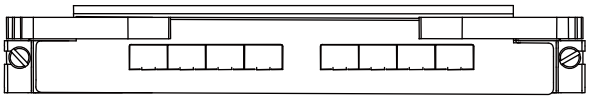
9J BJ DJ FJ 9K BK DK FK

Option 1 (CHA-1GL)



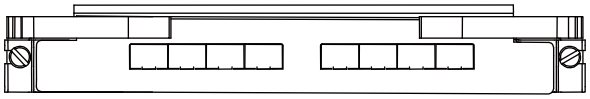
1L 3L 5L 7L 1M 3M 5M 7M

Basic (CHA-1GU)



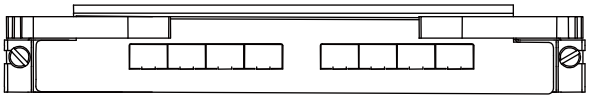
1J 3J 5J 7J 1K 3K 5K 7K

Option 3 (CHA-1HL)



1Q 3Q 5Q 7Q 1R 3R 5R 7R

Option 2 (CHA-1HU)



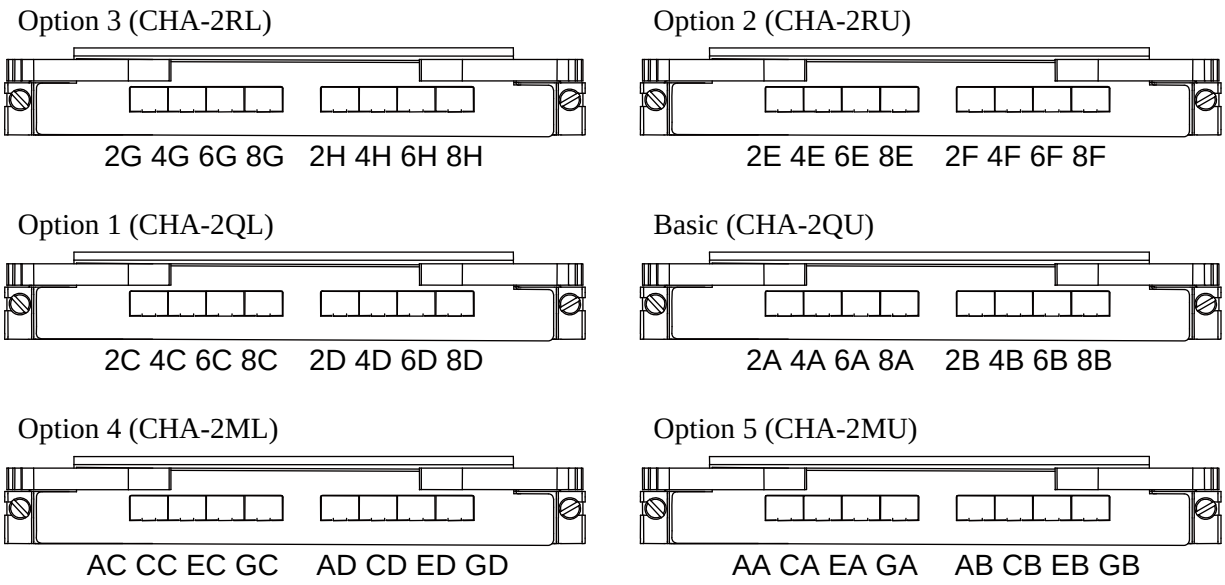
1N 3N 5N 7N 1P 3P 5P 7P

Fig. 4.1-4 Port Number of Fibre 8-port CHA PCB

3. MF Fibre 8-port CHA PCB (DKC-F710I-16MFL/16MFS/16MUL/16MUS/16MUSR)

[1] DKC-0

CHA PCB (Cluster 2)



CHA PCB (Cluster 1)

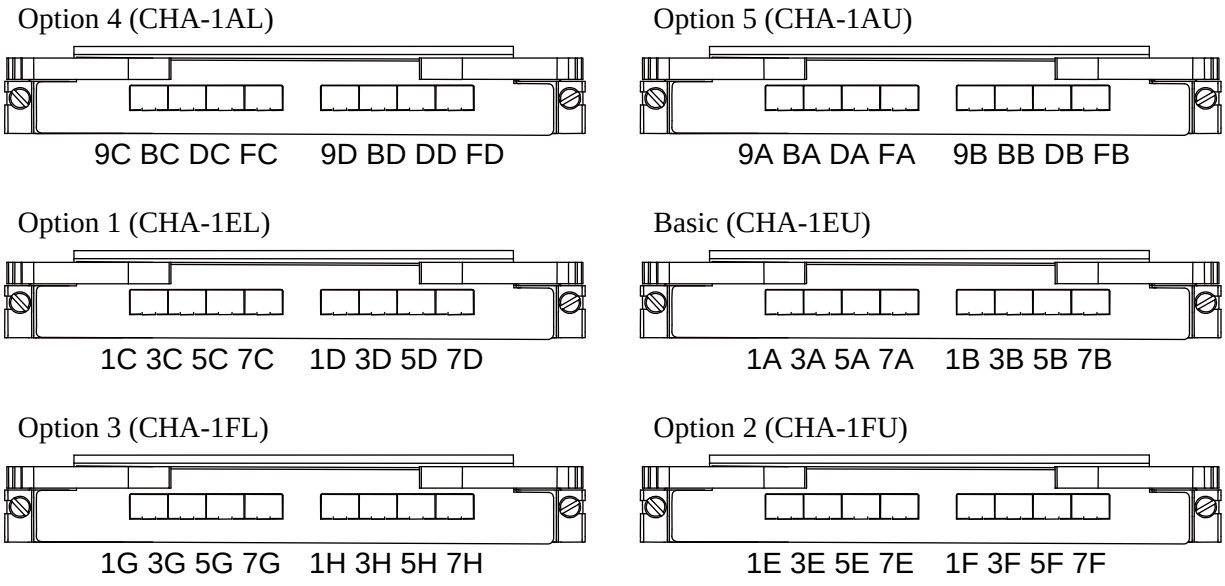
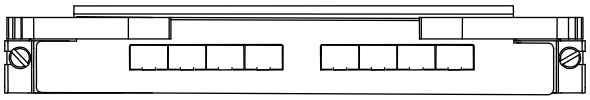


Fig. 4.1-5 Port Number of MF Fibre 8-port CHA PCB

[2] DKC-1

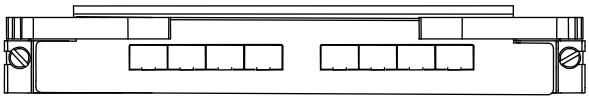
CHA PCB (Cluster 2)

Option 3 (CHA-2UL)



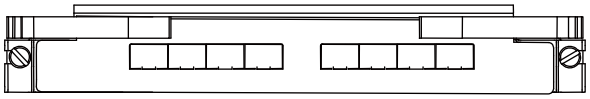
2Q 4Q 6Q 8Q 2R 4R 6R 8R

Option 2 (CHA-2UU)



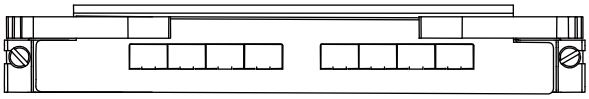
2N 4N 6N 8N 2P 4P 6P 8P

Option 1 (CHA-2TL)



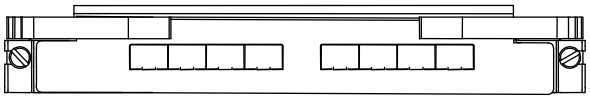
2L 4L 6L 8L 2M 4M 6M 8M

Basic (CHA-2TU)



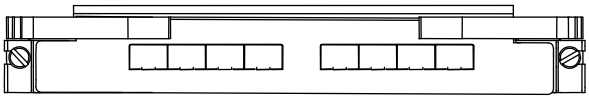
2J 4J 6J 8J 2K 4K 6K 8K

Option 4 (CHA-2XL)



AL CL EL GL AM CM EM GM

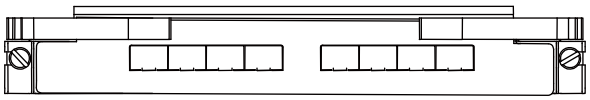
Option 5 (CHA-2XU)



AJ CJ EJ GJ AK CK EK GK

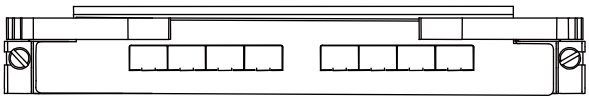
CHA PCB (Cluster 1)

Option 4 (CHA-1LL)



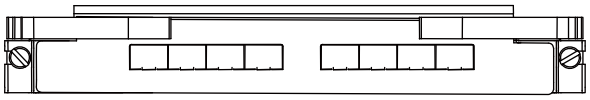
9L BL DL FL 9M BM DM FM

Option 5 (CHA-1LU)



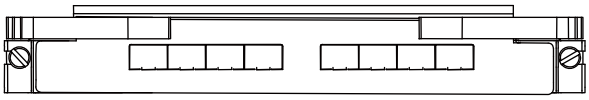
9J BJ DJ FJ 9K BK DK FK

Option 1 (CHA-1GL)



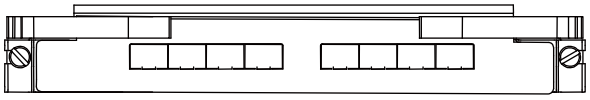
1L 3L 5L 7L 1M 3M 5M 7M

Basic (CHA-1GU)



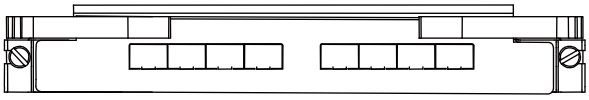
1J 3J 5J 7J 1K 3K 5K 7K

Option 3 (CHA-1HL)



1Q 3Q 5Q 7Q 1R 3R 5R 7R

Option 2 (CHA-1HU)



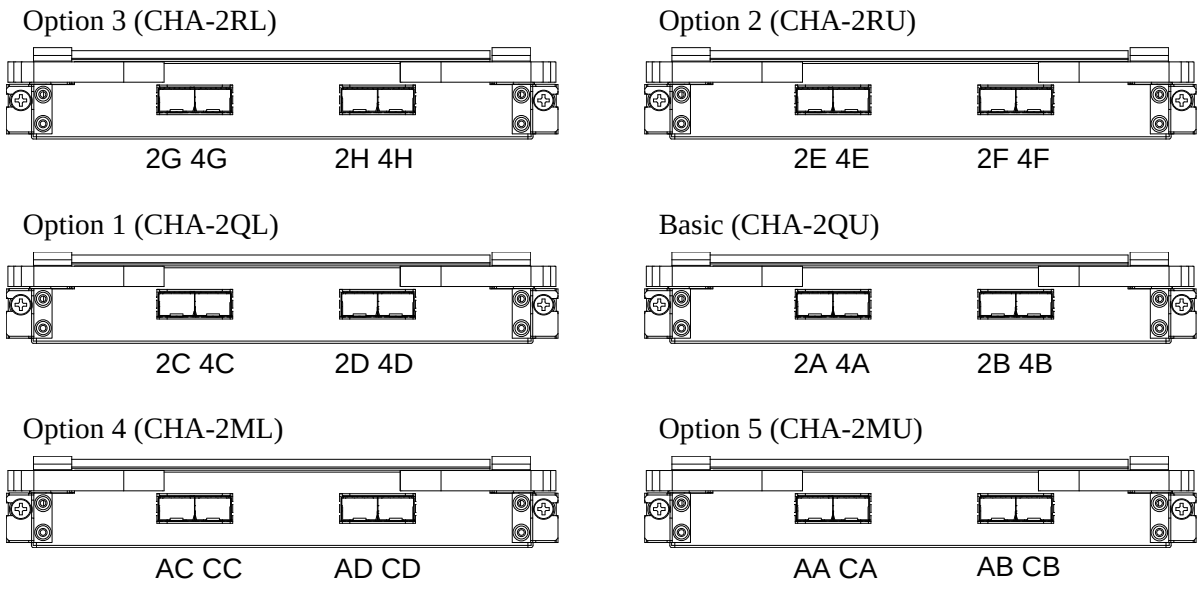
1N 3N 5N 7N 1P 3P 5P 7P

Fig. 4.1-6 Port Number of MF Fibre 8-port CHA PCB

4. FCoE 4-port CHA PCB (DKC-F710I-8FOE)

[1] DKC-0

CHA PCB (Cluster 2)



CHA PCB (Cluster 1)

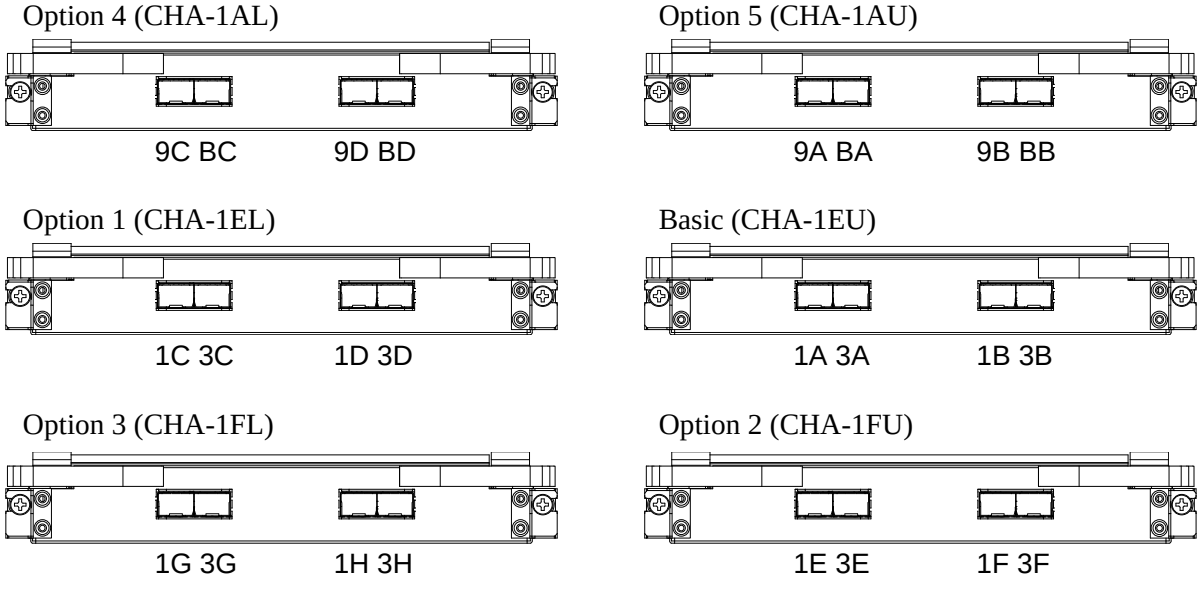
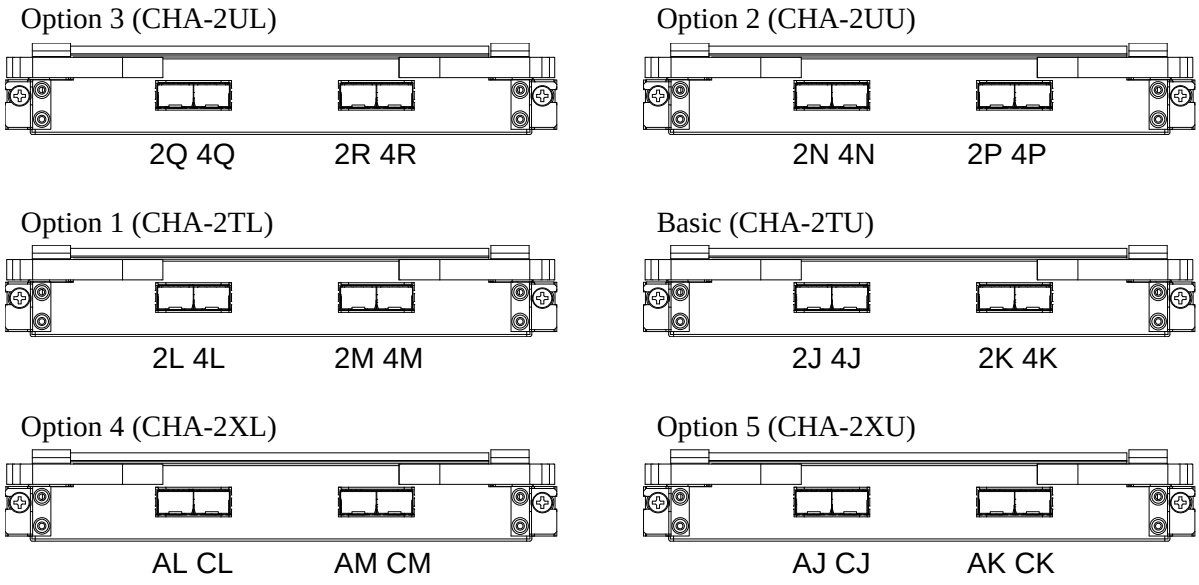


Fig. 4.1-7 Port Number of FCoE 4-port CHA PCB

[2] DKC-1

CHA PCB (Cluster 2)



CHA PCB (Cluster 1)

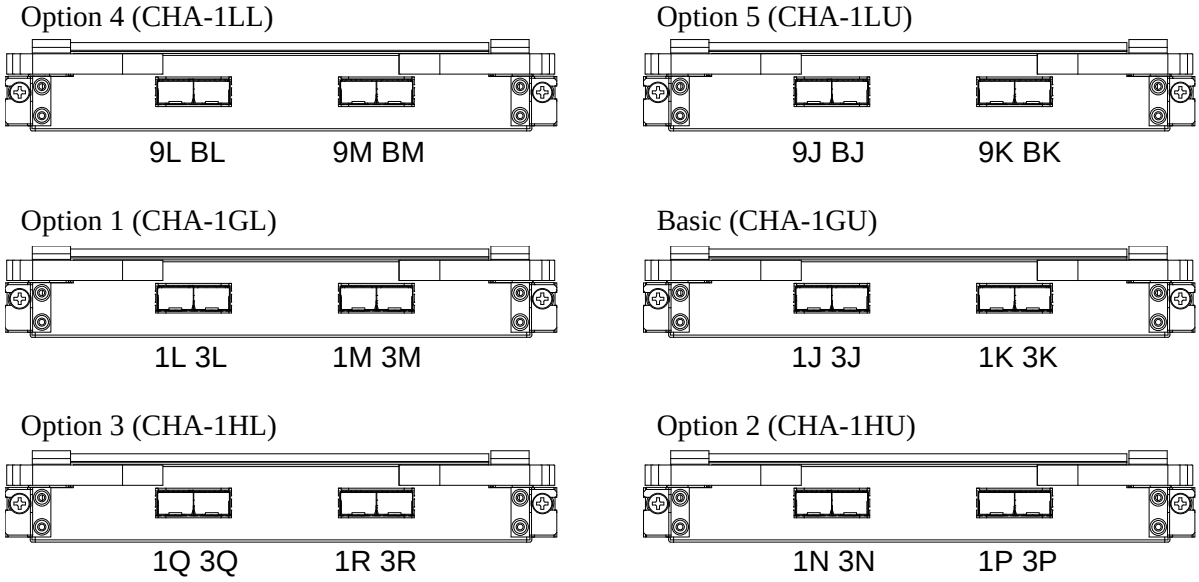
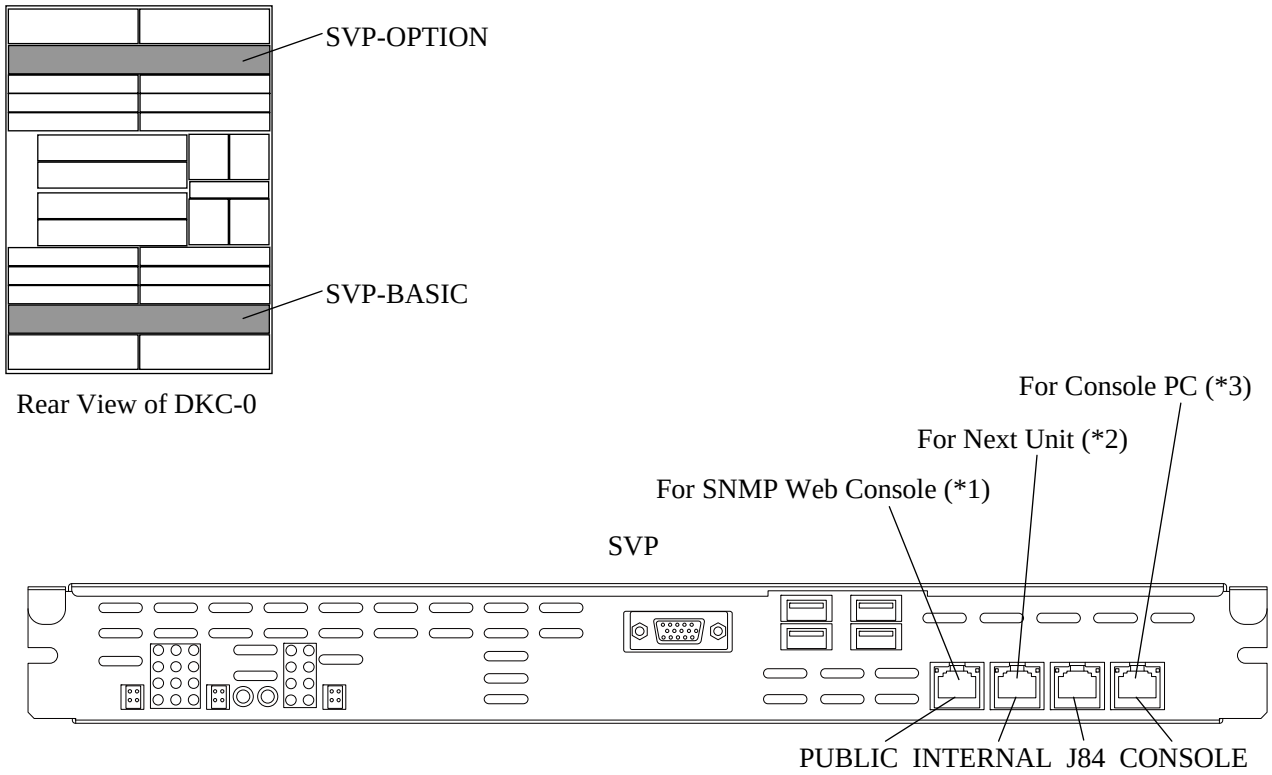


Fig. 4.1-8 Port Number of FCoE 4-port CHA PCB

4.2 SVP Interface



*1: The cables are connected to both SVP-BASIC and SVP-OPTION.
*2: Connect cable only to SVP-BASIC. It is unnecessary to connect to SVP-OPTION.
*3: Connect Console PC only to SVP-BASIC. Connect Console PC to SVP-OPTION only when a trouble occurs on SVP-BASIC.

Fig. 4.2-1 SVP Interface

5. Subsystem Cable Diagram

5.1 Cable Diagram

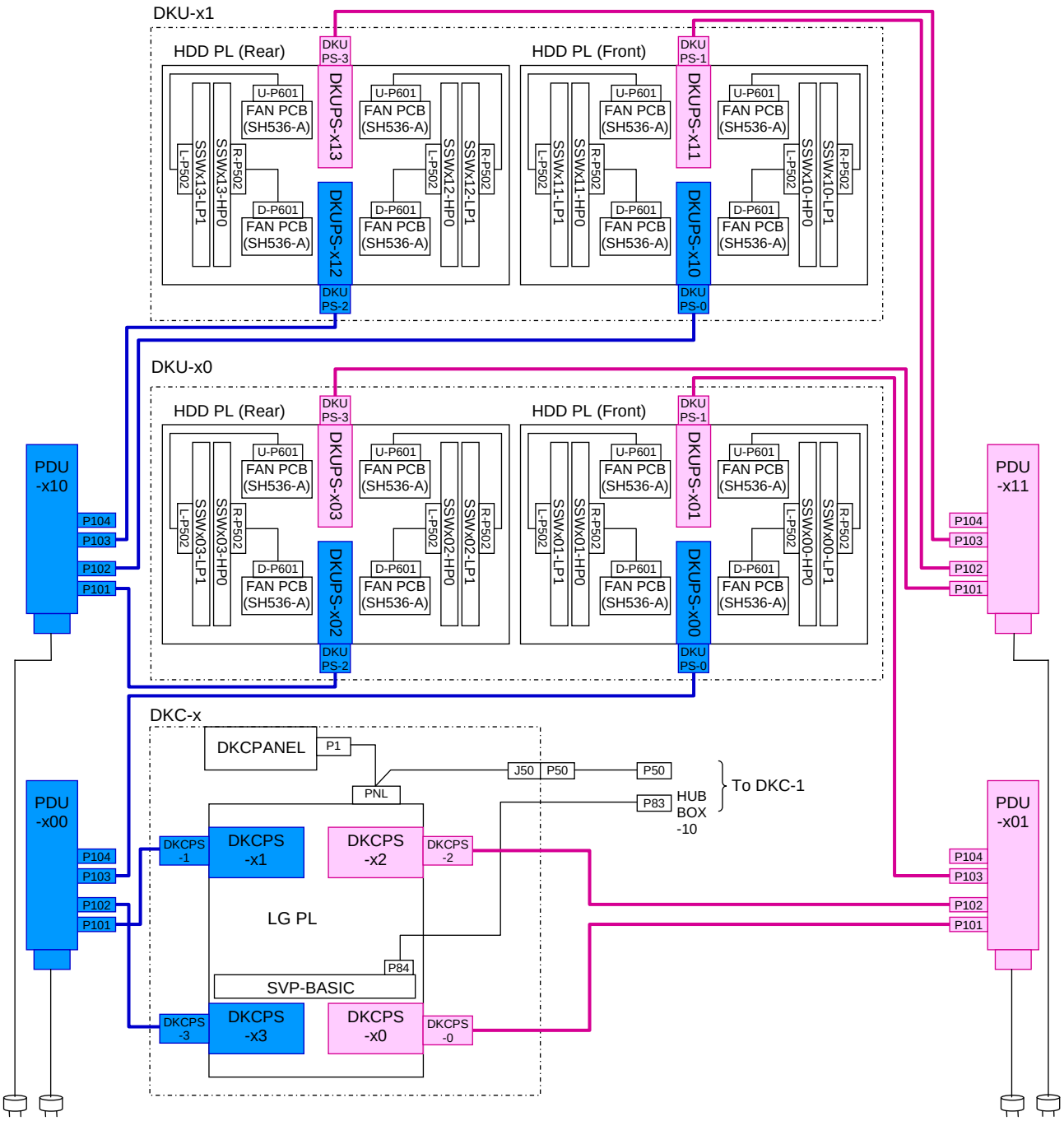


Fig. 5.1-1 Cable Diagram

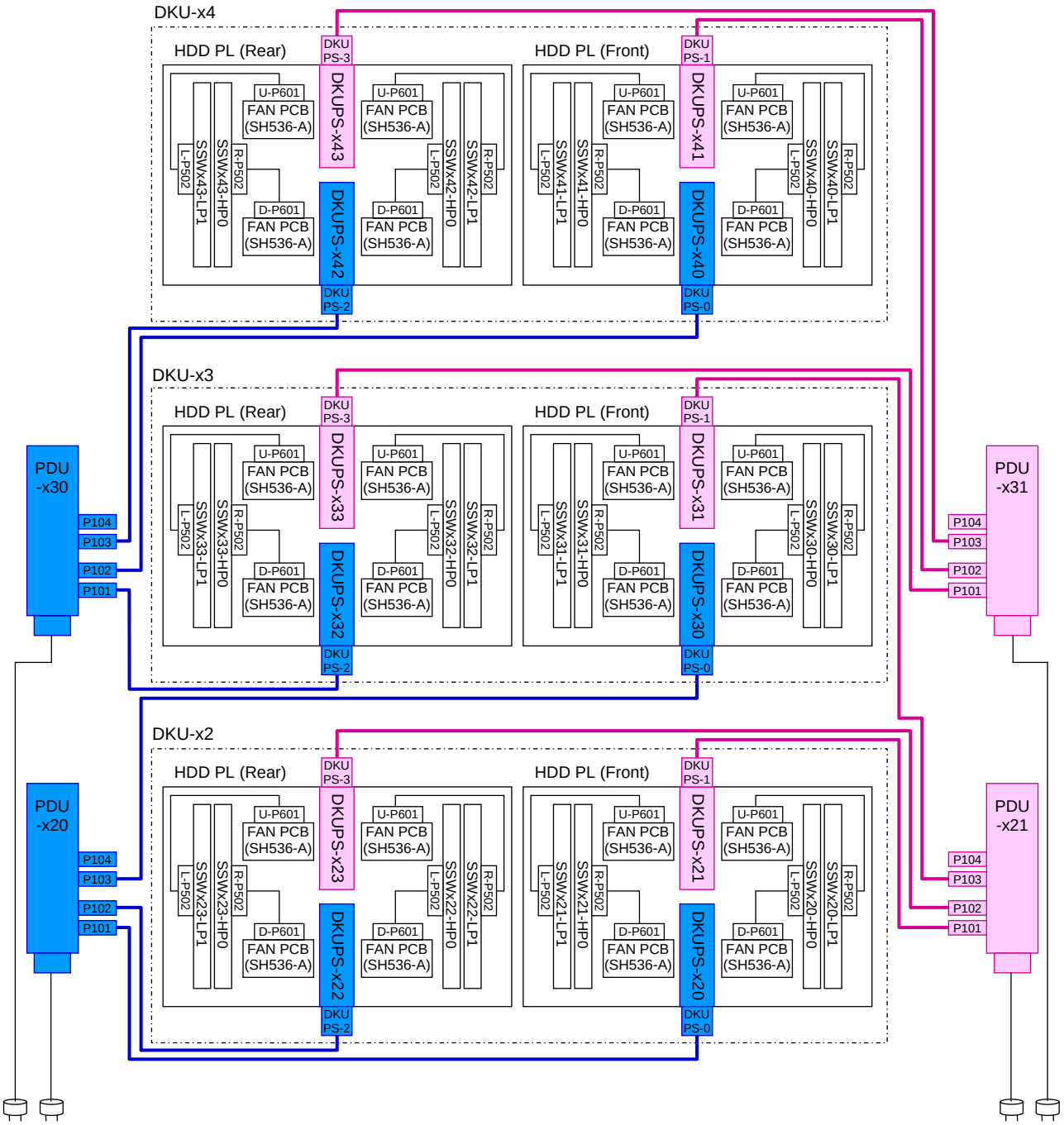


Fig. 5.1-2 Cable Diagram

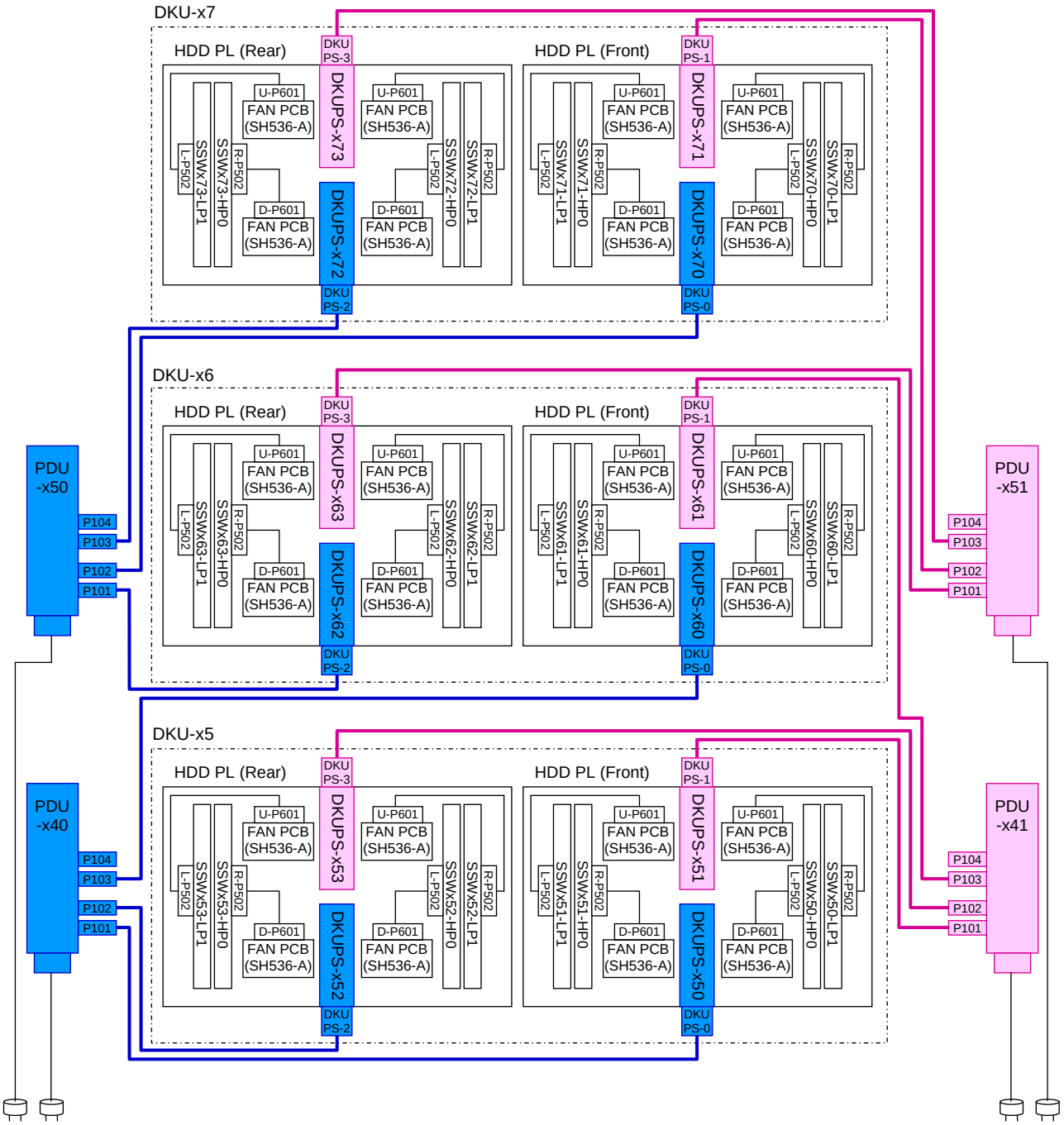


Fig. 5.1-3 Cable Diagram

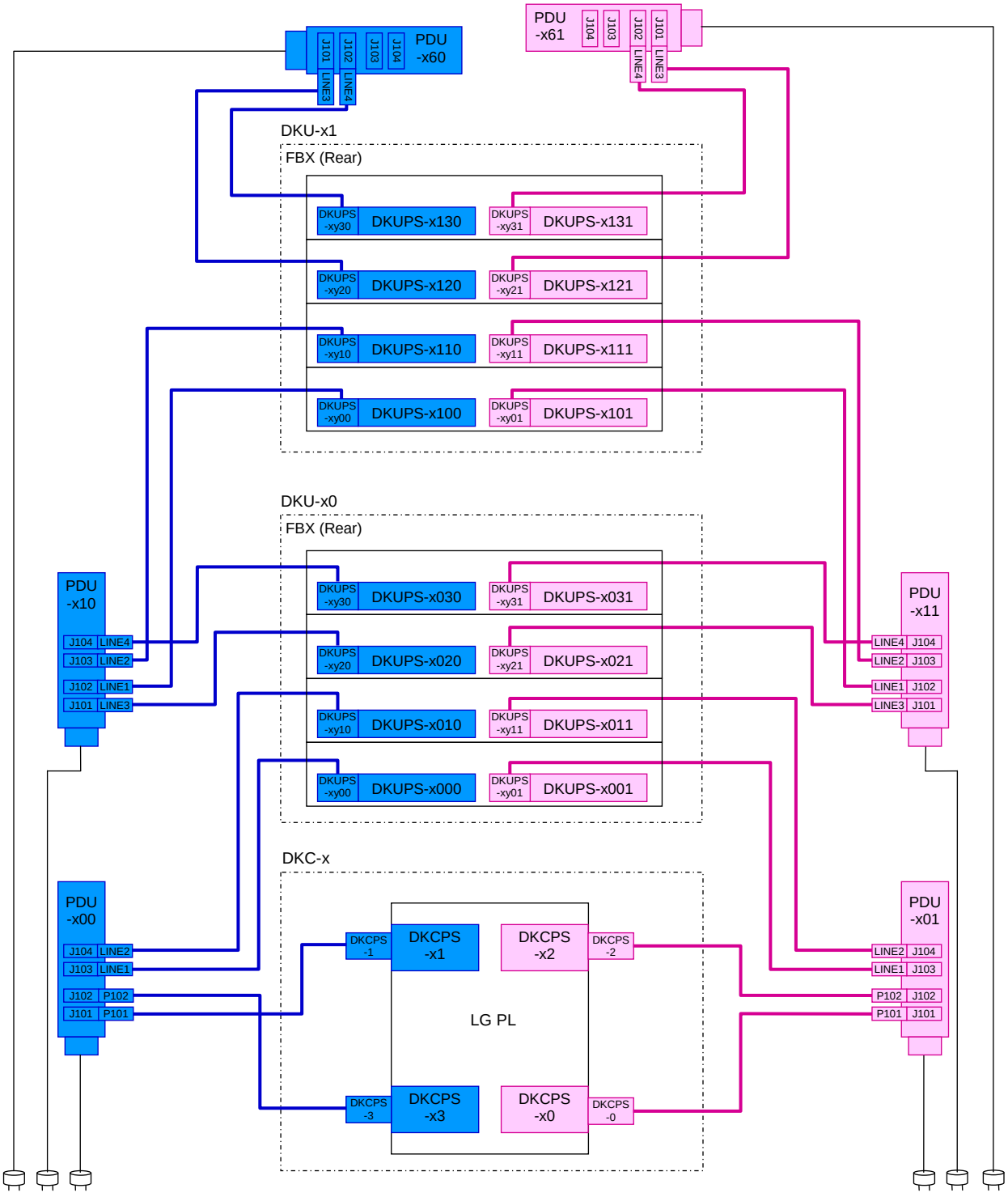
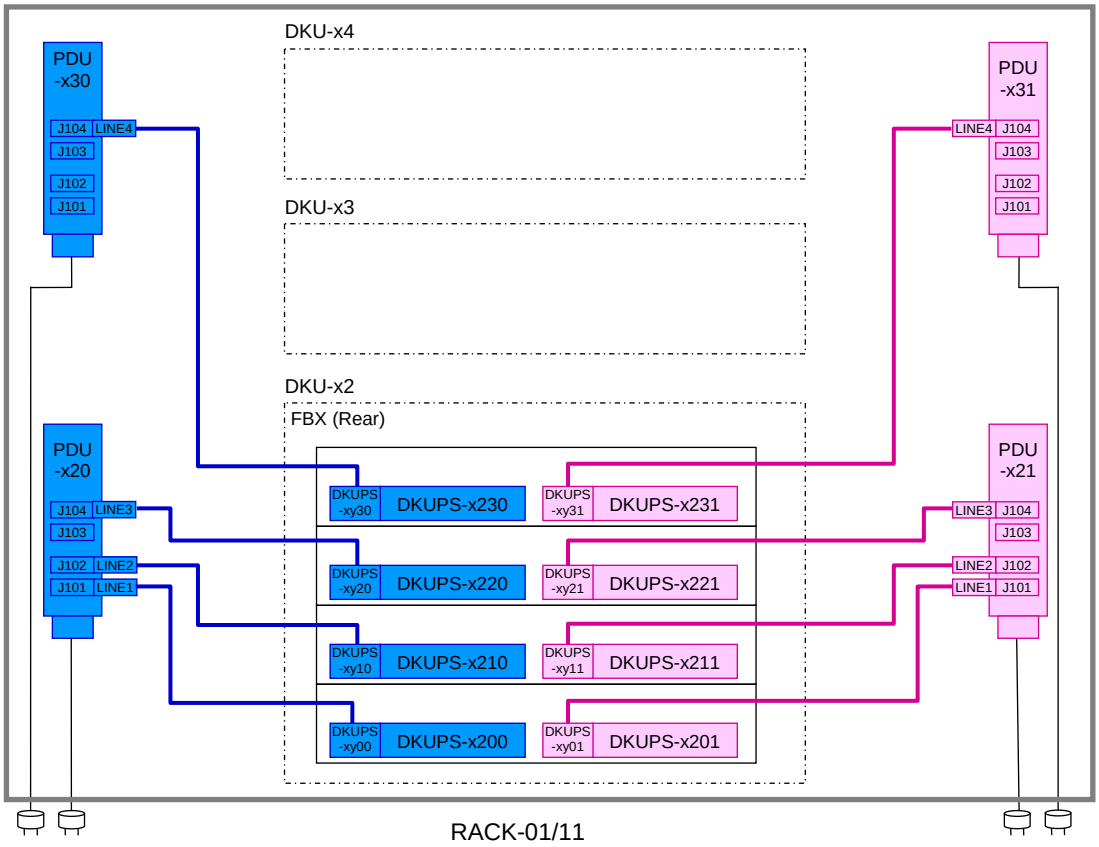


Fig. 5.1-3A Cable Diagram



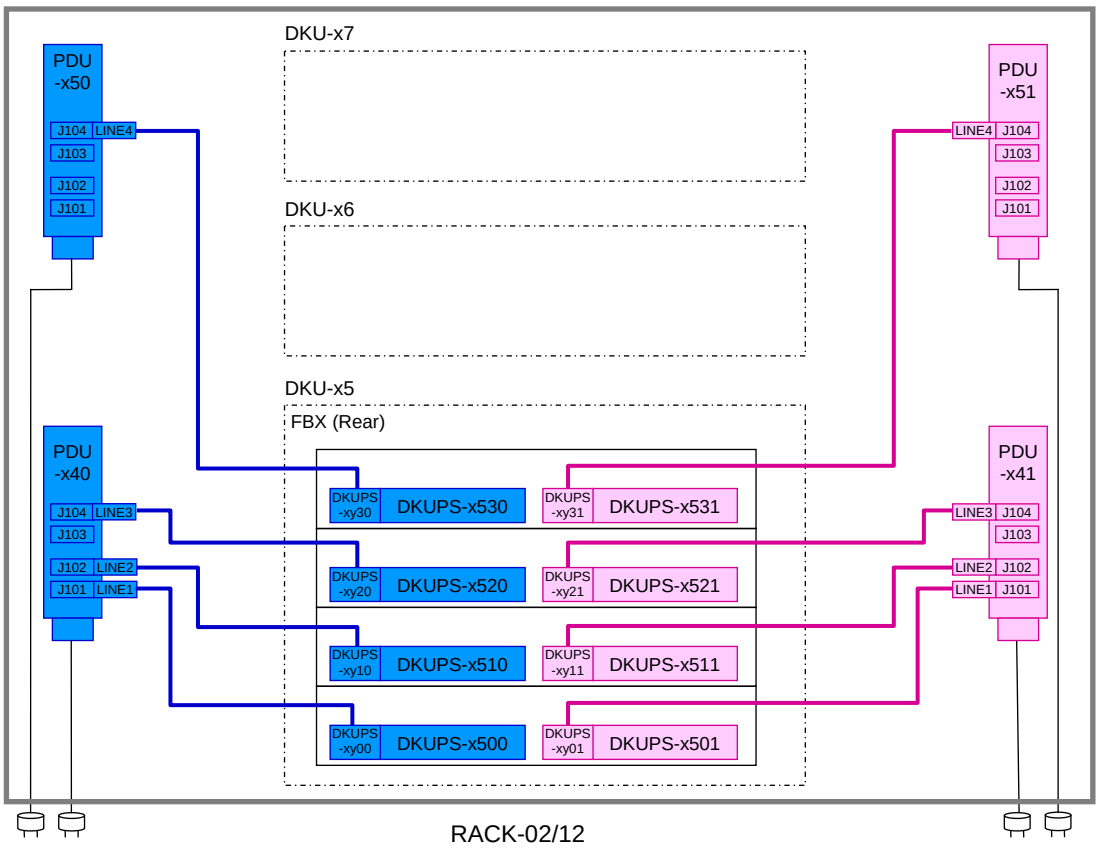
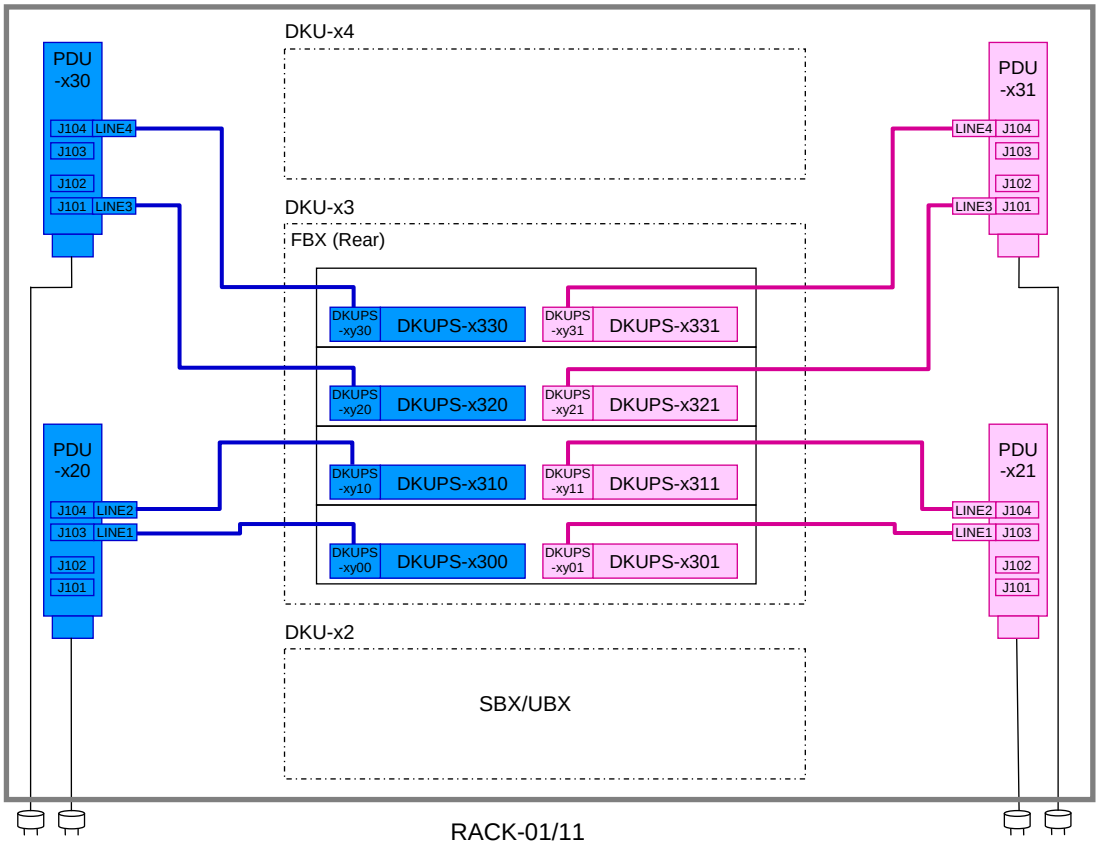
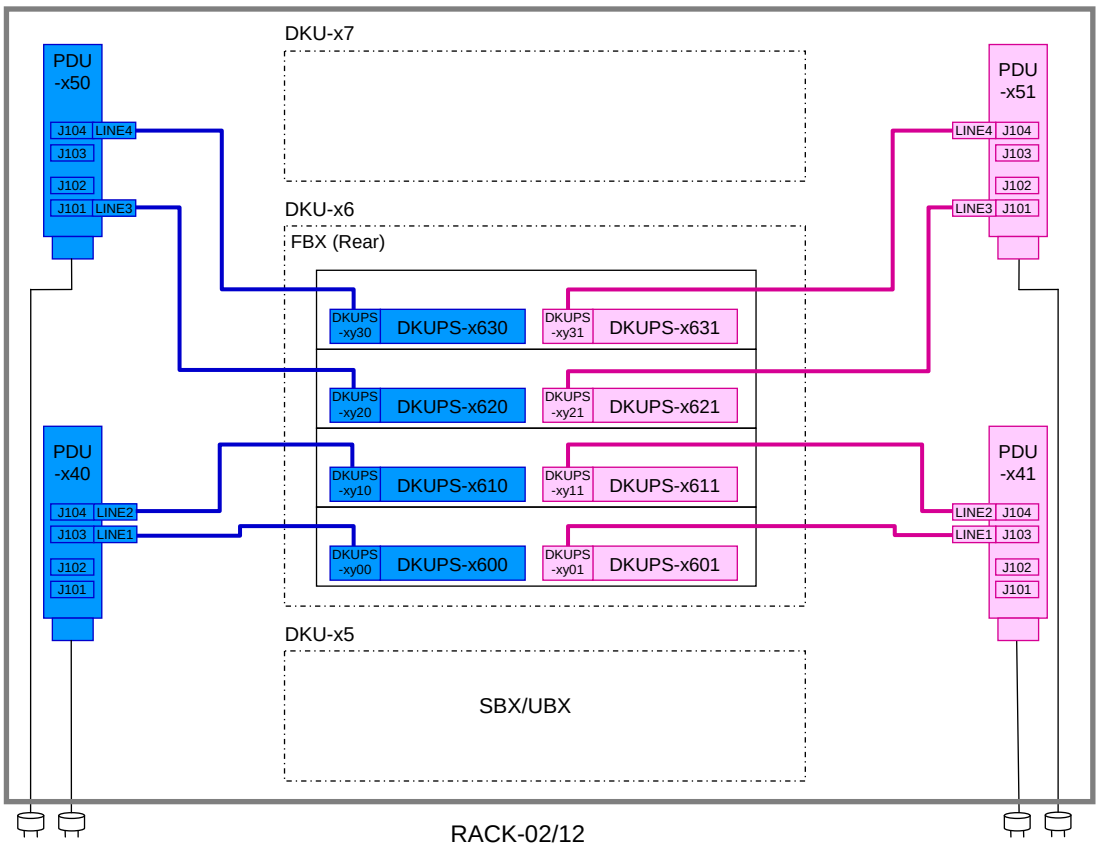


Fig. 5.1-3B Cable Diagram

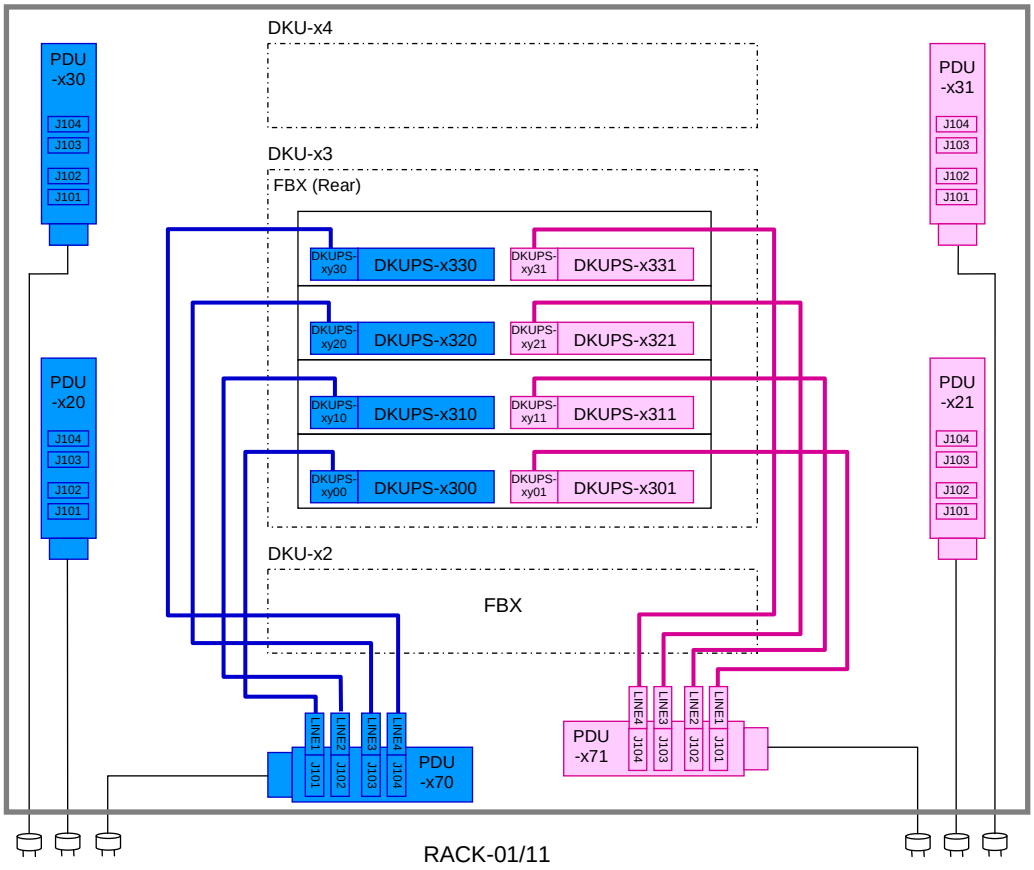


RACK-01/11



RACK-02/12

Fig. 5.1-3C Cable Diagram



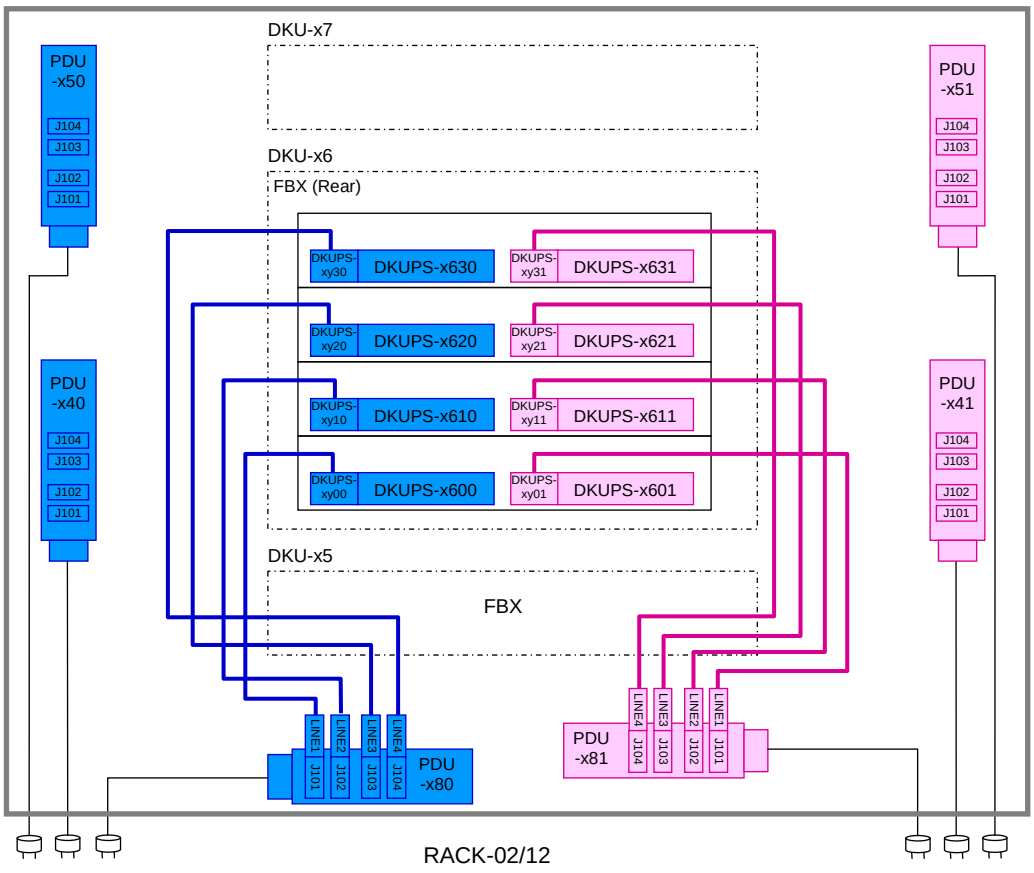
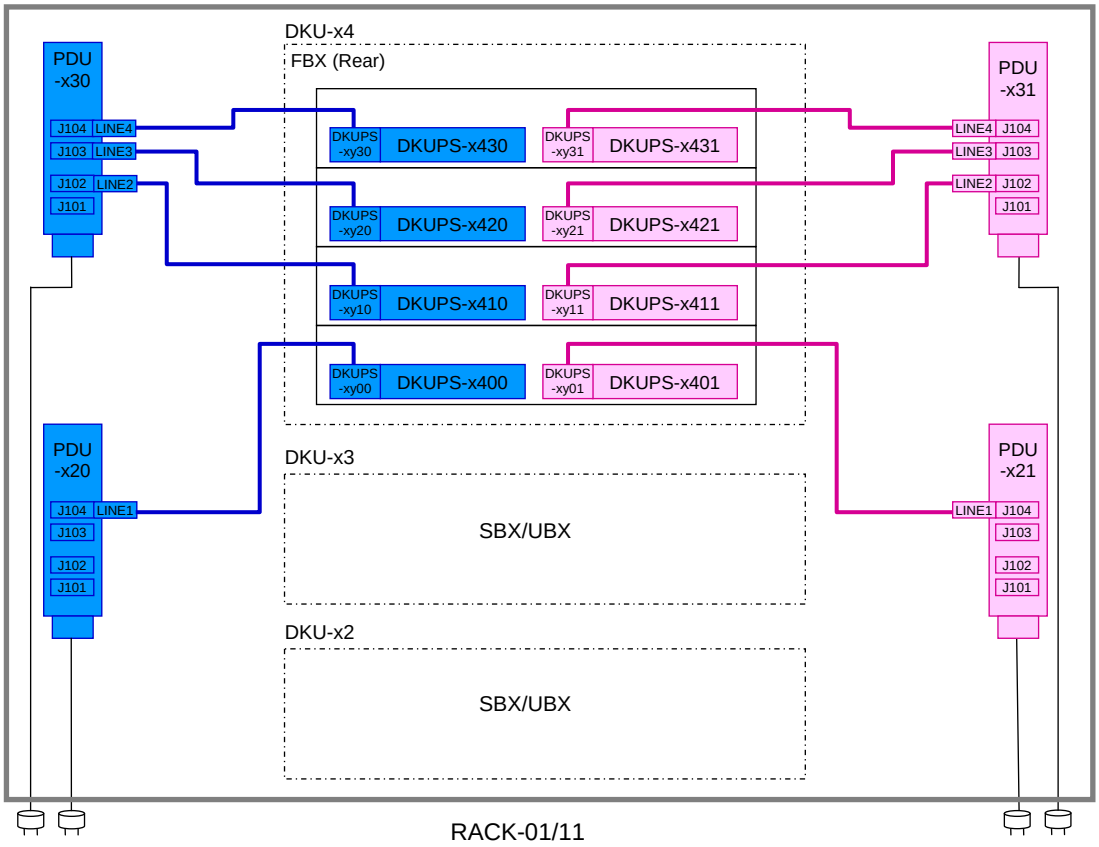


Fig. 5.1-3D Cable Diagram



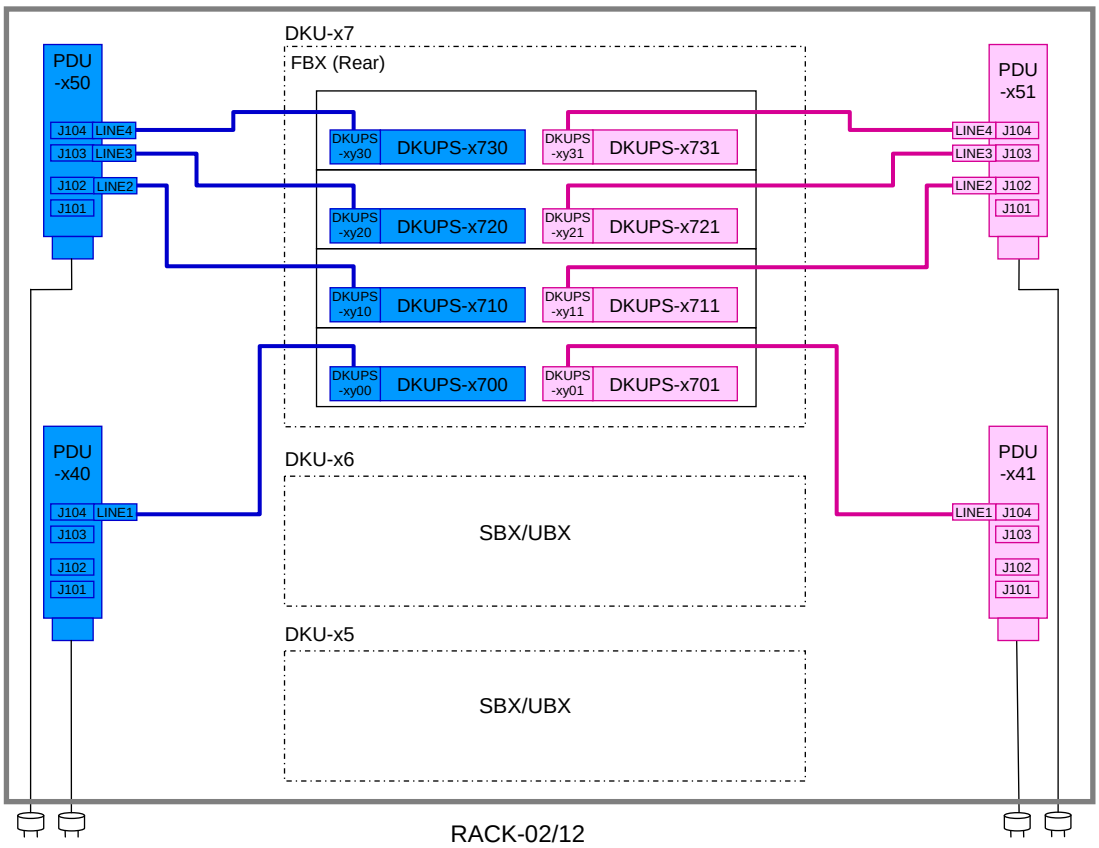
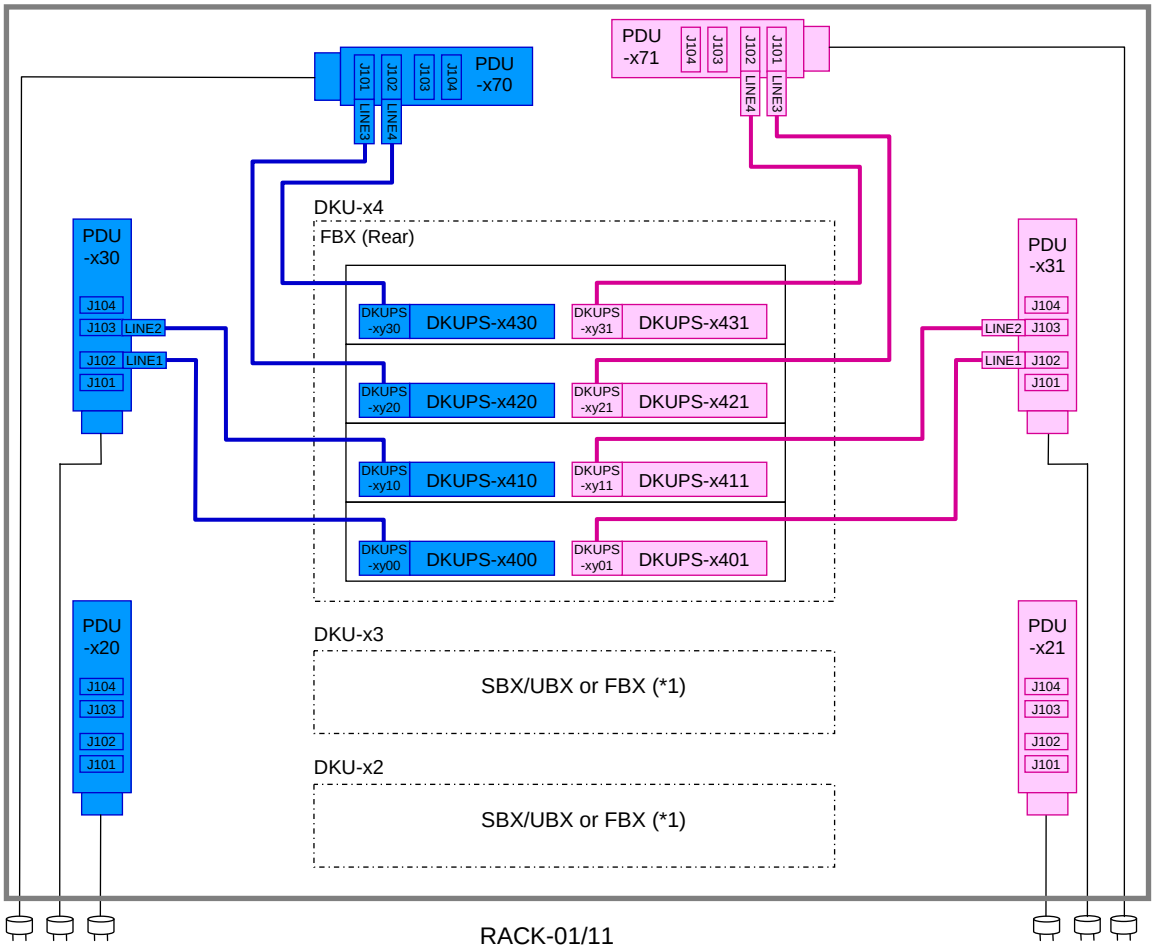
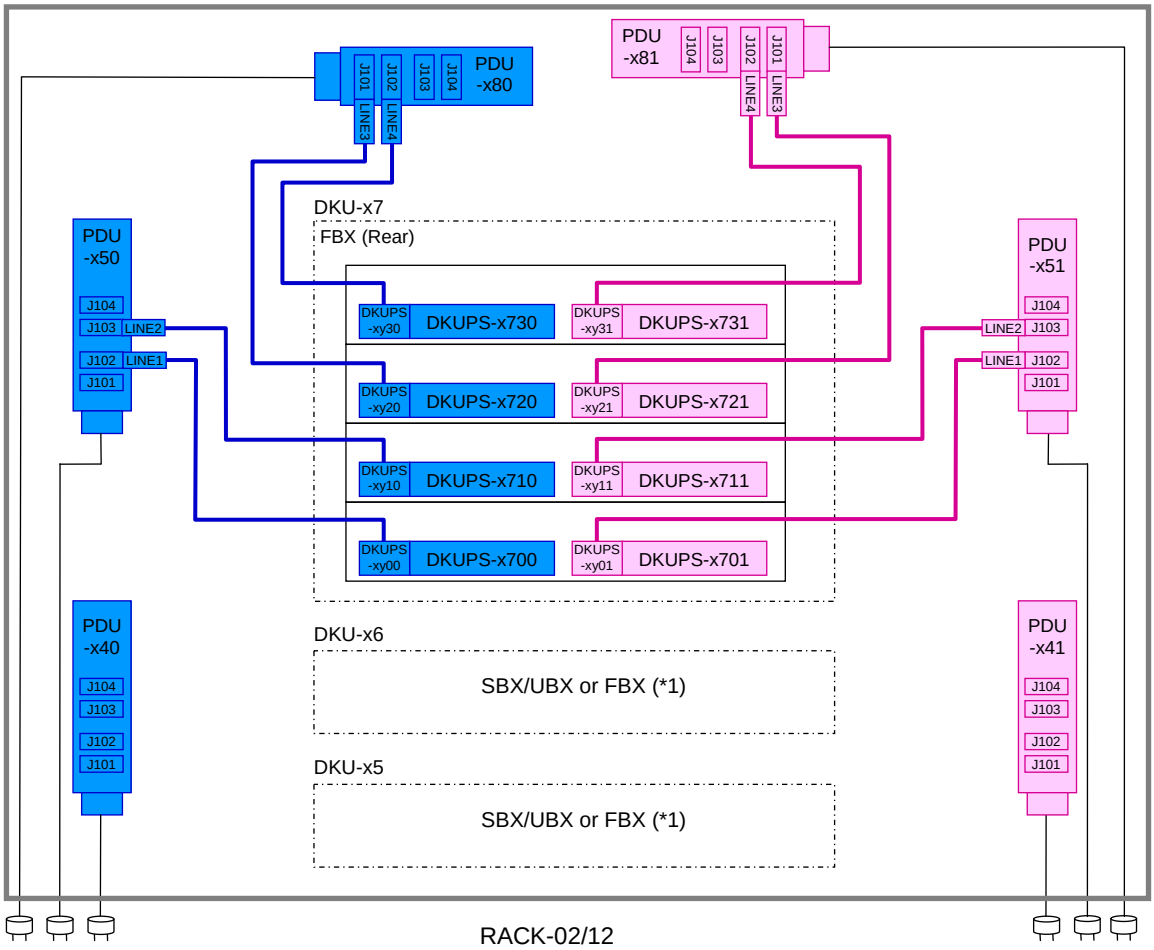


Fig. 5.1-3E Cable Diagram



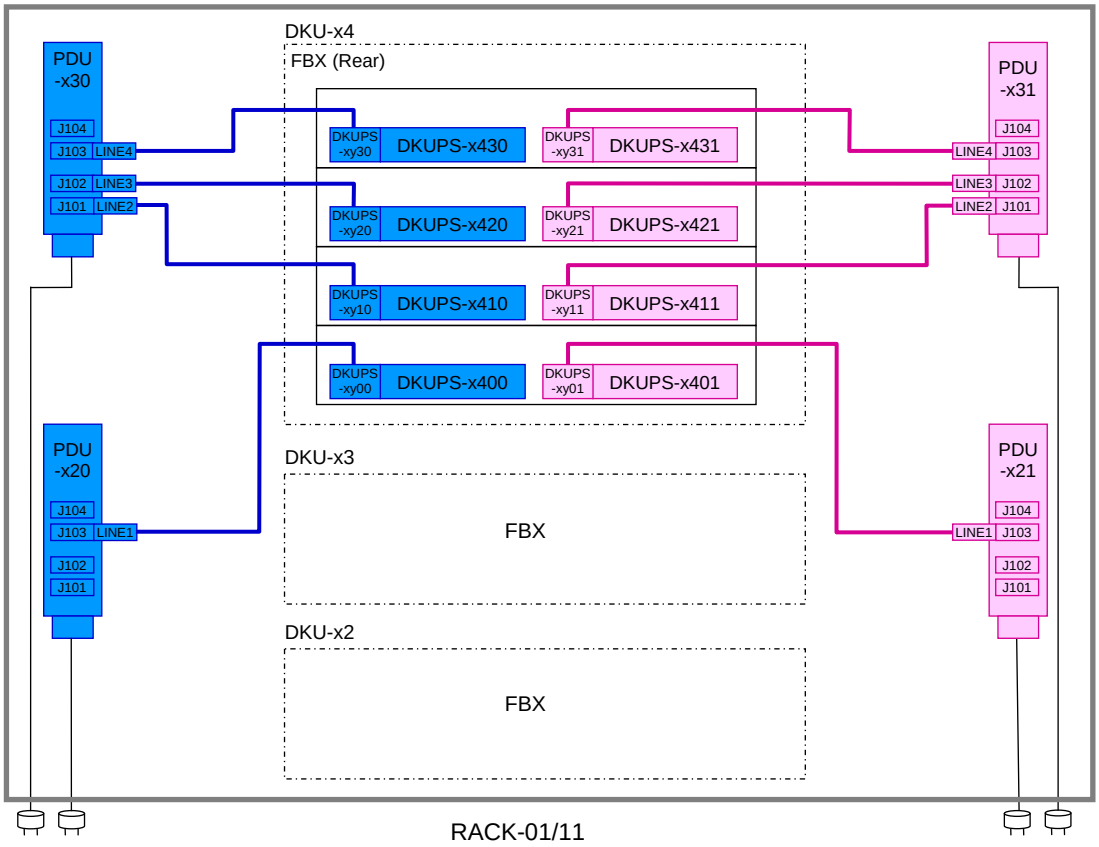
*1: When DKU-x2 is SBX/UBX and DKU-x3 is FBX or
When DKU-x2 is FBX and DKU-x3 is SBX/UBX.

Fig. 5.1-3F Cable Diagram

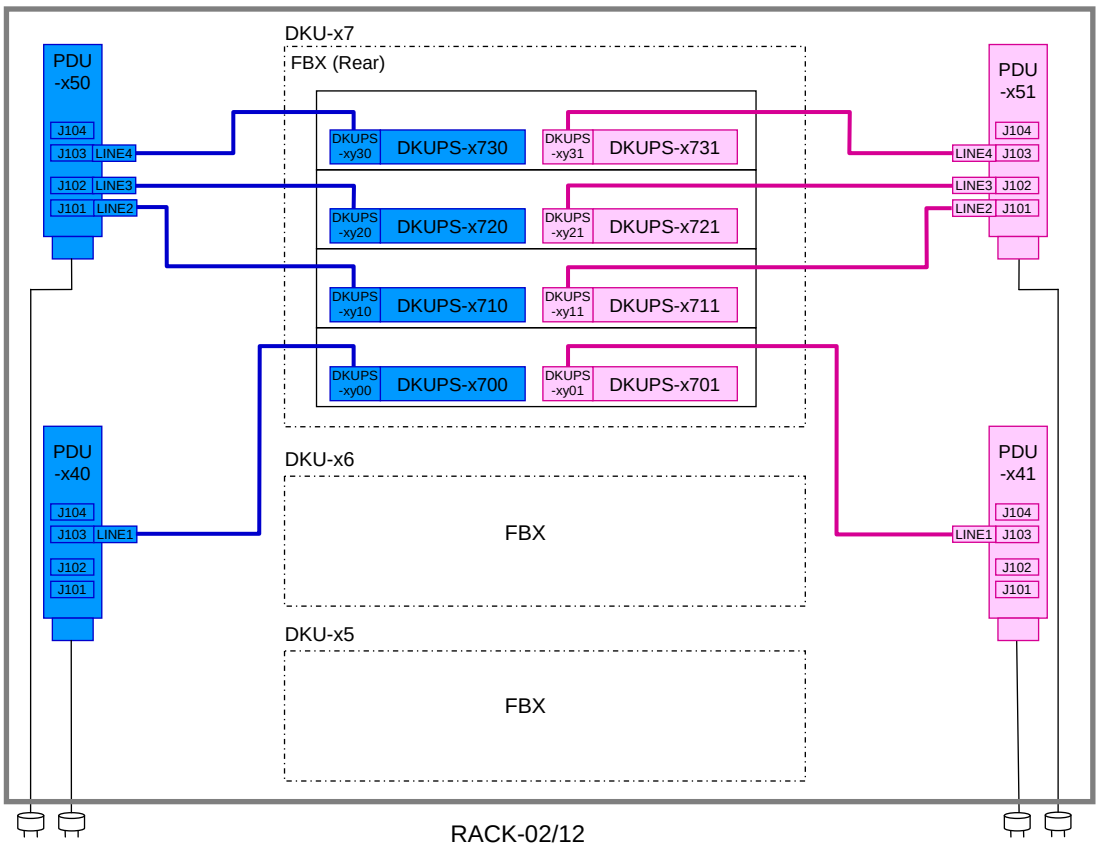


*1: When DKU-x5 is SBX/UBX and DKU-x6 is FBX or
When DKU-x5 is FBX and DKU-x6 is SBX/UBX.

Fig. 5.1-3G Cable Diagram



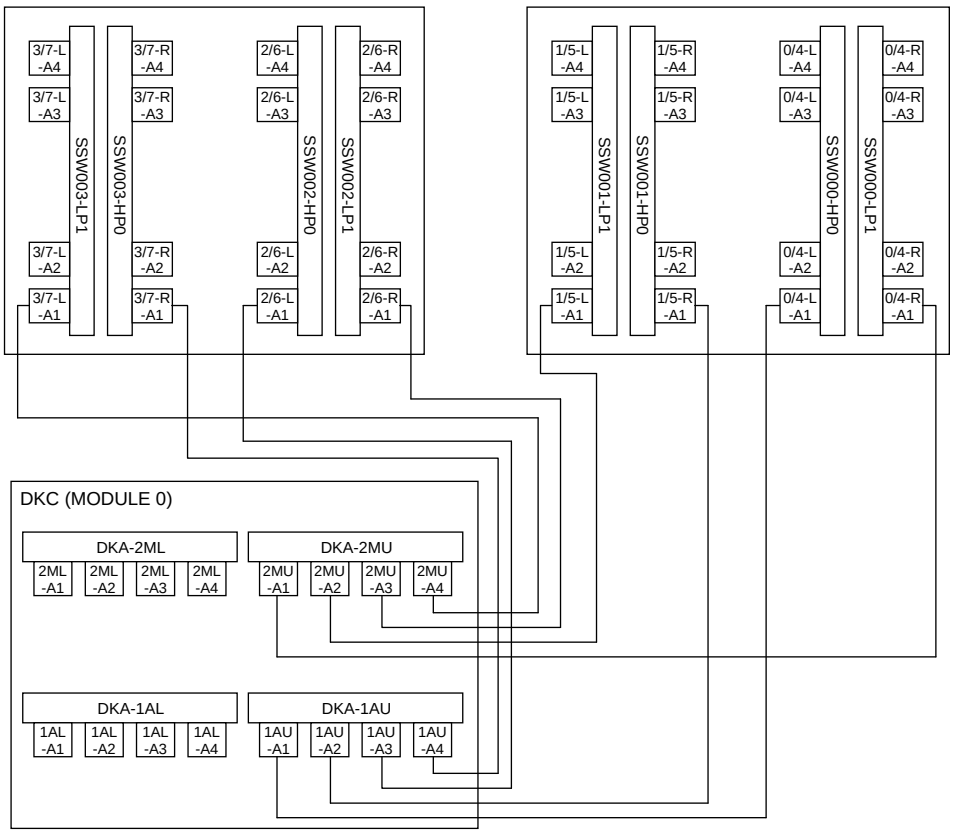
RACK-01/11



RACK-02/12

Fig. 5.1-3H Cable Diagram

1st DKA to DKU-00



1st DKA to DKU-10

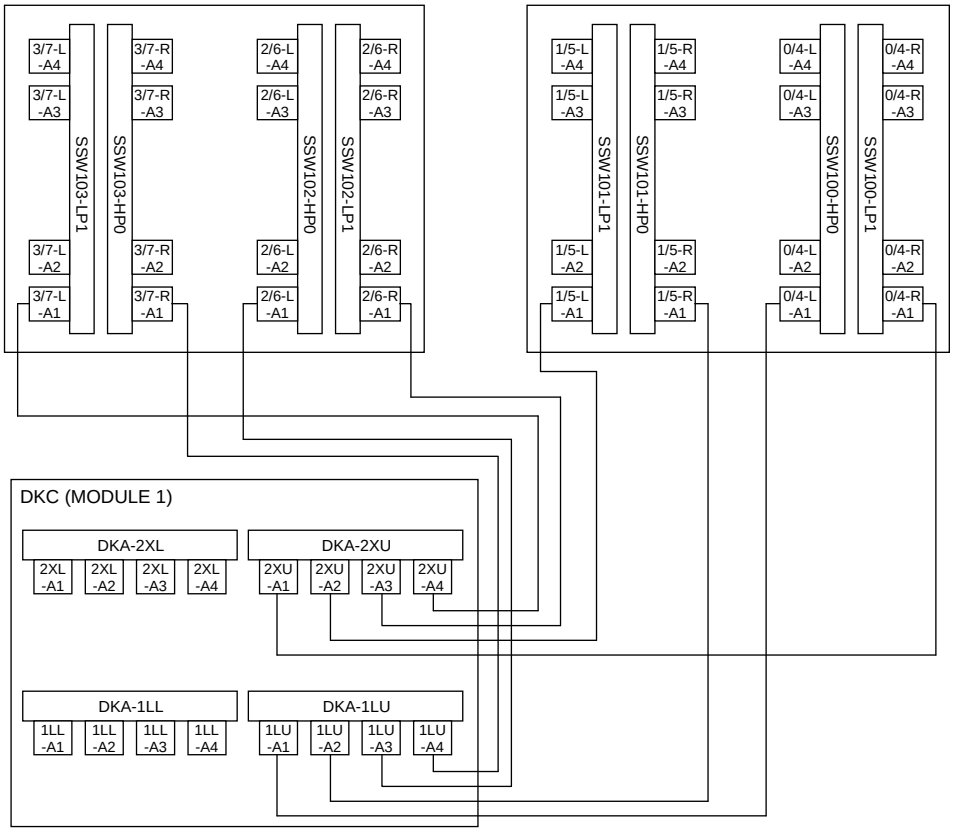
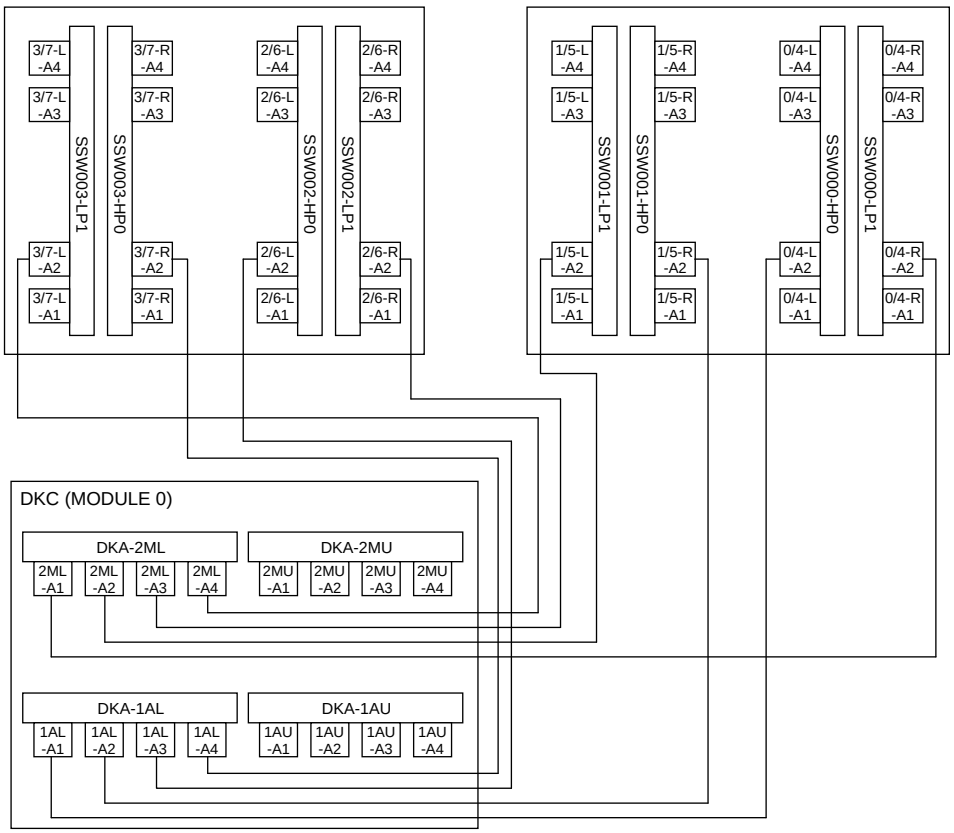


Fig. 5.1-4 Cable Diagram (BUC)

2nd DKA to DKU-00



2nd DKA to DKU-10

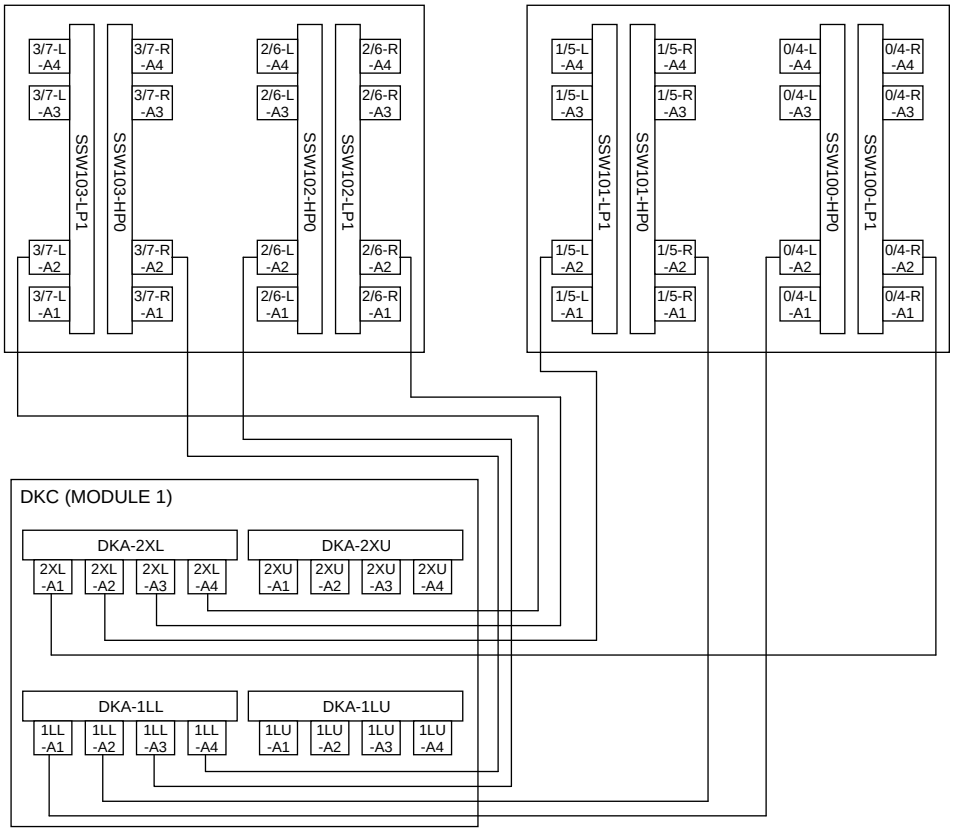


Fig. 5.1-5 Cable Diagram (HBUC)

DKU-xy to DKU-RKx(y+1)

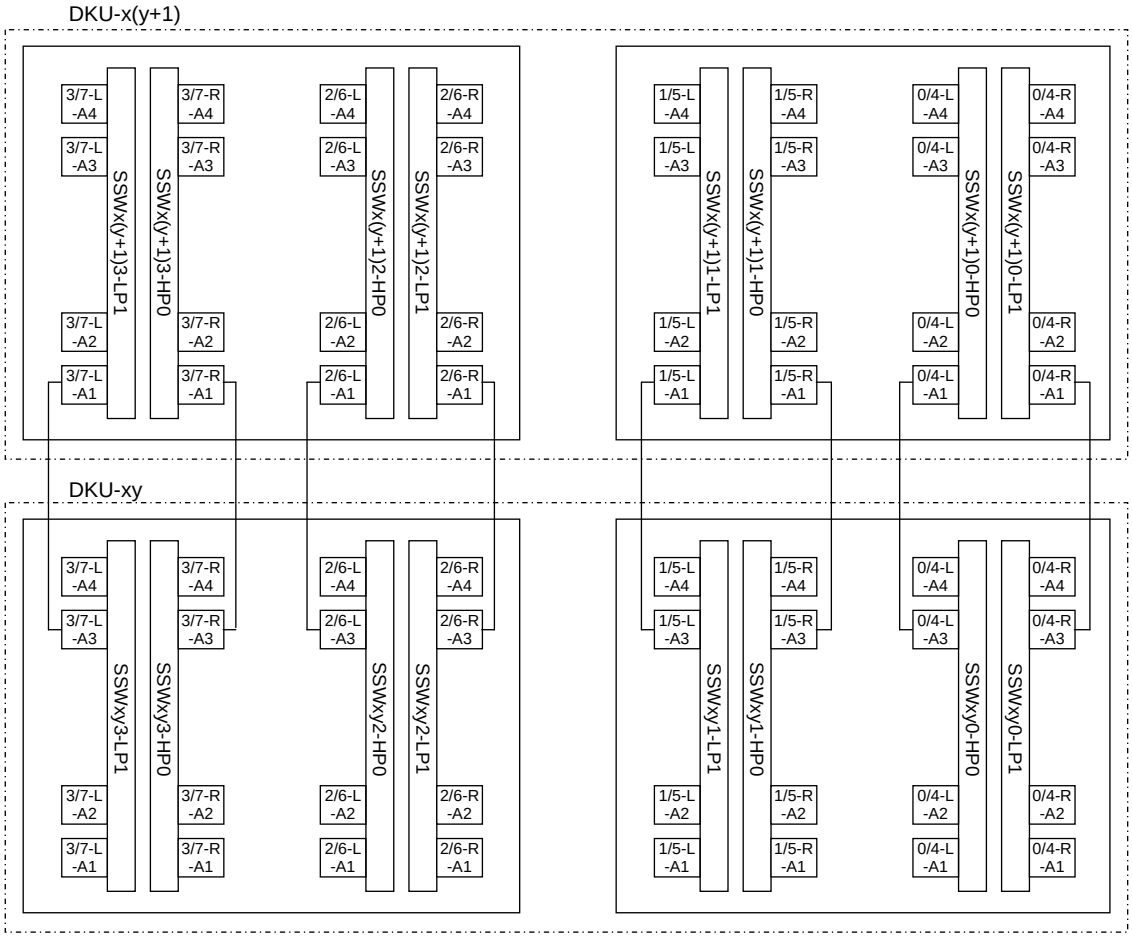


Fig. 5.1-6 Cable Diagram (UUC/RLEXC)

DKU-xy to DKU-x(y+1)

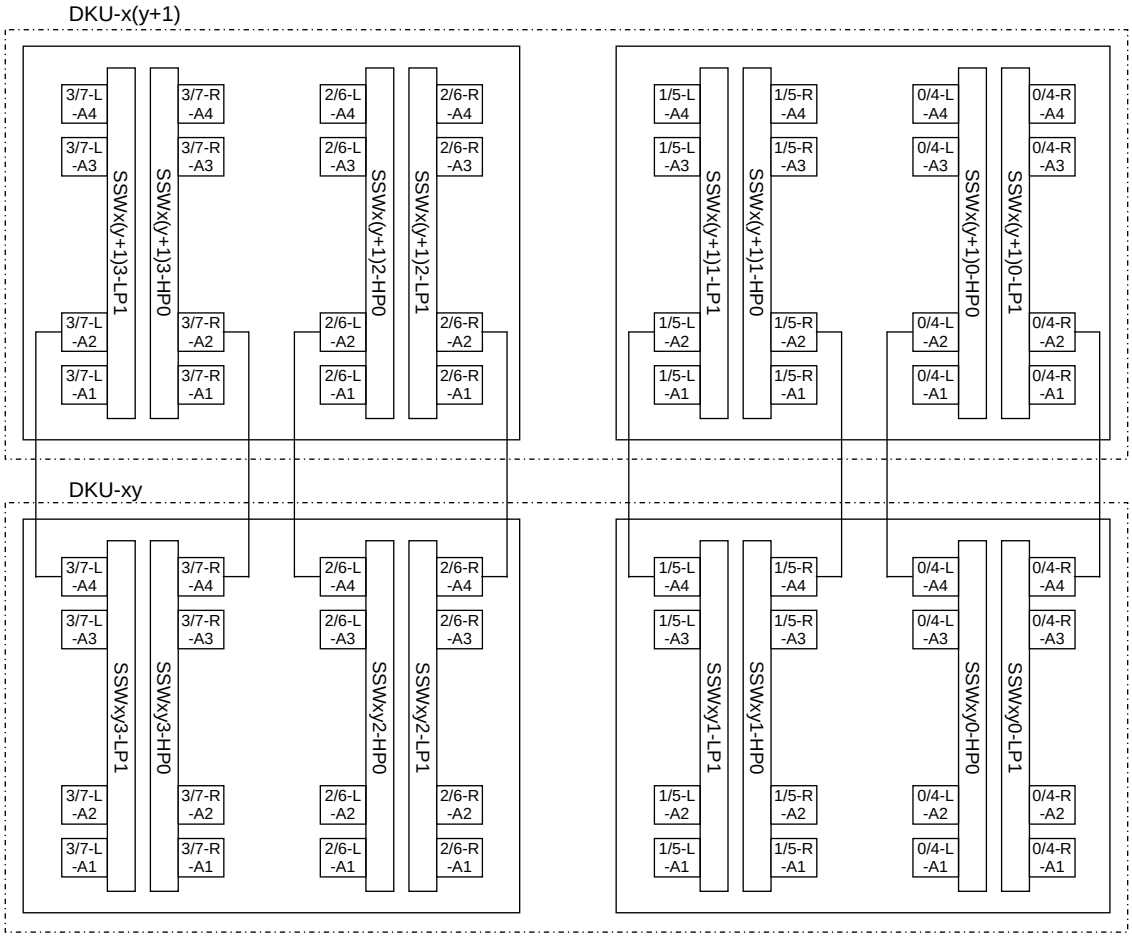


Fig. 5.1-7 Cable Diagram (HUUC/HRLEXC)

DKC-0 to DKC-1 (MDEXC)

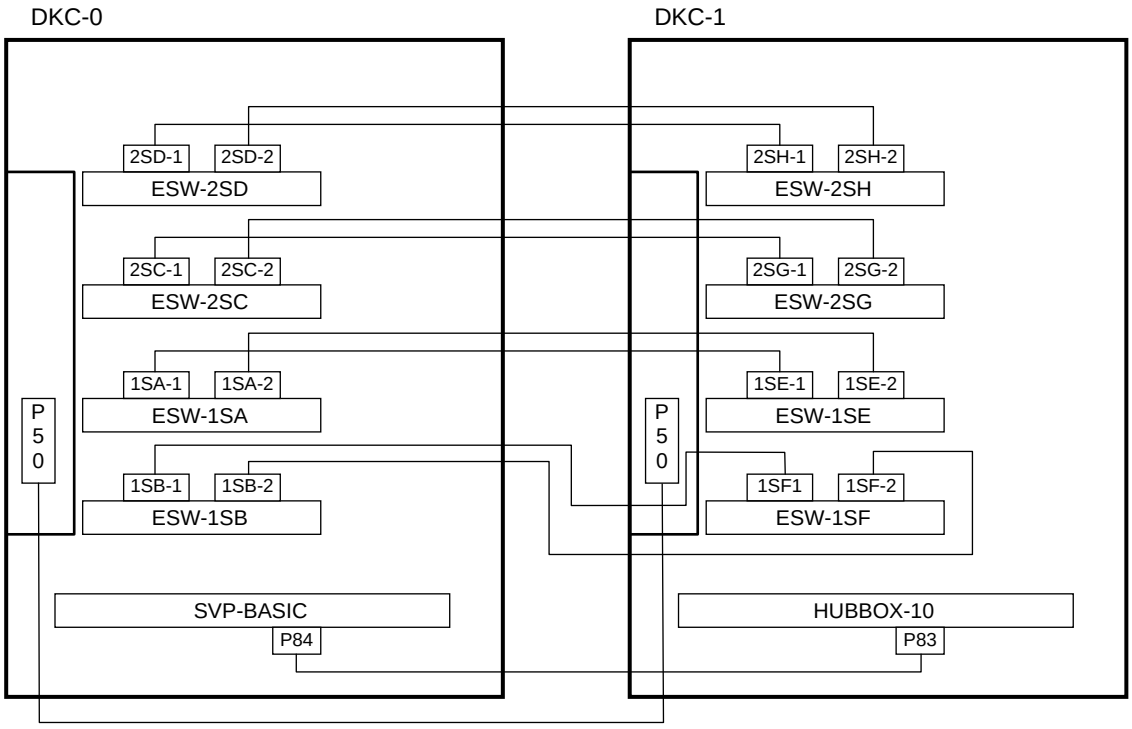
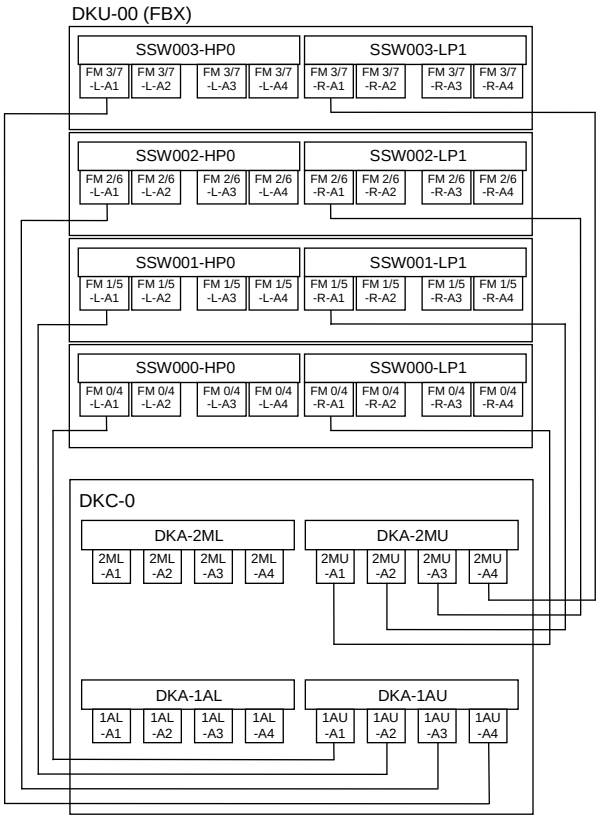


Fig. 5.1-8 Cable Diagram (MDEXC)

1st DKA to DKU-00



1st DKA to DKU-10

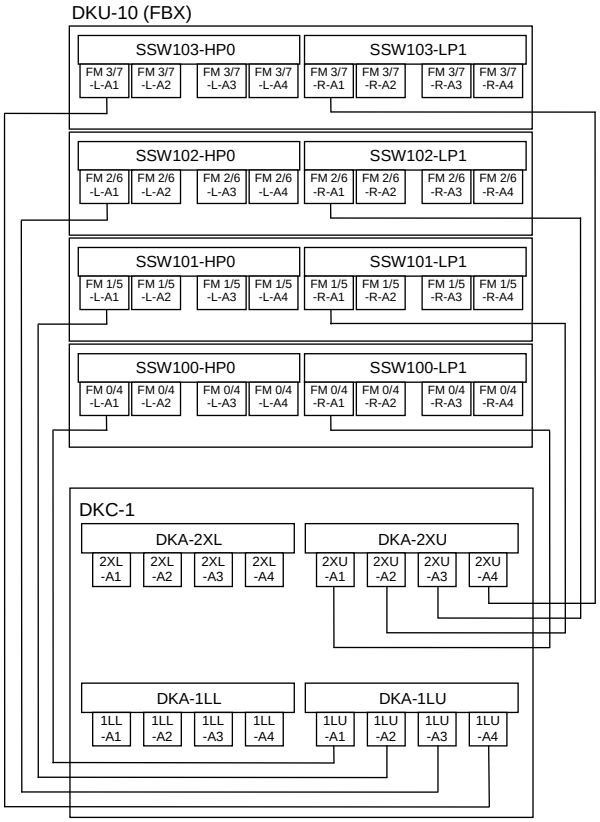
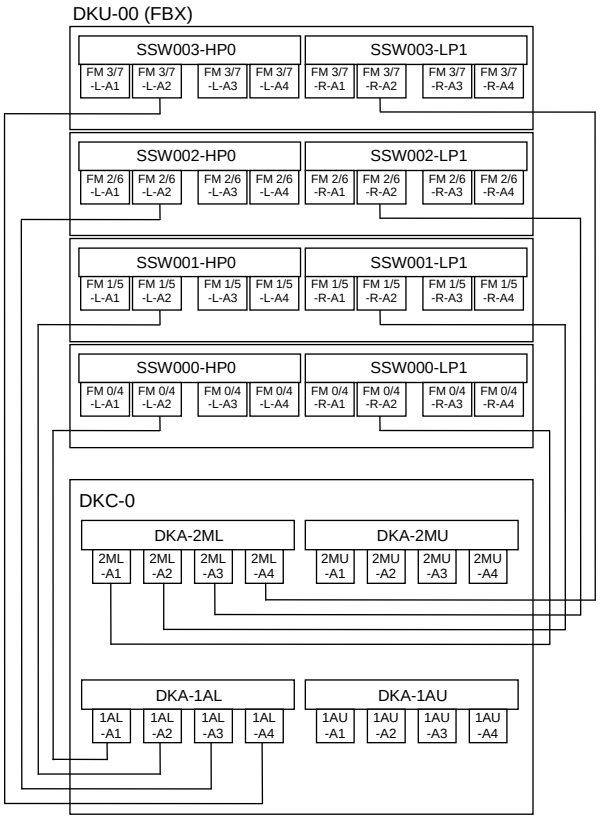


Fig. 5.1-9 Cable Diagram (BFC)

2nd DKA to DKU-00



2nd DKA to DKU-10

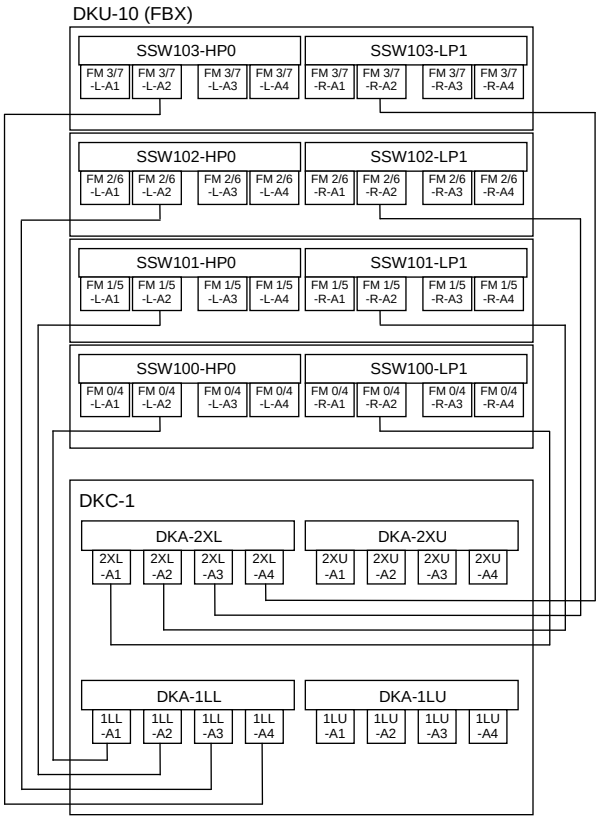


Fig. 5.1-10 Cable Diagram (HBFC)

FBX DKU to FBX DKU

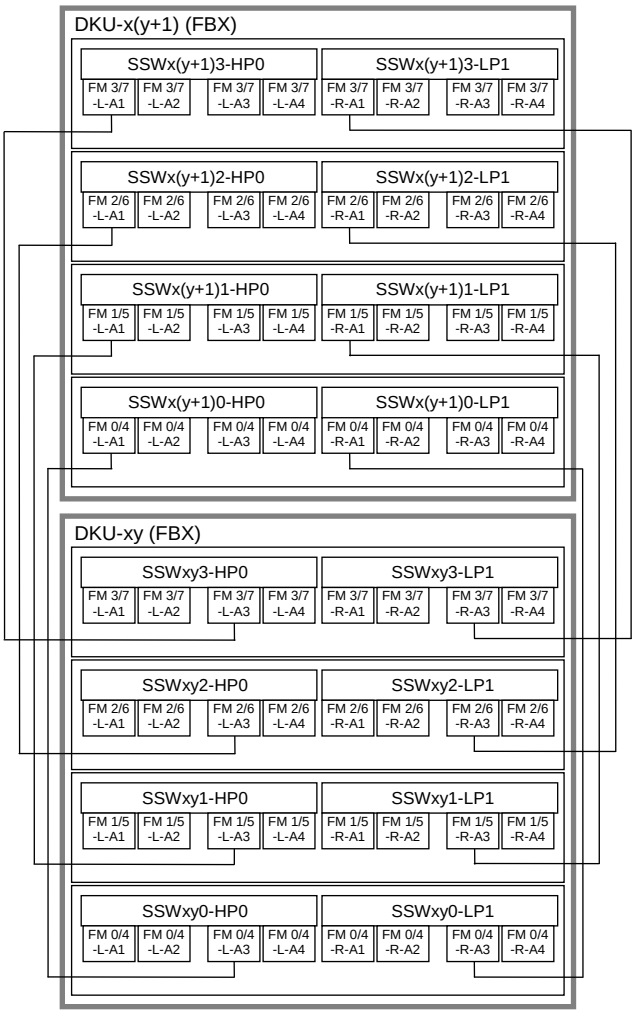


Fig. 5.1-11 Cable Diagram (FFC/FFEXC)

FBX DKU to FBX DKU

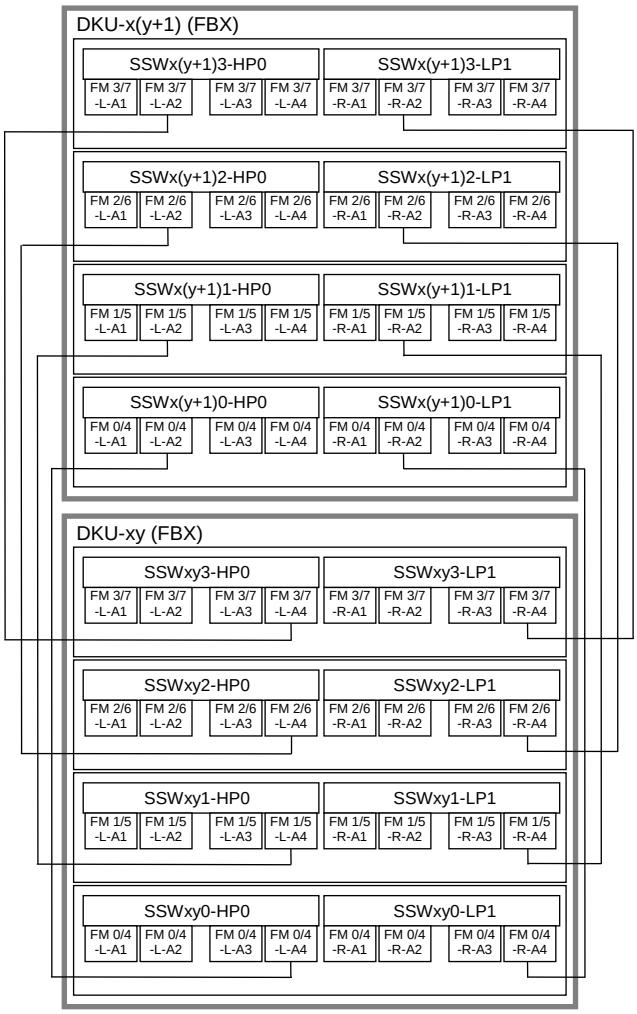


Fig. 5.1-12 Cable Diagram (HFFC/HFFEXC)

UBX/SBX(Upper) to FBX(Lower)

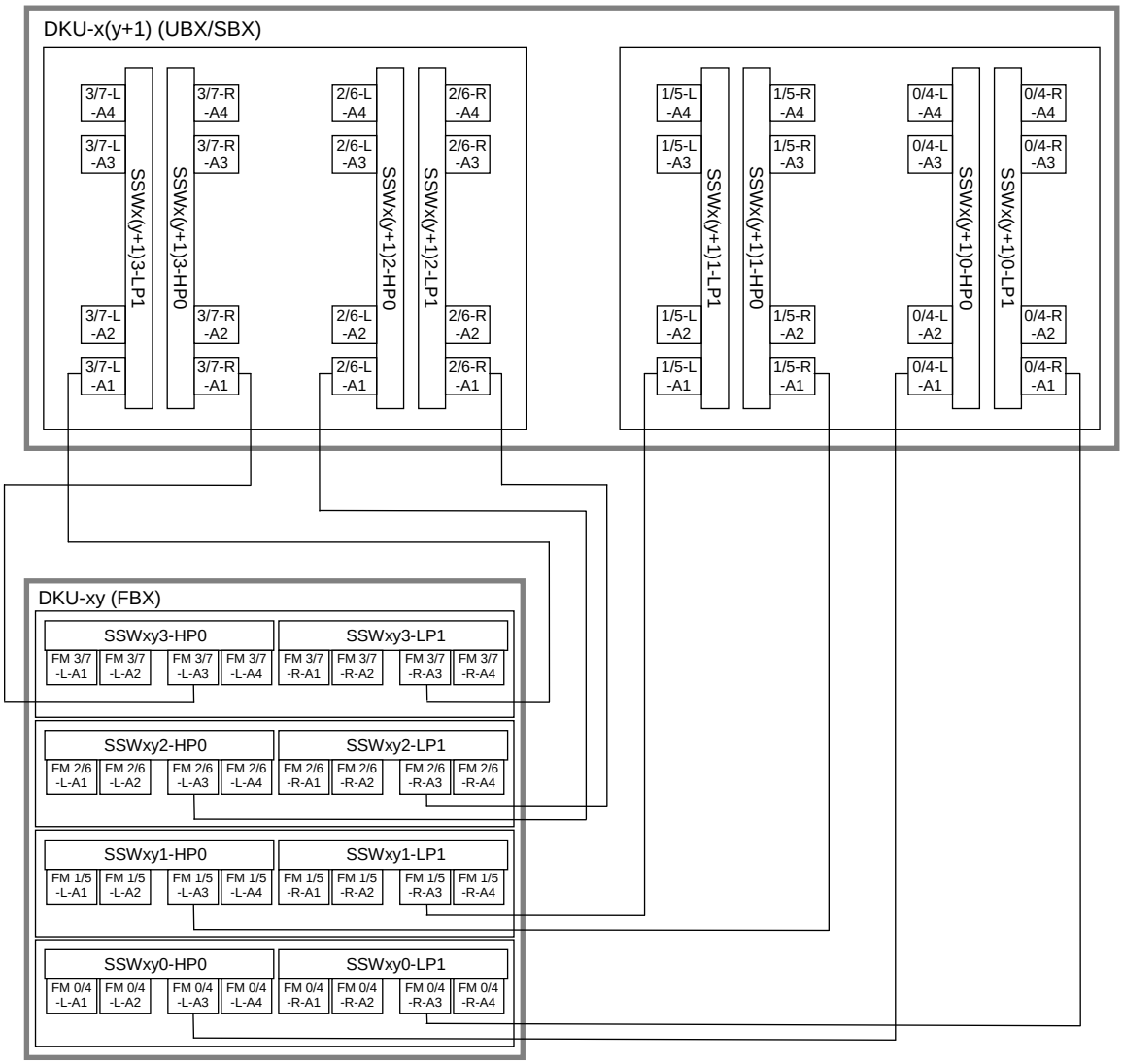


Fig. 5.1-13 Cable Diagram (UFC/UFEXC ①)

FBX(Upper) to UBX/SBX(Lower)

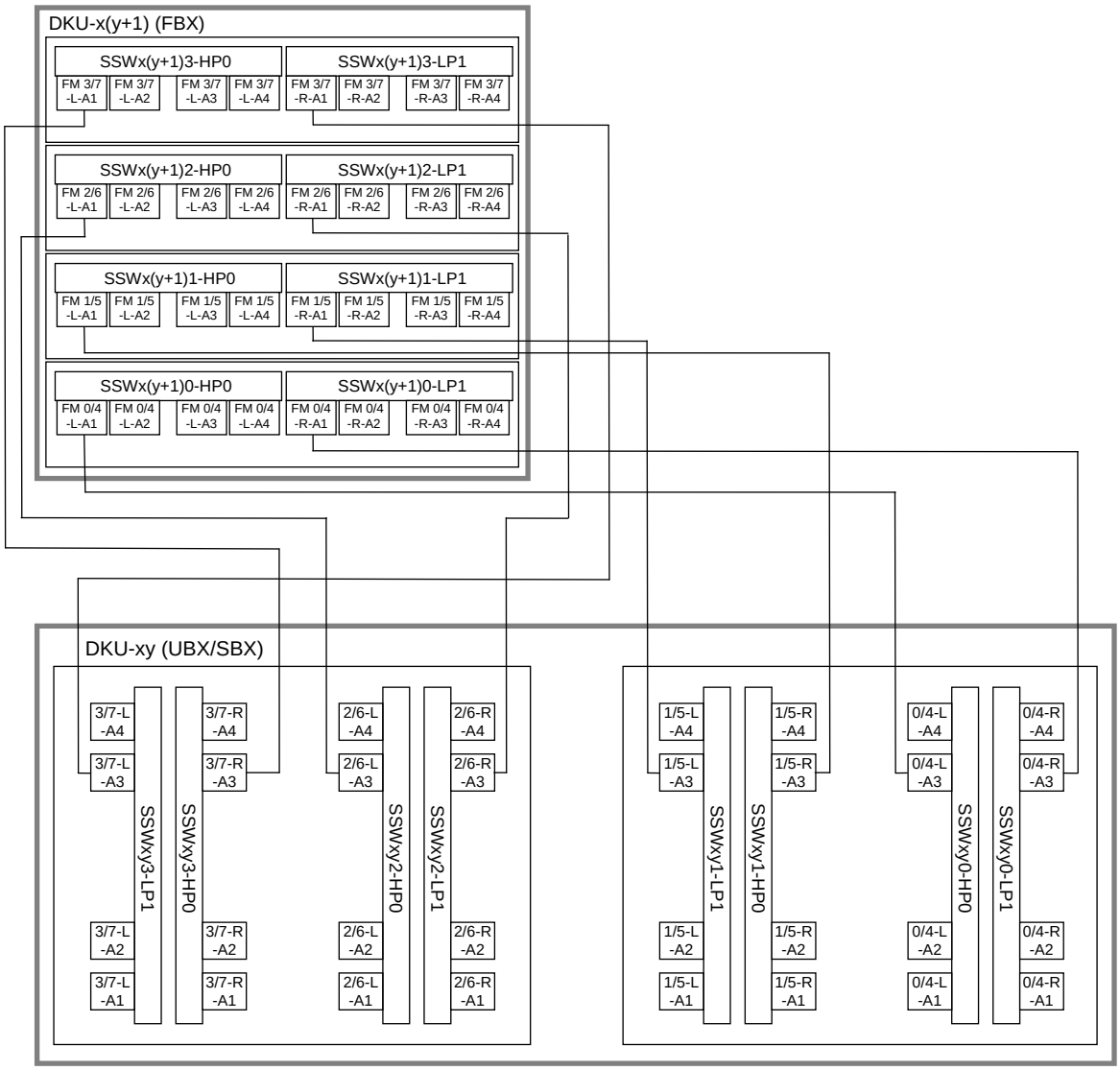


Fig. 5.1-14 Cable Diagram (UFC/UFEXC ②)

UBX/SBX(Upper) to FBX(Lower)

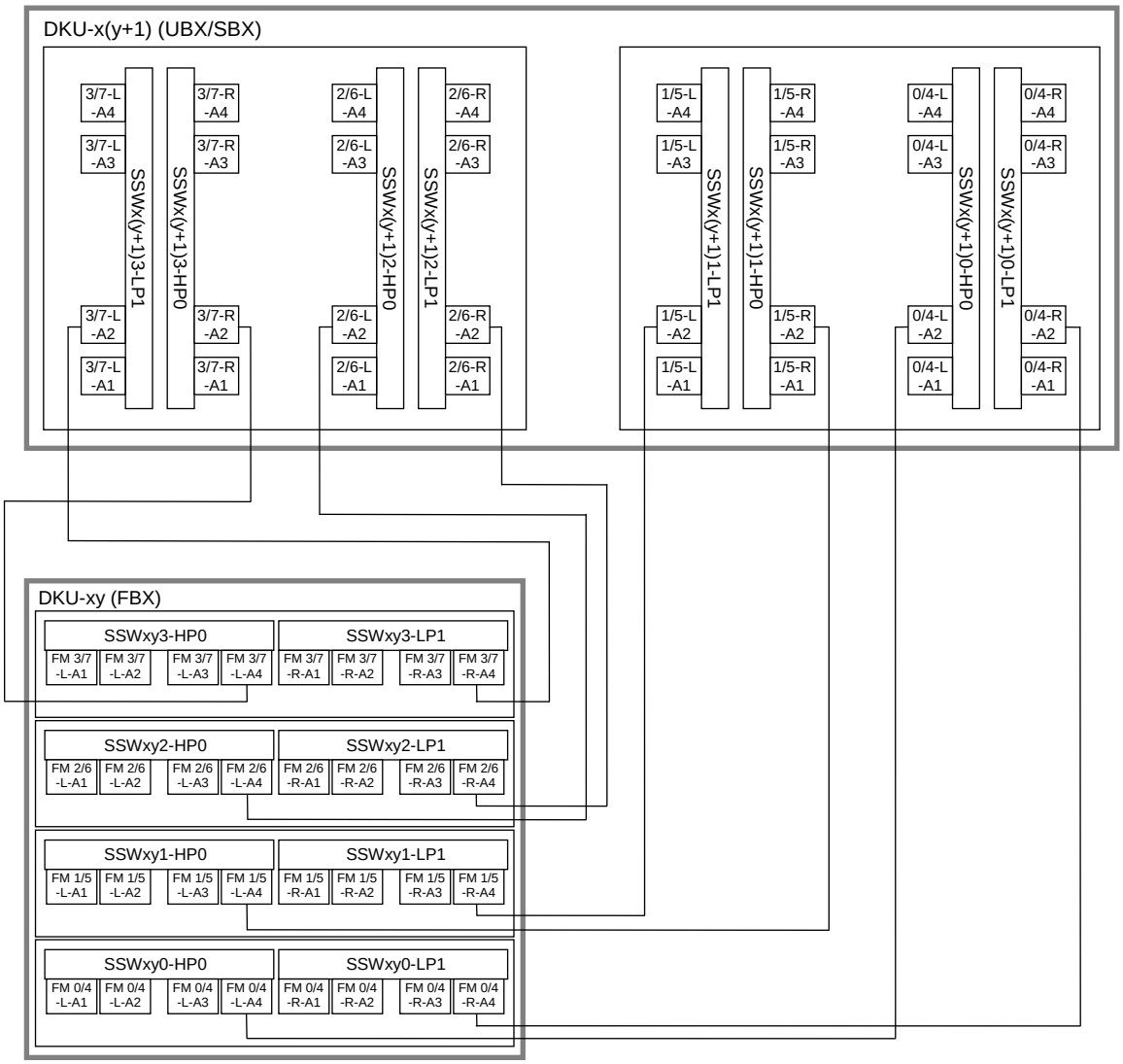


Fig. 5.1-15 Cable Diagram (HUFC/HUFEXC ①)

FBX(Upper) to UBX/SBX(Lower)

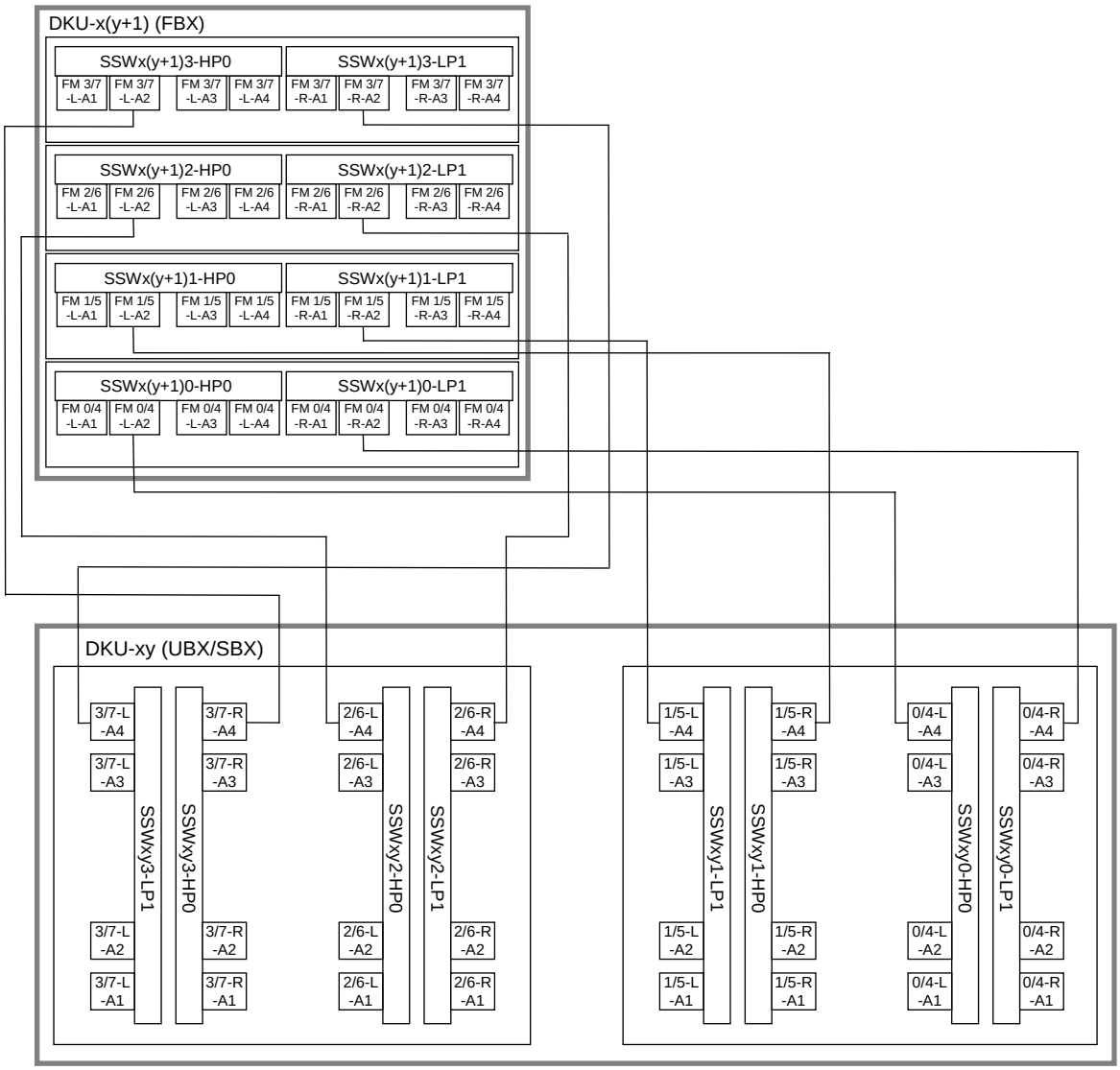
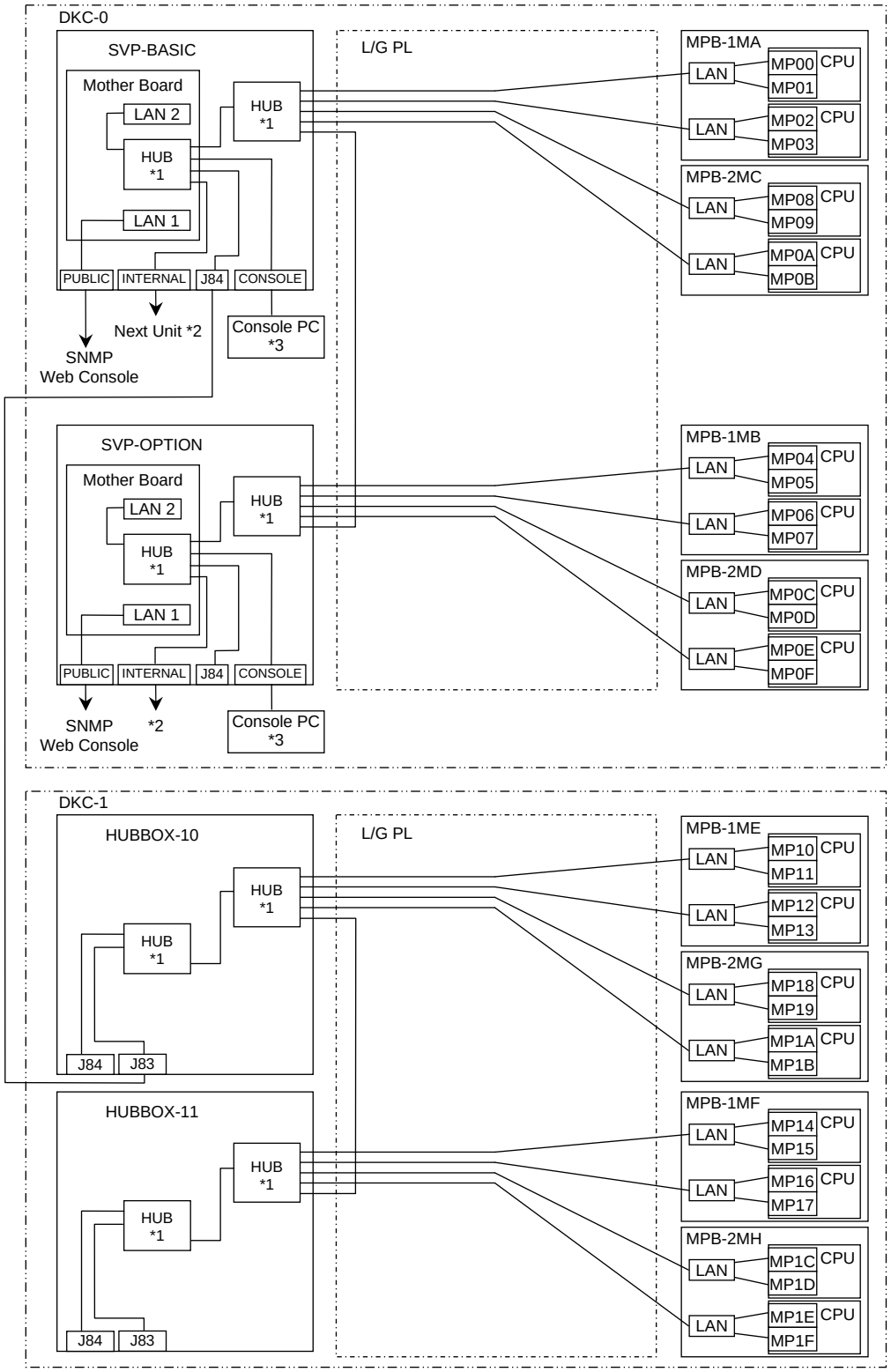


Fig. 5.1-16 Cable Diagram (HUFC/HUFEXC ②)

5.2 LAN Cable Diagram



*1: If the DC power is supplied though the power of SVP is turned off, the power of built-in HUB has been turned on.
*2: Connect cable only to SVP-BASIC. It is unnecessary to connect to SVP-OPTION.
*3: Connect Console PC only to SVP-BASIC. Connect Console PC to SVP-OPTION only when a trouble occurs on SVP-BASIC.

Fig. 5.2-1 LAN Cable Diagram

6. Jumper Setting

6.1 Jumper setting of each parts

Table 6.1-1 Jumper setting of each parts List

No.	Function Name	Jumper No.	Settings	Remarks
1	SVP	JP1	SVP PS ON/OFF INHIBIT	
		JP3	SVP PASSWORD/IP ADDRESS INITIALIZATION	
		JP4	HUB RESET	
2	HUBBOX	JP4	HUB RESET	
3	DKCPANEL	JP1	Not used	
		JP2	CHK RESET	
		JP3	SSVP ALARM RESET	(*1)
		JP4	SSVP DUMP	
		JP5	FORCE MODE	
		JP6	SYSFORCEOFF	
		JP7, JP8	Not used	
		JP9	Fixed Jumper	
4	SSW	JP20-1, JP20-2, JP21-1, JP22-1, JP22-2, JP23-1, JP24-1, JP24-2, JP25-1, JP26-1, JP26-2, JP27-1	Specification of DKU ID	
		JP21-2, JP23-2, JP25-2, JP27-2	Specification of DKC Front/Rear	
5	MPB	CEMD, CEDT	Specification of CE MODE	
6	CACHE	VOJP	Volatile Jumper	
7	SSVPMN	JP1	MODULE ID	
		JP2	AUTOPSON	
8	SSW (for FBX DKU)	P20-1 - JP27-2, JP31-1 - JP32-2	DKU ID	

*1: When the SVP High Reliability Kit has been installed and an SVP fail over (SIM=7FF3XX) is detected, at first, take actions to resolve the failure (SIM=7FF3XX).

[1] SVP

Table 6.1-2 Setting of Jumper Socket on the SVP

Function Name	Jumper No.	Settings	Remarks
SVP	JP1	The SVP Power ON/OFF Function of SSVP is inhibited by inserting Jumper.	
	JP3	The action at the time of JP3 insertion 1. All LED blinks (at interval of 1 second for 2 times, and 4 seconds) 2. The display of an IP address (for 12 seconds) 3. All LED putting out lights (for 10 seconds) 4. JP3 insertion check. If still inserted, the sequence continuation. If it has not inserted, sequence is end. 5. Initialize Password 6. Initialize IP Address 7. All LED blinks (at interval of 1 second for 10 times, and 20 seconds) 8. SVP Reboot	Refer to SVP01-180 for detailed action.
	JP4	The HUB function in SVP is reset.	

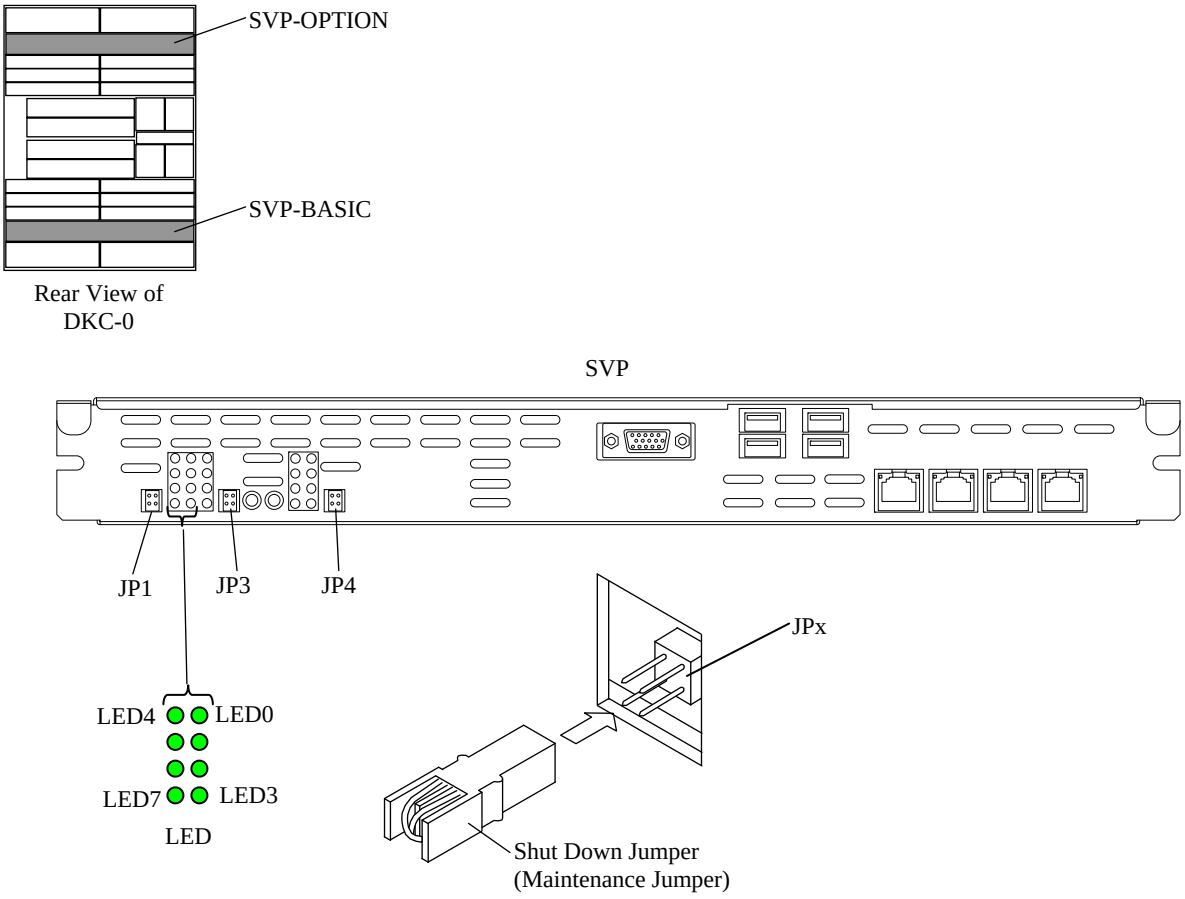


Fig. 6.1-1 Setting of Jumper on the SVP

[2] HUBBOX

Table 6.1-3 Setting of Jumper Socket on the HUBBOX

Function Name	Jumper No.	Settings	Remarks
HUBBOX	JP4	The HUB function in HUBBOX is reset.	

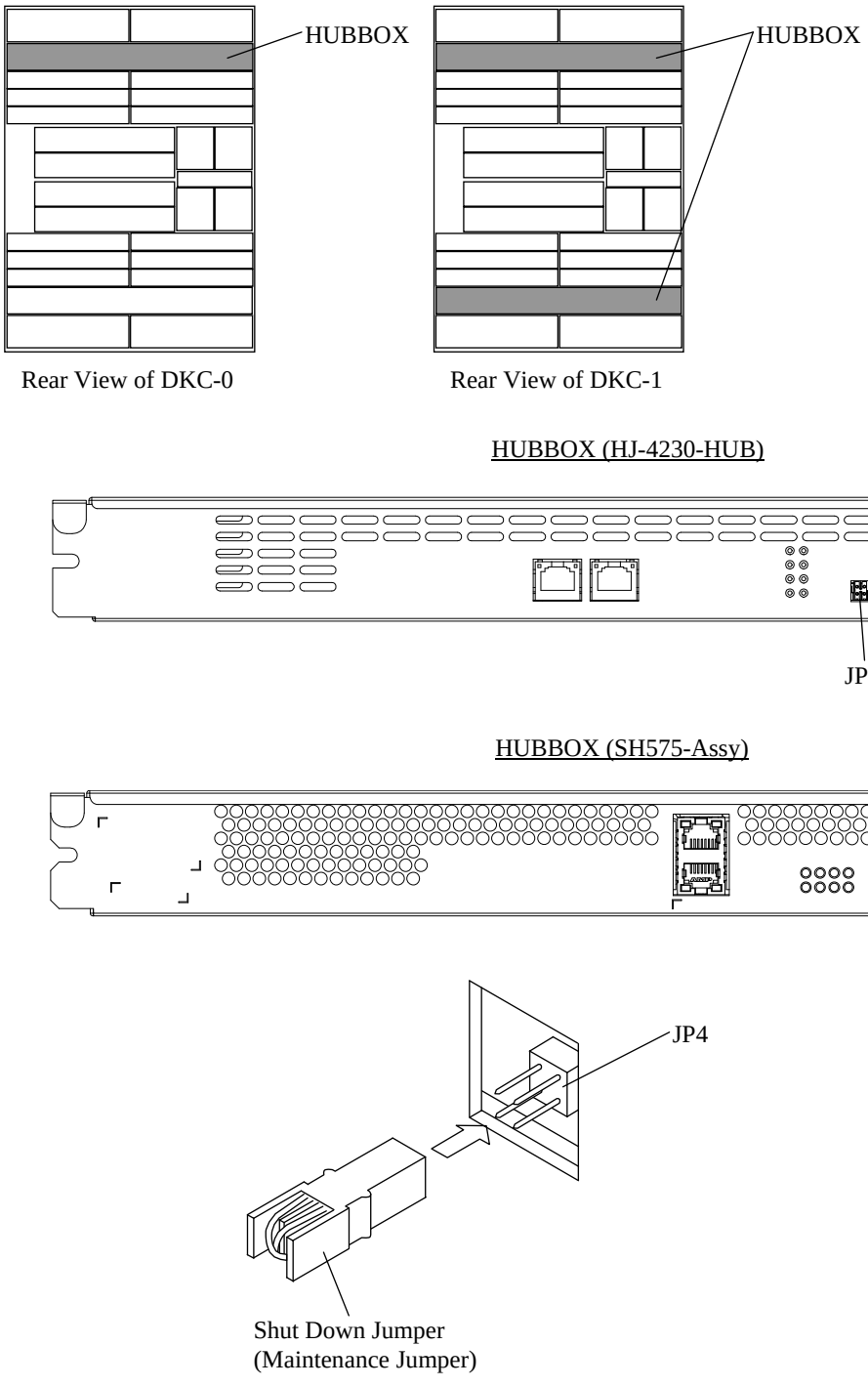


Fig. 6.1-2 Setting of Jumper on the HUBBOX

[3] DKCPANEL

Table 6.1-4 Setting of Jumper Socket on DKCPANEL PCB

Function Name	Jumper No.	Settings	Jumper Setting
DKCPANEL	JP1	Not used	
	JP2 (CHK RESET)	The PS ALARM and TH ALARM are reset.	JP2  Maintenance Jumper
	JP3 (SSVP ALARM RESET)	When the jumper is set, the SSVP detection alarm is reset. Then IMPL of the SVP is executed. When the SVP High Reliability Kit has been installed and an SVP fail over (SIM=7FF3XX) is detected, at first, take actions to resolve the failure (SIM=7FF3XX).	JP3  Maintenance Jumper
	JP4 (SSVP DUMP)	When the jumper is set, the data in SVP memory is written to the HDD.	JP4  Maintenance Jumper
	JP5 (FORCE MODE)	This jumper is used to turn off the DKC/DKU power forcibly. It assumes a special case, so that do not use it unless otherwise instructed. (Refer to TRBL16-10 to 20 for the detailed procedure.)	
	JP6 (SYSFORCEOFF)		
	JP7, JP8	Not used	
	JP9 (DISPLAY OFF)	The LED Turn Off Function is set on the appropriate DKCPANEL. It is not generally required to change the setting, because the setting is set to the lighting with the jumper.	JP9  Black Jumper (Fixation)

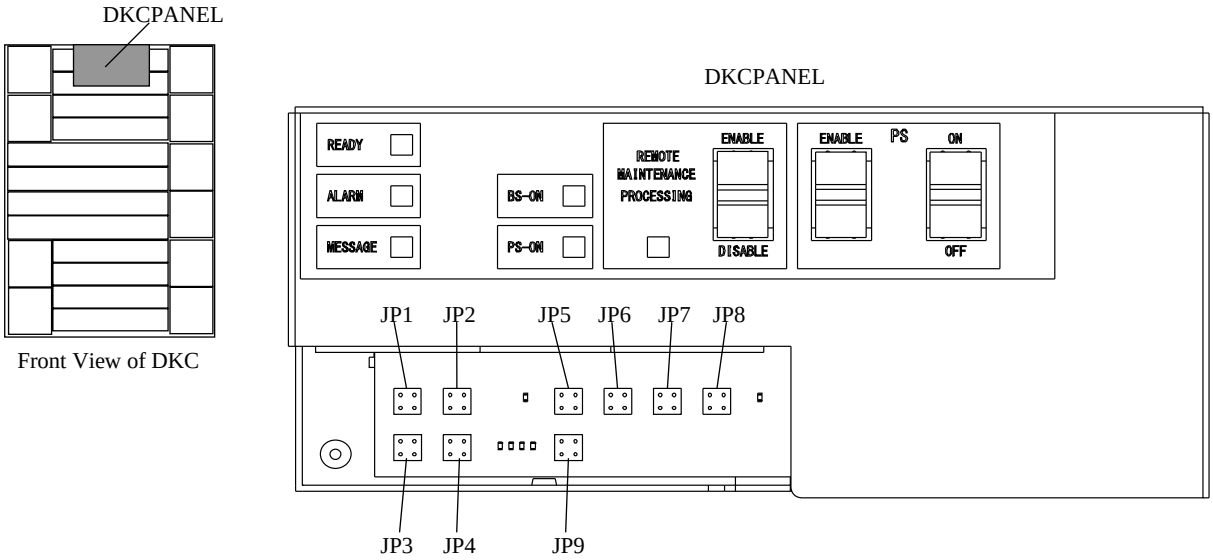


Fig. 6.1-3 Setting of Jumper Socket on DKCPANEL PCB

[4] SSW

Table 6.1-5 Jumper Setting of SSW

Function Name	Jumper No.	Settings	Remarks
SSW	JP20-1, JP20-2, JP21-1, JP22-1, JP22-2, JP23-1, JP24-1, JP24-2, JP25-1, JP26-1, JP26-2, JP27-1	Sets the address of DKU.	
	JP21-2, JP23-2, JP25-2, JP27-2	Sets the switch to the front or the rear of the DKU.	

Note: • Use the insulated radio pliers of the maintenance tools when setting the SSW jumpers.
• When the jumper setting of SSW is wrong, execute the replace process of SSW and change the jumper setting. The jumper setting will not be effective unless the SSW PCB is dismounted and mounted.

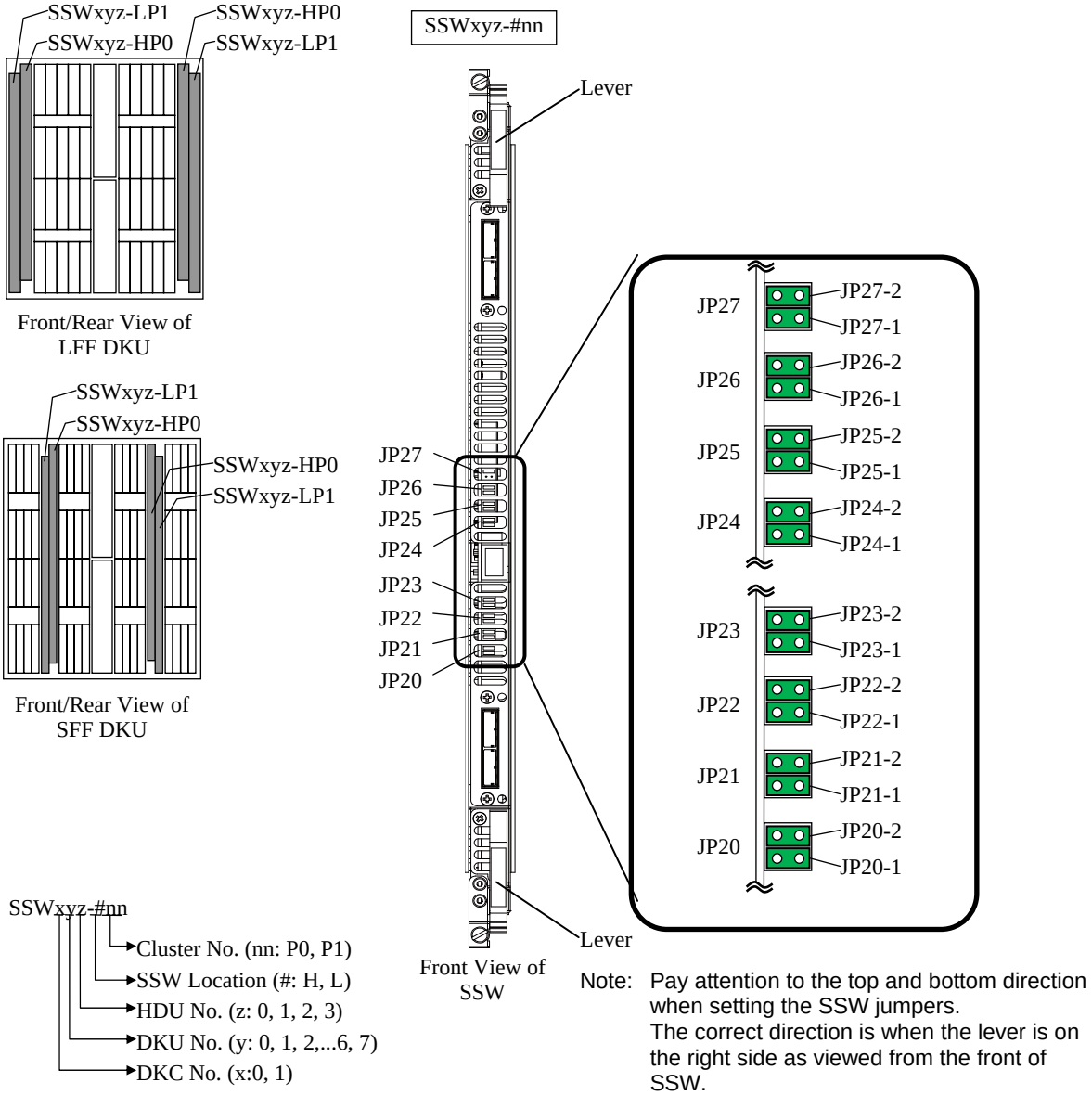


Fig. 6.1-4 Jumper Setting of SSW

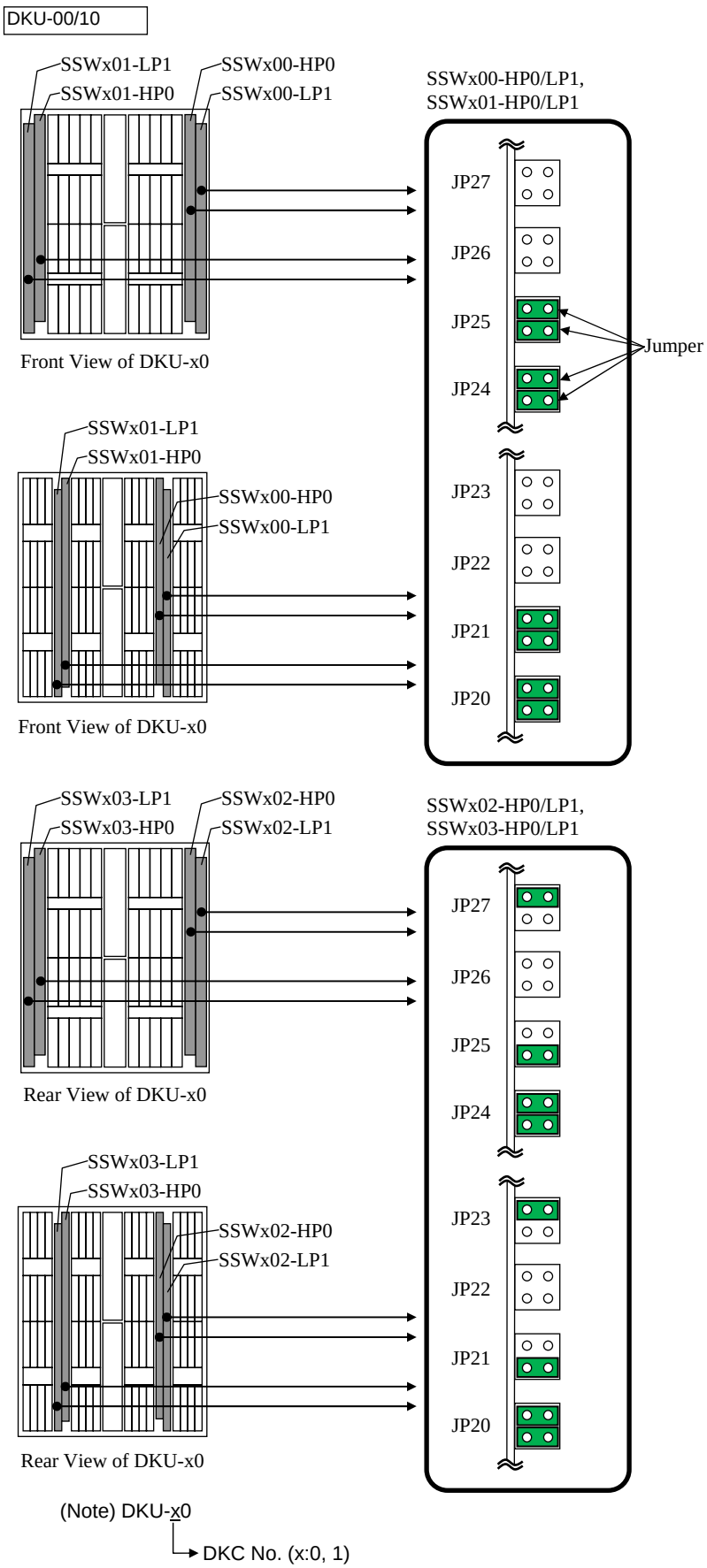


Fig. 6.1-5 Jumper Setting of SSW (DKU-00/10)

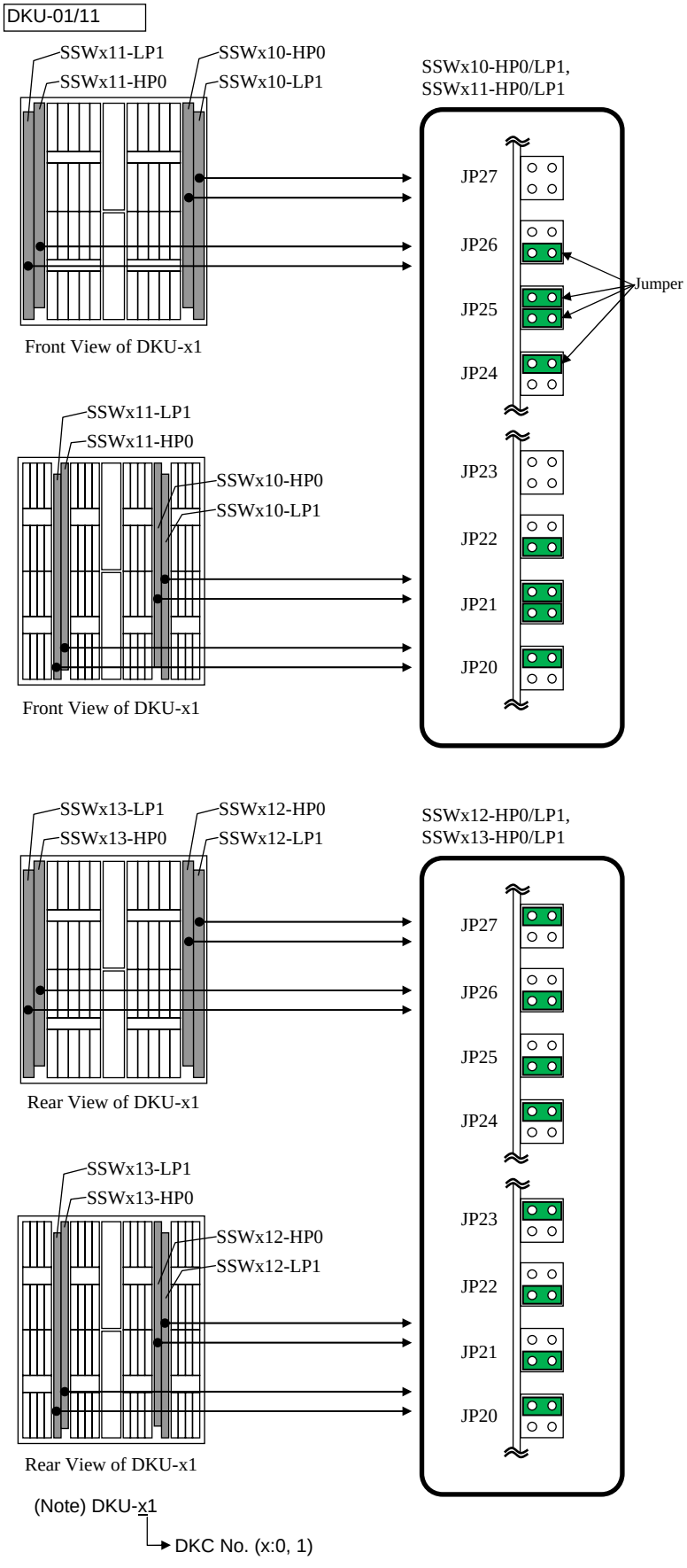


Fig. 6.1-6 Jumper Setting of SSW (DKU-01/11)

LOC06-80

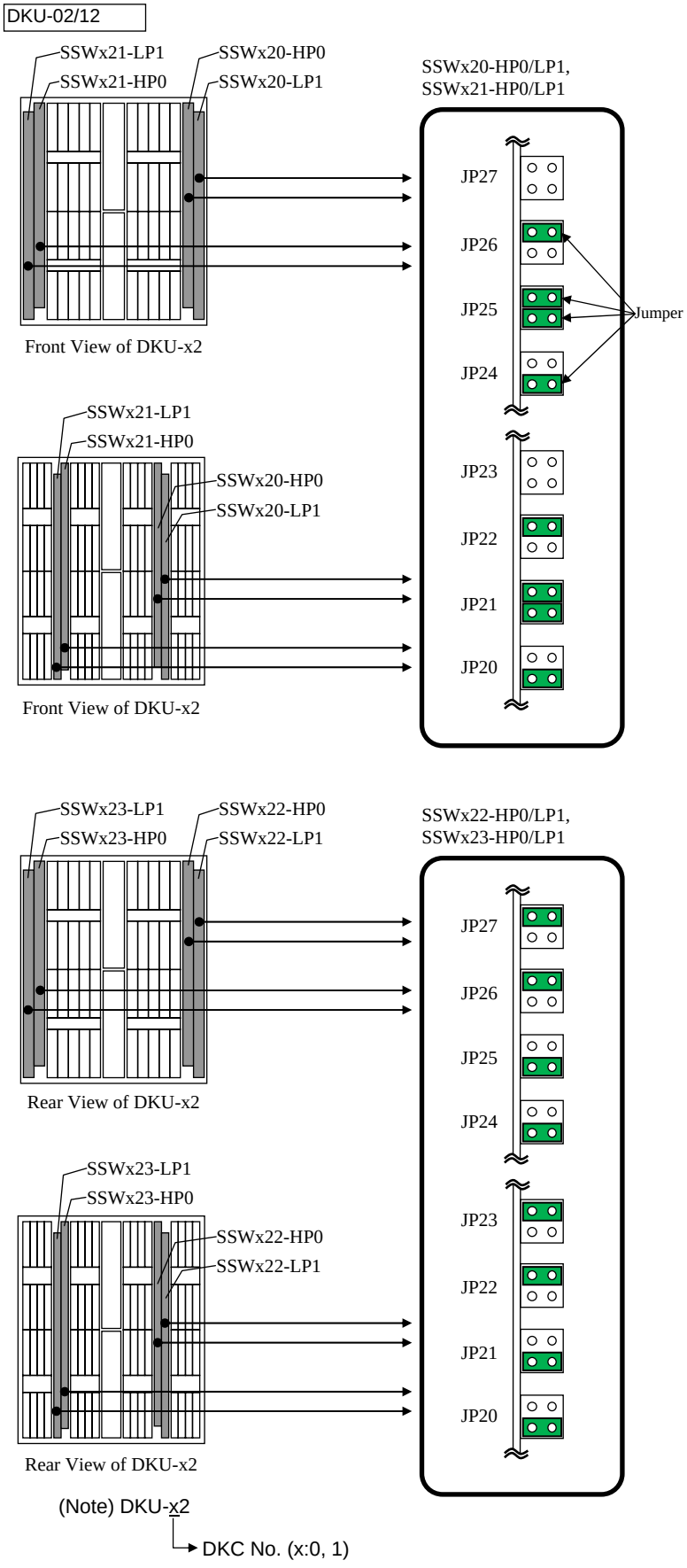


Fig. 6.1-7 Jumper Setting of SSW (DKU-02/12)

LOC06-90

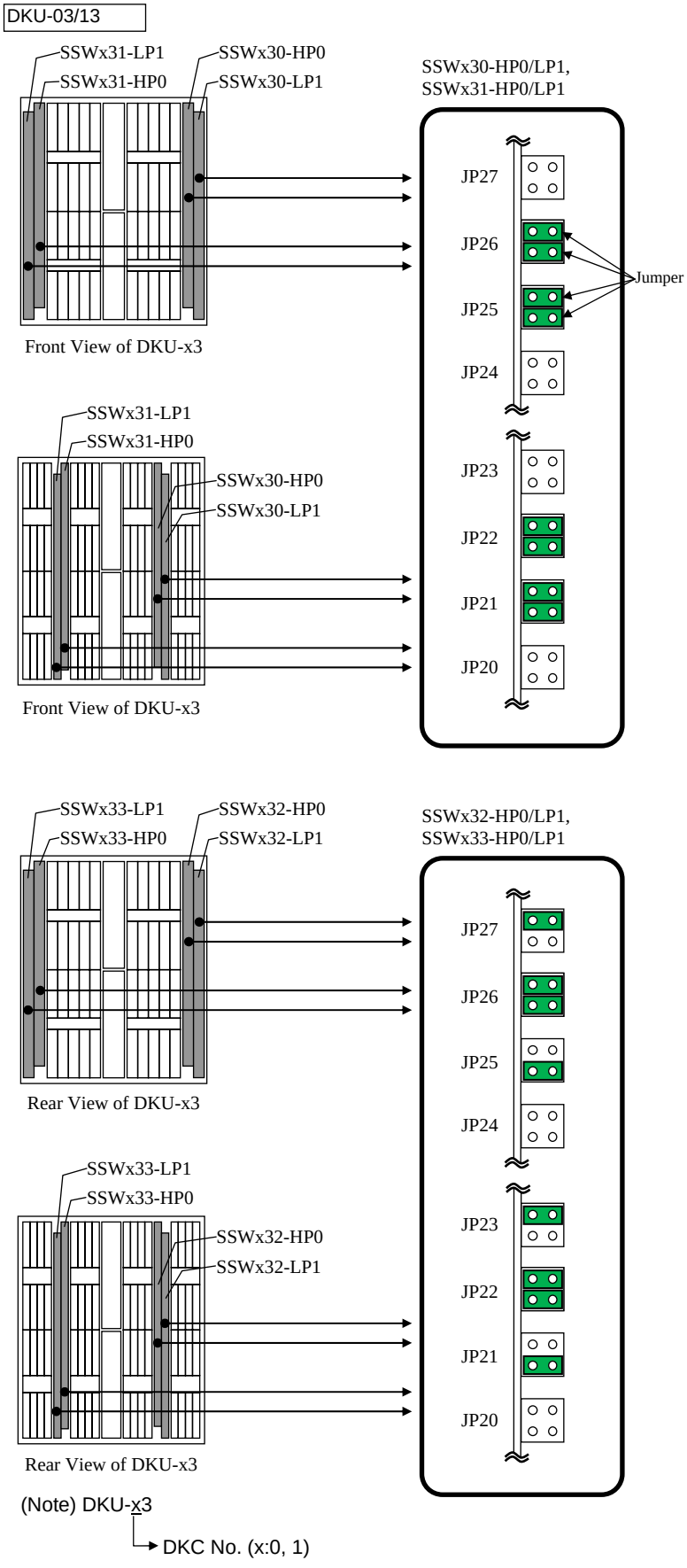


Fig. 6.1-8 Jumper Setting of SSW (DKU-03/13)

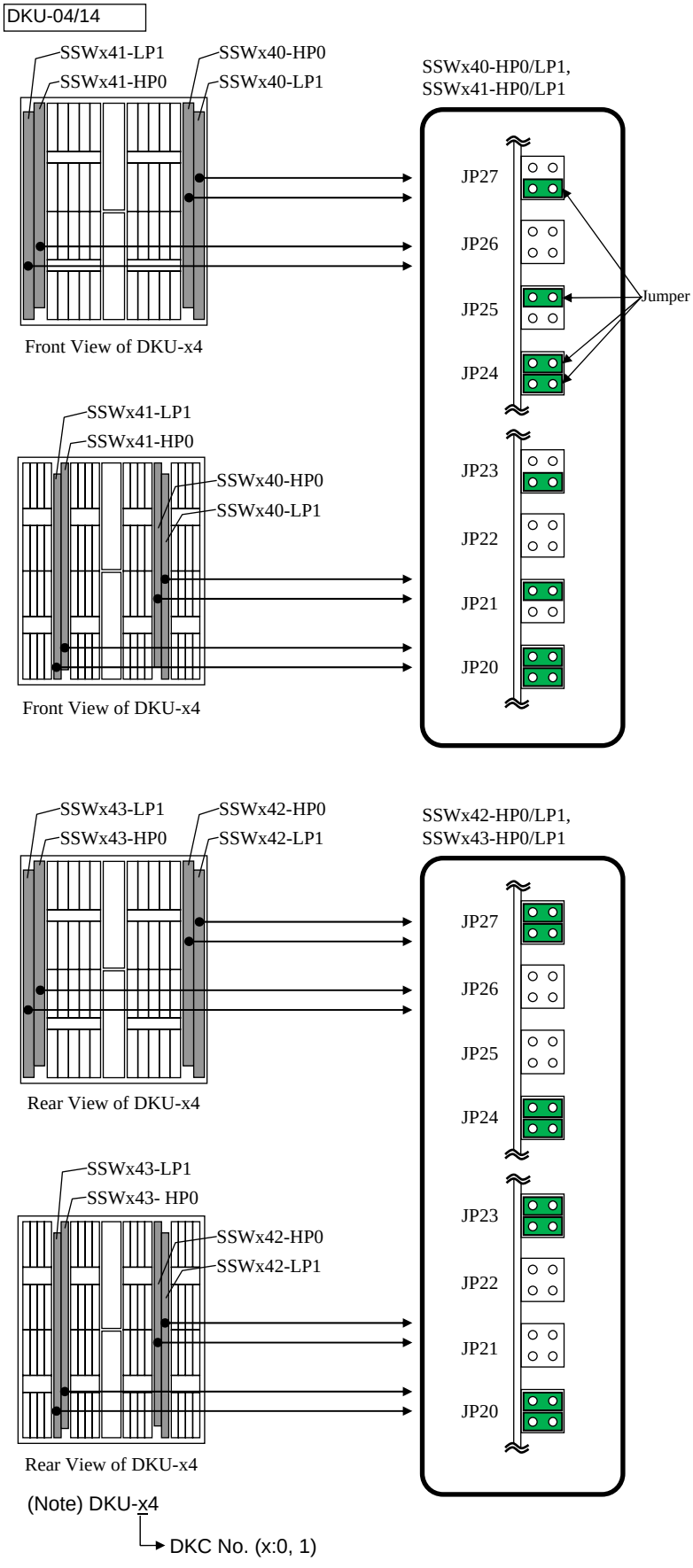


Fig. 6.1-9 Jumper Setting of SSW (DKU-04/14)

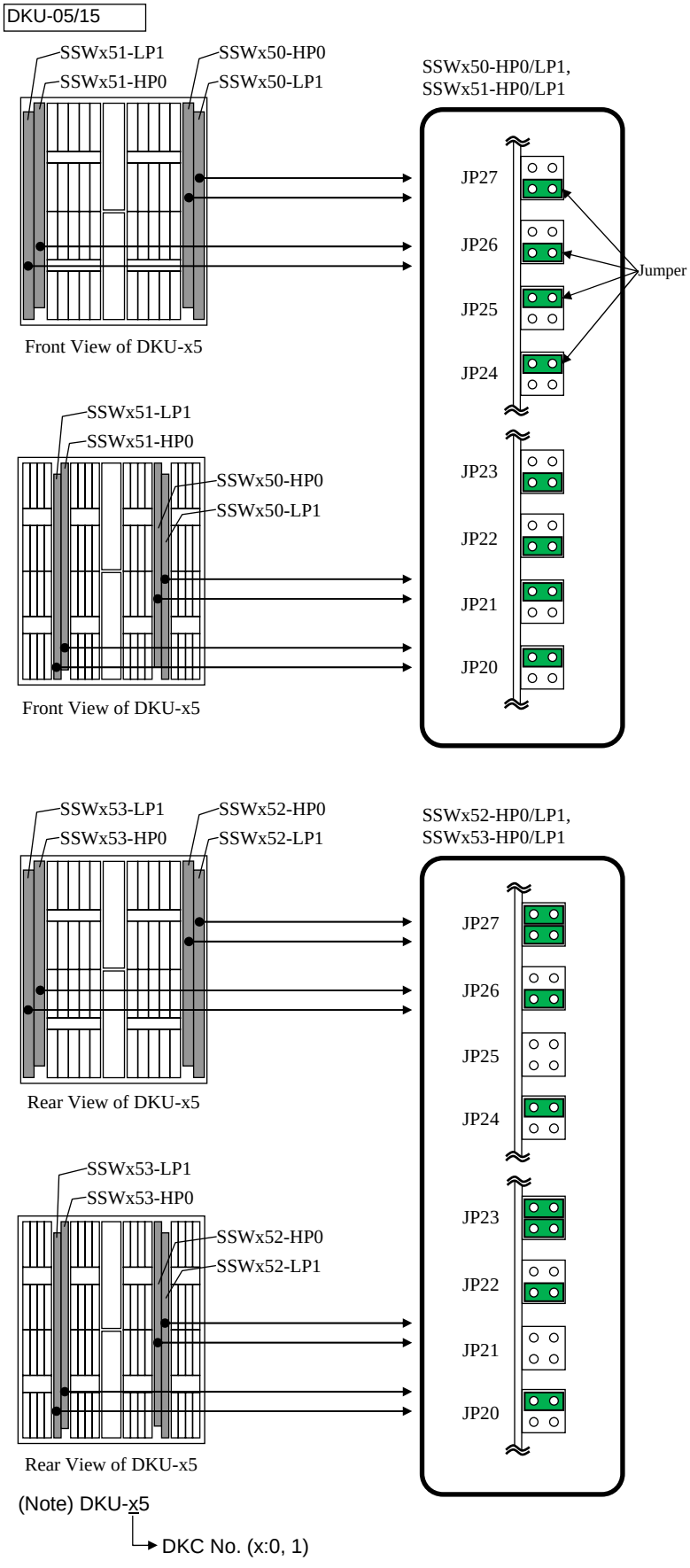


Fig. 6.1-10 Jumper Setting of SSW (DKU-05/15)

LOC06-120

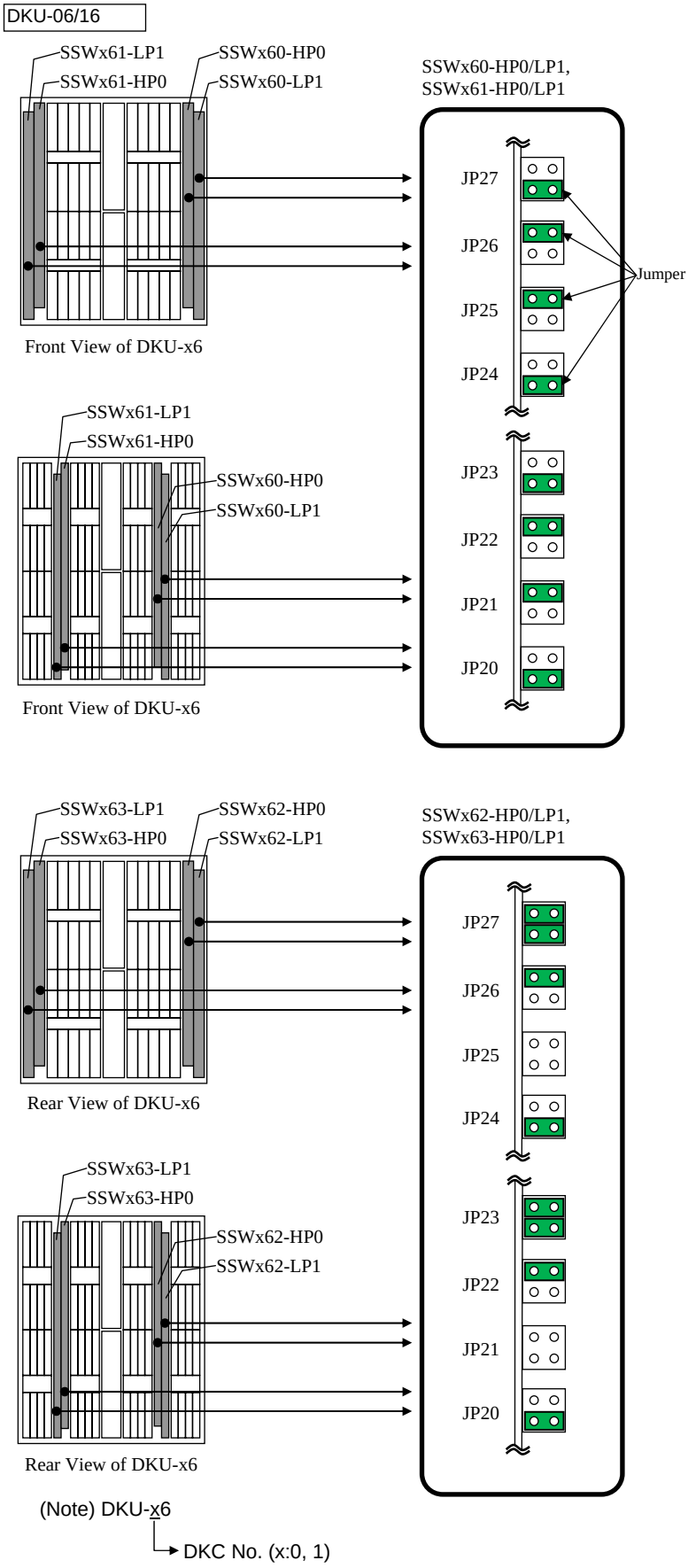


Fig. 6.1-11 Jumper Setting of SSW (DKU-06/16)

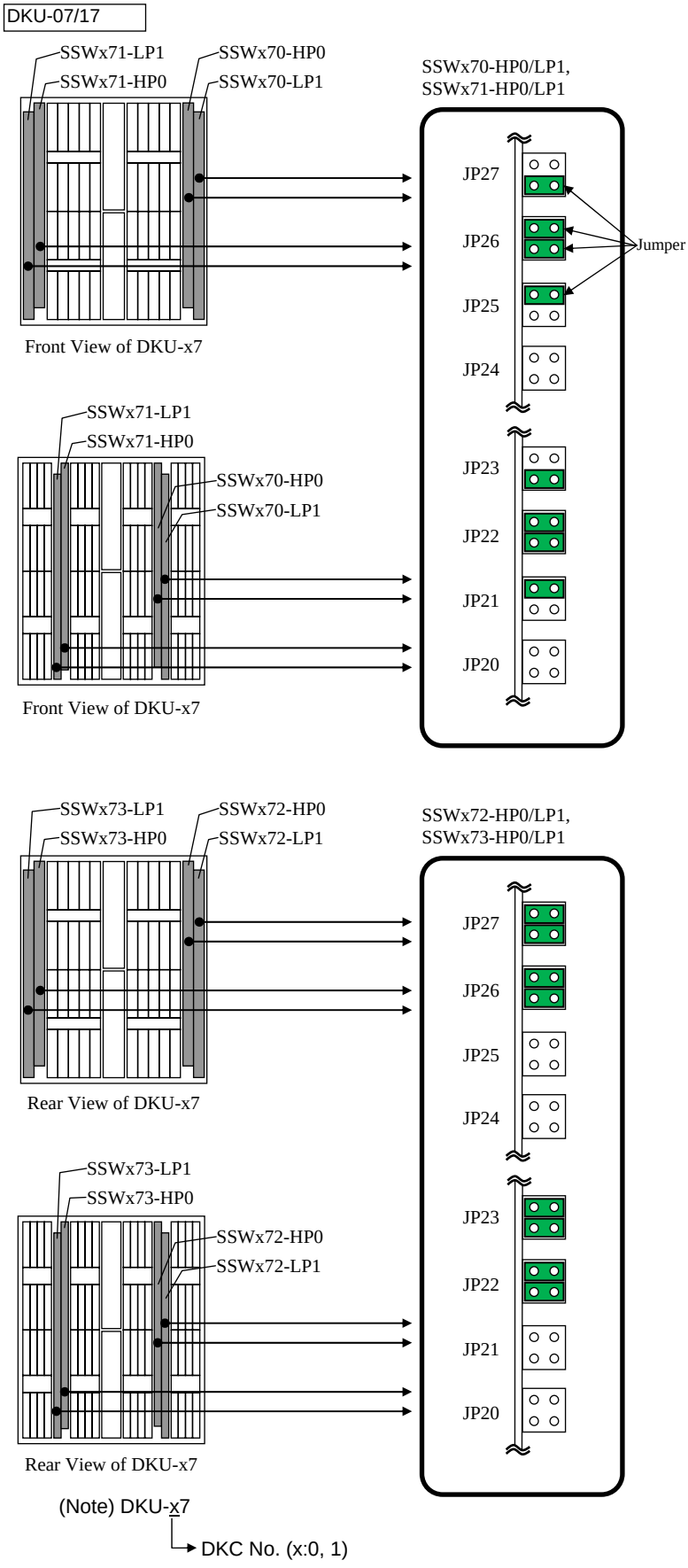
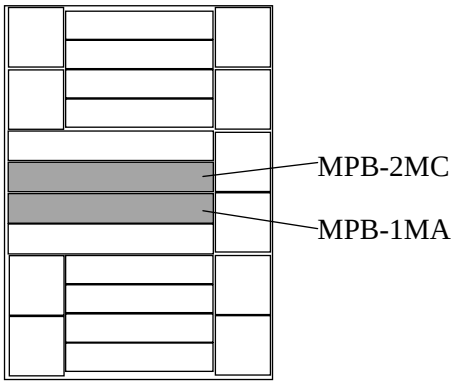


Fig. 6.1-12 Jumper Setting of SSW (DKU-07/17)

[5] MPB

Table 6.1-6 Installation of Jumper in MPB

Function Name	Jumper No.	Settings	Remarks
MPB	CEMD	All PCBs corresponding to the cluster are set to CE mode. The jumper is installed only in BASIC PCB (MPB-1MA/2MC).	Maintenance Jumper
	CEDT		Maintenance Jumper



Front View of DKC

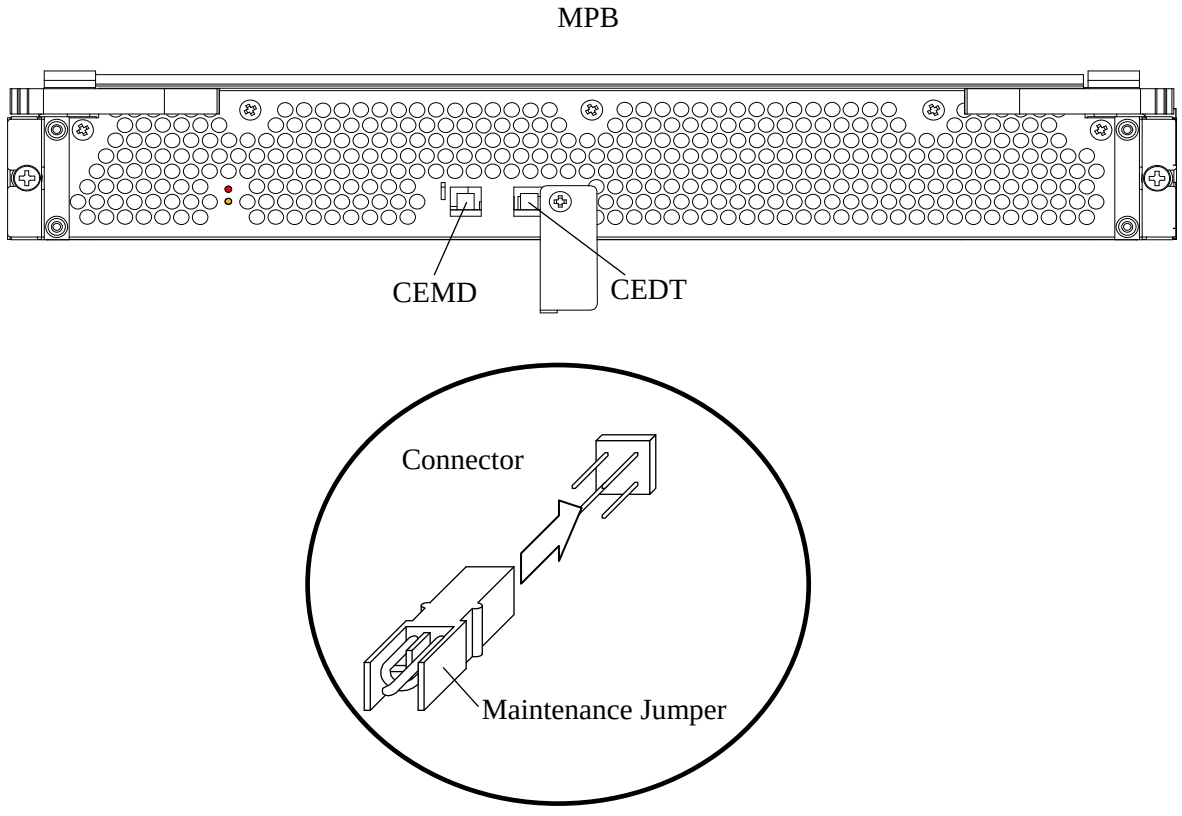
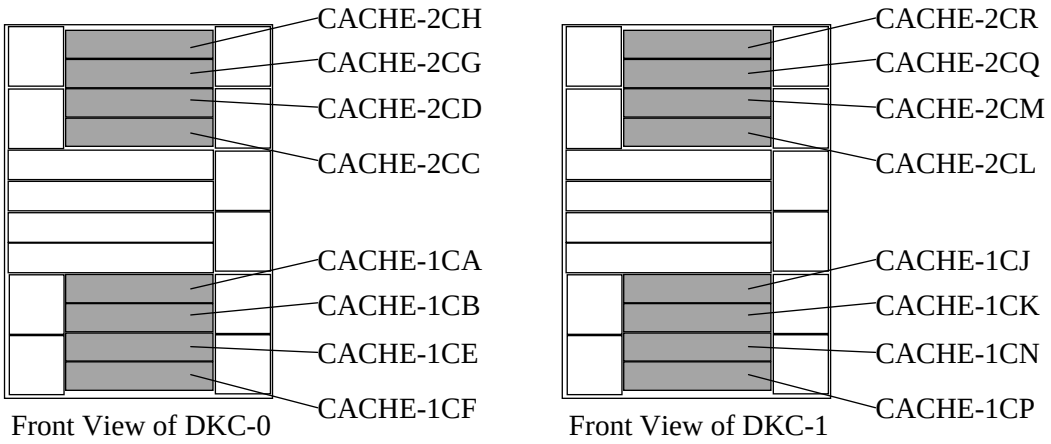


Fig. 6.1-13 Installation of Jumper in MPB

[6] CACHE

Table 6.1-7 Installation of Jumper in CACHE

Function Name	Jumper No.	Settings	Remarks
CACHE	VOJP (Volatile Jumper)	The cache data (control information or the like) is compulsorily made to volatilize by the PS ON/OFF operation.	Maintenance Jumper



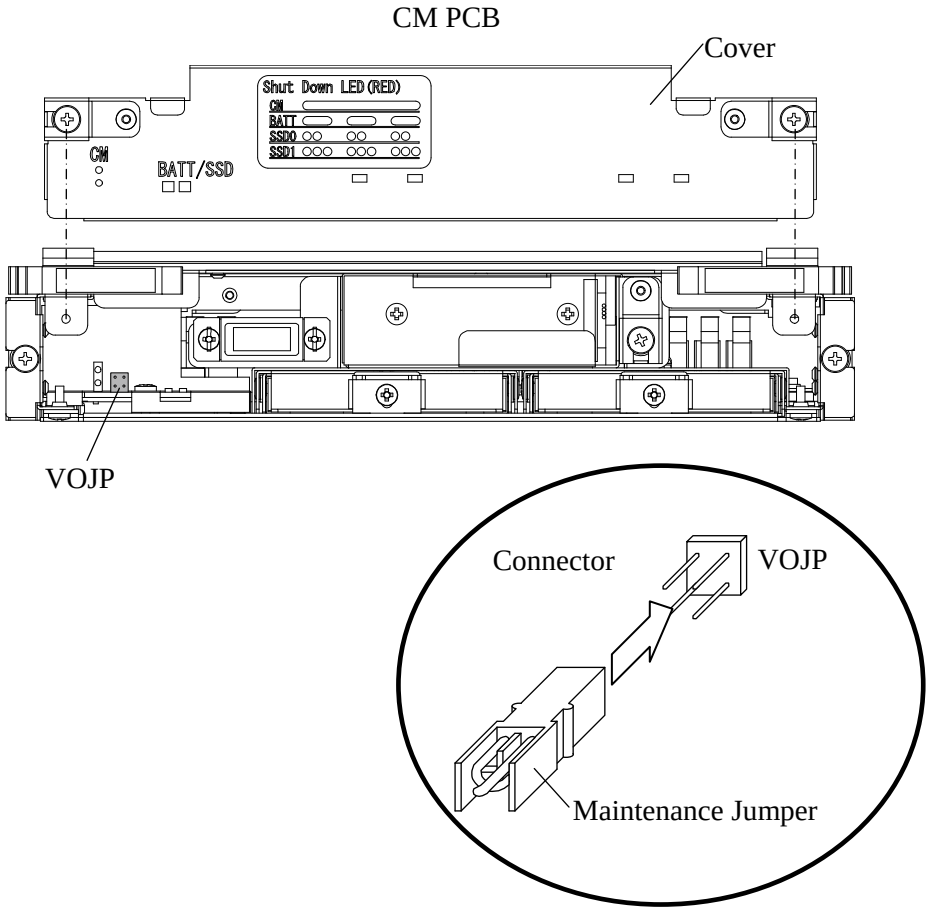
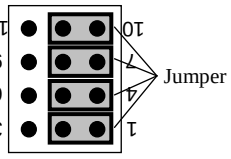
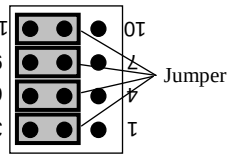
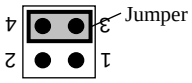
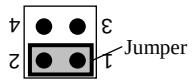


Fig. 6.1-14 Installation of Jumper in CACHE

[7] SSVPMN

Table 6.1-8 Installation of Jumper in SSVPMN

Function Name	Jumper No.	Settings	Remarks
SSVPMN	JP1 (MODULE ID)	Set the MODULE ID of the DKC equipped with SSVPMN. • MODULE0 … SSVPMN-0 (DKC-0) Plug the jumper cap on #1, 4, 7, 10.	For SSVPMN-0 JP1 
		• MODULE1 … SSVPMN-1 (DKC-1) Plug the jumper cap on #3, 6, 9, 12.	For SSVPMN-1 JP1 
	JP2 (AUTOPSON)	Power on the subsystem by switching PS ON/PS OFF or PCI control after turn on the AC power. (Initial setting) Set only the SSVPMN-0 in the Jumper. (#3-4) No setting of SSVPMN-1 is needed.	Disable Setting JP2 
		The subsystem is powered on automatically after turn on the AC power. Set only the SSVPMN-0 in the Jumper. (#1-2) No setting of SSVPMN-1 is needed.	Enable Setting JP2 

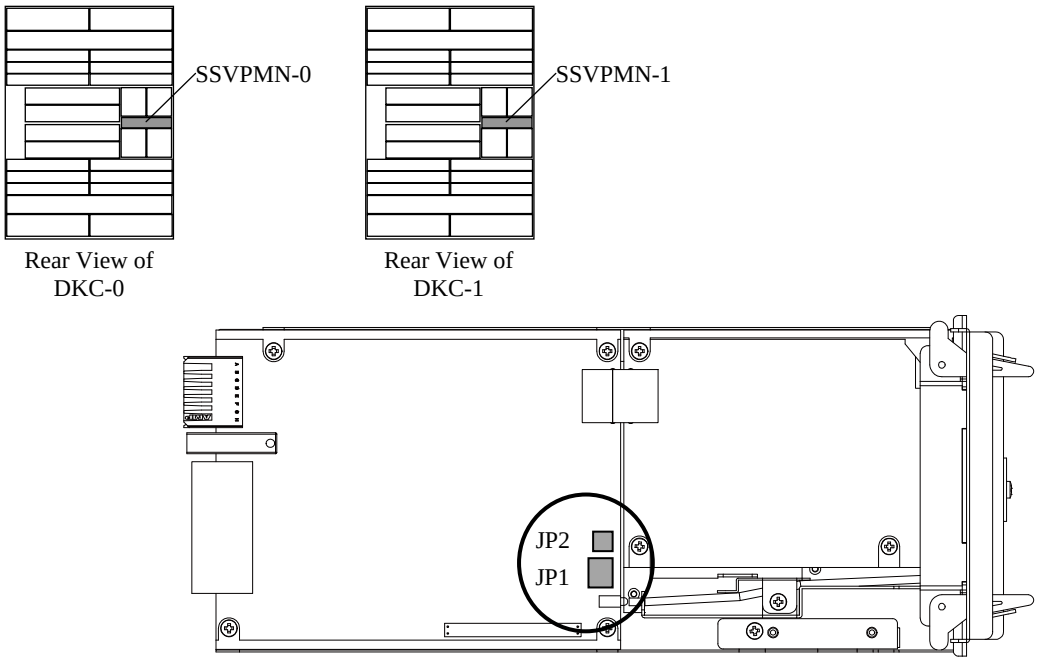


Fig. 6.1-15 Installation of Jumper in SSVPMN

[8] SSW for FBX DKU

Table 6.1-9 Jumper Setting of SSW

Function Name	Jumper No.	Settings	Remarks
SSW	JP20-1 - JP27-2, JP31-1 - JP32-2	Sets the address of DKU.	

Note: • Use the insulated radio pliers of the maintenance tools when setting the SSW jumpers.
• When the jumper setting of SSW is wrong, execute the replace process of SSW and change the jumper setting. The jumper setting will not be effective unless the SSW is dismounted and mounted.

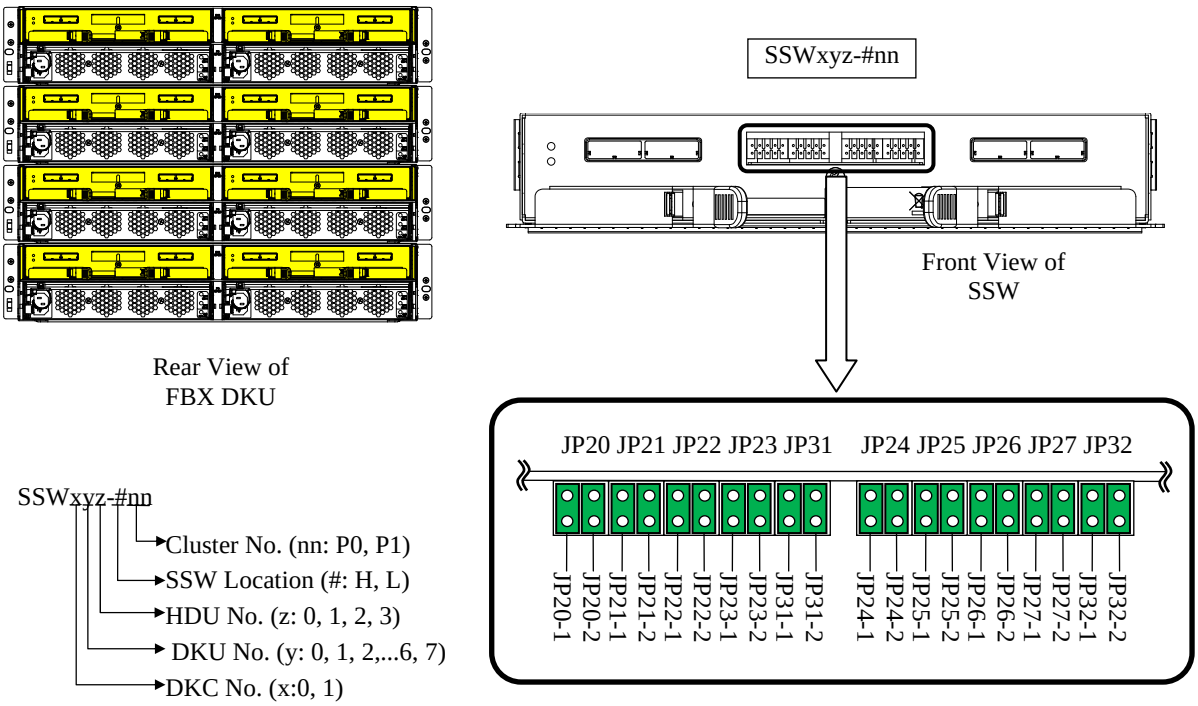


Fig. 6.1-16 Jumper Setting of SSW

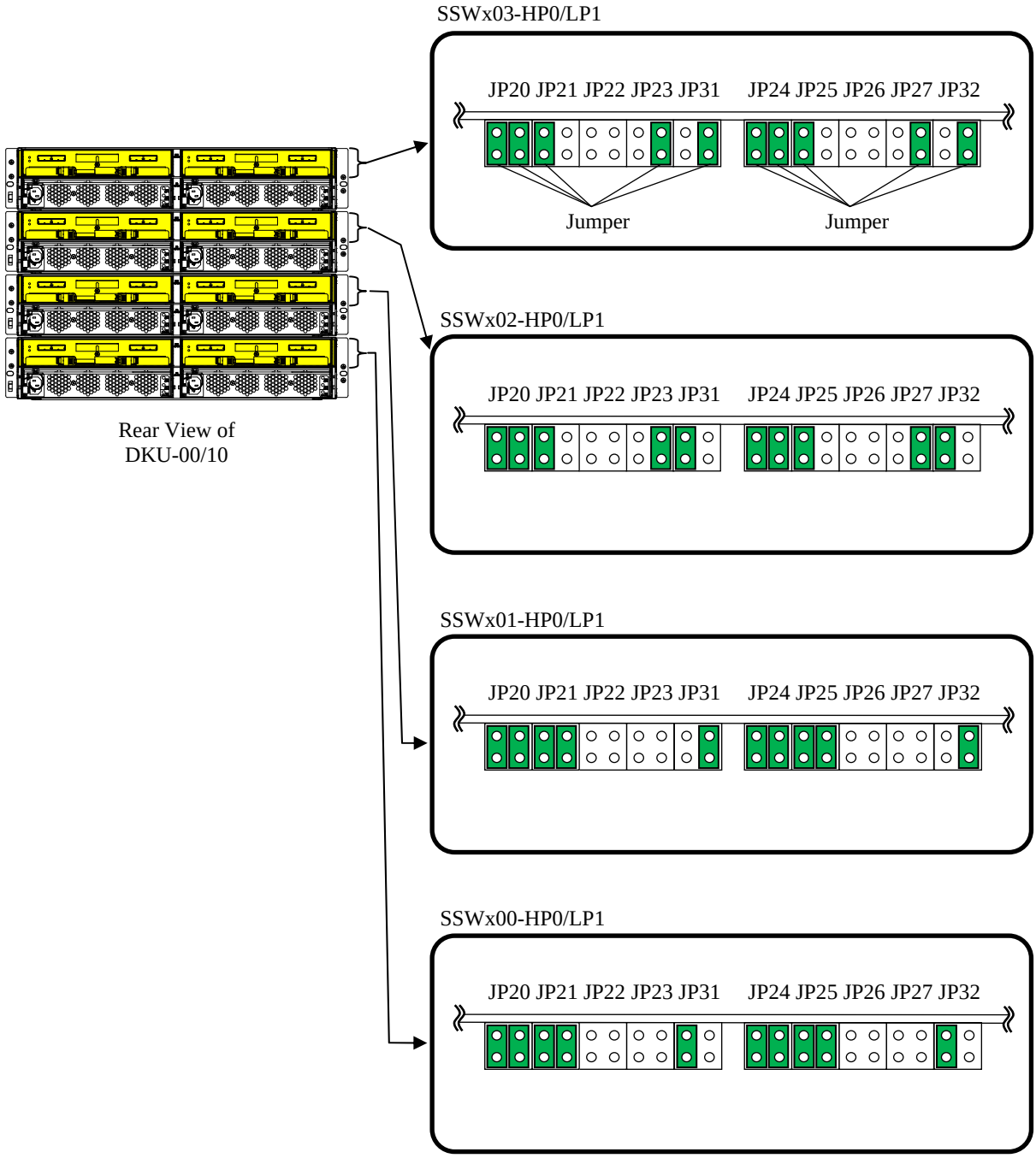


Fig. 6.1-17 Jumper Setting of SSW (DKU-00/10)

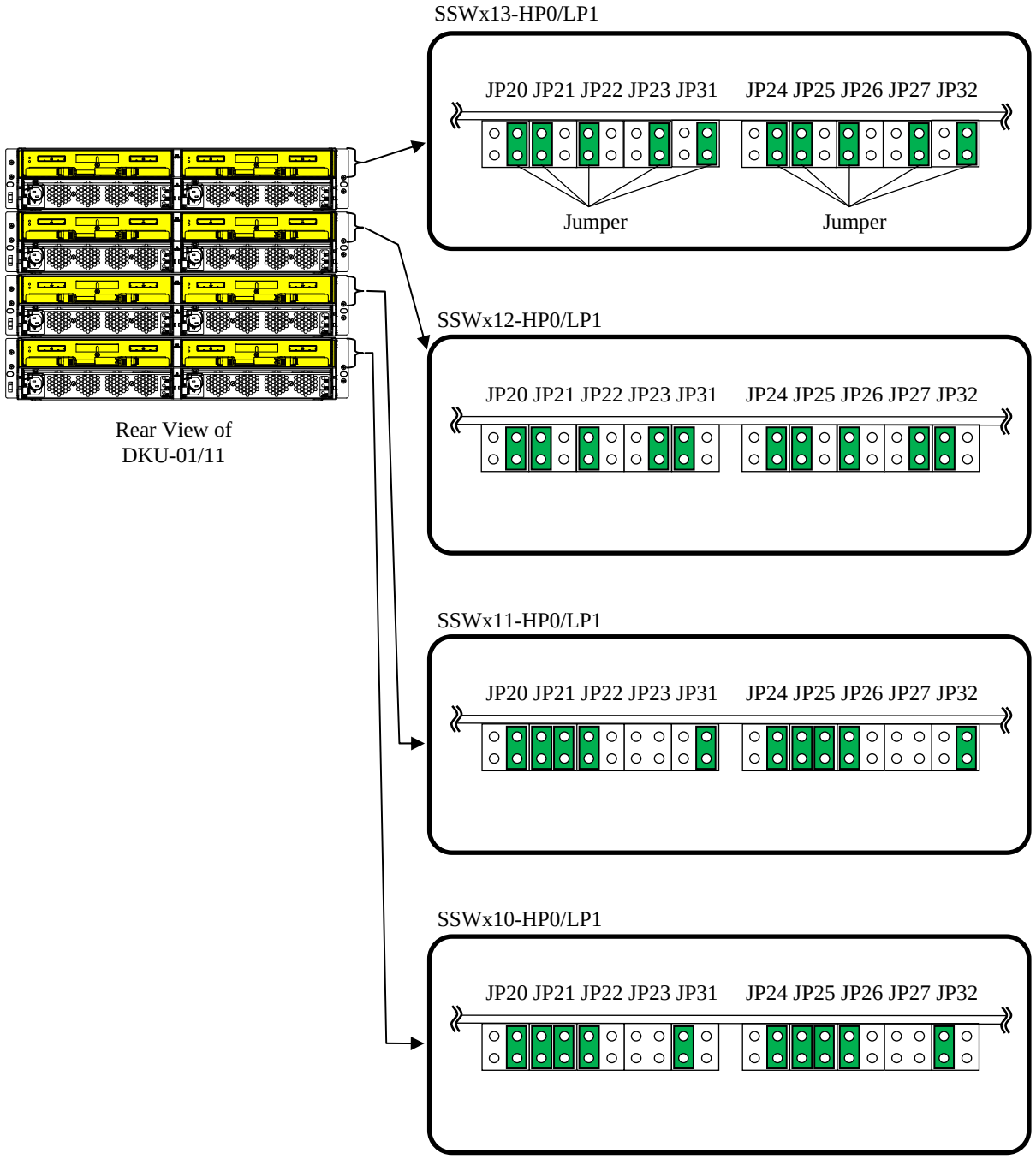


Fig. 6.1-18 Jumper Setting of SSW (DKU-01/11)

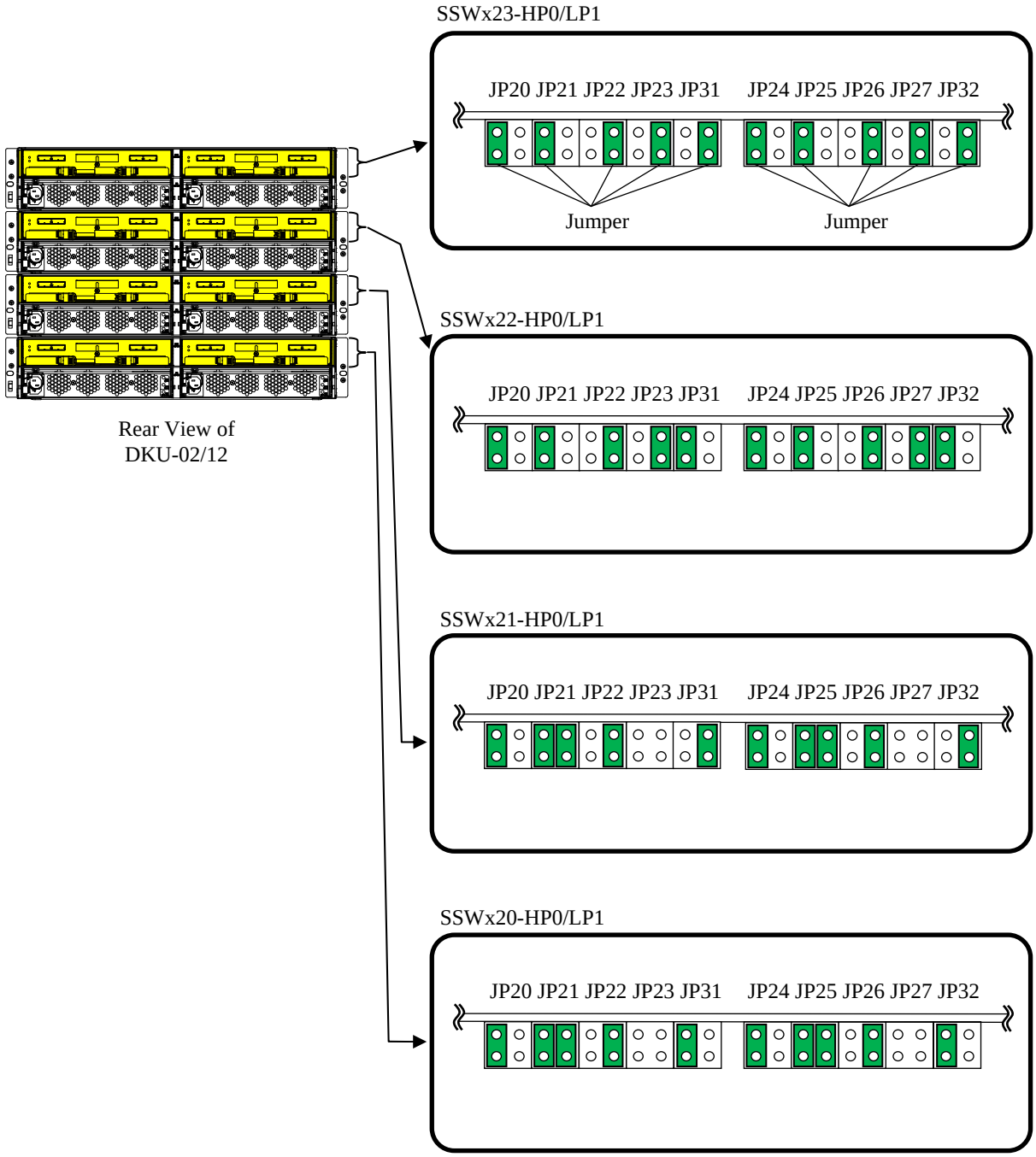


Fig. 6.1-19 Jumper Setting of SSW (DKU-02/12)

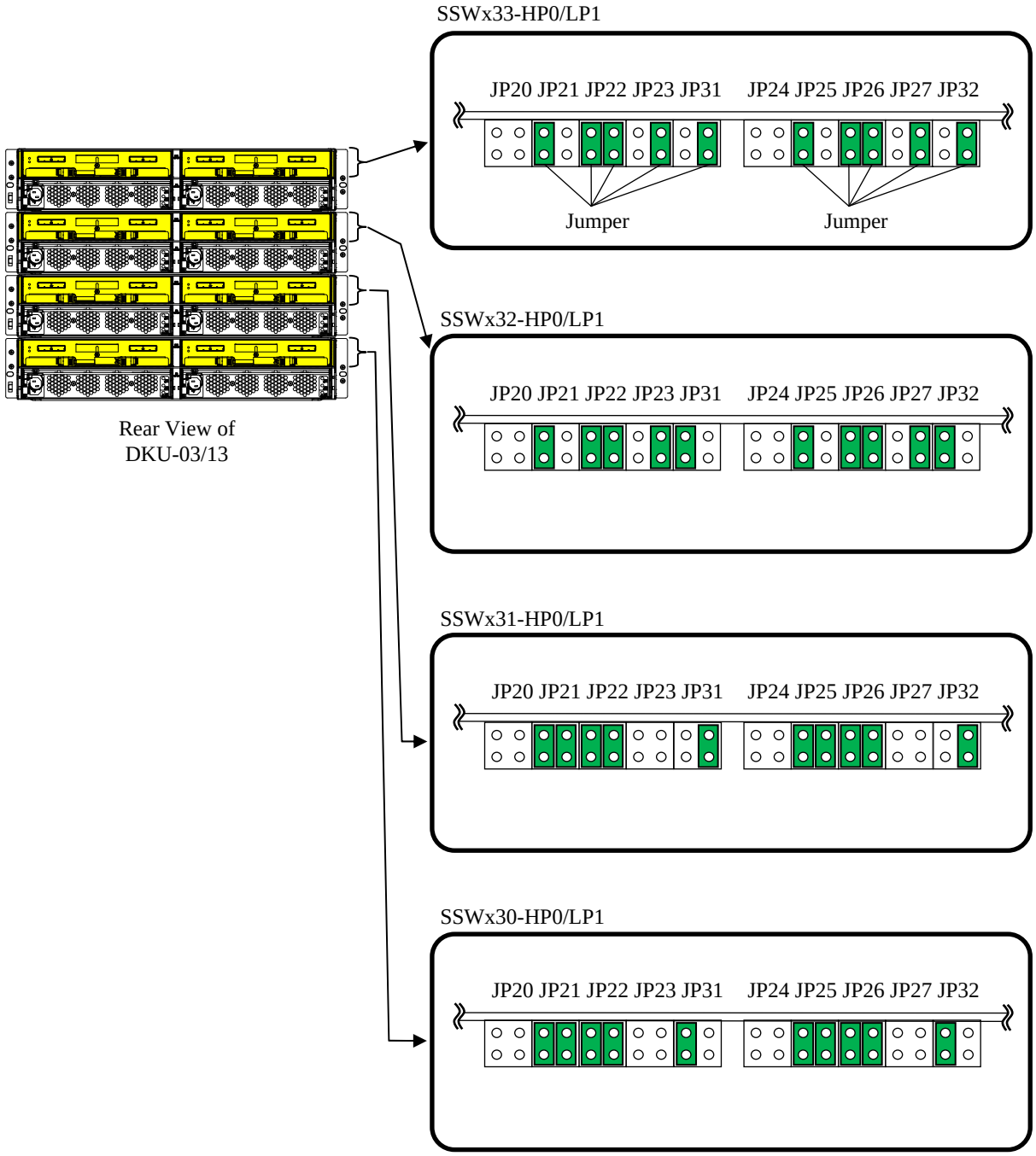


Fig. 6.1-20 Jumper Setting of SSW (DKU-03/13)

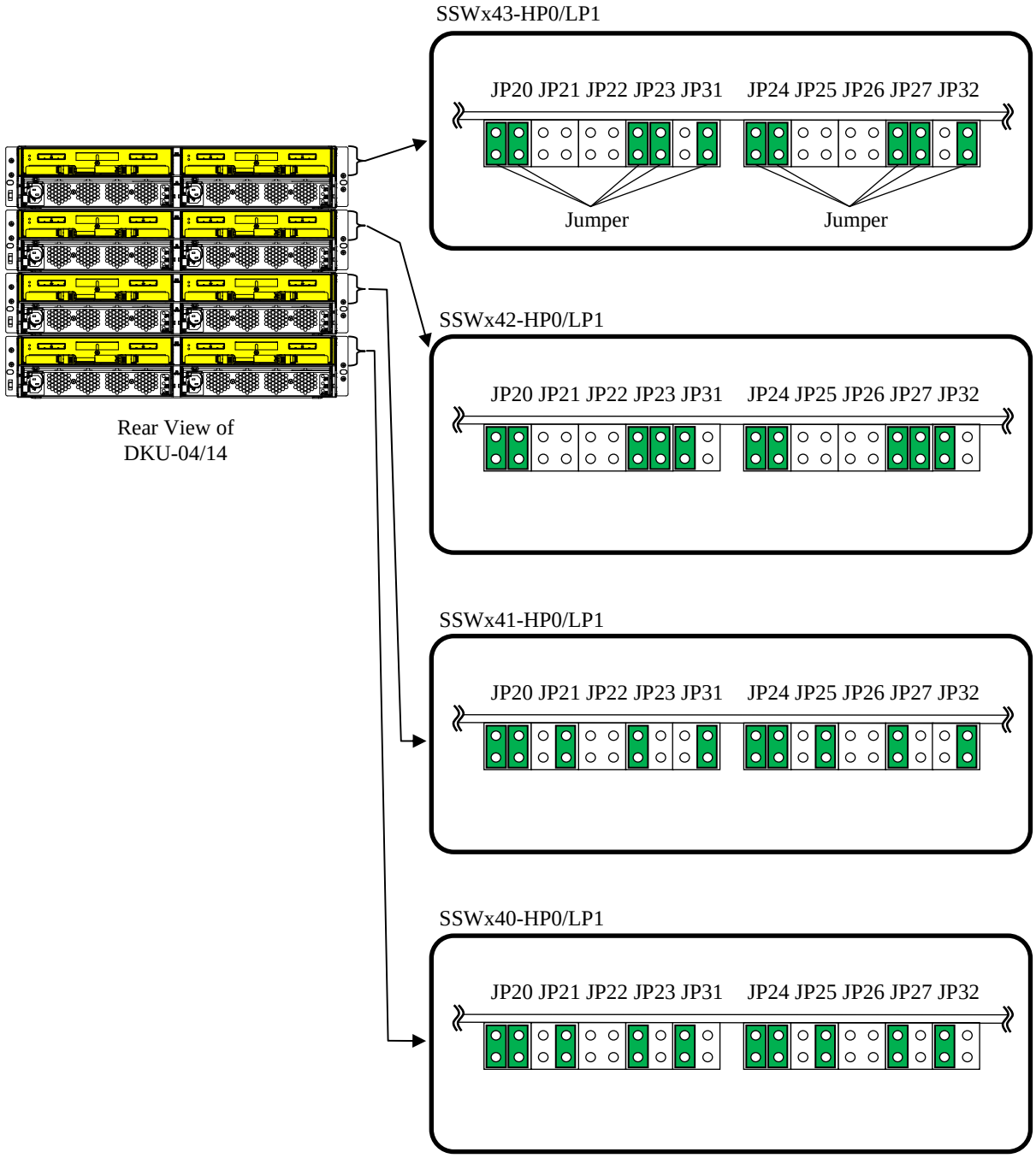


Fig. 6.1-21 Jumper Setting of SSW (DKU-04/14)

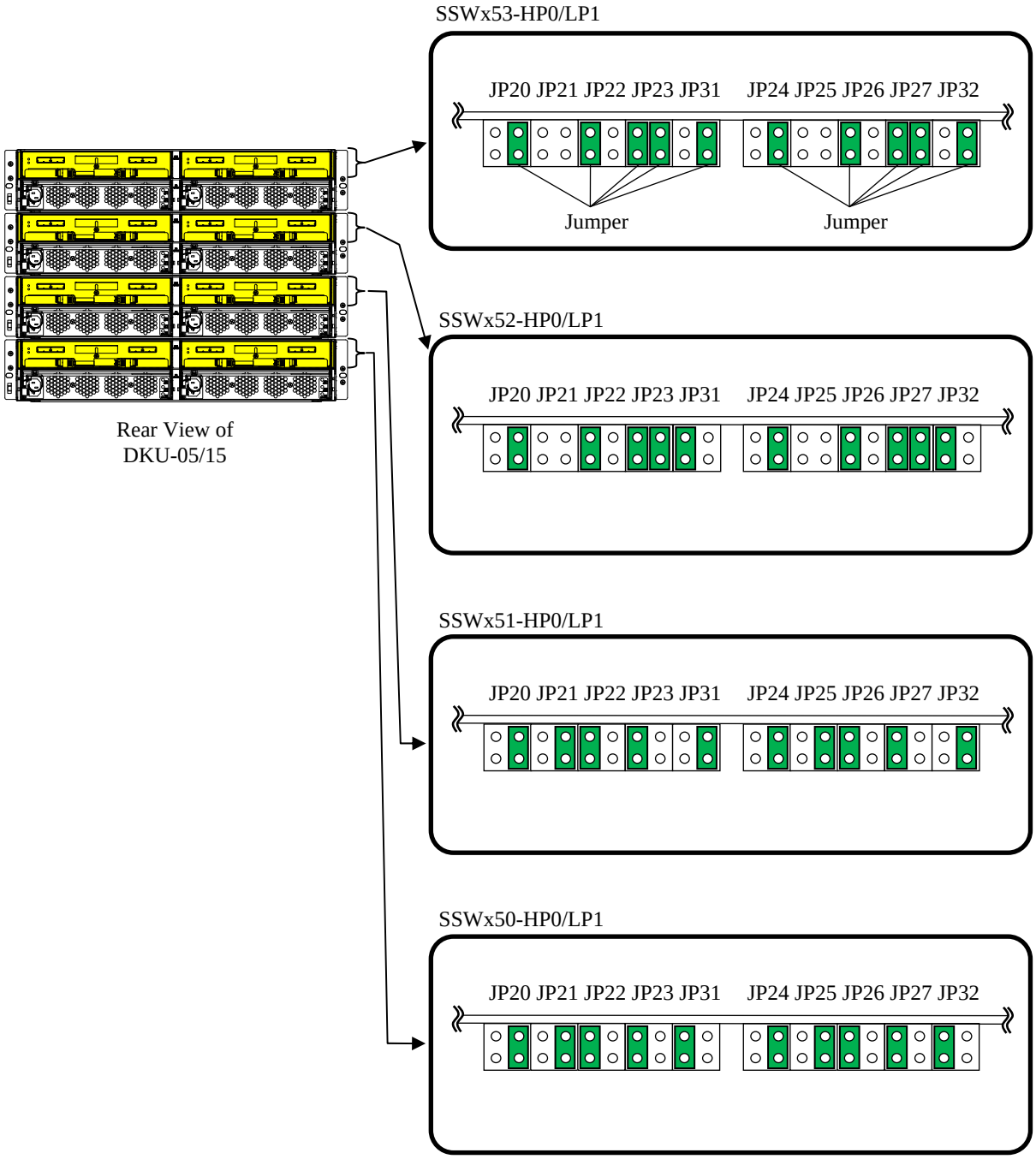


Fig. 6.1-22 Jumper Setting of SSW (DKU-05/15)

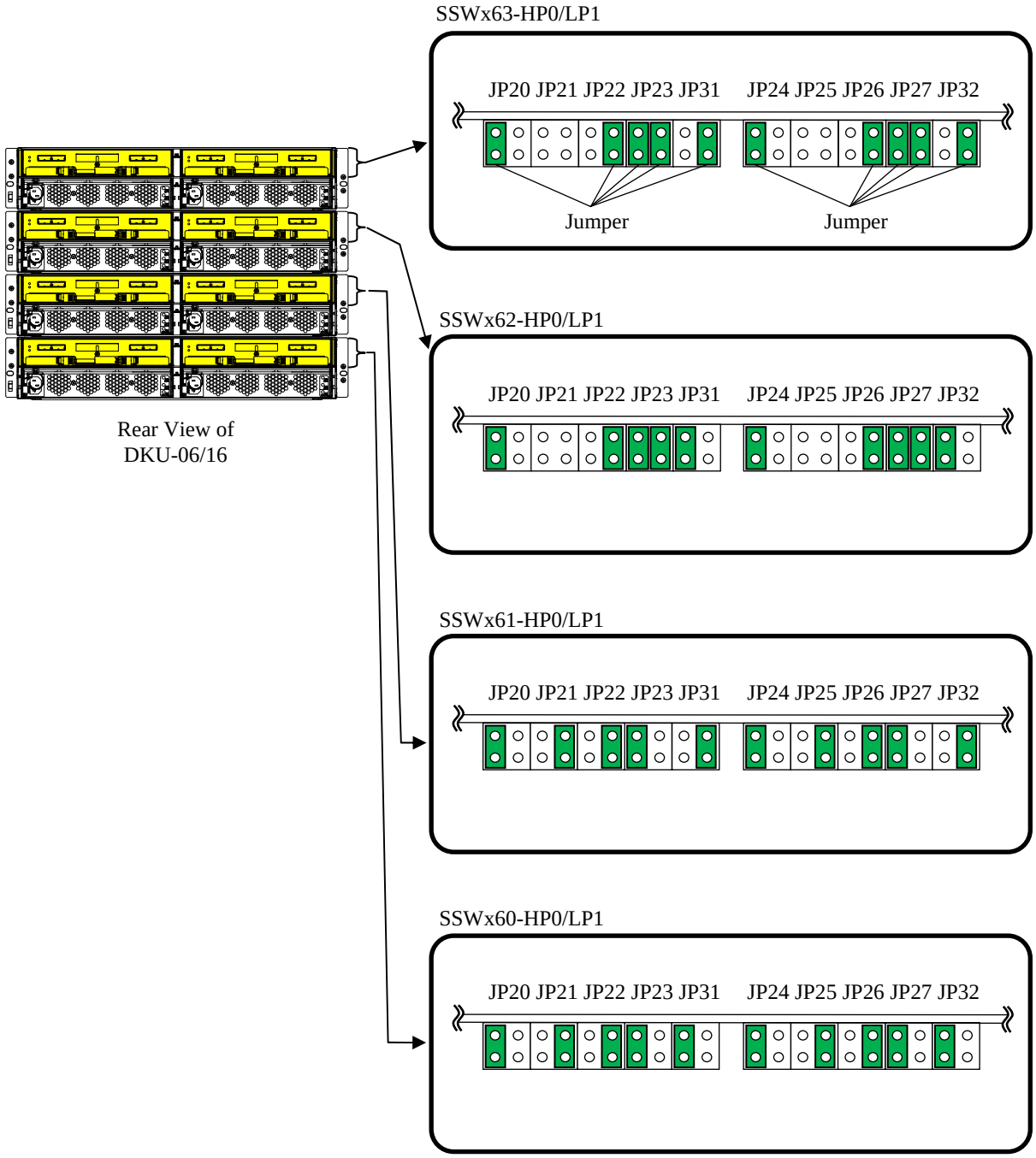


Fig. 6.1-23 Jumper Setting of SSW (DKU-06/16)

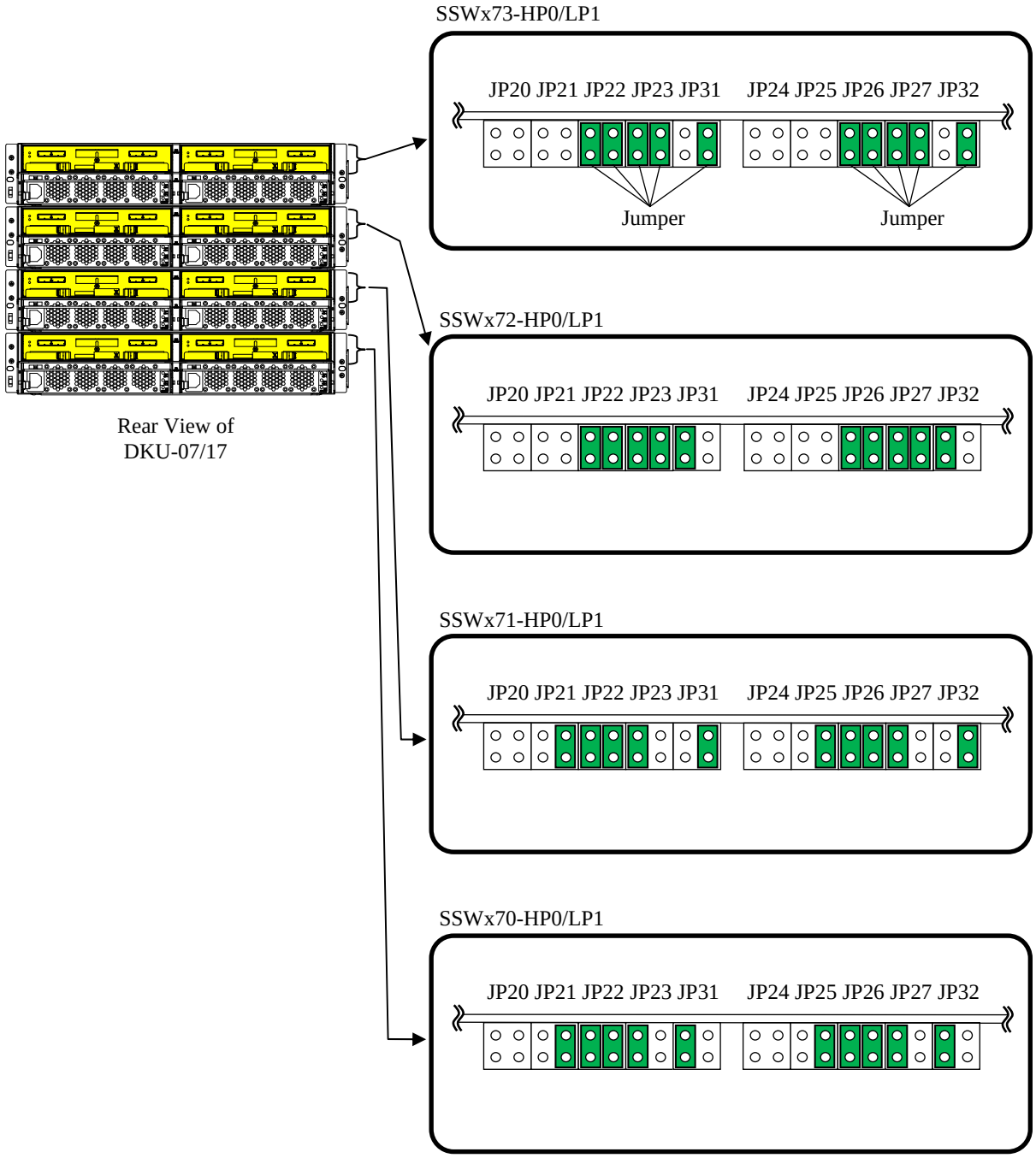


Fig. 6.1-24 Jumper Setting of SSW (DKU-07/17)